

ReleaseOrderPkg ID: SCGCQ01446742

Headline: Ventura FW - Full Flash Images for all Quanta (Package 50.2.2-0597) MR 7.2 Point

Package_Type: Ventura MR FW Package Broadcom - Quanta

Tier Account: LSI Internal

Package_Version: 50.2.2-0597

Date Generated: Sep 18, 2017

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Point Release (SCGCQ01446740) - NVDATA - 5.0200.00-0026



ReleaseOrderID: SCGCQ01446740 [Open In CQWeb](#) [Download](#)

Headline: 7.2 MR_SAS_NVDATA3.5_7.2_FC_Int 5.0200.00-0026

Release Version: 5.0200.00-0026

Product Org: MegaRAID

Product Gen: SAS3.5

Product Family: MR_FW

UCM Project: MR_SAS_NVDATA3.5_7.2_FC

Release Type: Point

UCM Stream: MR_SAS_NVDATA3.5_7.2_FC_Int

Owner: wyen

Release Baseline: baseline:MR_SAS_NVDATA3.5_7.2_FC_Int_2017-09-05.279@ \MegaRAID

Release Date / Time: 11-SEP-17

Release Type: Point

NVDATA Defects Fixed (10):

Defect ID: SCGCQ01374772

Headline: 7.2 Nvdata Disable external port for Nvme on Ventura 9480-8i8e Martinsville

Description Of Change: Disable external port for Nvme on Ventura 9480-8i8e Martinsville

Issue Description: 7.2 Nvdata Disable external port for Nvme on Ventura 9480-8i8e Martinsville

Steps To Reproduce: None

Defect ID: SCGCQ01378093

Headline: Junk characters are shown in HII PD menu on OEM 24i controller.

Description Of Change: The internal, external 'location' info of the port and the 'connector' strings on the different phy's was not populated in OEM 24i XML. Because of this the connectorPhyMap is not generated correctly on bootup, causing invalid data to be returned to the application.

The NVDATA is fixed with the correct information to resolve the issue.

Issue Description: Go to HII Pd menu and you will see the Junk characters with drives connected to OEM 24i.

Steps To Reproduce: Boot up 24 i controller with drives attached and go to HII Pd menu and you will see the Junk characters with drives connected to OEM 24i.

Defect ID: SCGCQ01385103

Headline: MR PARAMS page 0 does not get updated with the newer ROP that has NVDATA with a change in the same page.

Description Of Change: Modified NVDATA to have a default personality so that there is no need for CFGI update.

Issue Description: NVDATA does not have default personality which results in FW writing one(RAID) and that causes CFGI to mark a flag that prevents future updates as CFGI does not have way to differentiate user updated or FW updated.

Steps To Reproduce: Upgrade ROP that has new NVDATA change in MR_PARAMS_PAGE0

Defect ID: SCGCQ01387437

Headline: Spinup drive count is not set properly on OEM controller

Description Of Change: IO Unit page is updated with appropriate values.

Issue Description: Spinup drive count is not set properly

Steps To Reproduce: Flash FW pkg to a OEM-16i controller. Attach more than 4 drives to the controller either directly or through straight BP. Power up the system and count how many drives spinup at a time.

Defect ID: SCGCQ01393563

Headline: Drive Locate Not Functional with OEM controller (MR7.1 Firmware ROP is okay)

Description Of Change: Modified ManPage6, changes polarity of BP_Type -> PL detects SAS BP, -> use SGPIO/SAS instead of I2C/NVMe

Issue Description: Changes in PL to support NVMe, need to modify OEM NVDATA to support it.

Steps To Reproduce: Try locate the drives, it will not work.

Defect ID: SCGCQ01404306

Headline: MR 7.2 : OCR is not enable by default for iMR

Description Of Change: In iMR generic nvdata file LSIASAS3408.xsd, disableOnlineCtrlReset was set to 1 as default value.

Fix issue by setting iMR nvdata disableOnlineCtrlReset to 0 as default value (same as MR nvdata).

Also made the same change to iMR nvdata file LSIASAS3416.xsd.

Issue Description: MR 7.2 : OCR is not enable by default for iMR.

Steps To Reproduce: MR 7.2 Patch 2 ROP 612
iMR controller
Set controller to factory default using StorCLI or Cn
Reboot server
Check OCR setting

StorCLI /c0 show ocr -> it show disable , it should enable by default for GCA code
WOrk for MR but not iMR

Defect ID: SCGCQ01411350
Headline: 7.2 Nvdata correct drive strength from 50 to 66 Ohm.

Description Of Change: Hi all, for Drive Strength change.
Wayne and I decide to push this change from XSD.
All OEM/ROMB will need to check if they want this change or not,
and overwrite base on that.

Here are the GPIOB changes.

For 16 phys card (LSISAS3416 and LSISAS3516),

GPIOB 0-3 GPIO 40-43
GPIOB 6-9 GPIO 46-49
GPIOB 20-23 GPIO 60-63
GPIOB 0-3 GPIO 66-69

For 8 phys card (LSISAS3408 and LSISAS3508),

GPIOB 0-3 GPIO 32-35
GPIOB 6-9 GPIO 38-41

Following are the content for the changes.
Flags bits [7:6] for drv strength, set to 1 for 66 Ohm.

0 = 100 ohm impedance (lowest current)
1 = 66 ohm impedance
2 = 50 ohm impedance
3 = 33 ohm impedance (highest current)

For example:

<Flags>B0</Flags> -> <Flags>70</Flags>
Binary 1011 0000 -> 0111 0000
Bit 6 and Bit 7 of 10(50 ohm) becomes 01(66 ohm)

I will make sure internal Channel XML with pre-defined GPIOB (Not using default GPIO definition)
will have this change in additional to XSD changes.

Issue Description: 7.2 Nvdata correct drive strength from 50 to 66 Ohm.

Steps To Reproduce: None.

Defect ID: SCGCQ01422849
Headline: Change SGPIO driving strength to 50-Ohm for MR boards, changes OEM boards' connector name to "Port 0 - 3" instead of CN0

Description Of Change: Modified 2 XSD for the SGPIO electrical driving strength.
Modified 3 XML for the connector names correction.

Issue Description: Change SGPIO driving strength to 50-Ohm for MR boards.
Changes OEM boards' connector name to "Port 0 - 3" instead of CN0

Steps To Reproduce: Verify the connector names through GUI.

Defect ID: SCGCQ01427338
Headline: After flashing 7.2.1 593 package controller is not booting up seen this on OEM 8i MR and 8i iMR

Description Of Change: An additional reserved element in XSD for 3408 (iMR) and 3508(8i MR) was causing the NVDATA access to go beyond allocated memory. Adjusted the reserved element in XSD to match the NVDATA structure to resolve the issue.

Issue Description: After flashing 7.2.1 593 package controller is not booting up seen this on OEM 8i MR and 8i iMR

Fails with below error on blootup

T3: C0:DM_Cfg_InitConfigPages: Using MPB
T3: C0:Nvdata versions: persist=00380005 default=00550008
T3: C0:iopiSODAlloc: size = 280, MemType = 1
T3: C0:iopiSODAlloc: size = 38dc, MemType = 3
T3: C0:DM_Cfg_FlashErase: address=xe8fc0000 length=262144 regionEndAddr=xe9000000
T3: C0:DM_Cfg_FlashErase: status 0x0
T6: C0:iopiFault:FaultCode = 00000704 - Trigger Analyzer - Call DmPILCollectPILDebugInfo if not dumping anything
T6: C0:Collecting Trace Buffer

T6: C0:MonParse: "dm dm 5: bad command
T6: C0:Collecting Ring Buffer

Steps To Reproduce: Flash 7.2.1 ROP 593 on OEM 8i MR and 8i iMR controller and reboot.

Defect ID: SCGCQ01439340
Headline: OEM 9440-8i showing wrong HDD sequence numbers

Description Of Change: Updated manufacturing pages with appropriate values.

Issue Description: Group id support is not enabled.

Steps To Reproduce: Reproduce steps:
1. Connect physical slot 4~7 drives to 530-8i port 0
2. Boot system to uefi setup
3. Check the drives sequence

NVDATA Enhancements Implemented (3):

EnhancementRequest ID: SCGCQ01378946
Headline: Request to Update Drive Spin-Up Settings on Ventura Controllers

Description Of Change:

- Spin up 4 drives at a time. (was 2)
- No required minimum delay between groups (just wait for drives to come ready).
- Allow up to 12 seconds for a drive to come ready (was 6 they think).

EnhancementRequest ID: SCGCQ01381125
Headline: Enabling amber LED activity on SAS/SATA drives.

Description Of Change: Updated manufacturing page with appropriate values.

EnhancementRequest ID: SCGCQ01401586
Headline: NVDATA- Request to work around the IDT errata, we are being asked that our controller ignore the D3hot command

Description Of Change: This enhancement adds support for enabling/disabling D3 Hot Issue Workaround SCGCQ01401585 using NVDATA setting 'enableD3HotWorkAround' in MrParamsPage0.

UCM Project: SBR_Bld_Tools
Release Type: GCA
UCM Stream: SBR_Bld_Tools_MR7_Dev
Owner: jgrivich
Release Baseline: SBR_Bld_Tools_MR7_Dev_2016-03-11_PACKAGE_TOOLS@\\MegaRAID
Release Date / Time: 12-APR-16
Release Type: GCA

SBR_TOOLS Enhancements Implemented (1):

EnhancementRequest ID: SCGCQ01064293
Headline: Add Support for DeviceID and Subsystem Insertion into PCI SET file
Description Of Change: Ability to update the base PCI_SET.bin file with Device ID, Vendor ID, Subsystem ID, and Subsystem Vendor ID

Point Release (SCGCQ01448852) - Hii - 07.02.05.02

ReleaseOrderID: SCGCQ01448852_ [Open In CQWeb](#) [Download](#)
Headline: MR 7.2 Patch HEFU HII For Ventura version 07.02.05.02 (SIGNED)
Release Version: 07.02.05.02
Product Org: MegaRAID
Product Gen: SAS3.5
Product Family: MR_BIOS
UCM Project: MR_SAS_HEFU_HII_7.2
Release Type: Point
UCM Stream: MR_SAS_HEFU_HII_7.2_Patch
Owner: sramach
Release Baseline: MR_SAS_HEFU_HII_7.2_Patch_2017-09-13_v07.02.05.02Signed@\\MegaRAID
Release Date / Time: 13-SEP-17
Release Type: Point

Point Release (SCGCQ01428924) - UEFI_Driver - 0x07020401

ReleaseOrderID: SCGCQ01428924_ [Open In CQWeb](#) [Download](#)
Headline: MR 7.2 Patch UEFI Driver v0x07020401 (SIGNED)
Release Version: 0x07020401
Product Org: MegaRAID
Product Gen: SAS3.5
Product Family: MR_BIOS
UCM Project: MR_UEFI_Drv_7.2
Release Type: Point
UCM Stream: MR_UEFI_Drv_7.2_Patch
Owner: sramach
Release Baseline: MR_UEFI_Drv_7.2_Patch_2017-08-23_v0x07020401Signed@\\MegaRAID
Release Date / Time: 23-AUG-17
Release Type: Point

Point Release (SCGCQ01421892) - BIOS - 7.02.03.1

ReleaseOrderID: SCGCQ01421892_ [Open In CQWeb](#) [Download](#)
Headline: Point Release: MR_SAS_BIOS_7.2_Ventura - 7.02.03.1
Release Version: 7.02.03.1
Product Org: MegaRAID
Product Gen: SAS3.5
Product Family: MR_BIOS
UCM Project: MR_SAS_BIOS_7.2
Release Type: Point
UCM Stream: MR_SAS_BIOS_7.2_Patch
Owner: dk020286
Release Baseline: MR_SAS_BIOS_7.2_Patch_2017-08-16_v7.02.03.1@\\MegaRAID
Release Date / Time: 16-AUG-17
Release Type: Point

BIOS Defects Fixed (2):

Defect ID: SCGCQ01379327
Headline: System reset is initiated twice in BIOS
Description Of Change: Rearranged the code to make sure reboot code is executed only once based on condition.
First check for Fault code and reset required bit is set, if not check for fault value and reset required bit condition.
Issue Description: When reset required bit is set, OpRom sends warm reboot request twice.
When system is in Fault and reset required bit is set, OpRom issues warm reboot command once and if the reboot does not happen as soon as the command is sent, there is a check, in OpRom again for warm reboot if reset required bit is set. This leads reboot to happen twice.
Steps To Reproduce: No

Defect ID: SCGCQ01406972
Headline: System stops at post in legacy mode boot in the multi controllers configuration where-in one of the controller is in fault state
Description Of Change: We have delay in microseconds in the controller bring up/initialization code, if the controller doesn't respond even after 3 mins, OpROM will display appropriate message and continue. In multiple controller scenario, it is seen that is not working as expected.
Changed the delay from microseconds to milliseconds.
Issue Description: In multiple controller scenario, when one of the controller is in fault state, system stops in POST during initialization of the other controller.
Steps To Reproduce: 1. Have two controllers in the setup and boot to OS.
2. Corrupt first controller flash image, active and backup region from megamon. This controller becomes faulty.
3. Hard reboot the system. -->System stops in post during initialization of the second controller.

GCA Release (SCGCQ01145903) - ROMENV - 1.12

ReleaseOrderID: SCGCQ01145903_ [Open In CQWeb](#) [Download](#)
Headline: Point Release: MR_FW_BLD_TOOLS - 1.12 ROMENV
Release Version: 1.12
Product Org: MegaRAID
Product Gen: SAS2.5
Product Family: MR_FW
UCM Project: MR_FW_BLD_TOOLS
Release Type: GCA
UCM Stream: MR_FW_BLD_TOOLS_Dev
Owner: buildsvc
Release Baseline: MR_FW_BLD_TOOLS-2016-07-27-1.12_REL_1469655797@\\MegaRAID

Release Date / Time: 27-JUL-16
Release Type: GCA

ROMENV Enhancements Implemented (1):

EnhancementRequest ID: SCGCQ01145093
Headline: Modify RomEnv to use Flash Header CRC instead of EOF CRC when extended header keyword is present
Description Of Change: Modify RomEnv to use Flash Header CRC instead of EOF CRC when extended header keyword is present

Point Release (SCGCQ01106013) - MR_FW_BLD_TOOLS - 1.01

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ReleaseOrderID: SCGCQ01106013 [Open In CQWeb](#) [Download](#)
Headline: Point Release: MR_FW_BLD_TOOLS - BinToRom v1.01
Release Version: 1.01
Product Org: MegaRAID
Product Gen: SAS2.5
Product Family: MR_FW
UCM Project: MR_FW_BLD_TOOLS
Release Type: Point
UCM Stream: MR_FW_BLD_TOOLS_Dev
Owner: buildsvc
Release Baseline: MR_FW_BLD_TOOLS-2016-06-01-1.01_REL_1464821096@\\MegaRAID
Release Date / Time: 01-JUN-16
Release Type: Point

MR_FW_BLD_TOOLS Enhancements Implemented (1):

EnhancementRequest ID: SCGCQ01105905
Headline: Add Support for Environmental Offset
Description Of Change: Add Support for Environmental Offset

GCA Release (SCGCQ00791254) - FCODE - 4.17.08.00

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ReleaseOrderID: SCGCQ00791254 [Open In CQWeb](#) [Download](#)
Headline: Fcode 4.17.08.00
Release Version: 4.17.08.00
Product Org: MegaRAID
Product Gen: SAS3
Product Family: MR_BIOS
UCM Project: MR_FCODE
Release Type: GCA
UCM Stream: MR_FCODE_Rel
Owner: sramach
Release Baseline: MR_FCODE_Rel_2014-11-07_v4.17.08.00@\\MegaRAID
Release Date / Time: 07-NOV-14
Release Type: GCA

Point Release (SCGCQ01415846) - SBR_HW - 1.10

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ReleaseOrderID: SCGCQ01415846 [Open In CQWeb](#) [Download](#)
Headline: HW Settings v1.10 - Add HW_SET changes needed for ISSI SPI Flash Parts (IS25WP128 and IS25WP256D) support
Release Version: 1.10
Product Org: MegaRAID
Product Gen: SASx
Product Family: MR_FW
UCM Project: SBR_Bld_Tools
Release Type: Point
UCM Stream: SBR_Bld_Tools_MR7_Dev
Owner: wyen
Release Baseline: SBR_Bld_Tools_MR7_Dev_2016-03-11_HW_SET.862@\\MegaRAID
Release Date / Time:
Release Type: Point

Beta Release (SCGCQ01406157) - SBR_RMC - 283

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ReleaseOrderID: SCGCQ01406157 [Open In CQWeb](#) [Download](#)
Headline: Beta Release: MR_RMC_SAS3.5_7.2 - 283 SBR_RMC
Release Version: 283
Product Org: MegaRAID
Product Gen: SAS3.5
Product Family: MR_RMC_FW
UCM Project: MR_RMC_SAS3.5_7.2
Release Type: Beta
UCM Stream: MR_RMC_SAS3.5_7.2_Rel
Owner: buildsvc
Release Baseline: MR_RMC_SAS3.5_7.2-2017-07-27-283_REL_1501174741@\\MegaRAID
Release Date / Time: 27-JUL-17
Release Type: Beta

SBR_RMC Defects Fixed (1):

Defect ID: SCGCQ01351249
Headline: [HW]Ventura 7.3: Previous configuration completely missing at boot (SAS SERDES training error)
Description Of Change: Corrected the check which waits for the power domain to reach thresholds.
Issue Description: While doing double-dip COL, pinned cache occurred and a message "Previous configuration completely missing at boot"
Steps To Reproduce: 1. Start wrnd128k vdbench IO to all VDs
2. After 2 minutes, pull power via APC
3. After 5 seconds, reapply power.
After the 8th cycle or so, this error occurs.

Alpha Release (SCGCQ01314431) - SBR_PCE - 1.09

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ReleaseOrderID: SCGCQ01314431_ [Open In CQWeb](#) [Download](#)
Headline: PCE Settings v1.09 Update
Release Version: 1.09
Product Org: MegaRAID
Product Gen: SASx
Product Family: MR_FW
UCM Project: SBR_Bld_Tools
Release Type: Alpha
UCM Stream: SBR_Bld_Tools_MR7_Dev
Owner: st887734
Release Baseline: SBR_Bld_Tools_MR7_Dev_2016-09-27_SCGCQ01178301@\\MegaRAID
Release Date / Time: 03-APR-17
Release Type: Alpha

Alpha Release (SCGCQ01173982) - SBR_SPICO - 1.06



ReleaseOrderID: SCGCQ01173982_ [Open In CQWeb](#) [Download](#)
Headline: SPICO v1.06
Release Version: 1.06
Product Org: MegaRAID
Product Gen: SASx
Product Family: MR_FW
UCM Project: SBR_Bld_Tools
Release Type: Alpha
UCM Stream: SBR_Bld_Tools_MR7_Dev
Owner: jgrivich
Release Baseline: SBR_Bld_Tools_MR7_Dev_2016-09-01@\\MegaRAID
Release Date / Time: 01-SEP-16
Release Type: Alpha

Point Release (SCGCQ01419360) - Firmware - 5.020.00-0910



ReleaseOrderID: SCGCQ01419360_ [Open In CQWeb](#) [Download](#)
Headline: Point Release: MR_FW_SAS3.5_7.2_FC - 5.020.00-0910 Firmware
Release Version: 5.020.00-0910
Product Org: DCSG
Product Gen: SAS3.5
Product Family: MR_FW
UCM Project: MR_FW_SAS3.5_7.2_FC
Release Type: Point
UCM Stream: MR_FW_SAS3.5_7.2_FC_Rel
Owner: buildsvc
Release Baseline: MR_FW_SAS3.5_7.2_FC-2017-08-11-5.020.00-0910_REL_1502491530@\\MegaRAID
Release Date / Time: 15-AUG-17
Release Type: Point

Firmware Defects Fixed (3):

Defect ID: SCGCQ01399701
Headline: MR 6.14:Interrupting Offload will Lead to Next offload cycle to fail
Description Of Change: There are two meta-data's in the ONFI.

1. Flash config meta-data
2. Offload meta-data.

When offload metadata read is failed, firmware will reset the marker for all the bad blocks. So these bad blocks are assumed as good blocks. During Erase firmware refers the flash config meta data and skips the erase for these blocks. Since these bad blocks are included as part of offload descriptor, read will be failed on the next restore as the blocks are being used without erase.

1. Code is modified not to include these as part of offload descriptor.
2. Don't reset the marker for the bad blocks as we have the valid information of bad blocks in flash config meta-data
Issue Description: Hitting Cached Data Lost in the following case.

Steps :

Create Few Logical disks and start IO.
Power-OFF server and during Offload going on Remove SuperCap.
During system is powered –off, Reconnect the SuperCap and Power ON server.
We Expect the Restore Failure due to offload was not proper.
Now Again Start IO and Power Off (Note –do not remove Super cap).
Start the Server again to see Restore is successful. But again reports that cached Data Lost.
PFA logs with clog enabled.
Steps To Reproduce: Hitting Cached Data Lost in the following case.

Steps :

Create Few Logical disks and start IO.
Power-OFF server and during Offload going on Remove SuperCap.
During system is powered –off, Reconnect the SuperCap and Power ON server.
We Expect the Restore Failure due to offload was not proper.
Now Again Start IO and Power Off (Note –do not remove Super cap).
Start the Server again to see Restore is successful. But again reports that cached Data Lost.
PFA logs with clog enabled.

Defect ID: SCGCQ01404373
Headline: Exception Handler Return address 0x01362c7d when boot up
Description Of Change: Fix ddf module use PROFILE_MAX_LOGICAL_DRIVES instead of MAX_LOGICAL_DRIVES.

This cause LdbbmList variable to over index of what it was allocated.
Issue Description: Exception Handler Return address 0x01362c7d when boot up
Steps To Reproduce: Talladega 4G , bios 9 July 25, 2017
Boot up

Defect ID: SCGCQ01413557
Headline: Parameter Page Reads fail during ONFI bringup.
Description Of Change: When parameter page read fails, FW hard-codes the required values.
Issue Description: FW reads parameter page at the start of ONFI bringup. If parameter page reads fail, FW either crashes or continues to boot without restoring cache.
Steps To Reproduce: Flash latest firmware package on failing boards.

Point Release (SCGCQ01350333) - IT Bootloader - 02.25.05.00



ReleaseOrderID: SCGCQ01350333_ [Open In CQWeb](#) [Download](#)
Headline: MR PL/SCS Libs: SASFW_Ventura_Phase_2.0 - 02.25.05.00 Firmware
Release Version: 02.25.05.00
Product Org: SCS
Product Gen: SAS3.5
Product Family: SAS3.5_Controller_FW

UCM Project: SASFW_Ventura_Phase_2.0
Release Type: Point
UCM Stream: SASFW_Ventura_Phase_2.0_MR
Owner: buildsvc
Release Baseline: SASFW_Ventura_Phase_2.0-2017-05-22-02.25.05.00_REL_1495439297@
SAS_CTRL_FW
Release Date / Time: 22-MAY-17
Release Type: Point

IT Bootloader Defects Fixed (2):

Defect ID: SCGCQ01314177
Headline: Can't program SPDimage to SPD EEPROM
Description Of Change: SPD downloads are divided into 2 types:
Images <= 256 bytes - controller assumes a 2Kbit EEPROM is attached to I2C.
Images > 256bytes - controller assumes larger EEPROM

The controller firmware makes a number of assumptions about how to handle the EEPROM communication based on image length.
In this case, we were not properly handling the situation where the image length was exactly 256 bytes.
Issue Description: Attempting to program an SPD image through StoreCLI fails.
Controller returns INVALID_SGL
Steps To Reproduce: 1) Execute SPD programming command:
storfluo.efi /c0 set HWDDevice devicetype=5 fileone=vtboot.rom filetwo=9460.spd

Defect ID: SCGCQ01339127
Headline: ATA Passthrough MPT frame content has overwritten
Description Of Change: If the original frame is modified during the SCSI ATA passthrough handling , PL will make sure that the original frame is restored before releasing the raw internal frame .
Issue Description: During the handling of ATA pass-through command , PL backups the original message frame and then modifies the original frame as SATA passthrough frame . When the SATA passthrough IO is completed , the original frame is restored. In this issue , after sending the command to the drive , the drive stopped responding . Eventually task management is issued to clean up the IO . During task management cleanup, the raw internal frame, which used to actually issue STP command on behalf of the SATA passthrough command, is freed , causes the local pass-through context to be lost. Because of this, during task management completion when IOs in abort list are failed, original frame contents could not be restored as copy of original mid is lost with SATA passthrough context.
Steps To Reproduce: Issue SCSI ATA Passthrough command to a SATA drive that is not responding.

GCA Release (SCGCQ01343030) - BootBlock - 7.02.00.00-0016

ReleaseOrderID: SCGCQ01343030_Open In CQWeb Download
Headline: Ventura Boot Block - 7.02.00.00-0016
Release Version: 7.02.00.00-0016
Product Org: MegaRAID
Product Gen: SAS3.5
Product Family: MR_FW
UCM Project: MR_BOOTBLOCK_Ventura
Release Type: GCA
UCM Stream: MR_BOOTBLOCK_Ventura_Rel
Owner: schawala
Release Baseline: MR_BOOTBLOCK_Ventura_Rel_2017-05-10@MegaRAID
Release Date / Time: 07-JUL-17
Release Type: GCA

BootBlock Defects Fixed (2):

Defect ID: SCGCQ01320842
Headline: Montask in Pbam.c during COL A/C cycle testing
Description Of Change: The change is to set execute never bit for device memory regions for which boot block sets up TLBs
Issue Description: The issue was caused by speculative instruction fetch to an unintentional address since pbam is marked as device memory but we need to set "execute never" bit explicitly to avoid speculative prefetches on it
Steps To Reproduce: 1) Start IO on VD's (Diskio)
2) let IO run for 5 minutes
3) pull AC power
4) reapply power after 10 seconds
5) repeat steps 1-5

Defect ID: SCGCQ01324235
Headline: Increase padding
Description Of Change: Increase size of boot block image to 256kB so no other images are copied with it.
Issue Description: NOPAD package has a 128kB boot block image followed by a BIOS image. When downloaded, 256kB is written to SPI, which writes the first 128kB of the BIOS image when the boot block is written. When booting, the partial BIOS image is found and a message is printed when the CRC does not match. The regular BIOS image is found and correctly validated, so there is no functional effect.
Steps To Reproduce: Flash NOPAD package image. Reboot and observe console messages from boot block.

GCA Release (SCGCQ01108283) - SBR_CPLD - 5

ReleaseOrderID: SCGCQ01108283_Open In CQWeb Download
Headline: MegaRaid Ventura CPLDs
Release Version: 5
Product Org: MegaRAID
Product Gen: SASx
Product Family: MR_FW
UCM Project: SBR_Bld_Tools
Release Type: GCA
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