

SSBLOG SECTION

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SSBLOG05-270	• Byte27 = 81 (CHK3)
SSBLOG05-280	Byte27 = 81 (CHK3) Byte28-31 Detail
SSBLOG05-280	(1) Byte28 = 00 or 01 or 02 or 10 or F0
SSBLOG05-290	(2) Byte28 = 03 or 20 or 30
SSBLOG05-300	(3) Byte28 = 04
SSBLOG05-310	(4) Byte28 = 28 or 38
SSBLOG05-320	(5) Byte28 = 40 or 48 or 50 or 58
SSBLOG05-330	(6) Byte28 = 81 or 82 or 83 or 84 or 89 or 8A or 8B or 8C or 8D or 8E or 8F or 90 or C5 or C6 or C7 or C8 or C9 or CA or CB or CC or CD or CE or CF or D0

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SSBLOG05-340	Byte27 = 81 (CHK3) Byte40-7F Detail
SSBLOG05-340	(1) Byte28 = 00
SSBLOG05-380	(2) Byte28 = 01
SSBLOG05-420	(3) Byte28 = 02
SSBLOG05-460	(4) Byte28 = 03
SSBLOG05-500	(5) Byte28 = 04
SSBLOG05-540	(6) Byte28 = 10
SSBLOG05-580	(7) Byte28 = 20 or 30
SSBLOG05-620	(8) Byte28 = 28 or 38
SSBLOG05-660	(9) Byte28 = 40 or 48 or 50 or 58 or 8B or 8C or C9 or CA
SSBLOG05-700	(10) Byte28 = 81 or 82 or 83 or 84 or C5 or C6 or C7 or C8
SSBLOG05-740	(11) Byte28 = 89 or 8A or 8D or 8E or 8F or 90 or CB or CC or CD or CE or CF or D0
SSBLOG05-780	(12) Byte28 = F0
SSBLOG05-820	• Byte27 = 84 (CHK4)
SSBLOG05-830	Byte27 = 84 (CHK4) Byte28-31
SSBLOG05-830	(1) Byte28 = 00/01/02/03/04/0e (CHA LR CHK4), Byte28 = 0x40/41/42/43/44/4e (DKA LR CHK4), Byte28 = 80/81/82/83/84/8e (CHA MHUB CHK4)
SSBLOG05-840	(2) Byte28 = 05/06/07/08/09/0f/11 (CHA LR CHK4), Byte28 = 0x45/46/47/48/49/4f/51 (DKA LR CHK4), Byte28 = 85/86/87/88/89/8f/91 (CHA MHUB CHK4)
SSBLOG05-850	(3) Byte28 = 0a (CHA LR CHK4), Byte28 = 0x4a(DKA LR CHK4), Byte28 = 8a (CHA MHUB CHK4)
SSBLOG05-860	(4) Byte28 = 90 (CHA MHUB CHK4)
SSBLOG05-870	Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail
SSBLOG05-870	(1) Byte28 = 00/40/80
SSBLOG05-910	(2) Byte28 = 01/41/81
SSBLOG05-950	(3) Byte28 = 02/42/82
SSBLOG05-990	(4) Byte28 = 03/43/83
SSBLOG05-1030	(5) Byte28 = 04/44/84
SSBLOG05-1070	(6) Byte28 = 05/45/85
SSBLOG05-1100	(7) Byte28 = 06/46/86
SSBLOG05-1140	(8) Byte28 = 07/47/87
SSBLOG05-1180	(9) Byte28 = 08/48/88
SSBLOG05-1220	(10) Byte28 = 09/49/89
SSBLOG05-1260	(11) Byte28 = 0a/4a/8a
SSBLOG05-1270	(12) Byte28 = 0e/4e/8e
SSBLOG05-1290	(13) Byte28 = 0f/4f/8f
SSBLOG05-1330	(14) Byte28 = 90
SSBLOG05-1370	(15) Byte28 = 11/51/91

SSBLOG00-30

SSBLOG05-1380	• Byte27 = 89 & Byte0D Bit2 = 0 (FICON CHA CHK2)
SSBLOG05-1390	• Byte27 = 89 & Byte0D Bit2 = 1 (FIBRE CHA CHK2)
SSBLOG05-1400	Byte27 = 89 (CHA CHK2) Byte28-31 Detail
SSBLOG05-1400	(1) Byte28 = 01 & Byte0D Bit2 = 0
SSBLOG05-1410	(2) Byte28 = 02 & Byte0D Bit2 = 0
SSBLOG05-1420	(3) Byte28 = 00/01/02/03/10/11/12/13 & Byte0D Bit2 = 1
SSBLOG05-1430	(4) Byte28 = 04/14
SSBLOG05-1440	(5) Byte28 = 08/0c/18/1c
SSBLOG05-1450	(6) Byte28 = 09/0d/19/1d
SSBLOG05-1460	(7) Byte28 = 80/81/90/91
SSBLOG05-1470	(8) Byte28 = 85/95
SSBLOG05-1480	(9) Byte28 = 88/8c/98/9c
SSBLOG05-1490	(10) Byte28 = 8a/8e/9a/9e
SSBLOG05-1500	Byte27 = 89 (CHA CHK2) Byte40-7F Detail
SSBLOG05-1500	(1) Byte28 = 00/01/02/03/10/11/12/13
SSBLOG05-1540	(2) Byte28 = 04/14
SSBLOG05-1580	(3) Byte28 = 08/09/18/19
SSBLOG05-1620	(4) Byte28 = 0c/0d/1c/1d
SSBLOG05-1660	(5) Byte28 = 80/81/90/91
SSBLOG05-1700	(6) Byte28 = 85/95
SSBLOG05-1730	(7) Byte28 = 88/89/98/99
SSBLOG05-1770	(8) Byte28 = 8a/9a
SSBLOG05-1800	(9) Byte28 = 8e/9e
SSBLOG05-1830	• Byte27 = 8A (DKA CHK2)
SSBLOG05-1840	Byte27 = 8A (DKA CHK2) Byte28-31 Detail
SSBLOG05-1840	(1) Byte28 = 40/41/42/43/50/51/52/53
SSBLOG05-1850	(2) Byte28 = 44/54
SSBLOG05-1860	(3) Byte28 = 48/4c/58/5c
SSBLOG05-1870	(4) Byte28 = 49/4d/59/5d
SSBLOG05-1880	Byte27 = 8A (DKA CHK2) Byte40-7F Detail
SSBLOG05-1880	(1) Byte28 = 40/41/42/43/50/51/52/53
SSBLOG05-1920	(2) Byte28 = 44/54
SSBLOG05-1960	(3) Byte28 = 48/49/58/59
SSBLOG05-2000	(4) Byte28 = 4c/4d/5c/5d
SSBLOG05-2040	• Byte27 = 8B (DRR CHK2)
SSBLOG05-2050	Byte27 = 8B (DRR CHK2) Byte28-31 Detail
SSBLOG05-2050	(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d
SSBLOG05-2060	Byte27 = 8B (DRR CHK2) Byte40-7F Detail
SSBLOG05-2060	(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d

SSBLOG00-40

SSBLOG05-2100	• Byte27 = 8C (SVP Failure : SVP(PC))
SSBLOG05-2120	Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail
SSBLOG05-2120	(1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure / Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error / SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over / SVP change refusal / Ip address setting error / Subnet mask setting error / Virus detection error / SVP environment error / Situation observation error / Dummy PS/Off starting (normal)
SSBLOG05-2140	(2) SSVP Failure
SSBLOG05-2150	(3) CUDG detected error
SSBLOG05-2190	(4) BOOT detected error
SSBLOG05-2200	• Byte27 = 8D (Power Failure) Common information
SSBLOG05-2220	Byte27 = 8D (Power Failure) Byte28-31 detail
SSBLOG05-2250	• Byte27 = 8E (CHK1)
SSBLOG05-2250	(1) Byte28 = 20 or 21 or 22 or 23 or 24 or 25 or FE
SSBLOG05-2260	(2) Byte28 = 30
SSBLOG05-2270	(3) Byte28 = 40 or 41 or 42 or 43 or 44 or 45 or 50 or FF
SSBLOG05-2280	(4) Byte28 = 47
SSBLOG05-2290	(5) Byte28 = 51
SSBLOG05-2300	(6) Byte28 = 60 or 61 or 62 or 63 or 64 or 65 or 66 or 67 or 68
SSBLOG05-2310	(7) Byte28 = 80 or 81
SSBLOG05-2320	Byte27 = 8E (CHK1) Byte40-7F Detail
SSBLOG05-2320	(1) Byte28 = 20
SSBLOG05-2350	(2) Byte28 = 21 or 22 or 23 or 24 or 25
SSBLOG05-2380	(3) Byte28 = 30
SSBLOG05-2420	(4) Byte28 = 40
SSBLOG05-2460	(5) Byte28 = 41
SSBLOG05-2500	(6) Byte28 = 42 or 43 or 44 or 45 or 46 or 47 or 50 or 51
SSBLOG05-2540	(7) Byte28 = 60 or 61
SSBLOG05-2580	(8) Byte28 = 62
SSBLOG05-2620	(9) Byte28 = 63
SSBLOG05-2660	(10) Byte28 = 64 or 65 or 66 or 67
SSBLOG05-2700	(11) Byte28 = 80 or 81
SSBLOG05-2720	• Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)
SSBLOG05-2730	• Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset)
SSBLOG05-2760	• Byte27 = 8E & Byte34:bit4-7=3 (wait SENSE Timeout)
SSBLOG05-2780	• Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset)
SSBLOG05-2800	• Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset)
SSBLOG05-2820	• Byte27 = 8E & Byte34:bit4-7=8 (Force Reset)
SSBLOG05-2840	• Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset)

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SSBLOG05-2860	• Byte27 = 8F (Logical Inconsistency)
SSBLOG05-2880	• Byte27 = 9F (Trace data/Log data)
SSBLOG05-2900	• Byte27 = EC (Environment Monitor Detected DKC Failure)
SSBLOG05-2930	• Byte27 = EE (Environment Monitor Detected DKU Failure)
SSBLOG05-2960	• Byte27 = F0 (Operation Terminated)
SSBLOG05-2980	• Byte27 = F1 (Micro-program Detected Cache Failure)
SSBLOG05-3000	• Byte27 = F2 (CACHE ERR)
SSBLOG05-3010	Byte27 = F2 (CACHE ERR) Byte28-2C,30 Detail
SSBLOG05-3020	Byte27 = F2 (CACHE ERR) Byte40-7F Detail
SSBLOG05-3020	(1) Byte28 = x'1', x'2', x'5', x'6', x'7' : C_path error
SSBLOG05-3060	(2) Byte28 = x'3', x'4', x'8', x'9' : CACHE error
SSBLOG05-3100	• Byte27 = F5 (Remote Copy Paths Removed/Restored)
SSBLOG05-3120	• Byte27 = F6 (CFW Access not Authorized)
SSBLOG05-3140	• Byte27 = FA (NVS Terminated)
SSBLOG05-3160	• Byte27 = FB (TrueCopy for Mainframe Pair suspend)
SSBLOG05-3180	• Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP/Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support)
SSBLOG05-3190	• Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP/Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information
SSBLOG05-3190	(1) Byte28 = 01 : CHF_DXBF correctable error
SSBLOG05-3200	(2) Byte28 = 04 : DKF_DXBF correctable error
SSBLOG05-3210	(3) Byte28 = 05 : CHM_DXBF correctable error
SSBLOG05-3220	(4) Byte28 = 02 : Cache Memory correctable error
SSBLOG05-3240	(5) Byte28 = 84 : SW correctable error
SSBLOG05-3260	(6) Byte28 = 81 : LR correctable error
SSBLOG05-3310	(7) Byte28 = 82 : MHUB correctable error
SSBLOG05-3360	(8) Byte28 = 83 : CMA correctable error
SSBLOG05-3380	(9) Byte28 = 11 : MPA correctable error
SSBLOG05-3430	(10) Byte28 = 40 : Abnormal DC Voltage control warning
SSBLOG05-3460	(11) Byte28 = 41 : Abnormal CEMODE warning
SSBLOG05-3470	(12) Byte28 = 42 : Abnormal CEDT warning
SSBLOG05-3480	(13) Byte28 = C0 : SFP (Tachyon) warning
SSBLOG05-3500	(14) Byte28 = C2 : SFP (FIVE) warning
SSBLOG05-3520	(15) Byte28 = 45 : Battery warning
SSBLOG05-3550	(16) Byte28 = 46 : SSD warning
SSBLOG05-3580	(17) Byte28 = 48 : MPPK Abnormal temperature warning
SSBLOG05-3590	(18) Byte28 = 49 : PK Abnormal temperature warning
SSBLOG05-3650	(19) Byte28 = 43 : Forced volatile reboot warning
SSBLOG05-3661	(20) Byte28 = C5 : SFP (CHG) warning
SSBLOG05-3663	(21) Byte28 = 47 : SSD Config warning

SSBLOG00-60

SSBLOG05-3670	• Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception)
SSBLOG05-3690	• Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception)
SSBLOG05-3710	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): Common information
SSBLOG05-3720	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 40/41/42/43
SSBLOG05-3730	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 45
SSBLOG05-3740	• Byte36 = 6F (Subsystem Information)
SSBLOG05-3760	• Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 0 (SPC Detected Failure : DSP)
SSBLOG05-3780	• Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1 (SPC Detected Failure: SAS)
SSBLOG05-3810	• Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure: DSP)
SSBLOG05-3830	• Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure: DSP)
SSBLOG05-3850	• Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
SSBLOG05-3861	• Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity)
SSBLOG05-3870	• Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready)
SSBLOG05-3890	• Byte26: b4-7 = F & Byte3C = F1 (DKC SIM)
SSBLOG05-3920	• Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM)
SSBLOG05-3950	• Byte26: b4-7 = F & Byte3C = FE (Device SIM)
SSBLOG05-3970	• Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM)

1. Introduction

The internal SSB (SSB Log) information is presented as either 24-byte compatibility SSB format or ECKD 32-byte SSB format. The 24-byte SSB is used for DKC and the 32-byte SSB is used for DKU.

The internal SSB is divided into the following sections:

- Internal SSB common information
This information is used for SSB management, and same for all formats.
- External SSB information presented to host.
This is 32 bytes sense information presented to host as SSB.
- Format-dependent information
This is auxiliary information for error analysis.

See Fig. 1-1 and Fig. 1-2.

Note: The byte offset in SSB logs is described in hexadecimal number.

This section is corresponding to SSB log on SVP.

If you want to know about SSB which was reported to host, you should see SSB section.

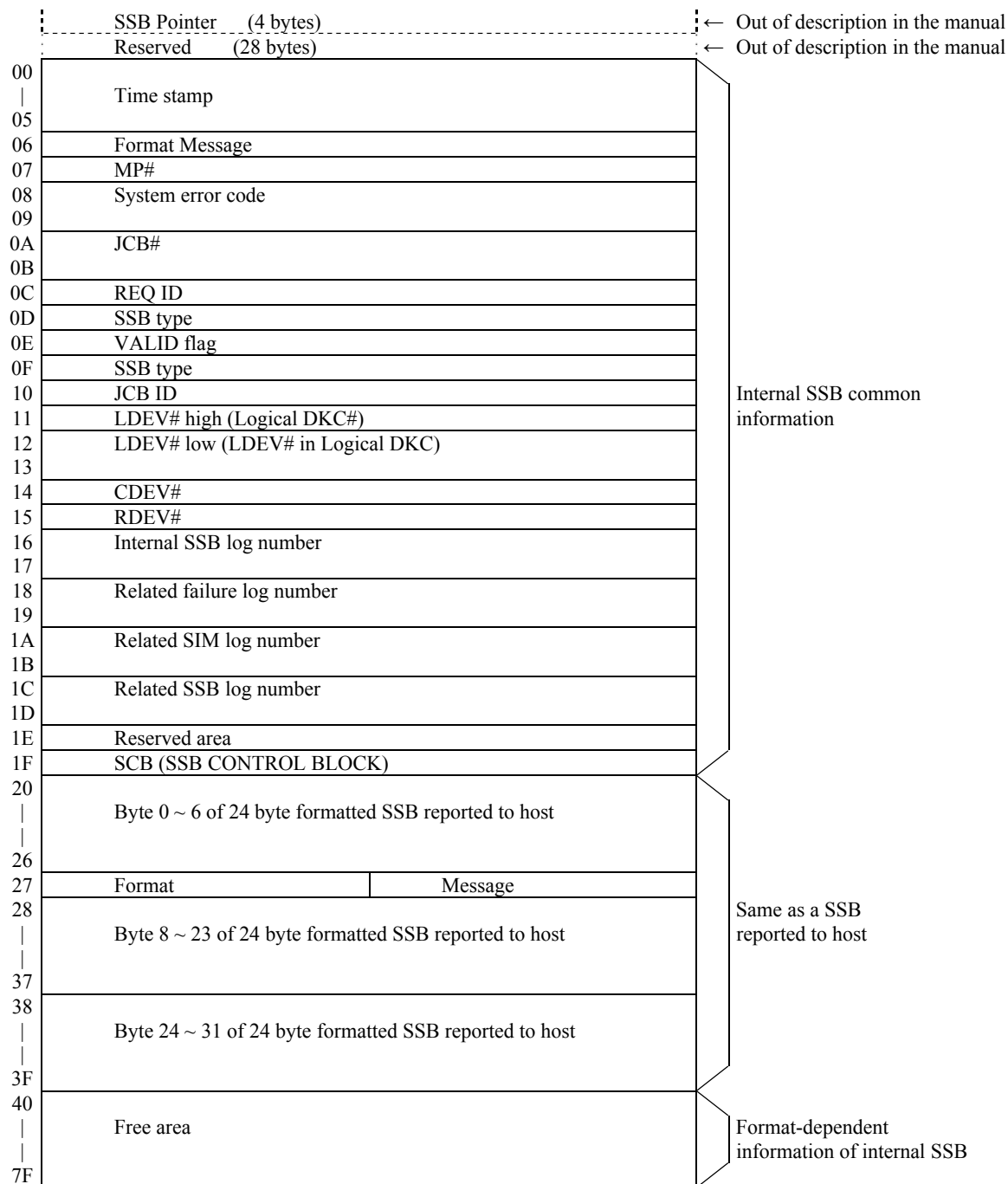


Fig. 1-1 Outline of 24-byte Format Internal SSB

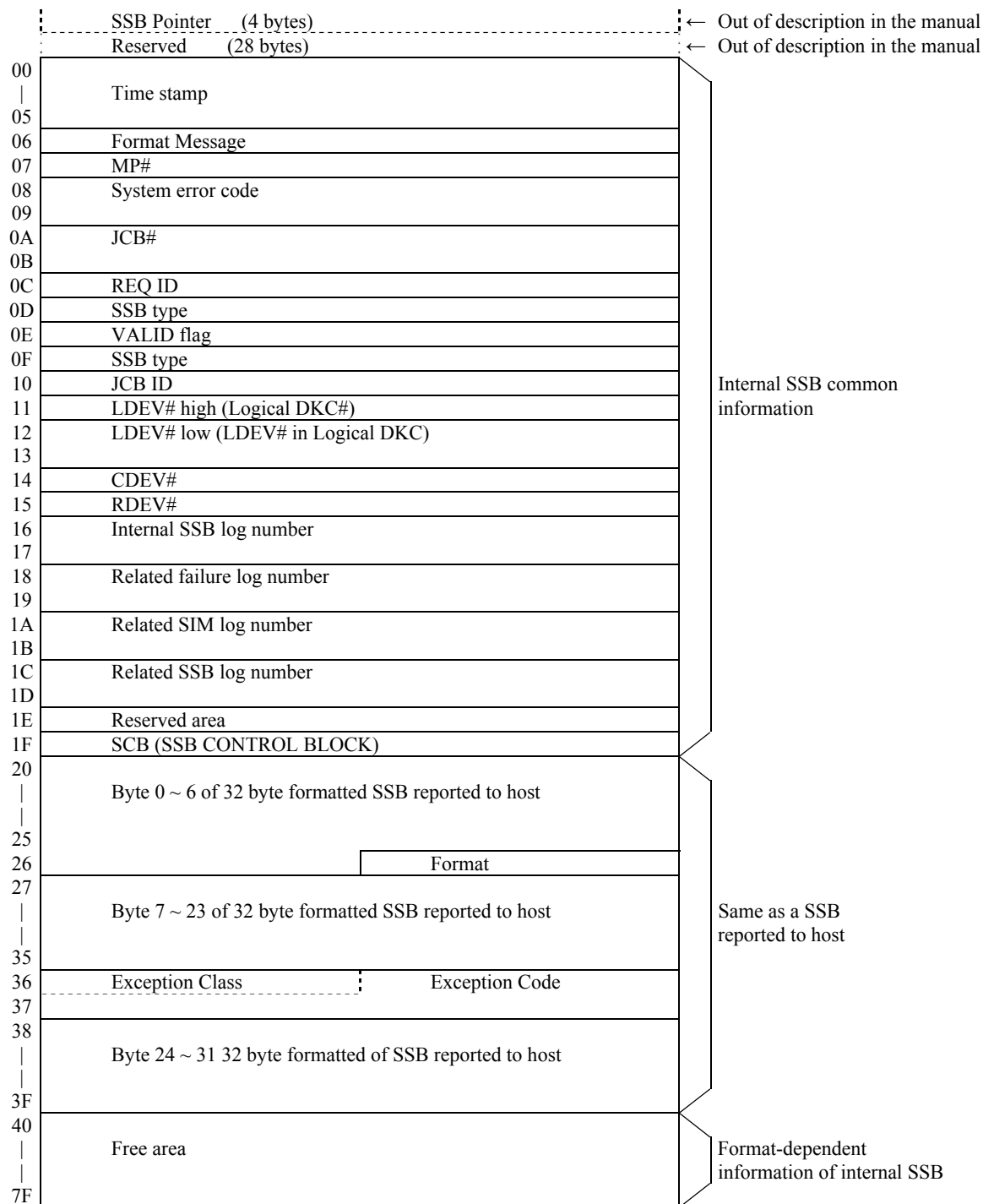


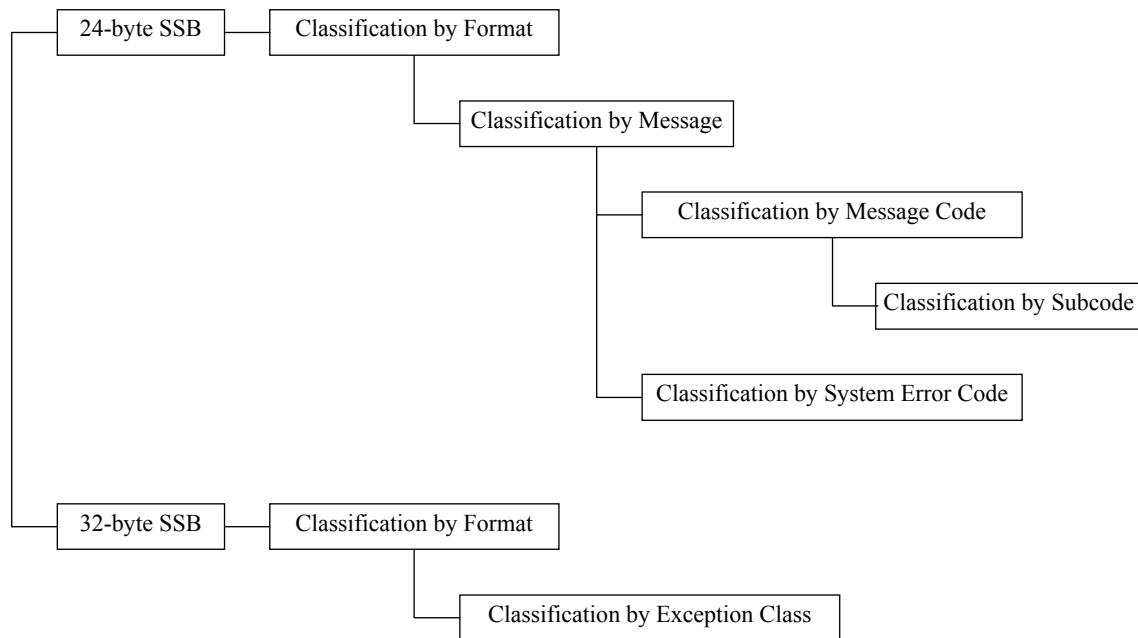
Fig. 1-2 Outline of 32-byte Format Internal SSB

	SSB Pointer (4 bytes)	← Out of description in the manual
	Reserved (28 bytes)	← Out of description in the manual
00	Time stamp	Internal SSB common information
05		
06		
07		
08		
09		
0A		
0B		
0C		
0D		
0E		
0F		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
1A	Format Message	
1B	MP#	
1C	System error code	
1D	JCB#	
1E	REQ ID	
1F	SSB type	
20	VALID flag	
21	SSB type	
22	JCB ID	
23	LDEV# high (Logical DKC#)	
24	LDEV# low (LDEV# in Logical DKC)	
25	CDEV#	
26	RDEV#	
27	Internal SSB log number	
28	Related failure log number	
29	Related SIM log number	
2A	Related SSB log number	
2B	Reserved area	
2C	SCB (SSB CONTROL BLOCK)	
2D	Byte 0 to 31 of SCSI host reported format SSB	
2E		
2F		
30		
31	Byte 32 to 95 of SCSI host reported format SSB	
32		
33		
34		
35		
36		
37		
38		
39		
3A		
3B		
3C		
3D		
3E		
3F		
40		
41		
42		
43		
44		
45		
46		
47		
48		
49		
4A		
4B		
4C		
4D		
4E		
4F		
50		
51		
52		
53		
54		
55		
56		
57		
58		
59		
5A		
5B		
5C		
5D		
5E		
5F		
60		
61		
62		
63		
64		
65		
66		
67		
68		
69		
6A		
6B		
6C		
6D		
6E		
6F		
70		
71		
72		
73		
74		
75		
76		
77		
78		
79		
7A		
7B		
7C		
7D		
7E		
7F		

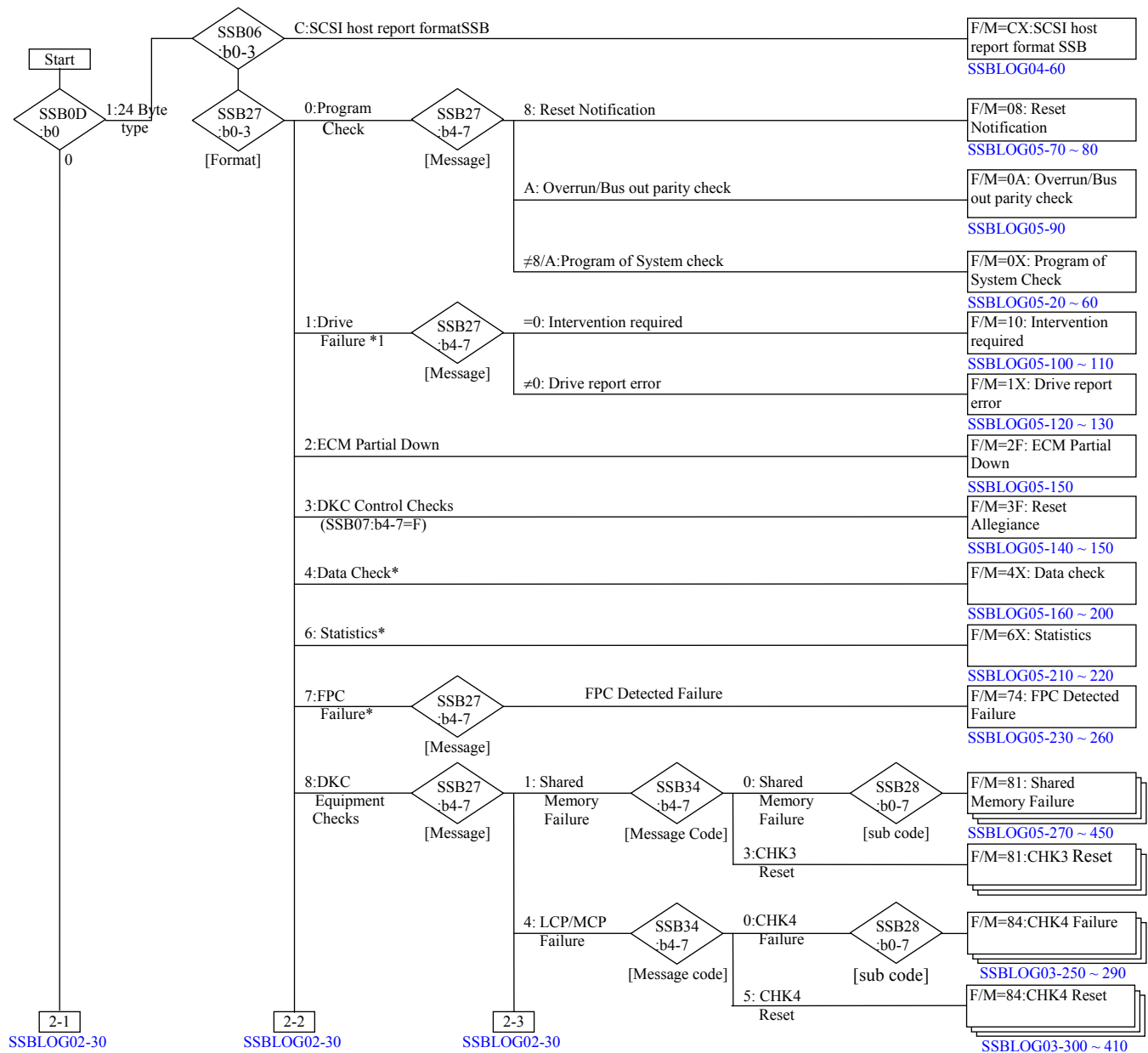
Fig. 1-3 Overview of SCSI Host Report Format Internal SSB

2. Classification of Internal SSB

The internal SSB is classified as the following figure, and Fig. 2-1 for detail.



SSBLOG02-20



*: For DKU86I (IBM 3380 emulation mode) only.

Fig. 2-1 Search method for SSB tables (1/5)

SSBLOG02-30

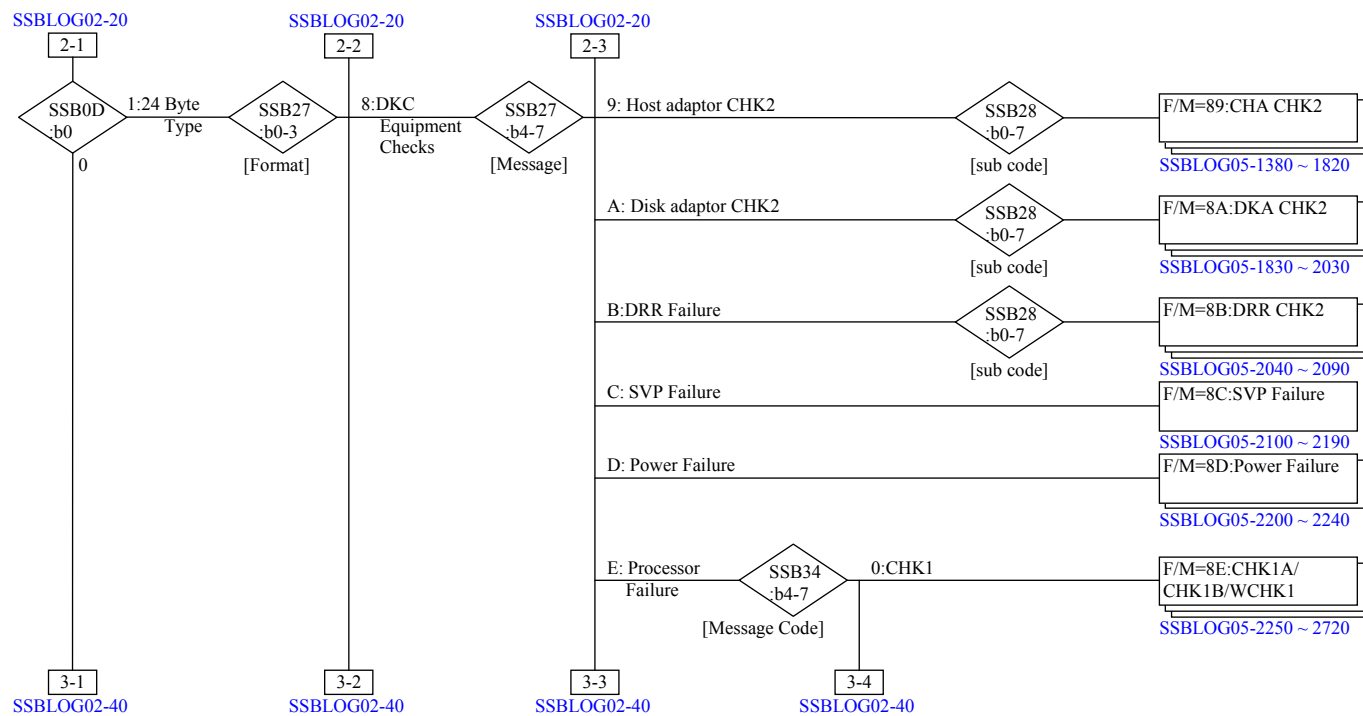


Fig. 2-1 Search method for SSB tables (2/5)

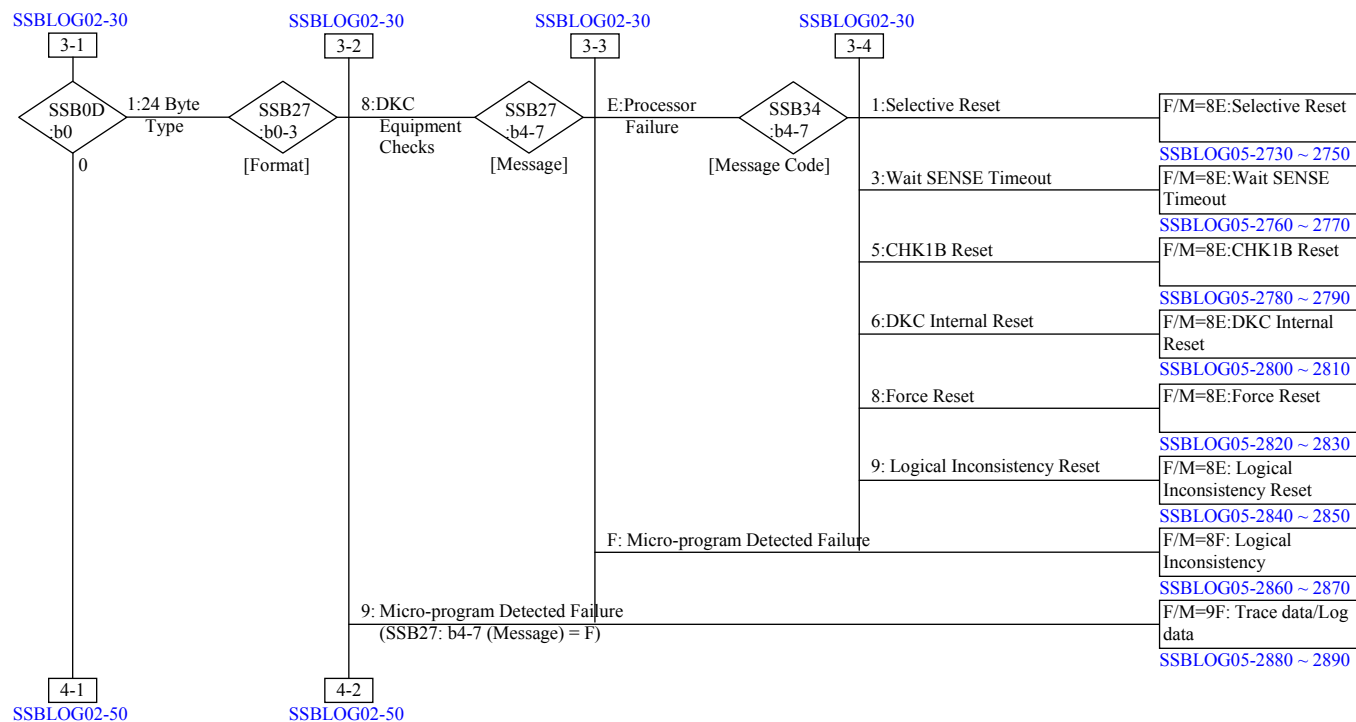
SSBLOG02-40

Fig. 2-1 Search method for SSB tables (3/5)

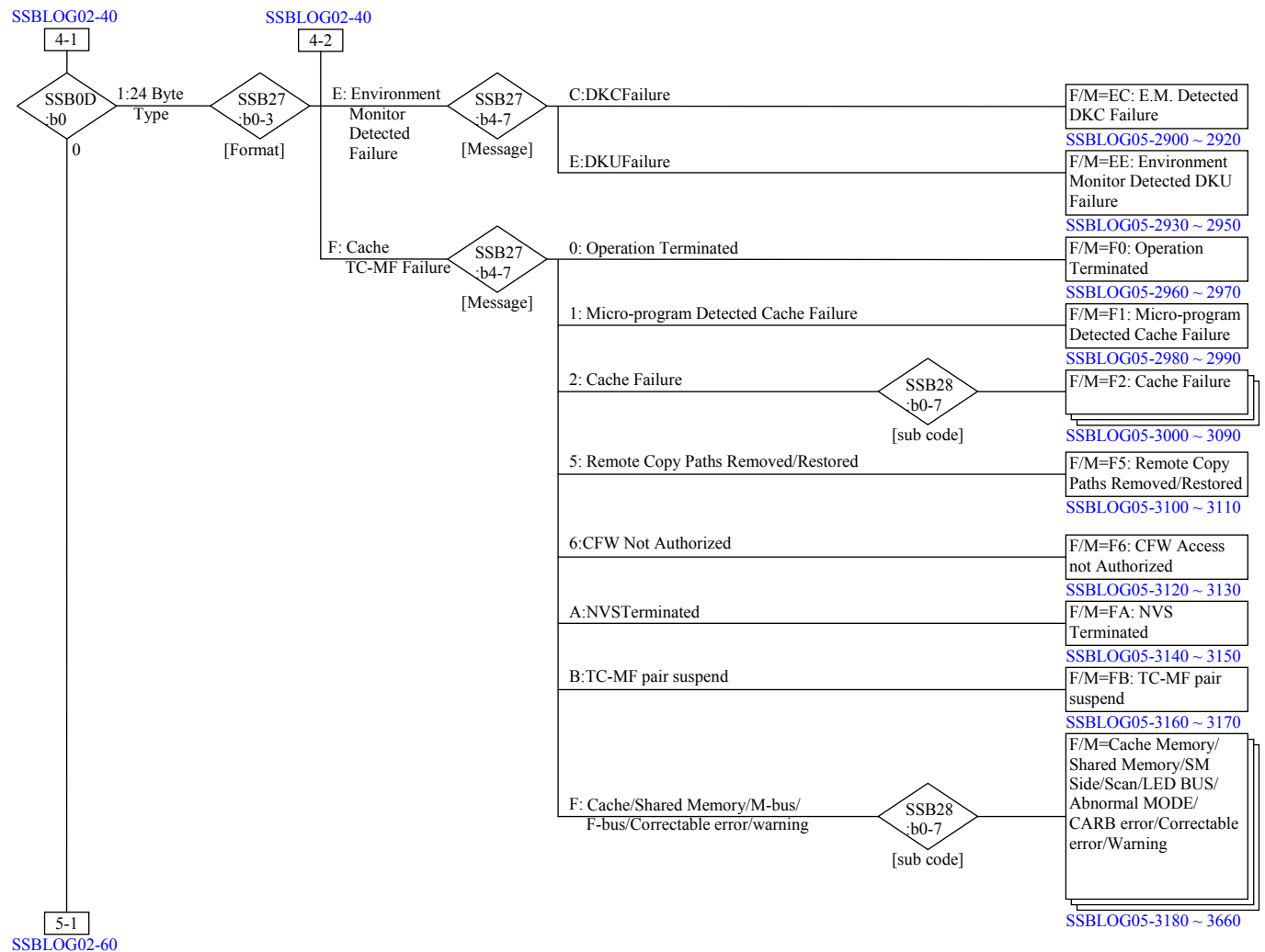
SSBLOG02-50

Fig. 2-1 Search method for SSB tables (4/5)

SSBLOG02-60

SSBLOG02-50

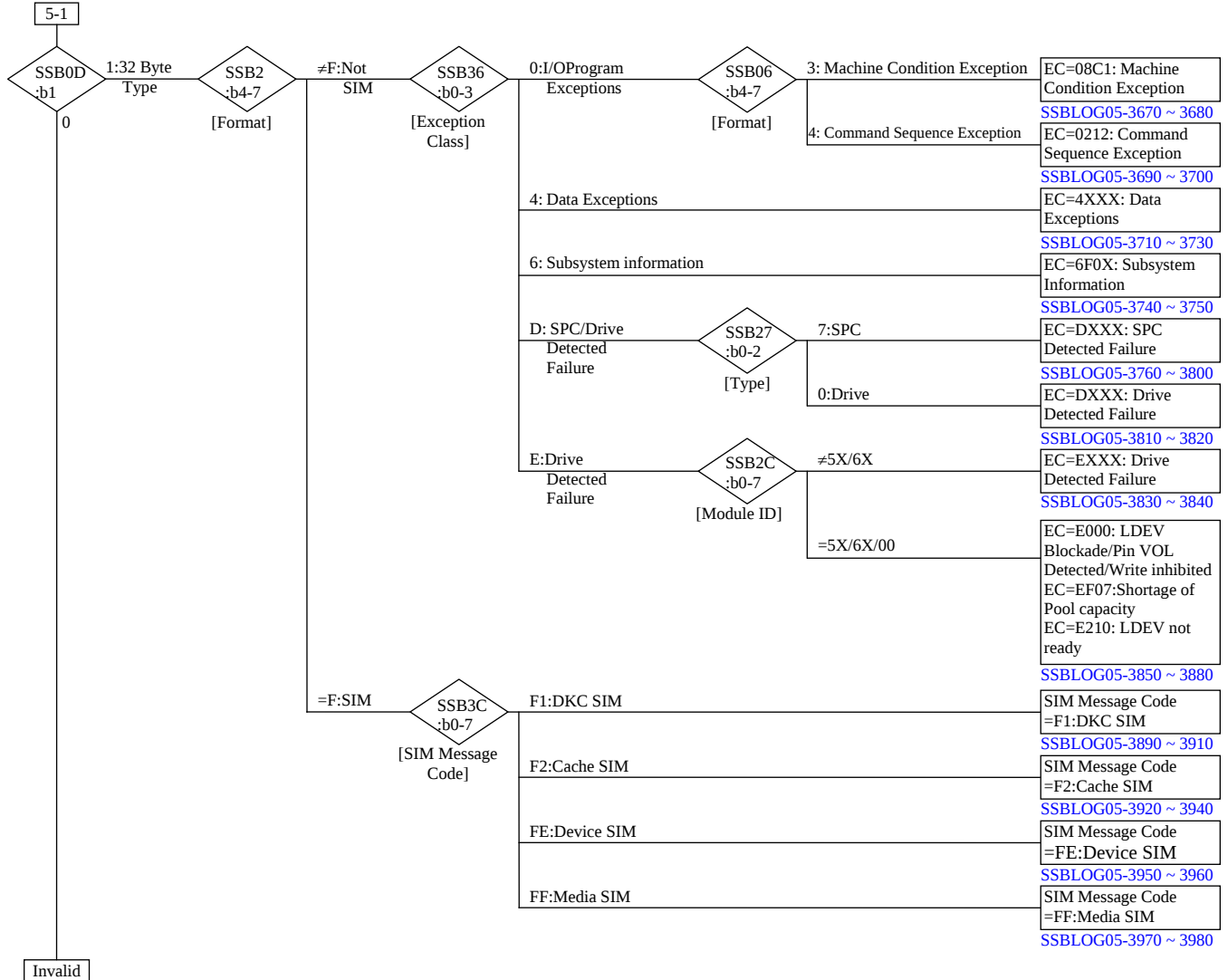


Fig. 2-1 Search method for SSB tables (5/5)

SSBLOG03-10**3. Common information description**

	0	s	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20								
	Host SSB Information							
3F								
40								
	Internal SSB Format-Dependent Information							
7F								

Common Information (1/2)

Byte	Bit	Meaning	Remarks
00 ~ 05		Time stamp The time error occurred	
06		Format Message	
07		Processor number (Internal ID) x'00' ~ x'7F': CHA0x00 ~ 0x7F x'80' ~ x'BF': DKA0x80 ~ 0xBF	
08 ~ 09		System error code	
	0 ~ 7	Module ID	
	8 ~ 15	Routine ID	
0A ~ 0B		JCB#	
0C		REQ ID	
0D		SSB type	
	0	24 byte type flag 0: Invalid 1: 24 byte type SSB	
	1	32 byte type flag 0: Invalid 1: 32 byte type SSB	
	2	Flag for SCSI host processor detected the error 0: SSB detected by CHA and DKA 1: SSB detected by CHS	
	3	Remote copy flag 0: Invalid 1: Remote copy valid	
	4	Separately stored log flag 0: General log 1: Separately stored log	
	5	Host report flag 0: Pending 1: Reported	
	6	SVP report flag 0: Pending 1: Reported	
	7	Forcible log flag 0: Normal 1: Forcibly ejected log	
0E		Valid flag	
	0	LDEV number valid or invalid 0: Invalid 1: Valid	
	1	PDEV number valid or invalid 0: Invalid 1: Valid	
	2	SSB number valid or invalid 0: Invalid 1: Valid	
	3	Related failure log number valid or invalid 0: Invalid 1: Valid	

SSBLOG03-30**Common Information (2/2)**

Byte	Bit	Meaning	Remarks
0E		Valid flag	
	4	Related SIM log number valid or invalid 0: Invalid 1: Valid	
	5	Related SSB log number valid or invalid 0: Invalid 1: Valid	
	6	PK information valid or invalid 0: Invalid 1: Valid	
	7	Reserve	
0F		SSB type	
	0	TC-MF type SSB flag 0: Sync TC-MF SSB 1: Semi Sync TC-MF SSB	
	1	RCU SIM flag 0: Invalid 1: RCU SIM flag	
	2 ~ 7	Reserved	
10		JCB ID	
11		LDEV# high (Logical DKC#)	
12 ~ 13		LDEV# low (LDEV# in Logical DKC)	
14		CDEV#	
15		RDEV#	
16 ~ 17		Internal SSB log number	
18 ~ 19		Related Failure log number	
1A ~ 1B		Related SIM log number	
1C ~ 1D		Related SSB log number	
1E		Reserved area	
1F		SSB control block Use when assemble BASIC SSB of Host SSB. See "SSB Key Code" table.	

SSB Key Code (1/4)

Code	Basic SSB	Meaning	Remarks
00	000000	Dummy SSB	
01	800000	Command reject (A program error or invalid link between BAD and ALT)	
02	400000	Intervention required (An addressed device is offline or not connect)	
03	200000	Bus out check (Channel bus out parity error)	
04	100000	Equipment check (Equipment check in subsystem)	
05	080000	Data check (Uncorrectable: retry failed or retry inhibited)	
06	040000	Overrun (Command retry failed when service overrun occurred)	
07	004000	Invalid track format (Track capacity over or track format error)	
08	002000	End of cylinder (A multi track operation outside LOCATE domain)	
09	000800	No record found	
0A	000400	File protected (A seek or multi track operation violates the file mask or extent)	
0B	100200	Equipment check and write inhibited (A write command on the write inhibited path by a DIAG CTL command)	
0C	080040	Data check and correctable (The retry succeeded for data check in PCI fetch mode)	
0D	000020	First log mode error	
0E	800400	Command rejected and file protected (A multi track operation outside primary track)	
0F	800200	Command reject and write inhibited	

SSB Key Code (2/4)

Code	Basic SSB	Meaning	Remarks
10	108000	Equipment check and permanent error (A retry failed for the equipment check)	
11	088000	Data check and permanent error (A retry failed for the data check)	
12	009010	SIM	
13	000000	Reserved	
14	000104	Imprecise ending (This error is for a previously completed CCW)	
15	800104	Command reject and imprecise ending	
16	400104	Intervention required and imprecise ending	
17	200104	Bus out check and imprecise ending	
18	100104	Equipment check and imprecise ending	
19	080104	Data check and imprecise ending	
1A	040104	Overrun and imprecise ending	
1B	004104	Invalid track format and imprecise ending	
1C	002104	End of cylinder and imprecise ending	
1D	000904	No record found and imprecise ending	
1E	000504	File protected and imprecise ending	
1F	100304	Equipment check and write inhibited and imprecise ending	

SSB Key Code (3/4)

Code	Basic SSB	Meaning	Remarks
20	080144	Equipment check and correctable and imprecise ending	
21	000124	First log mode error and imprecise ending	
22	800504	Command reject and file protected and imprecise ending	
23	800304	Command reject and write inhibited and imprecise ending	
24	108104	Equipment check and permanent error and imprecise ending	
25	088104	Data check and permanent error and imprecise ending	
26	009114	SIM and imprecise ending	
27	000000	Reserved	
28	808000	Command reject and permanent error	
29	00C000	Permanent error and invalid track format	
2A	008800	Permanent error and request inhibit write	
2B	088200	Data check and permanent error and write protected	
2C	000000	Reserved	
2D	000000	Reserved	
2E	000000	Reserved	
2F	000000	Reserved	

SSB Key Code (4/4)

Code	Basic SSB	Meaning	Remarks
30	000000	Reserved	
31	000000	Reserved	
32	010000	Incomplete domain (Received commands < LOCATE command parameter)	
33	804000	Command reject and invalid track format (A TLF mismatch in CKD conversion mode)	
34	000000	Reserved	
35	100010	Equipment check and environmental data present (Status change pending)	
36	800010	Command reject and environmental data present	
37	000000	Reserved	
38	000000	Reserved	
39	000000	Reserved	
3A	000000	Reserved	
3B	000000	Reserved	
3C	000000	Reserved	
3D	000000	Reserved	
3E	000000	Reserved	
3F	000000	Reserved	

4. Host SSB information and internal SSB format-depended information description

4.1 24-byte type SSB

00	0	1	2	3	4	5	6	7	
1F	Internal SSB common Information								
20	Command reject	Intervention REQ	Bus out check	Equipment check	Data check	Overrun	Not used	Incomplete domain	Basic SSB
21	Permanent error	Invalid track FMT	End of cylinder	Operator message	No Record found	File protect	Write inhibit	Imprecise ending	
22	Write INH request	Correctable	1st log mode ERR	Environmental DAT	Not used	Imprecise ending	Not used	Not used	
23	Count								
24	Device address								
25	Cylinder address (low)								
26	Cylinder address (high)				Head address				
27	Format (See next page)				Message (See next page)				
28	Host SSB Format-depended Information								
37									
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC Mode (1)	Duplex pair (0)	S-volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	*
3B	24 Byte SSB (1)	DEV ADR valid	TRK ADR valid	28 Bit Cylinder valid	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.								
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.								
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.								
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address								
40	Internal SSB Format-depended Information								
7F									

*: Configuration information

**: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB section.

Format and Message (1/2)

Format	Message	Meaning	
0 (program /system check)	0	No message	
	1	Invalid command	
	2	Invalid sequence	
	3	Data short	
	4	Invalid data	
	5	Violate file mask	
	6	Channel disconnect retry	
	7	Invalid retry command	
	8	System reset	
	9	Not used	
	A	Bus of parity check/Overrun	
	B	Invalid link between bad track and alternate track	
	C	Not used	
	D	Not used	
	E	Invalid command to secondary volume	
	F	Invalid status	
1	0	Intervention required	For DKU86I (IBM 3380 emulation mode) only
	1	Not used	
	2	Drive not ready	
	3	Not used	
	4	Drive report error	
	5 ~ F	Not used	
3	0 ~ E	Not used	
	F	Reset allegiance	
4	0	Data check in HA field	For DKU86I (IBM 3380 emulation mode) only
	1	Data check in C field	
	2	Data check in K field	
	3	Data check in D field	
	4	Not used	
	5	PA error	
	6 ~ F	Not used	
6	0 ~ 7	Statistics	
	8 ~ F	Not used	
7	0 ~ 3	Not used	
	4	FPC report error	
	5 ~ 8	Not used	
	9	SCSI bus parity error	
	A ~ F	Not used	

Format and Message (2/2)

Format	Message	Meaning
8	0	Not used
	1	Shared memory failure
	2 ~ 7	Not used
	8	LCP failure
	9	Host adaptor CHK2
	A	Disk adaptor CHK2
	B	DRR failure
	C	SVP failure
	D	Power failure
	E	Processor failure
	F	Micro-program detected error (Logical Inconsistency)
9	0 ~ E	Not used
	F	Micro-program detected error (Trace data/Log data)
E	0 ~ B	Not used
	C	Environmental monitor detected DKC failure
	D	Not used
	E	Environmental monitor detected DKU failure
	F	Not used
F	0	Operation Terminated
	1	Microprogram detected cache failure
	2	Cache failure
	3 ~ 4	Not used
	5	Remote Copy Paths Removed/Restored
	6	CFW not authorized
	7 ~ 9	Not used
	A	NVS terminated
	B	TrueCopy for Mainframe Pair suspend
	C ~ E	Not used
	F	Cache/Shared memory/M-bus/F-bus/J-bus warning

SSBLOG04-40

4.2 32-byte type SSB

	0	1	2	3	4	5	6	7	
00 1F	Internal SSB common Information								
20	Command reject	Intervention REQ	Not used	Equipment check	Data check	Not used	Not used	Incomplete domain	Basic SSB
21	Permanent error	Invalid TRK FMT	End of cylinder	Operator message	Not used	File protect	Write inhibit	precise ending	
22	EC = 0/1/2/3/F: Environmental data present (x'10') EC = 4/6/D/E: Storage control type (x'06': CACHE DKC)								
23	Count / Command overrun threshold is exceeded.								
24	Device address								
25	Device type (x'24':DKU871-3)								
26	SSB2/SSB5 valid	SSB4 valid	SSB29-31 valid	Not used	Format				
27 37	Host SSB Format-depended Information								
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC mode(1)	Duplex pair (0)	S-Volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	*
3B	32 Byte SSB (0)	Not used	Not used	Not used	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Case of Byte3B Bit0=0: Message code Bit0=1: Indicate cylinder address bit 12-19.								
3D	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 20-27.								
3E	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 0-7.								
3F	Case of Byte3B Bit0=0: 0x00 Bit0=0: Head address Bit0=1: Indicate cylinder address bit 8-11. Bit0=1: Head address								
40 7F	Internal SSB Format-depended Information								

*: Configuration information

**: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB section.

Exception Class (Byte36:bit0-3) and Format (Byte26:bit4-7)

Exception Class	Format	Meaning
0	0 ~ 2	Not used
	3	Machine condition exception
	4	I/O Program exception (Incomplete Domain)
	5 ~ F	Not used
2	0 ~ E	Not used
	F	DKC SIM
3	0 ~ E	Not used
	F	DKC SIM
4	0	Not used
	1	Data exception (PCI, Permanent) (*1)
	2 ~ E	Not used
	F	MEDIA SIM
5	0 ~ E	Not used
	F	DEVICE SIM/Media SIM
6	0	Not used
	1	Statistics (*1)
	2 ~ F	Not used
C	0 ~ E	Not used
	F	DKC SIM
D	0	Drive detected failure (*1)
	1	SPC detected failure (*1)
	0 ~ E	Not used
	F	DKC SIM
E	0	Drive detected failure/LDEV blockade/PIN VOL detected/ LDEV not ready/Write inhibited (*1)/Shortage of Pool capacity
	1 ~ E	Not used
	F	DEVICE SIM
F	0 ~ E	Not used
	F	DKC SIM/CACHE SIM

*1: For DKU87I (IBM 3390 emulation mode) only.

4.3 SCSI host report format SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common information (*1)							
20	Information byte valid	Error code (0x7)						
21	Segment No (0)							
22	File mark (0)	ECM (0)	ILI (0)	Not used	Sense key			
23 26	Information byte							
27	Additional sense data length (0x58)							
28 2B	Command specific information							
2C 2D	Sense code							
2E	Field Replaceable Unit (0)							
2F 30 31	Sense key specific information	Sense key specific information						
32 7F	Additional sense data valid	Additional sense data						

*1: 0xCX (X=sense key) is set to byte 7 (log type low-order byte). SSB on SVP is indicated as F/M=CX.

Details of SCSI host report format SSB information

Byte	Bit	Description	Setting	Remarks
20	0	Information byte valid/invalid 0: Invalid 1: Valid	CHS command Control	
	1 ~ 7	Error code (0x7) Indicates an I/O process error.		
21		Segment No. (0)		
22	0	File mark (0)		
	1	ECM (0)		
	2	ILI (0)		
	3	Not used		
	4 ~ 7	Sense key: For more details, see “sense keys list”.		
23 ~ 26		Information bytes: LBA related to sense key if information byte is valid.		
27		Additional sense data length (0x58) Indicates sense data length after byte 8 or later.		
28 ~ 2B		Command specific information		
2C ~ 2D		Sense codes: For more details, see “sense codes list”.		
2E		Field Replaceable Unit (0)		
2F ~ 31	0	Sense key specific information valid/invalid 0: Invalid 1: Valid		
	1 ~ 17	Sense key specific information		
32 ~ 7F	0	Additional sense data valid/invalid 0: Invalid 1: Valid	Error detection source	
	1 ~ END	Additional sense data		

Sense Keys List

Sense key	Description
0	NO SENSE
1	RECOVERED ERROR
2	NOT READY
3	MEDIUM ERROR
4	HARDWARE ERROR
5	ILLEGAL REQUEST
6	UNIT ATTENTION
7	DATA PROTECT
8	BLANK CHECK (No used)
9	VENDER UNIQUE
A	COPY ABORTED
B	ABORTED COMMAND
C	EQUAL (No used)
D	VOLUME OVERFLOW
E	MISCOMPARE
F	(RESERVED)

5. Format-depended information description

SSBLOG05-20

Byte27 = 0X (X≠8/A: System/Program Check) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type		Processor ID (Note)					
07	System error code							
08								
09	JCB#							
0A								
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'X' : X≠8/A)			
28	Command code (for F/M ≠ F)/Reason code (for F/M=F) (Note 1)							
29	Detail failure information 1 (Note 2)							
2A								
2B								
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID (Note 4)							
31	Routine ID (Note 4)							
32	PCB number							
33	Detail failure information 2 (Note 3)							
34	SSID of self subsystem							
35								
36	Symptom code (x'0F0X')				(x'2F00' for dummy SENSE)			
37	X=Message code							

SSBLOG05-30**Byte27 = 0X (X≠8/A: System/Program Check) (2/3)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
55								
56	Module ID (detected the error)							
57	Module ID (detected the error)							
58	Not used							
7F								

(Note 1) F/M = 0F: Byte08 Reason code

Code	Reason
00	No message
01	Trying to make cache available and cache status is pending.
02	Not used
03	Trying to make cache force unavailable and cache status is not pending with destage complete.
04	Trying to activate CFW and CFW status is pending.
05	Trying to activate caching (device) and cache status is pending.
06	Trying to destage (track) and NVS is failed.
07	Trying to make NVS unavailable and NVS is initializing.
08	Trying to deactivate caching , DFW or DISCARD (device) , and DFW status is failed.
09	Trying to force deactivate DFW (device) and status is not as follows. • EDFW status is pending or failed. • ENVN is failed and the data exists in NVS, but not in cache.
0A	Trying to make NVS available and DFW status (device) is pending or failed.
0B	Trying to make NVS available and NVS status is pending.
0C	When NVS is failed, trying to stop caching or DFW (device) and the data exists in NVS, but not in cache.
0D	Command requires NVS and NVS is unavailable.
0E	Command requires cache and cache is unavailable.
0F	Not used
10~28	Not used
29	PFS command (Set Special Interrupt Condition) is issued on an interface on which path group is not established.
2A	Trying to make path group include parallel channel and serial channel.
2B	Message buffer is full.
2C~34	Not used

(To be continued)

(Continued from preceding page)

Code	Reason
35	A Set Guaranteed Path subcommand is issued on a fenced path.
36~3E	Not used
3F	A Make NVS Avail for Subsystem subcommand is issued, but NVS is disable. (‘Disable NVS Capability’ is set to disable in VPD.)
40	Not used
41	A normal command is issued to a device reserved by maintenance
42~56	Not used
57	In the device for which the address is specified, the TrueCopy pair is in the state of Suspend. All the Write commands where Normal Authorization is shown are rejected.
5~7F	Not used
80	A special command is issued on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
81	An attention report on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
82~FF	Not used

Byte27 = 0X (X≠8/A: System/Program Check) (3/3)

(Note 2)

(1) Issued command information when F/M=02

B/b	0	1	2	3	4	5	6	7
29	SCH ID	SCH KEY	Locate, LC EXT	SK, SK CYL	RD IPL	RECAL	SUSP MPR	RD C
2A	DIAG CTL	DIAG CTL (LOC DOM)	DEF EXT	SET FM	SET SUB MOD	SP C	WR CKD	WR R0
2B	SET INT FID	DIAG CTL (PRE REM)	0	Write through	RD C chaining	All byte match	LC domain	ERASE execute
2C	Write through	0	0	Allow WR KD	Allow WR D	Allow WR CKD	Allow WR R0	Allow WR HA

(2) Short byte flag when F/M=03

Byte29 : x'00'
 Byte2A : x'40'(short byte)
 Byte2B : x'00'
 Byte2C : x'00'

(3) Mask information when F/M=05

Byte 29 : Mask for write control

x'00' : Allows all write operation except WR HA, WR R0.
 x'40' : Inhibits all write operation.
 x'80' : Allows update write operation.
 x'C0' : Allows all write operation.

Byte 2A : Mask for seek control

x'00' : Allows all seek commands and RECAL.
 x'08' : Allows SK CYL and SK HD only.
 x'10' : Allows SK HD only.
 x'18' : Inhibits all seek command and multi operation.

Byte 2B : Mask for access

x'00' : Allows normal access.
 x'02' : Allows device support.
 x'04' : Allows diagnostic access.
 x'06' : Allows device support inhibit data check correction and retry.

Byte 2C : Not used (x'00')

(4) Record address when other F/M

Byte 29 and 2A : Cylinder address
 Byte 2B : Head address
 Byte 2C : Record number

(Note 3) Bit 4-7 of byte 32 : Type of byte 33

x'08' : Byte 33 is cylinder address (high).

x'09' : Byte 33 is cylinder address (low).

x'0A' : Byte 33 is head address (high).

x'0B' : Byte 33 is head address (low).

x'0C' : Byte 33 is record number.

Byte 33 : One byte of CCHHR.

(Note 4) The details information of every Module ID & Routine ID

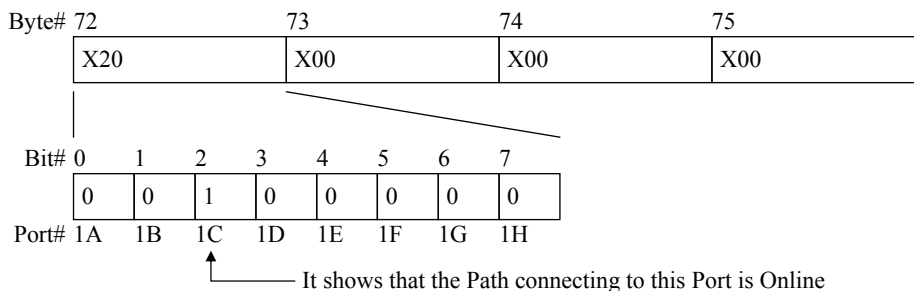
•Module ID & Routine ID=0xC0CA : The Specified R-VOL of TC-MF is still Online from the HOST.

(Add Pair operation from the Web Console was rejected)

Check the SSB.LOG on SVP of the specified RCU. SSB containing EC=C0CA on the specified RVOL number should be logged.

(1) Analyze the SSB.LOG

Both SSB are same format, from SSB byte72(0x48) though byte75(0x4B) show the Bit Map of Port indicating Online from the HOST. Each one bit corresponds to each port. It indicates the LCP Port on which the valid Path Group is established for this device.



(2) Run DSF/ANALYZE-ALLCHPID to trace the Path Group ID of the Online route.

(2.1) In case that DKC Emulation Type is 3990-6/3990-6E or 2105.

Run DSF/ANALYZE with ALLCHPID. The output shows the Path Group ID which is Online to the subsystem.

LOGICAL PATH		SYSTEM ADAPTER ID	FULL ESCON LINK ADDRESS	SP FENCE 0 1 2 3	HOST PATH GROUP ID		
NUMBER	type				CPU SERIAL#	CPU TYPE	CPU TIME STAMP
1	E	00	0101		XXXXXXXX	YYYY	ZZZZZZZZ
2-32	N/E	00-01					
33	E	02	0200		XXXXXXXX	YYYY	ZZZZZZZZ

“SYSTEM ADAPTER ID” corresponds to the number of the CHL adapter port.

(Ex; Port#1C is “02”).

Trace the Path Group ID indicated the SYSTEM ADAPTER ID corresponding to the value on SSB LOG of EC=C0CA or C0A6.

The relationship between SAID and Port ID is shown in [THEORY03-08-20](#), Table 3.8-4.

(2.2) In case that DKC Emulation Type is 3990-3 DSF/ANALYZE-ALLCHPID is not available for 3990-3. Therefore, trace the Path Online Route of the LCP.

SSBLOG05-70

Byte27 = 08 (Reset Notification) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type							
07	Processor ID (Note)							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	EPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)							
27	Format (x'0')							
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (Note 1)							
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number							
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'2FFF')							
37								

SSBLOG05-80**Byte27 = 08 (Reset Notification) (2/2)**

	0	1	2	3	4	5	6	7
38	Log & message control (x'00')							
39	Program action code (x'16')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
	Not used							
7F								

(Note 1) Hardware Level

Bit0 : Serial Channel or Parallel Channel	
Bit0=1(Serial Channel)	
Bit 1	: Not used
Bit 2~3	: Storage Path Number
Bit 4~7	: 0xa : Fixed
Bit 8	: Dual Frame
	0 : DUAL FRAME
	1 : MODULAR POWER
Bit 9~11	: Cache size
	000 : none Cache
	001 : 256MB
	010 : 512MB
	011 : 768MB
	100 : 1024MB
	101 : 1280MB
	110 : 1536MB
	111 : Over 1536MB
Bit 12~13	: Cluster hardware level
Bit 14~15	: Cache/NVS hardware level

SSBLOG05-90

Byte27 = 0A (Bus out parity check/Overrun on LCP interface (Byte3A:b4 = 1)) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'A')			
28	Command code							
29	Cylinder address							
2A	Head address							
2B	Record number							
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

SSBLOG05-91

Byte27 = 0A (Bus out parity check/Overrun on LCP interface (Byte3A:b4 = 1)) (2/2)

	0	1	2	3	4	5	6	7
30	Module ID							
31	Routine ID							
32	PCB number				BBF OVR	PND OVR	STOP	OVER
33	Not used ('x00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'0F0A')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						
40								
7F	Not used							

SSBLOG05-100

Byte27 = 10 (Intervention required) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	SEPARATE	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'0')			
28	READY	ENABLE	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('00000')					
2B	HOST TYPE	DKC TYPE	Not used (x'00')		DKU TYPE			
2C	Module ID (x'50')							
2D	Routine ID (x'10')							
2E	Command code							
2F	Not used (x'00')							

SSBLOG05-110**Byte27 = 10 (Intervention required) (2/2)**

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0000')			
33	Not used ('x00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
	Not used							
7F								

SSBLOG05-120

Byte27 = 1X (Drive report error) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'2/4')			
28	Additional Sense Code + Additional Sense Code Qualifier (*2)							
29								
2A	SCSI Command Code (*2)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used (x'0')				DKA#			
2F	CDEV#				RDEV#			
30	Type Code ('000')			0	Sense Key (*1)			
31	Not used (x'00')							
32	Not used (x'00')							
33	Not used ('x00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'9F1X' X: Message)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								

SSBLOG05-130**Byte27 = 1X (Drive report error) (2/2)**

	0	1	2	3	4	5	6	7
3C	Case of Byte3B		Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B		Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B		Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B		Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Status byte							
41	Message							
42	CDB							
4B								
4C	SPC status register							
4D	SPC nexus status register							
4E	SPC data block register							
4F	SPC data byte register							
50								
51								
52								
53	SPC SCSI control signal status register							
54	SCSI Extended Sense Data							
73								
74	Not used							
7F								

***1: Sense key**

x'0' : No sense
 x'1' : Recovered error
 x'2' : Not ready
 x'3' : Medium error
 x'4' : Hardware error
 x'5' : Illegal request
 x'6' : Unit attention
 x'7' : Data protect
 x'8' : Blank check (Not used)
 x'9' : Vender unique (Not used)
 x'A' : Copy aborted (Not used)
 x'B' : Aborted command
 x'C' : Equal (Not used)
 x'D' : Volume overflow (Not used)
 x'E' : Miscompare
 x'F' : (Reserved)

***2: SCSI command**

x'00' : Test unit ready
 x'03' : Request sense
 x'04' : Format unit
 x'07' : Reassign blocks
 x'12' : Inquiry
 x'15' : Mode select
 x'1A' : Mode sense
 x'1B' : Start/stop unit
 x'1C' : Receive diagnostic result
 x'1D' : Send diagnostic
 x'28' : Read (extend)
 x'2A' : Write (extend)
 x'2E' : Write and verify
 x'3B' : Write buffer

SSBLOG05-140

Byte27 = 3F (Reset Allegiance) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'3')				Message (x'F')			
28	Not used							
29	Logical path number							
2A	DCN status (*1)							
2B								
2C	Command code							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'F')			
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'3F3F')							
37								

SSBLOG05-150**Byte27 = 3F (Reset Allegiance) (2/2)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
7F								

*1:

	0	1	2	3	4	5	6	7
2A	DEVICE BUSY	ECI	COMMAND CHAINING	PATH RESERVE	STACK STATUS	WAIT SENSE	LONG BUSY	PROCESSOR CONNECT
2B	SINGLE PATH MODE	GUARANT PATH MODE	MCB CHAINING	0	BLOCK SWITCH	0	0	0

SSBLOG05-160

Byte27 = 4X (Data check, Common information) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)							
27	Format (x'4')							
28	Cylinder address (*1)							
29								
2A	Head address (*1)							
2B								
2C	Record number (*1)							
2D	Sector number (*1)							
2E	Controller ID							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number							
33	Not used (x'00')							
34	Command code							
35	SSID (low) of self subsystem							
36	Symptom code (x'4XYY') (X: *2) (YY: *3)							
37								

SSBLOG05-170

Byte27 = 4X (Data check, Common information) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B		Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.					
3D			Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B		Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F			Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					

*1: Not determined for occurrence in HA or RO

*2: Contents of message

x'0' : Data check in HA field
 x'1' : Data check in C field
 x'2' : Data check in K field
 x'3' : Data check in D field
 x'4' : Missing SYNC byte in HA field
 x'5' : Missing SYNC byte in C field (PA error)
 x'6' : Missing SYNC byte in K field
 x'7' : Missing SYNC byte in D field
 x'8' : Not used
 x'9' : Missing AM during retry
 x'A' to x'F' : Not used

*3: Contents of correction flag

Bits 0 and 1 : ECC correction bit
 10 : Correctable (Recovered)
 11 : Uncorrectable
 Bit 2 : Offset active
 Bits 3 to 7 : Not used

Byte27 = 4X (Data check ,When Byte27 = 43) (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56								
	Not used (x'00)							
7F								

*1: EXIT FLAG

x'08' : CHL I/F CHK2 time over	x'41' : 'Waiting free segment' requested
x'0D' : EOF	x'42' : 'Time over of waiting for staging' requested
x'0E' : DSB = 0E requested	x'43' : 'Staging for PA unmatched' requested
x'0F' : Logical error	x'45' : 'Destaging' requested
x'1E' : Make sense only	x'46' : 'High priority destaging' requested
x'1F' : Logical error (make sense only)	x'47' : 'Write through process' requested
x'30' : Selective reset requested	x'4B' : 'DSB = 4E retry' requested (data transfer)
x'40' : 'Staging' requested	x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F' : STG requested (Read or STG in progress)

*2: Slot failure

x'80' : Sector write error	x'08' : No decision
x'40' : Sector read error	
x'20' : ECC/LRC error	
x'10' : Incomplete data	

*3: Obtained information flag

bit0-1 : Block type	bit4 : Segment
00 : Data block	0 : No segment
01 : HA/RO block	1 : Segment exist
10 : Bit map block	bit5-7 : Reserved
bit2-3 : Side	
00 : Cache write side	
01 : NVS write side	
10 : Cache read side	

SSBLOG05-190

Byte27 = 4X (Data check ,When Byte27 = 45) (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	PACC in CBUF							
55								
56	PAHF in CBUF							
57								
58	Current CC in JCB							
59								
5A	Current HH in JCB							
5B								
5C								
	Reserved (x'00')							
7F								

***1: EXIT FLAG**

x'08' : CHL I/F CHK2 time over	x'41' : 'Waiting free segment' requested
x'0D' : EOF	x'42' : 'Time over of waiting for staging' requested
x'0E' : DSB = 0E requested	x'43' : 'Staging for PA unmatched' requested
x'0F' : Logical error	x'45' : 'Destaging' requested
x'1E' : Make sense only	x'46' : 'High priority destaging' requested
x'1F' : Logical error (make sense only)	x'47' : 'Write through process' requested
x'30' : Selective reset requested	x'4B' : 'DSB = 4E retry' requested (data transfer)
x'40' : 'Staging' requested	x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F' : STG requested (Read or STG in progress)

***2: Slot failure**

x'80' : Sector write error

x'40' : Sector read error

x'20' : ECC/LRC error

x'10' : Incomplete data

x'08' : No decision

***3: Obtained information flag**

bit0-1 : Block type

00 : Data block

01 : HA/RO block

10 : Bit map block

bit2-3 : Side

00 : Cache write side

01 : NVS write side

10 : Cache read side

bit4 : Segment

0 : No segment

1 : Segment exist

bit5-7 : Reserved

SSBLOG05-210

Byte27 = 6X (Statistics) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Controller ID							
24	Device address							
25	Command overrun threshold exceeded							
26	Data overrun threshold exceeded							
27	Format (x'6')				Message (x'X')			
28	Read/search byte count							
29								
2A								
2B								
2C	Seek count							
2D								
2E	Not used (x'00')							
2F	Manufacture code ('000000')						Factory code ('00')	
30	Not used (x'00')							
31	DKC serial number							
32								
33								
34	SSID of self SSID							
35								
36	Symptom code (x'6FXY')		X: 0: Statistics/RRBL command, 2: Channel data overrun					
37			Y: CHL# (parallel CHL)/LCP# (serial CHL))					

SSBLOG05-220

Byte27 = 6X (Statistics) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						
40								
7F	Not used							

SSBLOG05-230

Byte27 = 74 & Byte40: Bit7 = 0 (FPC Detected Failure: DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format ID							
04								
05								
06								
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VLD	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06': Cache DKC)							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								

SSBLOG05-240

Byte27 = 74 & Byte40: Bit7 = 0 (FPC Detected Failure: DSP) (2/2)

	0	1	2	3	4	5	6	7
3C		Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.					
3D		Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E		Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F		Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40		SSB edit format form (x'00')						
41		Not used						
42		CDB information						
4B								
4C		(x'AB')						
4D		(x'CD')						
4E		CMD BLK#						
4F		Data transfer end flag						
50		IO status						
51								
52		PDEV status						
53								
54		LOOP status						
55								
56								
57								
58		Frame Manager status						
59								
5A								
5B								
5C		TL status						
5D								
5E								
5F								
60		IO issued status1						
61								
62		IO issued status2						
63								
64		Not used						
65								
66								
67								
68		JOB require info						
69								
6A								
6B								
6C		Data transfer status						
6D								
6E								
6F								
70		Not used						
7F								

SSBLOG05-250

Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format ID							
04								
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								

SSBLOG05-251

Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						
40	SSB edit format (x'01')							
41	Not used							
42								
43								
44	ERQ index							
45								
46								
47								
48	IMQ index							
49								
4A								
4B								
4C	IO request information							
4D								
4E								
4F								

SSBLOG05-260

Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (3/3)

	0	1	2	3	4	5	6	7
50	FPC Config/Control register							
51								
52								
53								
54	FPC Status register							
55								
56								
57								
58	FM Configuration register							
59								
5A								
5B								
5C	FM Status register							
5D								
5E								
5F								
60	FM Link Status1 register							
61								
62								
63								
64	FM Link Status2 register							
65								
66								
67								
68	FCA register 1							
69								
6A								
6B								
6C	FCA register 2							
6D								
6E								
6F								
70	FPC PCI register							
71								
72	AL_PA							
73								
74	FPC Config Command							
75								
76								
77								
78	Fibre status							
79								
7A								
7B								
7C	Fibre error outbreak count							
7D								
7E								
7F								

SSBLOG05-270

Byte27 = 81 (CHK3) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (GDEV#)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'1')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self subsystem							
36	Symptom code (x'FF81' : LDEV type = DKU87I/x'EF81' : LDEV type = DKU86I)*							
37	(This information is not fixed until DKC reports SSB to HOST.)							

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Byte27 = 81 (CHK3) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Hardware information (See following pages)							
7F								

Byte27 = 81 (CHK3) Byte28-31 Detail

(1) Byte28 = 00 or 01 or 02 or 10 or F0

	0	1	2	3	4	5	6	7
28	Subcode (x'00' or x'01' or x'02' or x'10' or x'F0')							
29	MPA_location							
	Module number	MPA_LSI#						
2A	MPA_ERRSTS_MPx:B7							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2B	MPA_ERRSTS_MPx:B6							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	MPA_ERRSTS_MPx:B5							
	Not used	Not used	DBG_BLK Warning	R_BLK Warning	D3_BLK Warning	D2_BLK Warning	D1_BLK Warning	D0_BLK Warning
2D	MPA_ERRSTS_MPx:B4							
	M3_BLK Warning	M2_BLK Warning	M1_BLK Warning	M0_BLK Warning	E3_BLK Warning	E2_BLK Warning	E1_BLK Warning	E0_BLK Warning
2E	MPA_ERRSTS_MPx:B3							
	Not used	Not used	DBG_BLK CHK3	R_BLK CHK3	D3_BLK CHK3	D2_BLK CHK3	D1_BLK CHK3	D0_BLK CHK3
2F	MPA_ERRSTS_MPx:B2							
	M3_BLK CHK3	M2_BLK CHK3	M1_BLK CHK3	M0_BLK CHK3	E3_BLK CHK3	E2_BLK CHK3	E1_BLK CHK3	E0_BLK CHK3
30	MPA_ERRSTS_MPx:B1							
	Not used	Not used	DBG_BLK CHK1B	R_BLK CHK1B	D3_BLK CHK1B	D2_BLK CHK1B	D1_BLK CHK1B	D0_BLK CHK1B
31	MPA_ERRSTS_MPx:B0							
	M3_BLK CHK1B	M2_BLK CHK1B	M1_BLK CHK1B	M0_BLK CHK1B	E3_BLK CHK1B	E2_BLK CHK1B	E1_BLK CHK1B	E0_BLK CHK1B

Byte27 = 81 (CHK3) Byte28-31 Detail

(2) Byte28 = 03 or 20 or 30

	0	1	2	3	4	5	6	7
28	Subcode (x'03' or x'20' or x'30')							
29	SW_location							
	Module number	SW_LSI#						
2A	SW::REP_ERR:B3							
	SW WARNING	Not used	Not used	Not used	Not used	Not used	CPL_ERR	Not used
2B	SW::REP_ERR:B2							
	SB ERR	SWD ERR	SWC ERR	SWB ERR	SWA ERR	REG ERR	Not used	PORT16 ERR
2C	SW::REP_ERR:B1							
	PORT15 ERR	PORT14 ERR	PORT13 ERR	PORT12 ERR	PORT11 ERR	PORT10 ERR	PORT9 ERR	PORT8 ERR
2D	SW::REP_ERR:B0							
	PORT7 ERR	PORT6 ERR	PORT5 ERR	PORT4 ERR	PORT3 ERR	PORT2 ERR	PORT1 ERR	SW SERIOUS ERR
2E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte28-31 Detail

(3) Byte28 = 04

	0	1	2	3	4	5	6	7
28	Subcode (x'04')							
29	CMA location							
	Module number	CMA_LSI#						
2A	HSN Status:B7							
	HSN Status Byte7							
2B	HSN Status:B6							
	HSN Status Byte6							
2C	HSN Status:B5							
	HSN Status Byte5							
2D	HSN Status:B4							
	HSN Status Byte4							
2E	HSN Status:B3							
	HSN Status Byte3							
2F	HSN Status:B2							
	HSN Status Byte2							
30	HSN Status:B1							
	HSN Status Byte1							
31	HSN Status:B0							
	HSN Status Byte0							

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Byte27 = 81 (CHK3) Byte28-31 Detail

(4) Byte28 = 28 or 38

	0	1	2	3	4	5	6	7
28	Subcode (x'28' or x'38')							
29	SW_location							
	Module number	SW_LSI#						
2A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte28-31 Detail

(5) Byte28 = 40 or 48 or 50 or 58

	0	1	2	3	4	5	6	7
28	Subcode (x'40' or x'48' or x'50' or x'58')							
29	SW_location							
	Module number	SW_LSI#						
2A	MPA_ERRSTS_MPx:B7							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2B	MPA_ERRSTS_MPx:B6							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	MPA_ERRSTS_MPx:B5							
	Not used	Not used	DBG_BLK Warning	R_BLK Warning	D3_BLK Warning	D2_BLK Warning	D1_BLK Warning	Not used
2D	MPA_ERRSTS_MPx:B4							
	M3_BLK Warning	M2_BLK Warning	M1_BLK Warning	M0_BLK Warning	E3_BLK Warning	E2_BLK Warning	E1_BLK Warning	M3_BLK Warning
2E	MPA_ERRSTS_MPx:B3							
	Not used	Not used	DBG_BLK CHK3	R_BLK CHK3	D3_BLK CHK3	D2_BLK CHK3	D1_BLK CHK3	Not used
2F	MPA_ERRSTS_MPx:B2							
	M3_BLK CHK3	M2_BLK CHK3	M1_BLK CHK3	M0_BLK CHK3	E3_BLK CHK3	E2_BLK CHK3	E1_BLK CHK3	M3_BLK CHK3
30	MPA_ERRSTS_MPx:B1							
	Not used	Not used	DBG_BLK CHK1B	R_BLK CHK1B	D3_BLK CHK1B	D2_BLK CHK1B	D1_BLK CHK1B	Not used
31	MPA_ERRSTS_MPx:B0							
	M3_BLK CHK1B	M2_BLK CHK1B	M1_BLK CHK1B	M0_BLK CHK1B	E3_BLK CHK1B	E2_BLK CHK1B	E1_BLK CHK1B	M3_BLK CHK1B

Byte27 = 81 (CHK3) Byte28-31 Detail

- (6) Byte28 = 81 or 82 or 83 or 84 or 89 or 8A or 8B or 8C or 8D or 8E or 8F or 90 or C5 or C6 or C7 or C8 or C9 or CA or CB or CC or CD or CE or CF or D0

	0	1	2	3	4	5	6	7
28	Subcode (x'81' or x'82' or x'83' or x'89' or x'8A' or x'8B' or x'8C' or x'8D' or x'8E' or x'8F' or x'90' or x'C5' or x'C6' or x'C7' or x'C8' or x'C9' or x'CA' or x'CB' or x'CC' or x'CD' or x'CE' or x'CF' or x'D0')							
29	SW_location							
	Module number	SW_LSI#						
2A	SW::PORTn_PATH_ERR:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PORTn ERRINT ERR
2B	SW::PORTn_PATH_ERR:B0							
	PXT PATH ERR	PXR PATH ERR	Not used	TX PATH ERR	ARB PATH ERR	BF1 PATH ERR	BF0 PATH ERR	RX PATH ERR
2C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 00 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::E_ERRSTS_MPx:B3							
	Not used	Not used	Not used	Not used	Not used	E_BLK Error10 CHK3	E_BLK Error9 CHK3	E_BLK Error8 CHK3
41	MPA::E_ERRSTS_MPx:B2							
	E_BLK Error7 CHK3	E_BLK Error6 CHK3	E_BLK Error5 CHK3	E_BLK Error4 CHK3	E_BLK Error3 CHK3	E_BLK Error2 CHK3	E_BLK Error1 CHK3	E_BLK Error0 CHK3
42	MPA::E_ERRSTS_MPx:B1							
	E_BLK PCI_250M CHK1B	E_BLK PCI_125M CHK1B	Not used	Not used	Not used	E_BLK Error10 CHK1B	E_BLK Error9 CHK1B	E_BLK Error8 CHK1B
43	MPA::E_ERRSTS_MPx:B0							
	E_BLK Error7 CHK1B	E_BLK Error6 CHK1B	E_BLK Error5 CHK1B	E_BLK Error4 CHK1B	E_BLK Error3 CHK1B	E_BLK Error2 CHK1B	E_BLK Error1 CHK1B	E_BLK Error0 CHK1B
44	MPA::E_ERRSTS_MPx:B5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
45	MPA::E_ERRSTS_MPx:B4							
	E_BLK Error7 Warning	E_BLK Error6 Warning	E_BLK Error5 Warning	E_BLK Error4 Warning	E_BLK Error3 Warning	E_BLK Error2 Warning	E_BLK Error1 Warning	E_BLK Error0 Warning
46	MPA::E_ERRDTL07_CHK3:B7							
	Not used	Not used	No_Read CPL Error	INV Packet Error	Not used	Not used	Not used	INV DST Error
47	MPA::E_ERRDTL07_CHK3:B6							
	Not used	Not used	Not used	P code Error	Not used	Not used	Not used	Multi Error Indirect RD
48	MPA::E_ERRDTL07_CHK3:B5							
	Not used	Not used	Not used	Multi ErrorATM	Not used	Not used	Not used	Multi Error WND_RD
49	MPA::E_ERRDTL07_CHK3:B4							
	Not used	Not used	Not used	Indirect RD Order Error	Not used	Not used	Not used	Indirect WT Order Error
4A	MPA::E_ERRDTL07_CHK3:B3							
	Not used	Not used	Not used	ATM Order Error	Not used	Not used	Not used	DT length Indirect RD LM/LR
4B	MPA::E_ERRDTL07_CHK3:B2							
	Not used	Not used	Not used	DT length Indirect RD CMA	Not used	Not used	Not used	DT length MPA_REG
4C	MPA::E_ERRDTL07_CHK3:B1							
	Not used	Not used	Not used	Boundary Indirect RD LM/LR	Not used	Not used	Not used	Boundary WND_RD LM/LR
4D	MPA::E_ERRDTL07_CHK3:B0							
	Not used	Not used	Not used	Boundary CMA	Not used	Not used	Not used	Boundary MPA_REG
4E	MPA::E_ERRDTL08_CHK3:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
4F	MPA::E_ERRDTL08_CHK3:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not Init Access

SSBLOG05-350

Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 00 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::ER1_LOG_HDR0_No_MP[B7]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B7]							
51	MPA::ER1_LOG_HDR0_No_MP[B6]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B6]							
52	MPA::ER1_LOG_HDR0_No_MP[B5]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B5]							
53	MPA::ER1_LOG_HDR0_No_MP[B4]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B4]							
54	MPA::ER1_LOG_HDR0_No_MP[B3]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B3]							
55	MPA::ER1_LOG_HDR0_No_MP[B2]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B2]							
56	MPA::ER1_LOG_HDR0_No_MP[B1]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B1]							
57	MPA::ER1_LOG_HDR0_No_MP[B0]							
	MP Judge Impossible Error Header Log(0DW/1DW)[B0]							
58	MPA::ER1_LOG_HDR1_No_MP[B7]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B7]							
59	MPA::ER1_LOG_HDR1_No_MP[B6]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B6]							
5A	MPA::ER1_LOG_HDR1_No_MP[B5]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B5]							
5B	MPA::ER1_LOG_HDR1_No_MP[B4]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B4]							
5C	MPA::ER1_LOG_HDR1_No_MP[B3]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B3]							
5D	MPA::ER1_LOG_HDR1_No_MP[B2]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B2]							
5E	MPA::ER1_LOG_HDR1_No_MP[B1]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B1]							
5F	MPA::ER1_LOG_HDR1_No_MP[B0]							
	MP Judge Impossible Error Header Log(2DW/3DW)[B0]							

SSBLOG05-360

Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 00 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::ER1_LOG_HDR0_MPxx[B7]							
	MP Judge Possible Error Header Log(0DW/1DW)[B7]							
61	MPA::ER1_LOG_HDR0_MPxx[B6]							
	MP Judge Possible Error Header Log(0DW/1DW)[B6]							
62	MPA::ER1_LOG_HDR0_MPxx[B5]							
	MP Judge Possible Error Header Log(0DW/1DW)[B5]							
63	MPA::ER1_LOG_HDR0_MPxx[B4]							
	MP Judge Possible Error Header Log(0DW/1DW)[B4]							
64	MPA::ER1_LOG_HDR0_MPxx[B3]							
	MP Judge Possible Error Header Log(0DW/1DW)[B3]							
65	MPA::ER1_LOG_HDR0_MPxx[B2]							
	MP Judge Possible Error Header Log(0DW/1DW)[B2]							
66	MPA::ER1_LOG_HDR0_MPxx[B1]							
	MP Judge Possible Error Header Log(0DW/1DW)[B1]							
67	MPA::ER1_LOG_HDR0_MPxx[B0]							
	MP Judge Possible Error Header Log(0DW/1DW)[B0]							
68	MPA::ER1_LOG_HDR1_MPxx[B7]							
	MP Judge Possible Error Header Log(2DW/3DW)[B7]							
69	MPA::ER1_LOG_HDR1_MPxx[B6]							
	MP Judge Possible Error Header Log(2DW/3DW)[B6]							
6A	MPA::ER1_LOG_HDR1_MPxx[B5]							
	MP Judge Possible Error Header Log(2DW/3DW)[B5]							
6B	MPA::ER1_LOG_HDR1_MPxx[B4]							
	MP Judge Possible Error Header Log(2DW/3DW)[B4]							
6C	MPA::ER1_LOG_HDR1_MPxx[B3]							
	MP Judge Possible Error Header Log(2DW/3DW)[B3]							
6D	MPA::ER1_LOG_HDR1_MPxx[B2]							
	MP Judge Possible Error Header Log(2DW/3DW)[B2]							
6E	MPA::ER1_LOG_HDR1_MPxx[B1]							
	MP Judge Possible Error Header Log(2DW/3DW)[B1]							
6F	MPA::ER1_LOG_HDR1_MPxx[B0]							
	MP Judge Possible Error Header Log(2DW/3DW)[B0]							

SSBLOG05-370

Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 00 (4/4)

	0	1	2	3	4	5	6	7
70	Vector Number [B3]							
	Vector Number [B3]							
71	Vector Number [B2]							
	Vector Number [B2]							
72	Vector Number [B1]							
	Vector Number [B1]							
73	Vector Number [B0]							
	Vector Number [B0]							
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = 01 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::M_ERRSTS_MPx:B3							
	M_BLK PCI_250M CHK3	M_BLK PCI_125M CHK3	Not used	Not used	Not used	Not used	Not used	Not used
41	MPA::M_ERRSTS_MPx:B2							
	M_BLK Error7 CHK3	M_BLK Error6 CHK3	M_BLK Error5 CHK3	M_BLK Error4 CHK3	M_BLK Error3 CHK3	M_BLK Error2 CHK3	M_BLK Error1 CHK3	M_BLK Error0 CHK3
42	MPA::M_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	MPA::M_ERRSTS_MPx:B0							
	M_BLK Error7 CHK1B	M_BLK Error6 CHK1B	M_BLK Error5 CHK1B	M_BLK Error4 CHK1B	M_BLK Error3 CHK1B	M_BLK Error2 CHK1B	M_BLK Error1 CHK1B	M_BLK Error0 CHK1B
44	MPA::M_ERRSTS_MPx:B5							
	M_BLK PCI_250M Warning2	M_BLK PCI_250M Warning1	M_BLK PCI_250M Warning0	M_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
45	MPA::M_ERRSTS_MPx:B4							
	M_BLK Error7 Warning	M_BLK Error6 Warning	M_BLK Error5 Warning	M_BLK Error4 Warning	M_BLK Error3 Warning	M_BLK Error2 Warning	M_BLK Error1 Warning	M_BLK Error0 Warning
46	MPA::M_ERRDTL07_CHK3_MPx:B6							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Blockage Access
47	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-390

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = 01 (2/4)

	0	1	2	3	4	5	6	7
50	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-400

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = 01 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-410

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = 01 (4/4)

	0	1	2	3	4	5	6	7
70	Vector Number [B3]							
	Vector Number [B3]							
71	Vector Number [B2]							
	Vector Number [B2]							
72	Vector Number [B1]							
	Vector Number [B1]							
73	Vector Number [B0]							
	Vector Number [B0]							
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

SSBLOG05-420

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = 02 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::R_ERRSTS_MPx:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	MPA::R_ERRSTS_MPx:B2							
	Not used	Not used	Not used	R_BLK SIP_Error CHK3	R_BLK Error3 CHK3	R_BLK Error2 CHK3	R_BLK Error1 CHK3	R_BLK Error0 CHK3
42	MPA::R_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	MPA::R_ERRSTS_MPx:B0							
	Not used	Not used	Not used	R_BLK SIP_Error CHK1B	R_BLK Error3 CHK1B	R_BLK Error2 CHK1B	R_BLK Error1 CHK1B	R_BLK Error0 CHK1B
44	MPA::R_ERRSTS_MPx:B5							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
45	MPA::R_ERRSTS_MPx:B4							
	Not used	Not used	Not used	R_BLK SIP_Error Warning	R_BLK Error3 Warning	R_BLK Error2 Warning	R_BLK Error1 Warning	R_BLK Error0 Warning
46	MPA::R_ERRDTL02_CHK3:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	INV ADR Error
47	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	MPA::RREG02_ANLOG_H0H1[B7]							
	R_BLK Error Header Log(0DW/1DW)[B7]							
49	MPA::RREG02_ANLOG_H0H1[B6]							
	R_BLK Error Header Log(0DW/1DW)[B6]							
4A	MPA::RREG02_ANLOG_H0H1[B5]							
	R_BLK Error Header Log(0DW/1DW)[B5]							
4B	MPA::RREG02_ANLOG_H0H1[B4]							
	R_BLK Error Header Log(0DW/1DW)[B4]							
4C	MPA::RREG02_ANLOG_H0H1[B3]							
	R_BLK Error Header Log(0DW/1DW)[B3]							
4D	MPA::RREG02_ANLOG_H0H1[B2]							
	R_BLK Error Header Log(0DW/1DW)[B2]							
4E	MPA::RREG02_ANLOG_H0H1[B1]							
	R_BLK Error Header Log(0DW/1DW)[B1]							
4F	MPA::RREG02_ANLOG_H0H1[B0]							
	R_BLK Error Header Log(0DW/1DW)[B0]							

SSBLOG05-430

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = 02 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::RREG02_ANLOG_H2H3[B7]							
	R_BLK Error Header Log(2DW/3DW)[B7]							
51	MPA::RREG02_ANLOG_H2H3[B6]							
	R_BLK Error Header Log(2DW/3DW)[B6]							
52	MPA::RREG02_ANLOG_H2H3[B5]							
	R_BLK Error Header Log(2DW/3DW)[B5]							
53	MPA::RREG02_ANLOG_H2H3[B4]							
	R_BLK Error Header Log(2DW/3DW)[B4]							
54	MPA::RREG02_ANLOG_H2H3[B3]							
	R_BLK Error Header Log(2DW/3DW)[B3]							
55	MPA::RREG02_ANLOG_H2H3[B2]							
	R_BLK Error Header Log(2DW/3DW)[B2]							
56	MPA::RREG02_ANLOG_H2H3[B1]							
	R_BLK Error Header Log(2DW/3DW)[B1]							
57	MPA::RREG02_ANLOG_H2H3[B0]							
	R_BLK Error Header Log(2DW/3DW)[B0]							
58	MPA::RREG02_ANLOG_H4H5[B7]							
	R_BLK Error Header Log(4DW/5DW)[B7]							
59	MPA::RREG02_ANLOG_H4H5[B6]							
	R_BLK Error Header Log(4DW/5DW)[B6]							
5A	MPA::RREG02_ANLOG_H4H5[B5]							
	R_BLK Error Header Log(4DW/5DW)[B5]							
5B	MPA::RREG02_ANLOG_H4H5[B4]							
	R_BLK Error Header Log(4DW/5DW)[B4]							
5C	MPA::RREG02_ANLOG_H4H5[B3]							
	R_BLK Error Header Log(4DW/5DW)[B3]							
5D	MPA::RREG02_ANLOG_H4H5[B2]							
	R_BLK Error Header Log(4DW/5DW)[B2]							
5E	MPA::RREG02_ANLOG_H4H5[B1]							
	R_BLK Error Header Log(4DW/5DW)[B1]							
5F	MPA::RREG02_ANLOG_H4H5[B0]							
	R_BLK Error Header Log(4DW/5DW)[B0]							

SSBLOG05-440

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = 02 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::RREG02_ANLOG_H6H7[B7]							
	R_BLK Error Header Log(6DW/7DW)[B7]							
61	MPA::RREG02_ANLOG_H6H7[B6]							
	R_BLK Error Header Log(6DW/7DW)[B6]							
62	MPA::RREG02_ANLOG_H6H7[B5]							
	R_BLK Error Header Log(6DW/7DW)[B5]							
63	MPA::RREG02_ANLOG_H6H7[B4]							
	R_BLK Error Header Log(6DW/7DW)[B4]							
64	MPA::RREG02_ANLOG_H6H7[B3]							
	R_BLK Error Header Log(6DW/7DW)[B3]							
65	MPA::RREG02_ANLOG_H6H7[B2]							
	R_BLK Error Header Log(6DW/7DW)[B2]							
66	MPA::RREG02_ANLOG_H6H7[B1]							
	R_BLK Error Header Log(6DW/7DW)[B1]							
67	MPA::RREG02_ANLOG_H6H7[B0]							
	R_BLK Error Header Log(6DW/7DW)[B0]							
68	MPA::RREG02_ANLOG_H8H9[B7]							
	R_BLK Error Header Log(8DW/9DW)[B7]							
69	MPA::RREG02_ANLOG_H8H9[B6]							
	R_BLK Error Header Log(8DW/9DW)[B6]							
6A	MPA::RREG02_ANLOG_H8H9[B5]							
	R_BLK Error Header Log(8DW/9DW)[B5]							
6B	MPA::RREG02_ANLOG_H8H9[B4]							
	R_BLK Error Header Log(8DW/9DW)[B4]							
6C	MPA::RREG02_ANLOG_H8H9[B3]							
	R_BLK Error Header Log(8DW/9DW)[B3]							
6D	MPA::RREG02_ANLOG_H8H9[B2]							
	R_BLK Error Header Log(8DW/9DW)[B2]							
6E	MPA::RREG02_ANLOG_H8H9[B1]							
	R_BLK Error Header Log(8DW/9DW)[B1]							
6F	MPA::RREG02_ANLOG_H8H9[B0]							
	R_BLK Error Header Log(8DW/9DW)[B0]							

SSBLOG05-450

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = 02 (4/4)

	0	1	2	3	4	5	6	7
70	Vector Number [B3]							
	Vector Number [B3]							
71	Vector Number [B2]							
	Vector Number [B2]							
72	Vector Number [B1]							
	Vector Number [B1]							
73	Vector Number [B0]							
	Vector Number [B0]							
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

SSBLOG05-460

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = 03 (1/4)

	0	1	2	3	4	5	6	7
40	SW::SEQ REG ST LOG:B3							
	ST0							
41	SW::SEQ REG ST LOG:B2							
	ST1							
42	SW::SEQ REG ST LOG:B1							
	ST2							
43	SW::SEQ REG ST LOG:B0							
	ST3							
44	SW::PKT DEC SEQDET ERR:B1							
	Not used	Not used	WTPCT PERR(1)	WTPCT PERR(0)	FLG ERR(1)	FLG ERR(0)	WTVIO ERR(1)	WTVIO ERR(0)
45	SW::PKT DEC SEQDET ERR:B0							
	TXTOV ERR(1)	TXTOV ERR(0)	PKT ERR(1)	PKT ERR(2)	NONADR RD ERR(1)	NONADR RD ERR(0)	NONADR WT ERR(1)	NONADR WT ERR(0)
46	SW::SB STATUS LOG							
	SB CTL ERR	SB PATH ERR	NO WTEN	ACS NG (RDT)	ACS NG (ADR)	SBWT FORBIT	SBRD FORBIT	SBACS FORBIT
47	SW::SB PLG BLK							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	WTEN ERR
48	SW::SEQ REG STLOG:B3							
	ST0							
49	SW::SEQ REG STLOG:B2							
	ST1							
4A	SW::SEQ REG STLOG:B1							
	ST2							
4B	SW::SEQ REG STLOG:B0							
	ST3							
4C	SW::CPL DEC RCVPKT ERR							
	ATOM ERR	WRRD ERR	CMRP ERR	NPP ERR	Not used	Not used	TYPE ERR	FORM ERR
4D	SW::CPL GEN PERR1							
	PCRC GEN PERR(3)	PCRC GEN PERR(2)	PCRC GEN PERR(1)	PCRC GEN PERR(0)	Not used	Not used	HCRC GEN PERR(1)	HCRC GEN PERR(0)
4E	SW::CPL CNT PERR							
	Not used	Not used	Not used	PLEN PERR	Not used	Not used	Not used	CNT PERR
4F								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used

SSBLOG05-470

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = 03 (2/4)

	0	1	2	3	4	5	6	7
50	SW::CPL DEC PKT00DW LOG:B3							
	PKT LOG							
51	SW::CPL DEC PKT00DW LOG:B2							
	PKT LOG							
52	SW::CPL DEC PKT00DW LOG:B1							
	PKT LOG							
53	SW::CPL DEC PKT00DW LOG:B0							
	PKT LOG							
54	SW::CPL DEC PKT01DW LOG:B3							
	PKT LOG							
55	SW::CPL DEC PKT01DW LOG:B2							
	PKT LOG							
56	SW::CPL DEC PKT01DW LOG:B1							
	PKT LOG							
57	SW::CPL DEC PKT01DW LOG:B0							
	PKT LOG							
58	SW::CPL DEC PKT02DW LOG:B3							
	PKT LOG							
59	SW::CPL DEC PKT02DW LOG:B2							
	PKT LOG							
5A	SW::CPL DEC PKT02DW LOG:B1							
	PKT LOG							
5B	SW::CPL DEC PKT02DW LOG:B0							
	PKT LOG							
5C	SW::CPL DEC PKT03DW LOG:B3							
	PKT LOG							
5D	SW::CPL DEC PKT03DW LOG:B2							
	PKT LOG							
5E	SW::CPL DEC PKT03DW LOG:B1							
	PKT LOG							
5F	SW::CPL DEC PKT03DW LOG:B0							
	PKT LOG							

SSBLOG05-480

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = 03 (3/4)

	0	1	2	3	4	5	6	7
60	SW::CPL DEC PKT04DW LOG:B3							
	PKT LOG							
61	SW::CPL DEC PKT04DW LOG:B2							
	PKT LOG							
62	SW::CPL DEC PKT04DW LOG:B1							
	PKT LOG							
63	SW::CPL DEC PKT04DW LOG:B0							
	PKT LOG							
64	SW::CPL DEC PKT05DW LOG:B3							
	PKT LOG							
65	SW::CPL DEC PKT05DW LOG:B2							
	PKT LOG							
66	SW::CPL DEC PKT05DW LOG:B1							
	PKT LOG							
67	SW::CPL DEC PKT05DW LOG:B0							
	PKT LOG							
68	SW::CPL DEC PKT06DW LOG:B3							
	PKT LOG							
69	SW::CPL DEC PKT06DW LOG:B2							
	PKT LOG							
6A	SW::CPL DEC PKT06DW LOG:B1							
	PKT LOG							
6B	SW::CPL DEC PKT06DW LOG:B0							
	PKT LOG							
6C	SW::CPL DEC PKT07DW LOG:B3							
	PKT LOG							
6D	SW::CPL DEC PKT07DW LOG:B2							
	PKT LOG							
6E	SW::CPL DEC PKT07DW LOG:B1							
	PKT LOG							
6F	SW::CPL DEC PKT07DW LOG:B0							
	PKT LOG							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = 03 (4/4)

	0	1	2	3	4	5	6	7
70	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
71	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
72	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
73	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
74	Access Type [B3]							
75	Access Type [B2]							
76	Access Type [B1]							
77	Access Type [B0]							
78	Access Address [B7]							
79	Access Address [B6]							
7A	Access Address [B5]							
7B	Access Address [B4]							
7C	Access Address [B3]							
7D	Access Address [B2]							
7E	Access Address [B1]							
7F	Access Address [B0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = 04 (1/4)

	0	1	2	3	4	5	6	7
40	SW::ERRINT LOG:B1							
	Not used	Not used	Not used	P12_ERRINT _LOG	P11_ERRINT _LOG	P10_ERRINT _LOG	P09_ERRINT _LOG	P08_ERRIN T_LOG
41	SW::ERRINT LOG:B0							
	P07_ERRIN T_LOG	P06_ERRINT _LOG	P05_ERRINT _LOG	P04_ERRINT _LOG	P03_ERRINT _LOG	P02_ERRINT _LOG	P01_ERRINT _LOG	Not used
42	SW::RX_LOG_ERR:B1							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1 TOV	HCRC ERR
43	SW::RX_LOG_ERR:B0							
	PKTLEN ERR	ROUTING ERR	PCRC FLG ERR	PKTEND ERR	UNDERRUN ERR	HCRCFLG ERR	STP ERR	PCRC ERR
44	SW::BF0_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
45	SW::BF0_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
46	SW::BF0_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
47	SW::BF0_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
48	SW::BF1_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
49	SW::BF1_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
4A	SW::BF1_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
4B	SW::BF1_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
4C	SW::PCIE_ERR_STS:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
4D	SW::PCIE_ERR_STS:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL_UP ERR
4E	SW::PCIE_PORT_STATUS:B1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
4F	SW::PCIE_PORT_STATUS:B0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP

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Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = 04 (2/4)

	0	1	2	3	4	5	6	7
50	CMA::BODERR:B0							
	BODERR[B0]							
51	CMA::BODERR:B1							
	BODERR[B1]							
52	CMA::BODERR:B2							
	BODERR[B2]							
53	CMA::BODERR:B3							
	BODERR[B3]							
54	CMA::ANYERR:B0							
	ANYERR[B0]							
55	CMA::ANYERR:B1							
	ANYERR[B1]							
56	CMA::ANYERR:B2							
	ANYERR[B2]							
57	CMA::ANYERR:B3							
	ANYERR[B3]							
58	CMA::ANYWRN:B0							
	ANYWRN[B0]							
59	CMA::ANYWRN:B1							
	ANYWRN[B1]							
5A	CMA::ANYWRN:B2							
	ANYWRN[B2]							
5B	CMA::ANYWRN:B3							
	ANYWRN[B3]							
5C	CMA::SLVERR:B0							
	SLVERR[B0]							
5D	CMA::SLVERR:B1							
	SLVERR[B1]							
5E	CMA::SLVERR:B2							
	SLVERR[B2]							
5F	CMA::SLVERR:B3							
	SLVERR[B3]							

SSBLOG05-520

Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = 04 (3/4)

	0	1	2	3	4	5	6	7
60	CMA::MEMBLK0 DIMM configuration Byte0							
	MEMBLK0 DIMM configuration Byte0							
61	CMA::MEMBLK0 DIMM configuration Byte1							
	MEMBLK0 DIMM configuration Byte1							
62	CMA::MEMBLK0 DIMM configuration Byte2							
	MEMBLK0 DIMM configuration Byte2							
63	CMA::MEMBLK0 DIMM configuration Byte3							
	MEMBLK0 DIMM configuration Byte3							
64	CMA::MEMBLK0 DIMM check enable							
	MEMBLK0 DIMM check enable							
65	CMA::MEMBLK0 MEMCTL MEMCTLSIG							
	MEMBLK0 MEMCTL MEMCTLSIG							
66	CMA::MEMBLK0 MRDCTL uncorrectable error location							
	MEMBLK0 MRDCTL uncorrectable error location							
67	CMA::MEMBLK0 MRDCTL correctable error location							
	MEMBLK0 MRDCTL correctable error location							
68	CMA::MEMBLK0 MRDCTL DQS count error location							
	MEMBLK0 MRDCTL DQS count error location							
69	CMA::MEMBLK0 RTNCTL ANYERR Status							
	MEMBLK0 RTNCTL ANYERR Status							
6A	CMA::MEMBLK1 DIMM configuration Byte0							
	MEMBLK1 DIMM configuration Byte0							
6B	CMA::MEMBLK1 DIMM configuration Byte1							
	MEMBLK1 DIMM configuration Byte1							
6C	CMA::MEMBLK1 DIMM configuration Byte2							
	MEMBLK1 DIMM configuration Byte2							
6D	CMA::MEMBLK1 DIMM configuration Byte3							
	MEMBLK1 DIMM configuration Byte3							
6E	CMA::MEMBLK1 DIMM check enable							
	MEMBLK1 DIMM check enable							
6F	CMA::MEMBLK1 MEMCTL MEMCTLSIG							
	MEMBLK1 MEMCTL MEMCTLSIG							

SSBLOG05-530

Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = 04 (4/4)

	0	1	2	3	4	5	6	7
70	CMA::MEMBLK1 MRDCTL uncorrectable error location							
	MEMBLK1 MRDCTL uncorrectable error location							
71	CMA::MEMBLK1 MRDCTL correctable error location							
	MEMBLK1 MRDCTL correctable error location							
72	CMA::MEMBLK1 MRDCTL DQS count error location							
	MEMBLK1 MRDCTL DQS count error location							
73	CMA::MEMBLK1 RTNCTL ANYERR Status							
	MEMBLK1 RTNCTL ANYERR Status							
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = 10 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::D_ERRSTS_MPx:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 CHK3	D_BLK Error8 CHK3
41	MPA::D_ERRSTS_MPx:B2							
	D_BLK Error7 CHK3	D_BLK Error6 CHK3	D_BLK Error5 CHK3	D_BLK Error4 CHK3	D_BLK Error3 CHK3	D_BLK Error2 CHK3	D_BLK Error1 CHK3	D_BLK Error0 CHK3
42	MPA::D_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 CHK1B	D_BLK Error8 CHK1B
43	MPA::D_ERRSTS_MPx:B0							
	D_BLK Error7 CHK1B	D_BLK Error6 CHK1B	D_BLK Error5 CHK1B	D_BLK Error4 CHK1B	D_BLK Error3 CHK1B	D_BLK Error2 CHK1B	D_BLK Error1 CHK1B	D_BLK Error0 CHK1B
44	MPA::D_ERRSTS_MPx:B5							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 Warning	D_BLK Error8 Warning
45	MPA::D_ERRSTS_MPx:B4							
	D_BLK Error7 Warning	D_BLK Error6 Warning	D_BLK Error5 Warning	D_BLK Error4 Warning	D_BLK Error3 Warning	D_BLK Error2 Warning	D_BLK Error1 Warning	D_BLK Error0 Warning
46	MPA::D_ERRDTL02_CHK3:B4							
	Not used	Status Write Address Low Limit Error	Status Write Address High Limit Error	Status Write Address Boundary	Not used	Base Address Low Limit Error	Base Address High Limit Error	Base Address Boundary Error
47	MPA::D_ERRDTL02_CHK3:B0							
	Diff Counter Parity Error1	Diff Counter Parity Error2	PI Write Count Full Error	PI Write Count Parity Error	PI Parity Error	CI Parity Error	PI Inclement Error	PI Pass Error
48	MPA::D_ERRDTL05_CHK3:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Transmit Unmatch CM
49	MPA::D_ERRDTL05_CHK3:B2							
	Not used	Not used	Not used	Transmit Unmatch LM	Not used	Not used	Not used	Transmit Unmatch DXBF
4A	MPA::D_ERRDTL05_CHK3:B1							
	Not used	Not used	Not used	Mode Destination Paradox	Not used	Not used	Not used	Mode Access Type Paradox
4B	MPA::D_ERRDTL05_CHK3:B0							
	Not used	Not used	Not used	DT length Limit Error	Not used	Not used	Not used	DT length ODW Error
4C	MPA::D_ERRDTL06_CHK3:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	LM Address High Limit Error
4D	MPA::D_ERRDTL06_CHK3:B2							
	Not used	Not used	Not used	LM Address Low Limit Error	Not used	Not used	Not used	LM Address Boundary Error
4E	MPA::D_ERRDTL06_CHK3:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Transmit Address High Limit
4F	MPA::D_ERRDTL06_CHK3:B0							
	Not used	Not used	Not used	Transmit Address Low Limit	Not used	Not used	Not used	Transmit Address Boundary

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = 10 (2/4)

	0	1	2	3	4	5	6	7
50	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	MPA::D_ERRDTL07_CHK3:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	EOR Write Parameter Unmatch
53	MPA::D_ERRDTL07_CHK3:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	EOR Write Data Unmatch
54	MPA::D_ERRDTL09_CHK3:B3							
	Not used	Not used	Not used	DMA Mpath(R) Timeout	Not used	Not used	Not used	DMA Mpath(W) Timeout
55	MPA::D_ERRDTL09_CHK3:B2							
	Not used	Not used	Not used	DMA HSNSTS Warning	Not used	Not used	Not used	DMA HSNSTS Error
56	MPA::D_ERRDTL09_CHK3:B1							
	Not used	Not used	Not used	Blockade Path Access Error to M3	Not used	Not used	Not used	Blockade Path Access Error to M2
57	MPA::D_ERRDTL09_CHK3:B0							
	Not used	Not used	Not used	Blockade Path Access Error to M1	Not used	Not used	Not used	Blockade Path Access Error to M0
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	MPA::DREG00_ANLOG_H2H3[B7]							
	D_BLK Error Header Log(2DW/3DW)[B7]							
5D	MPA::DREG00_ANLOG_H2H3[B6]							
	D_BLK Error Header Log(2DW/3DW)[B6]							
5E	MPA::DREG00_ANLOG_H2H3[B5]							
	D_BLK Error Header Log(2DW/3DW)[B5]							
5F	MPA::DREG00_ANLOG_H2H3[B4]							
	D_BLK Error Header Log(2DW/3DW)[B4]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = 10 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::DREG00_ANLOG_H2H3[B3]							
	D_BLK Error Header Log(2DW/3DW)[B3]							
61	MPA::DREG00_ANLOG_H2H3[B2]							
	D_BLK Error Header Log(2DW/3DW)[B2]							
62	MPA::DREG00_ANLOG_H2H3[B1]							
	D_BLK Error Header Log(2DW/3DW)[B1]							
63	MPA::DREG00_ANLOG_H2H3[B0]							
	D_BLK Error Header Log(2DW/3DW)[B0]							
64	MPA::DREG00_ANLOG_C0C1[B7]							
	D_BLK Error Command Log(0DW/1DW)[B7]							
65	MPA::DREG00_ANLOG_C0C1[B6]							
	D_BLK Error Command Log(0DW/1DW)[B6]							
66	MPA::DREG00_ANLOG_C0C1[B5]							
	D_BLK Error Command Log(0DW/1DW)[B5]							
67	MPA::DREG00_ANLOG_C0C1[B4]							
	D_BLK Error Command Log(0DW/1DW)[B4]							
68	MPA::DREG00_ANLOG_C0C1[B3]							
	D_BLK Error Command Log(0DW/1DW)[B3]							
69	MPA::DREG00_ANLOG_C0C1[B2]							
	D_BLK Error Command Log(0DW/1DW)[B2]							
6A	MPA::DREG00_ANLOG_C0C1[B1]							
	D_BLK Error Command Log(0DW/1DW)[B1]							
6B	MPA::DREG00_ANLOG_C0C1[B0]							
	D_BLK Error Command Log(0DW/1DW)[B0]							
6C	MPA::DREG00_ANLOG_C2C3[B7]							
	D_BLK Error Command Log(2DW/3DW)[B7]							
6D	MPA::DREG00_ANLOG_C2C3[B6]							
	D_BLK Error Command Log(2DW/3DW)[B6]							
6E	MPA::DREG00_ANLOG_C2C3[B5]							
	D_BLK Error Command Log(2DW/3DW)[B5]							
6F	MPA::DREG00_ANLOG_C2C3[B4]							
	D_BLK Error Command Log(2DW/3DW)[B4]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = 10 (4/4)

	0	1	2	3	4	5	6	7
70	MPA::DREG00_ANLOG_C2C3[B3]							
	D_BLK Error Command Log(2DW/3DW)[B3]							
71	MPA::DREG00_ANLOG_C2C3[B2]							
	D_BLK Error Command Log(2DW/3DW)[B2]							
72	MPA::DREG00_ANLOG_C2C3[B1]							
	D_BLK Error Command Log(2DW/3DW)[B1]							
73	MPA::DREG00_ANLOG_C2C3[B0]							
	D_BLK Error Command Log(2DW/3DW)[B0]							
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = 20 or 30 (1/4)

	0	1	2	3	4	5	6	7
40	SW::PORT01 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
41	SW::PORT02 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
42	SW::PORT03 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
43	SW::PORT04 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
44	SW::PORT05 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
45	SW::PORT06 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
46	SW::PORT07 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
47	SW::PORT08 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
48	SW::PORT09 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
49	SW::PORT10 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4A	SW::PORT11 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4B	SW::PORT12 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4C	SW::PORT13 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4D	SW::PORT14 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4E	SW::PORT15 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR
4F	SW::PORT16 HW ERR							
	PXT HW ERR	PXR HW ERR	Not used	TX HW ERR	ARB HW ERR	BF1 HW ERR	BF0 HW ERR	RX HW ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = 20 or 30 (2/4)

	0	1	2	3	4	5	6	7
50	SW::REG HW ERR							
	Not used	Not used	Not used	Not used	CRG HW ERR	REG ACS HW ERR	ARBREG HW ERR	REG BLK HW ERR
51	SW::CPL HW ERR							
	Not used	Not used	Not used	Not used	Not used	Not used	ARBCPL HW ERR	CPLBLK HW ERR
52	SW::SB HW ERR							
	Not used	SB ARB	PLG BLK	SCANT TIM	SCANT SEQ	SCANT TX BLK	SCANT RX SEQ	SCNANT RX BLK
53								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
54	SW::SWA ERR:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
55	SW::SWA ERR:B0							
	Not used	Not used	Not used	Not used	Not used	SWA MICRO ERR	Not used	Not used
56	SW::SWB ERR:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
57	SW::SWB ERR:B0							
	Not used	Not used	Not used	Not used	Not used	SWB MICRO ERR	Not used	Not used
58	SW::SWC ERR:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
59	SW::SWC ERR:B0							
	Not used	Not used	Not used	Not used	Not used	SWC MICRO ERR	Not used	Not used
5A	SW::SWD ERR:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5B	SW::SWD ERR:B0							
	Not used	Not used	Not used	Not used	Not used	SWD MICRO ERR	Not used	Not used
5C	SW::REP_ERR:B3							
	SW WARNING	Not used	Not used	Not used	Not used	Not used		SW WARNING
5D	SW::REP_ERR:B2							
	SB ERR	SWD ERR	SWC ERR	SWB ERR	SWA ERR	REG ERR		SB ERR
5E	SW::REP_ERR:B1							
	PORT15 ERR	PORT14 ERR	PORT13 ERR	PORT12 ERR	PORT11 ERR	PORT10 ERR		PORT15 ERR
5F	SW::REP_ERR:B0							
	PORT7 ERR	PORT6 ERR	PORT5 ERR	PORT4 ERR	PORT3 ERR	PORT2 ERR		PORT7 ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = 20 or 30 (3/4)

	0	1	2	3	4	5	6	7
60	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
61	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
62	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
63	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
64	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
65	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
66	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
67	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
68	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
69	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6A	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6B	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6C	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6D	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6E	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6F	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used

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Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = 20 or 30 (4/4)

	0	1	2	3	4	5	6	7
70	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
71	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
72	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
73	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
74	Access Type [B3]							
75	Access Type [B2]							
76	Access Type [B1]							
77	Access Type [B0]							
78	Access Address [B7]							
79	Access Address [B6]							
7A	Access Address [B5]							
7B	Access Address [B4]							
7C	Access Address [B3]							
7D	Access Address [B2]							
7E	Access Address [B1]							
7F	Access Address [B0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(8) Byte28 = 28 or 38 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::R_ERRSTS_MPx:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	MPA::R_ERRSTS_MPx:B2							
	Not used	Not used	Not used	R_BLK SIP_Error CHK3	R_BLK Error3 CHK3	R_BLK Error2 CHK3	R_BLK Error1 CHK3	R_BLK Error0 CHK3
42	MPA::R_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	MPA::R_ERRSTS_MPx:B0							
	Not used	Not used	Not used	R_BLK SIP_Error CHK1B	R_BLK Error3 CHK1B	R_BLK Error2 CHK1B	R_BLK Error1 CHK1B	R_BLK Error0 CHK1B
44	MPA::R_ERRSTS_MPx:B5							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
45	MPA::R_ERRSTS_MPx:B4							
	Not used	Not used	Not used	R_BLK SIP_Error Warning	R_BLK Error3 Warning	R_BLK Error2 Warning	R_BLK Error1 Warning	R_BLK Error0 Warning
46	MPA::R_ERRDTL01_CHK3:B7							
	Not used	Not used	Not used	Not used	Not used	SBTMR Scan Polling Read Timeout	SBTMR Scan Read Timeout	SBTMR Scan Write Timeout
47	MPA::R_ERRDTL03_CHK3:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	SCANM1 Byte Enable Parity Error	SCANM0 Byte Enable Parity Error
48	MPA::SCANM_ERR_REG0:B3							
	SCANM TX Pointer Parity Error	SCANM SEQ Wait0 Counter Parity Error	SCANM SEQ Wait1 Counter Parity Error	SCANM SEQ Wait2 Counter Parity Error	SCANM SEQ Loop Counter Parity Error	SCANM RXSEQ Loop Counter Parity	SCANM RX pointer Parity Error	Not used
49	MPA::SCANM_ERR_REG0:B2							
	SCAN ADR Byte3 Parity Error	SCAN ADR Byte2 Parity Error	SCAN ADR Byte1 Parity Error	SCAN ADR Byte0 Parity Error	SCAN Write Data Byte3 Parity Error	SCAN Write Data Byte2 Parity Error	SCAN Write Data Byte1 Parity Error	SCAN Write Data Byte0 Parity Error
4A	MPA::SCANM_ERR_REG0:B1							
	XR Read Data Byte3 Parity Error	XR Read Data Byte2 Parity Error	XR Read Data Byte1 Parity Error	XR Read Data Byte0 Parity Error	Not used	Not used	Not used	Not used
4B	MPA::SCANM_ERR_REG0:B0							
	SCAN Read Data Byte7 Parity Error	SCAN Read Data Byte6 Parity Error	SCAN Read Data Byte5 Parity Error	SCAN Read Data Byte4 Parity Error	SCAN Read Data Byte3 Parity Error	SCAN Read Data Byte2 Parity Error	SCAN Read Data Byte1 Parity Error	SCAN Read Data Byte0 Parity Error
4C	MPA::SCANM_ERR_REG1:B3							
	Status Byte3 Parity Error	Status Byte2 Parity Error	Status Byte1 Parity Error	Status Byte0 Parity Error	SCAN Timer Byte3 Parity Error	SCAN Timer Byte2 Parity Error	SCAN Timer Byte1 Parity Error	SCAN Timer Byte0 Parity Error
4D	MPA::SCANM_ERR_REG1:B2							
	SCAN SEQ Parity Error	SCAN SEQ Encode Error	SCAN SEQ Decode Error	SCAN Timeout	SCAN RXSEQ Parity Error	SCAN RXSEQ Encode Error	SCAN RXSEQ Decode Error	LRC Error
4E	MPA::SCANM_ERR_REG1:B1							
	Receive Status7	Receive Status6	Receive Status5	Receive Status4	Receive Status3	Receive Status2	Receive Status1	Receive Status0
4F	MPA::SCANM_ERR_REG1:B0							
	Receive Add Status7	Receive Add Status6	Receive Add Status5	Receive Add Status4	Receive Add Status3	Receive Add Status2	Receive Add Status1	Receive Add Status0

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Byte27 = 81 (CHK3) Byte40-7F Detail

(8) Byte28 = 28 or 38 (2/4)

	0	1	2	3	4	5	6	7
50	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-640

Byte27 = 81 (CHK3) Byte40-7F Detail

(8) Byte28 = 28 or 38 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte40-7F Detail

(8) Byte28 = 28 or 38 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

SSBLOG05-660

Byte27 = 81 (CHK3) Byte40-7F Detail

(9) Byte28 = 40 or 48 or 50 or 58 or 8B or 8C or C9 or CA (1/4)

	0	1	2	3	4	5	6	7
40	MPA::M_ERRSTS_MPx:B3							
	M_BLK PCI_250M CHK3	M_BLK PCI_125M CHK3	Not used	Not used	Not used	Not used	Not used	Not used
41	MPA::M_ERRSTS_MPx:B2							
	M_BLK Error7 CHK3	M_BLK Error6 CHK3	M_BLK Error5 CHK3	M_BLK Error4 CHK3	M_BLK Error3 CHK3	M_BLK Error2 CHK3	M_BLK Error1 CHK3	M_BLK Error0 CHK3
42	MPA::M_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	MPA::M_ERRSTS_MPx:B0							
	M_BLK Error7 CHK1B	M_BLK Error6 CHK1B	M_BLK Error5 CHK1B	M_BLK Error4 CHK1B	M_BLK Error3 CHK1B	M_BLK Error2 CHK1B	M_BLK Error1 CHK1B	M_BLK Error0 CHK1B
44	MPA::M_ERRSTS_MPx:B5							
	M_BLK PCI_250M Warning2	M_BLK PCI_250M Warning1	M_BLK PCI_250M Warning0	M_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
45	MPA::M_ERRSTS_MPx:B4							
	M_BLK Error7 Warning	M_BLK Error6 Warning	M_BLK Error5 Warning	M_BLK Error4 Warning	M_BLK Error3 Warning	M_BLK Error2 Warning	M_BLK Error1 Warning	M_BLK Error0 Warning
46	MPA::M_ERRDTL02_CHK3:B3							
	Not used	Not used	MREG01 SEQ# Error Module0 LM	MREG01 SEQ# Error Module0 LR4	MREG01 SEQ# Error Module0 LR3	MREG01 SEQ# Error Module0 LR2	MREG01 SEQ# Error Module0 LR1	MREG01 SEQ# Error Module0 LR0
47	MPA::M_ERRDTL02_CHK3:B2							
	Table Paradox Error	Round Robin Error	Before FF Data Parity Error	Payload Illegal Error	Core I/F Flag Error	Table No Wait Ack Error	HSN Command Error	DSTID REQID Unmatch
48	MPA::M_ERRDTL02_CHK3:B1							
	HCRC Error	PCRC Error	Packet Flag Read Pointer Parity	Packet Flag Write Pointer Parity	Read Pointer Parity Error	Write Pointer Parity Error	Diff Counter Parity Error	Data Parity Error
49	MPA::M_ERRDTL02_CHK3:B0							
	MREG01 END OFF Error	MREG01 END ON Error	MREG01 STP OFF Error	MREG01 STP ON Error	MREG01 Own and DST Unmatch	MREG01 No PFLG Error	MREG01 LR256B Over Error	MREGX DIFF Counter Parity Error
4A	MPA::M_ERRDTL03_CHK3:B3							
	Not used	Not used	MREG01 SEQ# Error Module1 LM	MREG01 SEQ# Error Module1 LR4	MREG01 SEQ# Error Module1 LR3	MREG01 SEQ# Error Module1 LR2	MREG01 SEQ# Error Module1 LR1	MREG01 SEQ# Error Module1 LR0
4B	MPA::M_ERRDTL04_CHK3:B4							
	Not used	Not used	Not used	PXTB125 Data Parity Error	Not used	Not used	Not used	PXRB125 Data Parity Error
4C	MPA::M_ERRDTL04_CHK3:B3							
	Not used	SEQT0M Data Parity Error	SEQT0S Data Parity Error	SEQT0C Data Parity Error	Not used	SEQR0M Data Parity Error	SEQR0S Data Parity Error	SEQR0C Data Parity Error
4D	MPA::M_ERRDTL04_CHK3:B2							
	Not used	PXTB Data Parity Error	PXTC Data Parity Error	PXTH Data Parity Error	Not used	PXRB Data Parity Error	PXRC Data Parity Error	PXRH Data Parity Error
4E	MPA::M_ERRDTL04_CHK3:B1							
	Not used	KSEQ01_P Decode Error	KSEQ01_P Encode Error	KSEQ01_P Error	KSEQ01 Compare Error	KSEQ01_D Decode Error	KSEQ01_D Encode Error	KSEQ01_D Error
4F	MPA::M_ERRDTL04_CHK3:B0							
	Not used	KSEQ00_P Decode Error	KSEQ00_P Encode Error	KSEQ00_P Error	KSEQ00 Compare Error	KSEQ00_D Decode Error	KSEQ00_D Encode Error	KSEQ00_D Error

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Byte27 = 81 (CHK3) Byte40-7F Detail

(9) Byte28 = 40 or 48 or 50 or 58 or 8B or 8C or C9 or CA (2/4)

	0	1	2	3	4	5	6	7
50	MPA::M_ERRDTL07_CHK3_MPx:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	HSN Status Error
51	MPA::M_ERRDTL07_CHK3_MPx:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Path Timeout Error
52	MPA::M_ERRDTL_PCIE125:B1							
	Not used	Not used	Not used	Not used	Not used	PXRB Register Error	PXRB Error	PXRB Hardware Error
53	MPA::M_ERRDTL_PCIE125:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PXRB FATAL Error
54	MPA::M_ERRDTL_PCIE250:B7							
	SEQT0 Register Read Parity	PXTB Register Read Parity	PXTC Register Read Parity	PXTH Register Read Parity	SEQR0 Register Read Parity	PXRB Register Read Parity	PXRC Register Read Parity	PXRHL Register Read Parity
55	MPA::M_ERRDTL_PCIE250:B6							
	Not used	Not used	SEQT0C Error	SEQT0M Error	SEQT0S Error	PXTB Error	PXTC Error	PXTH Error
56	MPA::M_ERRDTL_PCIE250:B5							
	Not used	Not used	SEQR0C Error	SEQR0M Error	SEQR0S Error	PXRB Error	PXRC Error	PXRHL Error
57	MPA::M_ERRDTL_PCIE250:B4							
	Not used	Not used	SEQT0C Hardware Error	SEQT0M Hardware Error	SEQT0S Hardware Error	PXTB Hardware Error	PXTC Hardware Error	Not used
58	MPA::M_ERRDTL_PCIE250:B3							
	Not used	Not used	SEQR0C Hardware Error	SEQR0M Hardware Error	SEQR0S Hardware Error	PXRB Hardware Error	PXRC Hardware Error	Not used
59	MPA::M_ERRDTL_PCIE250:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	PXTC Micro Error	PXRC Path Error
5A	MPA::M_ERRDTL_PCIE250:B1							
	Not used	Not used	PXTHL TXVC Error B	PXTHL RXVC Error B	PXTHL DLP Error B	PXRHL TXVC Error B	PXRHL RXVC Error B	PXRHL DLP Error B
5B	MPA::M_ERRDTL_PCIE250:B0							
	Not used	Not used	PXTHL TXVC Error A	PXTHL RXVC Error A	PXTHL DLP Error A	PXRHL TXVC Error A	PXRHL RXVC Error A	PXRHL DLP Error A
5C	SW::ERRINT_LOG:B1							
	Not used	Not used	Not used	P12_ERRIN T_LOG	P11_ERRIN T_LOG	P10_ERRIN T_LOG	P09_ERRIN T_LOG	P08_ERRIN T_LOG
5D	SW::ERRINT_LOG:B0							
	P07_ERRIN T_LOG	P06_ERRIN T_LOG	P05_ERRIN T_LOG	P04_ERRIN T_LOG	P03_ERRIN T_LOG	P02_ERRIN T_LOG	P01_ERRIN T_LOG	Not used
5E	SW::RX_LOG_ERR:B1							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1 TOV	HCRC ERR
5F	SW::RX_LOG_ERR:B0							
	PKTLEN ERR	ROUTING ERR	PCRC FLG ERR	PKTEND ERR	UNDERRUN ERR	HCRCFLG ERR	STP ERR	PCRC ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(9) Byte28 = 40 or 48 or 50 or 58 or 8B or 8C or C9 or CA (3/4)

	0	1	2	3	4	5	6	7
60	SW::BF0_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
61	SW::BF0_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
62	SW::BF0_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
63	SW::BF0_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
64	SW::BF0_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
65	SW::BF0_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
66								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
67								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
68	SW::BF1_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
69	SW::BF1_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
6A	SW::BF1_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
6B	SW::BF1_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
6C	SW::BF1_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
6D	SW::BF1_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
6E	SW::PCIE_ERR_STS:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
6F	SW::PCIE_ERR_STS:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL UP ERR

SSBLOG05-690

Byte27 = 81 (CHK3) Byte40-7F Detail

(9) Byte28 = 40 or 48 or 50 or 58 or 8B or 8C or C9 or CA (4/4)

	0	1	2	3	4	5	6	7
70	SW::PCIE_MFTLP_CODE_LOG							
	TLM MFTLP CODE(7)	TLM MFTLP CODE(6)	TLM MFTLP CODE(5)	TLM MFTLP CODE(4)	TLM MFTLP CODE(3)	TLM MFTLP CODE(2)	TLM MFTLP CODE(1)	TLM MFTLP CODE(0)
71	SW::PCIE_PORT_STATUS:B1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
72	SW::PCIE_PORT_STATUS:B0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP
73	SW::TXVC_MFTLP_CODE_LOG							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

SSBLOG05-700

Byte27 = 81 (CHK3) Byte40-7F Detail

(10) Byte28 = 81 or 82 or 83 or 84 or C5 or C6 or C7 or C8 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::M_ERRSTS_MPx:B3							
	M_BLK PCI_250M CHK3	M_BLK PCI_125M CHK3	Not used	Not used	Not used	Not used	Not used	Not used
41	MPA::M_ERRSTS_MPx:B2							
	M_BLK Error7 CHK3	M_BLK Error6 CHK3	M_BLK Error5 CHK3	M_BLK Error4 CHK3	M_BLK Error3 CHK3	M_BLK Error2 CHK3	M_BLK Error1 CHK3	M_BLK Error0 CHK3
42	MPA::M_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	MPA::M_ERRSTS_MPx:B0							
	M_BLK Error7 CHK1B	M_BLK Error6 CHK1B	M_BLK Error5 CHK1B	M_BLK Error4 CHK1B	M_BLK Error3 CHK1B	M_BLK Error2 CHK1B	M_BLK Error1 CHK1B	M_BLK Error0 CHK1B
44	MPA::M_ERRSTS_MPx:B5							
	M_BLK PCI_250M Warning2	M_BLK PCI_250M Warning1	M_BLK PCI_250M Warning0	M_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
45	MPA::M_ERRSTS_MPx:B4							
	M_BLK Error7 Warning	M_BLK Error6 Warning	M_BLK Error5 Warning	M_BLK Error4 Warning	M_BLK Error3 Warning	M_BLK Error2 Warning	M_BLK Error1 Warning	M_BLK Error0 Warning
46	MPA::M_ERRDTL02_CHK3:B3							
	Not used	Not used	MREG01 SEQ# Error Module0 LM	MREG01 SEQ# Error Module0 LR4	MREG01 SEQ# Error Module0 LR3	MREG01 SEQ# Error Module0 LR2	MREG01 SEQ# Error Module0 LR1	MREG01 SEQ# Error Module0 LR0
47	MPA::M_ERRDTL02_CHK3:B2							
	Table Paradox Error	Round Robin Error	Before FF Data Parity Error	Payload Illegal Error	Core I/F Flag Error	Table No Wait Ack Error	HSN Command Error	DSTID REQID Unmatch
48	MPA::M_ERRDTL02_CHK3:B1							
	HCRC Error	PCRC Error	Packet Flag Read Pointer Parity	Packet Flag Write Pointer Parity	Read Pointer Parity Error	Write Pointer Parity Error	Diff Counter Parity Error	Data Parity Error
49	MPA::M_ERRDTL02_CHK3:B0							
	MREG01 END OFF Error	MREG01 END ON Error	MREG01 STP OFF Error	MREG01 STP ON Error	MREG01 Own and DST Unmatch	MREG01 No PFLG Error	MREG01 LR256B Over Error	MREGX DIFF Counter Parity Error
4A	MPA::M_ERRDTL03_CHK3:B3							
	Not used	Not used	MREG01 SEQ# Error Module1 LM	MREG01 SEQ# Error Module1 LR4	MREG01 SEQ# Error Module1 LR3	MREG01 SEQ# Error Module1 LR2	MREG01 SEQ# Error Module1 LR1	MREG01 SEQ# Error Module1 LR0
4B	MPA::M_ERRDTL04_CHK3:B4							
	Not used	Not used	Not used	PXTB125 Data Parity Error	Not used	Not used	Not used	PXRB125 Data Parity Error
4C	MPA::M_ERRDTL04_CHK3:B3							
	Not used	SEQT0M Data Parity Error	SEQT0S Data Parity Error	SEQT0C Data Parity Error	Not used	SEQR0M Data Parity Error	SEQR0S Data Parity Error	SEQR0C Data Parity Error
4D	MPA::M_ERRDTL04_CHK3:B2							
	Not used	PXTB Data Parity Error	PXTC Data Parity Error	PXTH Data Parity Error	Not used	PXRB Data Parity Error	PXRC Data Parity Error	PXRH Data Parity Error
4E	MPA::M_ERRDTL04_CHK3:B1							
	Not used	KSEQ01_P Decode Error	KSEQ01_P Encode Error	KSEQ01_P Error	KSEQ01 Compare Error	KSEQ01_D Decode Error	KSEQ01_D Encode Error	KSEQ01_D Error
4F	MPA::M_ERRDTL04_CHK3:B0							
	Not used	KSEQ00_P Decode Error	KSEQ00_P Encode Error	KSEQ00_P Error	KSEQ00 Compare Error	KSEQ00_D Decode Error	KSEQ00_D Encode Error	KSEQ00_D Error

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Byte27 = 81 (CHK3) Byte40-7F Detail

(10) Byte28 = 81 or 82 or 83 or 84 or C5 or C6 or C7 or C8 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::M_ERRDTL07_CHK3_MPx:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	HSN Status Error
51	MPA::M_ERRDTL07_CHK3_MPx:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Path Timeout Error
52	MPA::M_ERRDTL_PCIE125:B1							
	Not used	Not used	Not used	Not used	Not used	PXRB Register Error	PXRB Error	PXRB Hardware Error
53	MPA::M_ERRDTL_PCIE125:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PXRB FATAL Error
54	MPA::M_ERRDTL_PCIE250:B7							
	SEQT0 Register Read Parity	PXTB Register Read Parity	PXTC Register Read Parity	PXTH Register Read Parity	SEQR0 Register Read Parity	PXRB Register Read Parity	PXRC Register Read Parity	PXRHL Register Read Parity
55	MPA::M_ERRDTL_PCIE250:B6							
	Not used	Not used	SEQT0C Error	SEQT0M Error	SEQT0S Error	PXTB Error	PXTC Error	PXTH Error
56	MPA::M_ERRDTL_PCIE250:B5							
	Not used	Not used	SEQR0C Error	SEQR0M Error	SEQR0S Error	PXRB Error	PXRC Error	PXRHL Error
57	MPA::M_ERRDTL_PCIE250:B4							
	Not used	Not used	SEQT0C Hardware Error	SEQT0M Hardware Error	SEQT0S Hardware Error	PXTB Hardware Error	PXTC Hardware Error	Not used
58	MPA::M_ERRDTL_PCIE250:B3							
	Not used	Not used	SEQR0C Hardware Error	SEQR0M Hardware Error	SEQR0S Hardware Error	PXRB Hardware Error	PXRC Hardware Error	Not used
59	MPA::M_ERRDTL_PCIE250:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	PXTC Micro Error	PXRC Path Error
5A	MPA::M_ERRDTL_PCIE250:B1							
	Not used	Not used	PXTHL TXVC Error B	PXTHL RXVC Error B	PXTHL DLP Error B	PXRHL TXVC Error B	PXRHL RXVC Error B	PXRHL DLP Error B
5B	MPA::M_ERRDTL_PCIE250:B0							
	Not used	Not used	PXTHL TXVC Error A	PXTHL RXVC Error A	PXTHL DLP Error A	PXRHL TXVC Error A	PXRHL RXVC Error A	PXRHL DLP Error A
5C	LR::INT STATUS:B3							
	LR INT Status Byte3							
5D	LR::INT STATUS:B2							
	LR INT Status Byte2							
5E	LR::INT STATUS:B1							
	LR INT Status Byte1							
5F	LR::INT STATUS:B0							
	LR INT Status Byte0							

SSBLOG05-720

Byte27 = 81 (CHK3) Byte40-7F Detail

(10) Byte28 = 81 or 82 or 83 or 84 or C5 or C6 or C7 or C8 (3/4)

	0	1	2	3	4	5	6	7
60	SW::ERRINT LOG:B1							
	Not used	Not used	Not used	P12 ERRINT_ LOG	P11 ERRINT_ LOG	P10 ERRINT_ LOG	P09 ERRINT_ LOG	P08 ERRINT_ LOG
61	SW::ERRINT LOG:B0							
	P07 ERRINT_ LOG	P06 ERRINT_ LOG	P05 ERRINT_ LOG	P04 ERRINT_ LOG	P03 ERRINT_ LOG	P02 ERRINT_ LOG	P01 ERRINT_ LOG	Not used
62	SW::RX LOG ERR:B1							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1 TOV	HCRC ERR
63	SW::RX LOG ERR:B0							
	PKTLEN ERR	ROUTING ERR	PCRC FLG ERR	PKTEND ERR	UNDERRUN ERR	HCRCFLG ERR	STP ERR	PCRC ERR
64	SW::BF0 LOG ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
65	SW::BF0 LOG ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
66								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
67								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
68	SW::BF0 LOG ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
69	SW::BF0 LOG ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
6A	SW::PCIE_ERR_STS:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
6B	SW::PCIE_ERR_STS:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL UP ERR
6C	SW::PCIE_MFTLP_CODE LOG							
	TLM MFTLP CODE(7)	TLM MFTLP CODE(6)	TLM MFTLP CODE(5)	TLM MFTLP CODE(4)	TLM MFTLP CODE(3)	TLM MFTLP CODE(2)	TLM MFTLP CODE(1)	TLM MFTLP CODE(0)
6D	SW::PCIE_PORT_STATUS:B1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
6E	SW::PCIE_PORT_STATUS:B0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP
6F	SW::TXVC_MFTLP_CODE LOG							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR

SSBLOG05-730

Byte27 = 81 (CHK3) Byte40-7F Detail

(10) Byte28 = 81 or 82 or 83 or 84 or C5 or C6 or C7 or C8 (4/4)

	0	1	2	3	4	5	6	7
70	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
71	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
72	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
73	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
74	Access Type [B3]							
75	Access Type [B2]							
76	Access Type [B1]							
77	Access Type [B0]							
78	Access Address [B7]							
79	Access Address [B6]							
7A	Access Address [B5]							
7B	Access Address [B4]							
7C	Access Address [B3]							
7D	Access Address [B2]							
7E	Access Address [B1]							
7F	Access Address [B0]							

SSBLOG05-740

Byte27 = 81 (CHK3) Byte40-7F Detail

(11) Byte28 = 89 or 8A or 8D or 8E or 8F or 90 or CB or CC or CD or CE or CF or D0 (1/4)

	0	1	2	3	4	5	6	7
40	SW①::ERRINT_LOG:B1							
	Not used	Not used	Not used	P12 ERRINT_ LOG	P11 ERRINT_ LOG	P10 ERRINT_ LOG	Not used	Not used
41	SW①::ERRINT_LOG:B0							
	P07 ERRINT_ LOG	P06 ERRINT_ LOG	P05 ERRINT_ LOG	P04 ERRINT_ LOG	P03 ERRINT_ LOG	P02 ERRINT_ LOG	P01 ERRINT_ LOG	Not used
42	SW①::RX_LOG_ERR:B1							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1 TOV	HCRC ERR
43	SW①::RX_LOG_ERR:B0							
	PKTLEN ERR	ROUTING ERR	PCRC FLG ERR	PKTEND ERR	UNDERRUN ERR	HCRCFLG ERR	STP ERR	PCRC ERR
44	SW①::BF0_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
45	SW①::BF0_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
46	SW①::BF0_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
47	SW①::BF0_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
48	SW①::BF0_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
49	SW①::BF0_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
4A								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
4B								
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
4C	SW①::BF1_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
4D	SW①::BF1_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
4E	SW①::BF1_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
4F	SW①::BF1_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(11) Byte28 = 89 or 8A or 8D or 8E or 8F or 90 or CB or CC or CD or CE or CF or D0 (2/4)

	0	1	2	3	4	5	6	7
50	SW①::BF1_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
51	SW①::BF1_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
52	SW①::PCIE_ERR_STS:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
53	SW①::PCIE_ERR_STS:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL UP ERR
54	SW①::PCIE_MFTLP_CODE_LOG							
	TLM MFTLP CODE(7)	TLM MFTLP CODE(6)	TLM MFTLP CODE(5)	TLM MFTLP CODE(4)	TLM MFTLP CODE(3)	TLM MFTLP CODE(2)	TLM MFTLP CODE(1)	TLM MFTLP CODE(0)
55	SW①::PCIE_PORT_STATUS:B1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
56	SW①::PCIE_PORT_STATUS:B0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP
57	SW①::TXVC_MFTLP_CODE_LOG							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR
58	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
59	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5A	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5B	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5C	SW①::ERRINT_LOG:B1							
	Not used	Not used	Not used	P12_ERRIN T_LOG	P11_ERRIN T_LOG	P10_ERRIN T_LOG	P09_ERRIN T_LOG	P08_ERRIN T_LOG
5D	SW①::ERRINT_LOG:B0							
	P07 ERRINT_ LOG	P06 ERRINT_ LOG	P05 ERRINT_ LOG	P04 ERRINT_ LOG	P03 ERRINT_ LOG	P02 ERRINT_ LOG	P01 ERRINT_ LOG	Not used
5E	SW①::RX_LOG_ERR:B1							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1 TOV	HCRC ERR
5F	SW①::RX_LOG_ERR:B0							
	PKTLEN ERR	ROUTING ERR	PCRC FLG ERR	PKTEND ERR	UNDERRUN ERR	HCRCFLG ERR	STP ERR	PCRC ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(11) Byte28 = 89 or 8A or 8D or 8E or 8F or 90 or CB or CC or CD or CE or CF or D0 (3/4)

	0	1	2	3	4	5	6	7
60	SW@::BF0_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
61	SW@::BF0_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
62	SW@::BF0_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
63	SW@::BF0_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
64	SW@::BF0_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
65	SW@::BF0_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
66	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
67	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
68	SW@::BF1_ERR:B3							
	Not used	Not used	Not used	BFCTL1 RDPT ERR	BFCTL1 RGFDC ERR	BFCTL1 PRM ERR	BFCTL1 SEQCNT ERR	BFCTL1 SEQTM ERR
69	SW@::BF1_ERR:B2							
	BFCTL1 SEQCMP ERR	BFCTL1 SEQS ERR	BFCTL1 SEQM ERR	Not used	Not used	Not used	Not used	BFCTL0 RDPT ERR
6A	SW@::BF1_ERR:B1							
	BFCTL0 RGFDC ERR	BFCTL0 PRM ERR	BFCTL0 SEQCNT ERR	BFCTL0 SEQTM ERR	BFCTL0 SEQCMP ERR	BFCTL0 SEQS ERR	BFCTL0 SEQM ERR	Not used
6B	SW@::BF1_ERR:B0							
	BF_RGF ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG ERR	BF_WTPT ERR
6C	SW@::BF1_LOG_ERR:B2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
6D	SW@::BF1_LOG_ERR:B0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
6E	SW@::PCIE_ERR_STS:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
6F	SW@::PCIE_ERR_STS:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL_UP ERR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(11) Byte28 = 89 or 8A or 8D or 8E or 8F or 90 or CB or CC or CD or CE or CF or D0 (4/4)

	0	1	2	3	4	5	6	7
70	SW@::PCIE_MFTLP_CODE_LOG							
	TLM MFTLP CODE(7)	TLM MFTLP CODE(6)	TLM MFTLP CODE(5)	TLM MFTLP CODE(4)	TLM MFTLP CODE(3)	TLM MFTLP CODE(2)	TLM MFTLP CODE(1)	TLM MFTLP CODE(0)
71	SW@::PCIE_PORT_STATUS:B1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
72	SW@::PCIE_PORT_STATUS:B0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP
73	SW@::TXVC_MFTLP_CODE_LOG							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(12) Byte28 = F0 (1/4)

	0	1	2	3	4	5	6	7
40	HSN Status:B7							
	HSN Status Byte7							
41	HSN Status:B6							
	HSN Status Byte6							
42	HSN Status:B5							
	HSN Status Byte5							
43	HSN Status:B4							
	HSN Status Byte4							
44	HSN Status:B3							
	HSN Status Byte3							
45	HSN Status:B2							
	HSN Status Byte2							
46	HSN Status:B1							
	HSN Status Byte1							
47	HSN Status:B0							
	HSN Status Byte0							
48	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte40-7F Detail

(12) Byte28 = F0 (2/4)

	0	1	2	3	4	5	6	7
50	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-800

Byte27 = 81 (CHK3) Byte40-7F Detail

(12) Byte28 = F0 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 81 (CHK3) Byte40-7F Detail

(12) Byte28 = F0 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Access Type [B3]							
	Access Type [B3]							
75	Access Type [B2]							
	Access Type [B2]							
76	Access Type [B1]							
	Access Type [B1]							
77	Access Type [B0]							
	Access Type [B0]							
78	Access Address [B7]							
	Access Address [B7]							
79	Access Address [B6]							
	Access Address [B6]							
7A	Access Address [B5]							
	Access Address [B5]							
7B	Access Address [B4]							
	Access Address [B4]							
7C	Access Address [B3]							
	Access Address [B3]							
7D	Access Address [B2]							
	Access Address [B2]							
7E	Access Address [B1]							
	Access Address [B1]							
7F	Access Address [B0]							
	Access Address [B0]							

SSBLOG05-820

Byte27 = 84 (CHK4) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'4')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number(*1)				Message code(x'0')			
35	SSID of self subsystem(low)							
36	Symptom code (x'FF89')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

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Byte27 = 84 (CHK4) (2/2)

	0	1	2	3	4	5	6	7
40	Hardware information (See following pages)							
7F								

Byte27 = 84 (CHK4) Byte28-31

- (1) Byte28 = 00/01/02/03/04/0e (CHA LR CHK4), Byte28 = 0x40/41/42/43/44/4e (DKA LR CHK4),
Byte28 = 80/81/82/83/84/8e (CHA MHUB CHK4)

	0	1	2	3	4	5	6	7
28	Subcode (x'00' or x'40' ox x'80') Subcode (x'01' or x'41' ox x'81') Subcode (x'02' or x'42' ox x'82') Subcode (x'03' or x'43' ox x'83') Subcode (x'04' or x'44' ox x'84') Subcode (x'0e' or x'4e' ox x'8e')			HSN CHK4 DXBF CHK4 PPCI Common CHK4 PPCI CHK4 REG CHK4 SMBUS CHK4				
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK4_INT1(b31 – 24)							
	(RSV)	(RSV)	XREG1 CHK4	XREG0 CHK4	(RSV)	(RSV)	SMBUS1 CHK4	SMBUS0 CHK4
2E	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
2F	CHK4_INT1(b15 – 08)							
	(RSV)	(RSV)	(RSV)	DA CHK4	(RSV)	(RSV)	PAT CHK4	PAR CHK4
30	CHK4_INT1(b07 – 00)							
	(RSV)	(RSV)	PAT1 CHK4	PAR1 CHK4	(RSV)	(RSV)	PAT0 CHK4	PAR0 CHK4
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

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Byte27 = 84 (CHK4) Byte28-31

- (2) Byte28 = 05/06/07/08/09/0f/11 (CHA LR CHK4),
 Byte28 = 0x45/46/47/48/49/4f/51 (DKA LR CHK4),
 Byte28 = 85/86/87/88/89/8f/91 (CHA MHUB CHK4)

	0	1	2	3	4	5	6	7
28	Subcode (x'05' or x'45' ox x'85') Subcode (x'06' or x'46' ox x'86') Subcode (x'07' or x'47' ox x'87') Subcode (x'08' or x'48' ox x'88') Subcode (x'09' or x'49' ox x'89') Subcode (x'0f' or x'4f' ox x'8f') Subcode (x'11' or x'51' ox x'91')				LRP CHK4 LRP_DMA CHK4 BRG CHK4 XD CHK4 PCDX CHK4 PPR CHK4 No HW Error			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK4_INT0(b31 – 24)							
	FOP1 CHK4	FOP0 CHK4	FOP1 STOP	FOP0 STOP	XD CHK4	INT CHK4	FDMA1 CHK4	FDMA0 CHK4
2E	CHK4_INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK4_INT0(b15 – 08)							
	(RSV)	PPR CHK4	BRG1 CHK4	BRG0 CHK4	(RSV)	(RSV)	DRR1 CHK4	DRR0 CHK4
30	CHK4_INT0(b07 – 00)							
	PCDX CHK4	PCDXCS CHK4	(RSV)	DMAA CHK4	DMA3 CHK4	DMA2 CHK4	DMA1 CHK4	DMA0 CHK4
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

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Byte27 = 84 (CHK4) Byte28-31

- (3) Byte28 = 0a (CHA LR CHK4), Byte28 = 0x4a(DKA LR CHK4),
Byte28 = 8a (CHA MHUB CHK4)

	0	1	2	3	4	5	6	7
28	Subcode (x'0a' or x'4a' or x'8a') PPCI_MSG CHK4							
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK4_INT3(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	FIVE1 MCK	FIVE1 MCK1	FIVE1 MCK0
2E	CHK4_INT3(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	FIVE0 MCK	FIVE0 MCK1	FIVE0 MCK0
2F	CHK4_INT3(b15 – 08)							
	MFCMN5 CHK4	MFCMN4 CHK4	MFCMN3 CHK4	MFCMN2 CHK4	MFCMN1 CHK4	MFCMN0 CHK4	MFPDMA1 CHK4	MFPDMA0 CHK4
30	CHK4_INT3(b07 – 00)							
	PPR MSG(7)	PPR MSG(6)	PPR MSG(5)	PPR MSG(4)	PPR MSG(3)	PPR MSG(2)	PPR MSG(1)	PPR MSG(0)
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

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Byte27 = 84 (CHK4) Byte28-31

(4) Byte28 = 90 (CHA MHUB CHK4)

	0	1	2	3	4	5	6	7
28	Subcode (x'90')			MF CHK4				
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK4_INT2(b31 – 24)							
	MFCCW CHK4 (15)	MFCCW CHK4 (14)	MFCCW CHK4 (13)	MFCCW CHK4 (12)	MFCCW CHK4 (11)	MFCCW CHK4 (10)	MFCCW CHK4 (9)	MFCCW CHK4 (8)
2E	CHK4_INT2(b23 – 16)							
	MFCCW CHK4 (7)	MFCCW CHK4 (6)	MFCCW CHK4 (5)	MFCCW CHK4 (4)	MFCCW CHK4 (3)	MFCCW CHK4 (2)	MFCCW CHK4 (1)	MFCCW CHK4 (0)
2F	CHK4_INT2(b15 – 08)							
	MFFLD CHK4 (15)	MFFLD CHK4 (14)	MFFLD CHK4 (13)	MFFLD CHK4 (12)	MFFLD CHK4 (11)	MFFLD CHK4 (10)	MFFLD CHK4 (9)	MFFLD CHK4 (8)
30	CHK4_INT2(b07 – 00)							
	MFFLD CHK4 (7)	MFFLD CHK4 (6)	MFFLD CHK4 (5)	MFFLD CHK4 (4)	MFFLD CHK4 (3)	MFFLD CHK4 (2)	MFFLD CHK4 (1)	MFFLD CHK4 (0)
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(1) Byte28 = 00/40/80 (1/4)

	0	1	2	3	4	5	6	7
40	LR: HA0_CORE_ERR(bit31 -24)							
	VC0TX BUFF UDF	VC0TX BUF OVF	VC0TX CTL PERR	VC0TX TYPE ERR	VC0TX ECC 2BERR	RETRY ECC 2BERR	Reserved	Reserved
41	LR: HA0_CORE_ERR(bit23 -16)							
	Reserved	VC0RX BUF OVF	VC0RX CTL PERR	VC0RX TYPE ERR	VC0RX ECC 2BERR	Reserved	Reserved	Reserved
42	LR: HA0_CORE_ERR(bit15 -08)							
	TRERR	DLPERR	RBOVR	MALF TLP	Reserved	Reserved	Reserved	LKTR STATE
43	LR: HA0_CORE_ERR(bit07 -00)							
	BADTLP	RNPROVR	ACKTO	PLPERR	BAD DLLP	NULL TLP	RX1 ERR (DIS=1)	RX0 ERR (DIS=1)
44	LR: HA0_TXERR_A1(bit31 -24)							
	VC0TX ERR3	VC0TX ERR2	VC0TX ERR1	VC0TX ERR0	CTL OPTY PERR	STSCNT1 PERR	ENC ERR	WDCNT PERR
45	LR: HA0_TXERR_A1 (bit23 -16)							
	TO ERR	TOCNT PERR	EFLG ERR	RBCNT PERR	RACNT PERR	ADRCMP PERR	PKT PNC ERR	STSCNT2 PERR
46	LR: HA0_TXERR_A1 (bit15 -08)							
	RAMLC ERR	SEQ0CNT PERR	TXDT PERR	SEQ1CNT PERR	Reserved	Reserved	Reserved	Reserved
47	LR: HA0_TXERR_A1 (bit07 -00)							
	TXBUFF 2BERR	TXBUFF ADR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: HA0_TXERR_B1(bit31 -24)							
	TIDPTY PERR	DT_PERR	MPNO CNV ERR	TXADR RANGE ERR	Reserved	Reserved	Reserved	Reserved
49	LR: HA0_TXERR_B1 (bit23 -16)							
	FUNC PERR	TAG PERR	ID PERR	LADR PERR	HC PERR	LEN PERR	Reserved	Reserved
4A	LR: HA0_TXERR_B1 (bit15 -08)							
	H4 PERR	H5 PERR	H6 PERR	H7 PERR	WACNT PERR	Reserved	Reserved	Reserved
4B	LR: HA0_TXERR_B1 (bit07 -00)							
	PORT SELECT ERR	PORT SELECT ERR2	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR: HA0_TXERR_B 2(bit31 -24)							
	PKT PNCERR	TICPTY PERR(14:08)						
4D	LR: HA0_TXERR_B 2 (bit23 -16)							
	TICPTY PERR(07:00)							
4E	LR: HA0_TXERR_B 2 (bit15 -08)							
	LST ERR	LASTPORT ERR	PACTON ERR	REQOFF ERR	ACTON ERR	ACTOFF ERR	DEC ERR	ENC ERR
4F	LR: HA0_TXERR_B 2 (bit07 -00)							
	TO ERR	TOCNT PERR	WDCNT PERR	CUTVCMP ERR	POLCNT PERR	BSYCNT PERR	WB0CNT PERR	WB1CNT PERR

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(1) Byte28 = 00/40/80 (2/4)

	0	1	2	3	4	5	6	7
50	LR: HA0_EXPRESS_PLL(bit31 -24)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
51	LR: HA0_EXPRESS_PLL (bit23 -16)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
52	LR: HA0_EXPRESS_PLL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: HA0_EXPRESS_PLL (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: HA0_RXERR_A1(bit31 -24)							
	HCTLOPTY PERR	H_DT PERR	H_UDOVER ERR	H_UDCNT PERR	H_WCNT PERR	H_RCNT PERR	Reserved	ENC ERR
55	LR: HA0_RXERR_A1 (bit23 -16)							
	TO ERR	TOCNT PERR	WDCNT PERR	CTLOPTY PERR	RX1 ERR (DIS=0)	RX0 ERR (DIS=0)	CUTV PERR	PKT TYPE ERR
56	LR: HA0_RXERR_A1 (bit15 -08)							
	WBCNT PERR	STSCNT3 PERR	STSCNT4 PERR	RXDIS DET	Reserved	Reserved	Reserved	Reserved
57	LR: HA0_RXERR_A1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: HA0_RXERR_A2(bit31 -24)							
	DATA PERR	HCRC ERR	PCRC (ECRC) ERR	WACNT PERR	Reserved	Reserved	ENC ERR	WDCNT PERR
59	LR: HA0_RXERR_A2 (bit23 -16)							
	TO ERR	TOCNT PERR	SEQ0NO ERR	DSTADR ERR	SEQ1NO ERR	Reserved	Reserved	Reserved
5A	LR: HA0_RXERR_A2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: HA0_RXERR_A2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR: HA0_RXERR_B1(bit31 -24)							
	DEC ERR	ENC ERR	TO ERR	TOCNT PERR	WDCNT PERR	RBCNT PERR P0	RACNT PERR	ADRCMP ERR
5D	LR: HA0_RXERR_B1 (bit23 -16)							
	EFLG ERR P0	PKT TYPE ERR	H0 EP PKT	RBCNT PERR P1	EFLG ERR P1	PSEL ERR	Reserved	RIDPTY PERR
5E	LR: HA0_RXERR_B1 (bit15 -08)							
	Reserved	RICPTY PERR(14:08)						
5F	LR: HA0_RXERR_B1 (bit07 -00)							
	RICPTY PERR(07:00)							

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(1) Byte28 = 00/40/80 (3/4)

	0	1	2	3	4	5	6	7
60	LR: HA0_RXERR_B2 (bit31 -24)							
	DATA PERR	RAMLRC ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR: HA0_RXERR_B2 (bit23 -16)							
	RXBUFF 2BERR0	RXBUFF ADR0	RXBUFF 2BERR1	RXBUFF ADR1	Reserved	Reserved	Reserved	Reserved
62	LR: HA0_RXERR_B2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: HA0_RXERR_B2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR: HA0_CORE_MON(bit31 -24)							
	RCV DETECT(07:00)							
65	LR: HA0_CORE_MON (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	LINK WIDTH(03:00)			
66	LR: HA0_CORE_MON (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR: HA0_CORE_MON (bit07 -00)							
	LINKDN	VCNEG0 PEND	LKTR STATE	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR: HA0_FLOW_CTL_MON1(bit31 -24)							
	FC RAVA0PH(07:00)							
69	LR: HA0_FLOW_CTL_MON1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RP0 INF H	RP0 INF D
6A	LR: HA0_FLOW_CTL_MON1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	FC RAVA0PD(11:08)			
6B	LR: HA0_FLOW_CTL_MON1 (bit07 -00)							
	FC RAVA0PD(07:00)							
6C	LR: HA0_LINK_CORE_MON(bit31 -24)							
	TXTLP PEND	Reserved	REPLAY NUM (1)	REPLAY NUM (0)	Reserved	Reserved	DRBUFF AVA(09:08)	
6D	LR: HA0_LINK_CORE_MON (bit23 -16)							
	DRBUFF AVA(07:00)							
6E	LR: HA0_LINK_CORE_MON (bit15 -08)							
	TAGBUF AVA(07:00)							
6F	LR: HA0_LINK_CORE_MON (bit07 -00)							
	Reserved	Reserved	LTSSM STATE(05:00)					

SSBLOG05-900

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(1) Byte28 = 00/40/80 (4/4)

	0	1	2	3	4	5	6	7
70	LR: HA0_SEQMON_A1(bit31 -24)							
	TCONT SEQST(07:00)							
71	LR: HA0_SEQMON_A1 (bit23 -16)							
	RCONT SEQST(07:00)							
72	LR: HA0_SEQMON_A1 (bit15 -08)							
	RADB SEQST(07:00)							
73	LR: HA0_SEQMON_A1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: HA0_SEQMON_B1 (bit31 -24)							
	TARB SEQST(07:00)							
75	LR: HA0_SEQMON_B1 (bit23 -16)							
	RUIF SEQST(07:00)							
76	LR: HA0_SEQMON_B1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: HA0_SEQMON_B1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: HA0_CORE_MODE_A1(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VC0_TX ORD_CP	VC0_TX ORD_CN
79	LR: HA0_CORE_MODE_A1 (bit23 - 16)							
	SCRAM EN_B	Reserved	Reserved	Reserved	TE RXSTF MODE	Reserved	Reserved	Reserved
7A	LR: HA0_CORE_MODE_A1 (bit15 - 08)							
	Reserved	FC_VC0 TCM(06:00)						
7B	LR: HA0_CORE_MODE_A1 (bit07 - 00)							
	Reserved	MAX PSIZE(02:00)			Reserved	Reserved	Reserved	EXT SYNC
7C	LR: BRG0_PBUF0_COUNTER1(bit31 - 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR: BRG0_PBUF0_COUNTER1 (bit23 - 16)							
	XFRCNT(07:00)							
7E	LR: BRG0_PBUF0_COUNTER1 (bit15 - 08)							
	CTL DIFFCNT(07:00)							
7F	LR: BRG0_PBUF0_COUNTER1 (bit07 - 00)							
	BUF DIFFCNT(07:00)							

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(2) Byte28 = 01/41/81 (1/4)

	0	1	2	3	4	5	6	7
40	LR: DX_CTL_ERR1(bit31 - 24)							
	ADR PERR	LEN PERR	LENOVR ERR	LNE0 ERR	WLAST ERR	DDR2 OVRE	Reserved	Reserved
41	LR: DX_CTL_ERR1(bit23 - 16)							
	WLEN PERR	RDTIM PERR	RDLEN PERR	LST ADR PERR	Reserved	Reserved	Reserved	Reserved
42	LR: DX_CTL_ERR1(bit15 - 08)							
	MEMINIT ADR PERR	MEMINIT DEC ERR	MEMINIT ENC ERR	Reserved	MEMINIT CMP ERR	CMP DT PERR	RDT PERR	CUDG DT PERR
43	LR: DX_CTL_ERR1(bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	WDT PERR
44	LR: DX_CTL_ERR2(bit31 - 24)							
	ADR PERR	ADR OUT PERR	LDADR PERR	TIMER PERR	CNT PERR	LDCNT PERR	LDCNT0 PERR	CNT ERR
45	LR: DX_CTL_ERR2(bit23 - 16)							
	TRFC DCNT PERR	SEQ ENC PERR	DLL LOCK DOWN	LST ADR PERR	DDR2 ADR CMPER	DDR2 CMD CMPER	DDR2 CTL CMPER	Reserved
46	LR: DX_CTL_ERR2(bit15 - 08)							
	COR CYCL PERR	COR ADR PERR	Reserved	Reserved	RMW ECC ERR3	RMW ECC ERR2	RMW ECC ERR1	RMW ECC ERR0
47	LR: DX_CTL_ERR2(bit07 - 00)							
	ECC UNC ERR3	ECC UNC ERR2	ECC UNC ERR1	ECC UNC ERR0	ECC ADR ERR3	ECC ADR ERR2	ECC ADR ERR1	ECC ADR ERR0
48	LR: DX_CTL_ERR3(bit31 - 24)							
	CUDG CNT ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR: DX_CTL_ERR3(bit23 - 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR: DX_CTL_ERR3(bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR: DX_CTL_ERR3(bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR: DA_ERR_A1(bit31 - 24)							
	WDCNT PERR	RACNT PERR	ADRCMP ERR	RBCNT PERR	ACT ERR	EFLG ERR	Reserved	ENCERR
4D	LR: DA_ERR_A1 (bit23 - 16)							
	TO0 ERR	TOCNT0 PERR	TO1 ERR	TOCNT1 PERR	Reserved	Reserved	Reserved	Reserved
4E	LR: DA_ERR_A1 (bit15 - 08)							
	RAMLRCA ERR	RAMLRCA ERR	DT PERR	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: DA_ERR_A1 (bit07 - 00)							
	WBUFFA 2BERR	WBUFFA ADR	WBUFFB 2BERR	WBUFFB ADR	Reserved	Reserved	Reserved	Reserved

SSBLOG05-920

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(2) Byte28 = 01/41/81 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DA_ERR_A2(bit31 - 24)							
	RACNT PERR	RBCNT PERR	Reserved	ENCERR	TO ERR	TOCNT PERR	LST ERR	WDCNT PERR
51	LR: DA_ERR_A2 (bit23 - 16)							
	CUTV PERR	DT PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR: DA_ERR_A2 (bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: DA_ERR_A2 (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DA_ERR_B1(bit31 - 24)							
	P0D0ES CMPERR	P0D0SE CMPERR	P1D0ES CMPERR	P1D0SE CMPERR	P2D0ES CMPERR	P2D0SE CMPERR	P3D0ES CMPERR	P3D0SE CMPERR
55	LR: DA_ERR_B1 (bit23 - 16)							
	P0D1ES CMPERR	P0D1SE CMPERR	P1D1ES CMPERR	P1D1SE CMPERR	P2D1ES CMPERR	P2D1SE CMPERR	P3D1ES CMPERR	P3D1SE CMPERR
56	LR: DA_ERR_B1 (bit15 - 08)							
	P0D2ES CMPERR	P0D2SE CMPERR	P1D2ES CMPERR	P1D2SE CMPERR	P2D2ES CMPERR	P2D2SE CMPERR	P3D2ES CMPERR	P3D2SE CMPERR
57	LR: DA_ERR_B1 (bit07 - 00)							
	P0D3ES CMPERR	P0D3SE CMPERR	P1D3ES CMPERR	P1D3SE CMPERR	P2D3ES CMPERR	P2D3SE CMPERR	P3D3ES CMPERR	P3D3SE CMPERR
58	LR: DA_ERR_B2(bit31 - 24)							
	WIPTY PERR	WADR PERR	WLEN PERR	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR: DA_ERR_B2 (bit23 - 16)							
	WDATA PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR: DA_ERR_B2 (bit15 - 08)							
	WACNT PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: DA_ERR_B2 (bit07 - 00)							
	WDBCNT PERR	WBCNT PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR: DA_ERR_B3(bit31 - 24)							
	Reserved	CICPTY PERR(14:08)						
5D	LR: DA_ERR_B3 (bit23 - 16)							
	CICPTY PERR(07:00)							
5E	LR: DA_ERR_B3 (bit15 - 08)							
	LST ERR	LASTPORT ERR	PACTON ERR	REQOFF ERR	ACTON ERR	ACTOFF ERR	DEC ERR	ENC ERR
5F	LR: DA_ERR_B3 (bit07 - 00)							
	TO ERR	TOCNT PERR	WDCNT PERR	CUTV PERR	WDB NUM ERR	WDB CNT PERR	ACPON ERR	Reserved

SSBLOG05-930

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(2) Byte28 = 01/41/81 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DA_ERR_B4(bit31 - 24)							
	CICPTY PERR	DEC ERR	ENC ERR	LST ERR	REQOFF ERR	ACPN ERR	ACTON ERR	ACTOFF ERR
61	LR: DA_ERR_B4 (bit23 - 16)							
	TO ERR	TOCNT PERR	WADR PERR	WLEN PERR	WDATA PERR	WIPTY PERR	Reserved	Reserved
62	LR: DA_ERR_B4 (bit15 - 08)							
	CICPTY PERR	DEC ERR	ENC ERR	LST ERR	REQOFF ERR	ACPN ERR	ACTON ERR	ACTOFF ERR
63	LR: DA_ERR_B4 (bit07 - 00)							
	TO ERR	TOCNT PERR	WADR PERR	WLEN PERR	WDATA PERR	WIPTY PERR	Reserved	Reserved
64	LR: DA_ERR_B5(bit31 - 24)							
	DEC ERR	ENC ERR	RACNT PERR	ADRCMP ERR	TO ERR	TOCNT PERR	WDCNT PERR	RBCNT PERR
65	LR: DA_ERR_B5 (bit23 - 16)							
	EFLG ERR	RAMLRCA ERR	RAMLRCA ERR	DT PERR	PORTNUM ERR	Reserved	Reserved	Reserved
66	LR: DA_ERR_B5 (bit15 - 08)							
	RBUFA 2BERR	RBUFA ADR	RBUFB 2BERR	RBUFB ADR	Reserved	Reserved	Reserved	Reserved
67	LR: DA_ERR_B5 (bit07 - 00)							
	DEC ERR	ENC ERR	DAT PERR	Reserved	DEC ERR	ENC ERR	DAT PERR	Reserved
68	LR: DX_CTL_MON1 (bit31 - 24)							
	UIOLHNCM(03:00)				UIOLHPCM(03:00)			
69	LR: DX_CTL_MON1 (bit23 - 16)							
	UIOLHFIX	Reserved	C0DLLOCK MONI	C1DLLOCK MONI	Reserved	COR RMW FULL	Reserved	MEMINIT MONI
6A	LR: DX_CTL_MON1 (bit15 - 08)							
	MEMINIT CST(07:00)							
6B	LR: DX_CTL_MON1 (bit07 - 00)							
	DXSEQ CST(07:00)							
6C	LR: DX_CTL_MON2 (bit31 - 24)							
	DXSEQ CST(07:00) (4th)							
6D	LR: DX_CTL_MON2 (bit23 - 16)							
	DXSEQ CST(07:00) (3rd)							
6E	LR: DX_CTL_MON2 (bit15 - 08)							
	DXSEQ CST(07:00) (2nd)							
6F	LR: DX_CTL_MON2 (bit07 - 00)							
	DXSEQ CST(07:00) (1st)							

SSBLOG05-940

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(2) Byte28 = 01/41/81 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DA_SEQMON_A1(bit31 - 24)							
	CARB SEQST(07:00)							
71	LR: DA_SEQMON_A1 (bit23 - 16)							
	RCONT SEQST(07:00)							
72	LR: DA_SEQMON_A1 (bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: DA_SEQMON_A1 (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: DA_SEQMON_B1(bit31 - 24)							
	FARB SEQST(07:00)							
75	LR: DA_SEQMON_B1 (bit23 - 16)							
	WFOP0 SEQST(07:00)							
76	LR: DA_SEQMON_B1 (bit15 - 08)							
	WFOP1 SEQST(07:00)							
77	LR: DA_SEQMON_B1 (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: DA_SEQMON_B2(bit31 - 24)							
	RUIF SEQST(07:00)							
79	LR: DA_SEQMON_B2 (bit23 - 16)							
	RFOP0 SEQST(07:00)							
7A	LR: DA_SEQMON_B2 (bit15 - 08)							
	RFOP1 SEQST(07:00)							
7B	LR: DA_SEQMON_B2 (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-950

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(3) Byte28 = 02/42/82 (1/4)

	0	1	2	3	4	5	6	7
40	LR: PAC_TXERR_B1(bit31 - 24)							
	TIDPTY PERR	DT_PERR	MPNO CNV ERR	TXADR RANGE ERR	Reserved	Reserved	Reserved	Reserved
41	LR: PAC_TXERR_B1(bit23 – 16)							
	FUNC PERR	TAG PERR	ID PERR	LADR PERR	HC PERR	LEN PERR	Reserved	Reserved
42	LR: PAC_TXERR_B1(bit15 - 08)							
	H4 PERR	H5 PERR	H6 PERR	H7 PERR	WACNT PERR	Reserved	Reserved	Reserved
43	LR: PAC_TXERR_B1(bit07 - 00)							
	PORT SELECT ERR	PORT SELECT ERR2	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR: PAC_TXERR_B2(bit31 - 24)							
	PKT PNCERR	TICPTY PERR(14:08)						
45	LR: PAC_TXERR_B2(bit23 – 16)							
	TICPTY PERR(07:00)							
46	LR: PAC_TXERR_B2(bit15 - 08)							
	LST ERR	LASTPORT ERR	PACTON ERR	REQOFF ERR	ACTON ERR	ACTOFF ERR	DEC ERR	ENC ERR
47	LR: PAC_TXERR_B2(bit07 - 00)							
	TO ERR	TOCNT PERR	WDCNT PERR	CUTVCMP ERR	POLCNT PERR	BSYCNT PERR	WB0CNT PERR	WB1CNT PERR
48	LR: PAC_RXERR_B1(bit31 - 24)							
	DEC ERR	ENC ERR	TO ERR	TOCNT PERR	WDCNT PERR	RBCNT PERR P0	RACNT PERR	ADRCMP ERR
49	LR: PAC_RXERR_B1(bit23 – 16)							
	EFLG ERR P0	PKT TYPE ERR	H0 EP PKT	RBCNT PERR P1	EFLG ERR P1	PSEL ERR	Reserved	RIDPTY PERR
4A	LR: PAC_RXERR_B1(bit15 - 08)							
	Reserved	RICPTY PERR(14:08)						
4B	LR: PAC_RXERR_B1(bit07 - 00)							
	RICPTY PERR(07:00)							
4C	LR: PAC_RXERR_B2(bit31 - 24)							
	DATA PERR	RAMLRC ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR: PAC_RXERR_B2(bit23 – 16)							
	RXBUFF 2BERR0	RXBUFF ADR0	RXBUFF 2BERR1	RXBUFF ADR1	Reserved	Reserved	Reserved	Reserved
4E	LR: PAC_RXERR_B2(bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: PAC_RXERR_B2(bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-960

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(3) Byte28 = 02/42/82 (2/4)

	0	1	2	3	4	5	6	7
50	LR: PAC_EXPRESS_PLL(bit31 - 24)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
51	LR: PAC_EXPRESS_PLL (bit23 - 16)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
52	LR: PAC_EXPRESS_PLL (bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: PAC_EXPRESS_PLL (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: PAC_SEQMON_B1(bit31 - 24)							
	TARB SEQST(07:00)							
55	LR: PAC_SEQMON_B1 (bit23 - 16)							
	RUIF SEQST(07:00)							
56	LR: PAC_SEQMON_B1 (bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR: PAC_SEQMON_B1 (bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-970

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(3) Byte28 = 02/42/82 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(3) Byte28 = 02/42/82 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(4) Byte28 = 03/43/83 (1/4)

	0	1	2	3	4	5	6	7
40	LR: PA0_CORE_ERR(bit31-24)							
	VC0TX BUFF UDF	VC0TX BUF OVF	VC0TX CTL PERR	VC0TX TYPE ERR	VC0TX ECC 2BERR	RETRY ECC 2BERR	Reserved	Reserved
41	LR: PA0_CORE_ERR(bit23-16)							
	Reserved	VC0RX BUF OVF	VC0RX CTL PERR	VC0RX TYPE ERR	VC0RX ECC 2BERR	Reserved	Reserved	Reserved
42	LR: PA0_CORE_ERR(bit15-08)							
	TRERR	DLERR	RBOVR	MALF TLP	Reserved	Reserved	Reserved	LKTR STATE
43	LR: PA0_CORE_ERR(bit07-00)							
	BADTLP	RNPROVR	ACKTO	PLPERR	BAD DLLP	NULL TLP	RX1 ERR (DIS=1)	RX0 ERR (DIS=1)
44	LR: PA0_TXERR_A1(bit31-24)							
	VC0TX ERR3	VC0TX ERR2	VC0TX ERR1	VC0TX ERR0	CTL OPTY PERR	STSCNT1 PERR	ENC ERR	WDCNT PERR
45	LR: PA0_TXERR_A1(bit23-16)							
	TO ERR	TOCNT PERR	EFLG ERR	RBCNT PERR	RACNT PERR	ADRCMP PERR	PKT PNC ERR	STSCNT2 PERR
46	LR: PA0_TXERR_A1(bit15-08)							
	RAMLC ERR	SEQ0CNT PERR	TXDT PERR	SEQ1CNT PERR	Reserved	Reserved	Reserved	Reserved
47	LR: PA0_TXERR_A1(bit07-00)							
	TXBUFF 2BERR	TXBUFF ADR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: PA0_RXERR_A1(bit31-24)							
	HCTLOPTY PERR	H_DT PERR	H_UDOVER ERR	H_UDCNT PERR	H_WCNT PERR	H_RCNT PERR	Reserved	ENC ERR
49	LR: PA0_RXERR_A1(bit23-16)							
	TO ERR	TOCNT PERR	WDCNT PERR	CTLOPTY PERR	RX1 ERR (DIS=0)	RX0 ERR (DIS=0)	CUTV PERR	PKT TYPE ERR
4A	LR: PA0_RXERR_A1(bit15-08)							
	WBCNT PERR	STSCNT3 PERR	STSCNT4 PERR	RXDIS DET	Reserved	Reserved	Reserved	Reserved
4B	LR: PA0_RXERR_A1(bit07-00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR: PA0_RXERR_A2(bit31-24)							
	DATA PERR	HCRC ERR	PCRC (ECRC) ERR	WACNT PERR	Reserved	Reserved	ENC ERR	WDCNT PERR
4D	LR: PA0_RXERR_A2(bit23-16)							
	TO ERR	TOCNT PERR	SEQ0NO ERR	DSTADR ERR	SEQ1NO ERR	Reserved	Reserved	Reserved
4E	LR: PA0_RXERR_A2(bit15-08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: PA0_RXERR_A2(bit07-00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1000

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(4) Byte28 = 03/43/83 (2/4)

	0	1	2	3	4	5	6	7
50	LR: PAC_EXPRESS_PLL(bit31 -24)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
51	LR: PAC_EXPRESS_PLL (bit23 -16)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
52	LR: PAC_EXPRESS_PLL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: PAC_EXPRESS_PLL (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: PA0_CORE_MON(bit31 -24)							
	RCV DETECT(07:00)							
55	LR: PA0_CORE_MON (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	LINK WIDTH(03:00)			
56	LR: PA0_CORE_MON (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR: PA0_CORE_MON (bit07 -00)							
	LINKDN	VCNEG0 PEND	LKTR STATE	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: PA0_LINK_CORE_MON(bit31 -24)							
	TXTLP PEND	Reserved	REPLAY NUM (1)	REPLAY NUM (0)	Reserved	Reserved	DRBUFF AVA(09:08)	
59	LR: PA0_LINK_CORE_MON (bit23 -16)							
	DRBUFF AVA(07:00)							
5A	LR: PA0_LINK_CORE_MON (bit15 -08)							
	TAGBUF AVA(07:00)							
5B	LR: PA0_LINK_CORE_MON (bit07 -00)							
	Reserved	Reserved	LTSSM STATE(05:00)					
5C	LR: PA0_SEQMON_A1(bit31 -24)							
	TCONT SEQST(07:00)							
5D	LR: PA0_SEQMON_A1 (bit23 -16)							
	RCONT SEQST(07:00)							
5E	LR: PA0_SEQMON_A1 (bit15 -08)							
	RADB SEQST(07:00)							
5F	LR: PA0_SEQMON_A1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1010

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(4) Byte28 = 03/43/83 (3/4)

	0	1	2	3	4	5	6	7
60	LR: PA1_CORE_ERR(bit31-24)							
	VC0TX BUFF UDF	VC0TX BUF OVF	VC0TX CTL PERR	VC0TX TYPE ERR	VC0TX ECC 2BERR	RETRY ECC 2BERR	Reserved	Reserved
61	LR: PA1_CORE_ERR(bit23-16)							
	Reserved	VC0RX BUF OVF	VC0RX CTL PERR	VC0RX TYPE ERR	VC0RX ECC 2BERR	Reserved	Reserved	Reserved
62	LR: PA1_CORE_ERR(bit15-08)							
	TRERR	DLERR	RBOVR	MALF TLP	Reserved	Reserved	Reserved	LKTR STATE
63	LR: PA1_CORE_ERR(bit07-00)							
	BADTLP	RNPROVR	ACKTO	PLPERR	BAD DLLP	NULL TLP	RX1 ERR (DIS=1)	RX0 ERR (DIS=1)
64	LR: PA1_TXERR_A1(bit31-24)							
	VC0TX ERR3	VC0TX ERR2	VC0TX ERR1	VC0TX ERR0	CTL OPTY PERR	STSCNT1 PERR	ENC ERR	WDCNT PERR
65	LR: PA1_TXERR_A1(bit23-16)							
	TO ERR	TOCNT PERR	EFLG ERR	RBCNT PERR	RACNT PERR	ADRCMP PERR	PKT PNC ERR	STSCNT2 PERR
66	LR: PA1_TXERR_A1(bit15-08)							
	RAMLCR ERR	SEQ0CNT PERR	TXDT PERR	SEQ1CNT PERR	Reserved	Reserved	Reserved	Reserved
67	LR: PA1_TXERR_A1(bit07-00)							
	TXBUFF 2BERR	TXBUFF ADR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR: PA1_RXERR_A1(bit31-24)							
	HCTLOPTY PERR	H_DT PERR	H_UDOVER ERR	H_UDCNT PERR	H_WCNT PERR	H_RCNT PERR	Reserved	ENC ERR
69	LR: PA1_RXERR_A1(bit23-16)							
	TO ERR	TOCNT PERR	WDCNT PERR	CTLOPTY PERR	RX1 ERR (DIS=0)	RX0 ERR (DIS=0)	CUTV PERR	PKT TYPE ERR
6A	LR: PA1_RXERR_A1(bit15-08)							
	WBCNT PERR	STSCNT3 PERR	STSCNT4 PERR	RXDIS DET	Reserved	Reserved	Reserved	Reserved
6B	LR: PA1_RXERR_A1(bit07-00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	LR: PA1_RXERR_A2(bit31-24)							
	DATA PERR	HCRC ERR	PCRC (ECRC) ERR	WACNT PERR	Reserved	Reserved	ENC ERR	WDCNT PERR
6D	LR: PA1_RXERR_A2(bit23-16)							
	TO ERR	TOCNT PERR	SEQ0NO ERR	DSTADR ERR	SEQ1NO ERR	Reserved	Reserved	Reserved
6E	LR: PA1_RXERR_A2(bit15-08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR: PA1_RXERR_A2(bit07-00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1020

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(4) Byte28 = 03/43/83 (4/4)

	0	1	2	3	4	5	6	7
70	LR: PAC_EXPRESS_PLL(bit31 -24)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
71	LR: PAC_EXPRESS_PLL (bit23 -16)							
	PLL LOCK	PLL LOCK	LINK DOWN	LINK DOWN (x8)	Reserved	Reserved	Reserved	Reserved
72	LR: PAC_EXPRESS_PLL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: PAC_EXPRESS_PLL (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: PA1_CORE_MON(bit31 -24)							
	RCV DETECT(07:00)							
75	LR: PA1_CORE_MON (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	LINK WIDTH(03:00)			
76	LR: PA1_CORE_MON (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: PA1_CORE_MON (bit07 -00)							
	LINKDN	VCNEG0 PEND	LKTR STATE	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: PA1_LINK_CORE_MON(bit31 -24)							
	TXTLP PEND	Reserved	REPLAY NUM (1)	REPLAY NUM (0)	Reserved	Reserved	DRBUFF AVA(09:08)	
79	LR: PA1_LINK_CORE_MON (bit23 -16)							
	DRBUFF AVA(07:00)							
7A	LR: PA1_LINK_CORE_MON (bit15 -08)							
	TAGBUF AVA(07:00)							
7B	LR: PA1_LINK_CORE_MON (bit07 -00)							
	Reserved	Reserved	LTSSM STATE(05:00)					
7C	LR: PA1_SEQMON_A1(bit31 -24)							
	TCONT SEQST(07:00)							
7D	LR: PA1_SEQMON_A1 (bit23 -16)							
	RCONT SEQST(07:00)							
7E	LR: PA1_SEQMON_A1 (bit15 -08)							
	RADB SEQST(07:00)							
7F	LR: PA1_SEQMON_A1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1030

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(5) Byte28 = 04/44/84 (1/4)

	0	1	2	3	4	5	6	7
40	LR: HARX_STATUS (bit31 -24)							
	RXDT PCHK	HCRC	PCRC	Reserved	CMD CHK	LADR PCHK	ID PCHK	TAG PCHK
41	LR: HARX_STATUS (bit23 -16)							
	HSN CPTY CHK	HSN DPTY CHK	FUNC PCHK	LEN PCHK	ADR PCHK	CMD PCHK	LEN CHK	HSN TERR PCHK
42	LR: HARX_STATUS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR: HARX_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RXBF OVFLOW	XBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
44	LR: RAIF_STATUS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: RAIF_STATUS (bit23 -16)							
	RGARB ADRBE PCHK	RGARB WDT PCHK	RGARB RDT PCHK	RGARB CMP	Reserved	RGARB ARB	RGARB XR RSV	RGARB BEWR VALID
46	LR: RAIF_STATUS (bit15 -08)							
	Reserved	Reserved	RAERR PCHK	TXBF OVFLOW	FUNC PCHK	WDT PCHK	WEBE PCHK	ADR PCHK
47	LR: RAIF_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RGARB SEQ	TXBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
48	LR: HATX_STATUS (bit31 -24)							
	Reserved	Reserved	Reserved	RGARB XR	RGARB DATA	RGARB CTL	RAIF DATA	RAIF TIMEOUT
49	LR: HATX_STATUS (bit23 -16)							
	Reserved	BSY ACT PCHK	TAG PCHK	REQID PCHK	LADR PCHK	RAERR PCHK	RDT PCHK	HSNERR PCHK
4A	LR: HATX_STATUS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR: HATX_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	Reserved	XBF PCHK	TXBF PCHK	TIMEOUT	TIMER PCHK
4C	LR: HARX_CHK_LOG_FST (bit31 -24)							
	RXDT PCHK	HCRC	PCRC	Reserved	CMD CHK	LADR PCHK	ID PCHK	TAG PCHK
4D	LR: HARX_CHK_LOG_FST (bit23 -16)							
	HSN CPTY CHK	HSN DPTY CHK	FUNC PCHK	LEN PCHK	ADR PCHK	CMD PCHK	LEN CHK	HSN TERR PCHK
4E	LR: HARX_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: HARX_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RXBF OVFLOW	XBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK

SSBLOG05-1040

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(5) Byte28 = 04/44/84 (2/4)

	0	1	2	3	4	5	6	7
50	LR: RAIF_CHK_LOG_FST (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR: RAIF_CHK_LOG_FST (bit23 -16)							
	RGARB ADRBE PCHK	RGARB WDT PCHK	RGARB RDT PCHK	RGARB CMP	Reserved	RGARB ARB	RGARB XR RSV	RGARB BEWR VALID
52	LR: RAIF_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	RAERR PCHK	TXBF OVFLOW	FUNC PCHK	WDT PCHK	WEBE PCHK	ADR PCHK
53	LR: RAIF_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RGARB SEQ	TXBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
54	LR: HATX_CHK_LOG_FST (bit31 -24)							
	Reserved	Reserved	Reserved	RGARB XR	RGARB DATA	RGARB CTL	RAIF DATA	RAIF TIMEOUT
55	LR: HATX_CHK_LOG_FST (bit23 -16)							
	Reserved	BSY ACT PCHK	TAG PCHK	REQID PCHK	LADR PCHK	RAERR PCHK	RDT PCHK	HSNERR PCHK
56	LR: HATX_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR: HATX_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	Reserved	XBF PCHK	TXBF PCHK	TIMEOUT	TIMER PCHK
58	LR: ACSADR_MONI_CRT (bit31 -24)							
	ACS REG ADR(31:24)							
59	LR: ACSADR_MONI_CRT (bit23 -16)							
	ACS REG ADR(23:16)							
5A	LR: ACSADR_MONI_CRT (bit15 -08)							
	ACS REG ADR(15:08)							
5B	LR: ACSADR_MONI_CRT (bit07 -00)							
	ACS REG ADR(07:00)							
5C	LR: CHK_LOG (bit31 -24)							
	ALL_MST REQOFF	ALL_RXDT PER ERR	ALL_RXDT PER FUNC	ALL_RXDT PER WRBE	ALL_RXDT PER ADR	ALL_RXDT PER WDT	ALL_RXDT PER RDT	ALL_RXDT CMPCHK
5D	LR: CHK_LOG (bit23 -16)							
	ALL_VALID CHK	ALL_XR RSV	ALL_XR WRBE	ALL_PENC GNTZERO	ALL_PENC GNTX	ALL_SEQ PTY	ALL_SEQ ENC	ALL_SEQ DEC
5E	LR: CHK_LOG (bit15 -08)							
	FST_MST REQOFF	FST_RXDT PER ERR	FST_RXDT PER FUNC	FST_RXDT PER WRBE	FST_RXDT PER ADR	FST_RXDT PER WDT	FST_RXDT PER RDT	FST_RXDT CMPCHK
5F	LR: CHK_LOG (bit07 -00)							
	FST_VALID CHK	FST_XR RSV	FST_XR WRBE	FST_PENC GNTZERO	FST_PENC GNTX	FST_SEQ PTY	FST_SEQ ENC	FST_SEQ DEC

SSBLOG05-1050

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(5) Byte28 = 04/44/84 (3/4)

	0	1	2	3	4	5	6	7
60	LR: HARX_STATUS (bit31 -24)							
	RXDT PCHK	HCRC	PCRC	Reserved	CMD CHK	LADR PCHK	ID PCHK	TAG PCHK
61	LR: HARX_STATUS (bit23 -16)							
	HSN CPTY CHK	HSN DPTY CHK	FUNC PCHK	LEN PCHK	ADR PCHK	CMD PCHK	LEN CHK	HSN TERR PCHK
62	LR: HARX_STATUS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: HARX_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RXBF OVFLOW	XBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
64	LR: RAIF_STATUS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR: RAIF_STATUS (bit23 -16)							
	RGARB ADRBE PCHK	RGARB WDT PCHK	RGARB RDT PCHK	RGARB CMP	Reserved	RGARB ARB	RGARB XR RSV	RGARB BEWR VALID
66	LR: RAIF_STATUS (bit15 -08)							
	Reserved	Reserved	RAERR PCHK	TXBF OVFLOW	FUNC PCHK	WDT PCHK	WEBE PCHK	ADR PCHK
67	LR: RAIF_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RGARB SEQ	TXBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
68	LR: HATX_STATUS (bit31 -24)							
	Reserved	Reserved	Reserved	RGARB XR	RGARB DATA	RGARB CTL	RAIF DATA	RAIF TIMEOUT
69	LR: HATX_STATUS (bit23 -16)							
	Reserved	BSY ACT PCHK	TAG PCHK	REQID PCHK	LADR PCHK	RAERR PCHK	RDT PCHK	HSNERR PCHK
6A	LR: HATX_STATUS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR: HATX_STATUS (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	Reserved	XBF PCHK	TXBF PCHK	TIMEOUT	TIMER PCHK
6C	LR: HARX_CHK_LOG_FST (bit31 -24)							
	RXDT PCHK	HCRC	PCRC	Reserved	CMD CHK	LADR PCHK	ID PCHK	TAG PCHK
6D	LR: HARX_CHK_LOG_FST (bit23 -16)							
	HSN CPTY CHK	HSN DPTY CHK	FUNC PCHK	LEN PCHK	ADR PCHK	CMD PCHK	LEN CHK	HSN TERR PCHK
6E	LR: HARX_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR: HARX_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RXBF OVFLOW	XBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK

SSBLOG05-1060

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(5) Byte28 = 04/44/84 (4/4)

	0	1	2	3	4	5	6	7
70	LR: RAIF_CHK_LOG_FST (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR: RAIF_CHK_LOG_FST (bit23 -16)							
	RGARB ADRBE PCHK	RGARB WDT PCHK	RGARB RDT PCHK	RGARB CMP	Reserved	RGARB ARB	RGARB XR RSV	RGARB BEWR VALID
72	LR: RAIF_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	RAERR PCHK	TXBF OVFLOW	FUNC PCHK	WDT PCHK	WEBE PCHK	ADR PCHK
73	LR: RAIF_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RGARB SEQ	TXBF PCHK	RXBF PCHK	TIMEOUT	TIMER PCHK
74	LR: HATX_CHK_LOG_FST (bit31 -24)							
	Reserved	Reserved	Reserved	RGARB XR	RGARB DATA	RGARB CTL	RAIF DATA	RAIF TIMEOUT
75	LR: HATX_CHK_LOG_FST (bit23 -16)							
	Reserved	BSY ACT PCHK	TAG PCHK	REQID PCHK	LADR PCHK	RAERR PCHK	RDT PCHK	HSNERR PCHK
76	LR: HATX_CHK_LOG_FST (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: HATX_CHK_LOG_FST (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	Reserved	XBF PCHK	TXBF PCHK	TIMEOUT	TIMER PCHK
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1070

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(6) Byte28 = 05/45/85 (1/3)

	0	1	2	3	4	5	6	7
40	LR: LRP0_ERR_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR: LRP0_ERR_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR: LRP0_ERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR: LRP0_ERR_STS (bit07 -00)							
	FIF XRPC	FIF DXFO	FCTL	FPCI TX	FPCI RX	Reserved	ERR1	ERR0
44	LR: LRP0_CORE_ERR (bit31 -24)							
	ALU CMP ERR	SEQE ERR	TIMOUT	0 JUMP	TIMER PERR	X2 PERR	X1 PERR	X0 PERR
45	LR: LRP0_CORE_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: LRP0_CORE_ERR (bit15 -08)							
	Reserved	Reserved	CSAD ECC	CSUC ECC	CSO DTP	CSO ADP	CSI DTP	CSI ADP
47	LR: LRP0_CORE_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: LRP0_DS_ERR (bit31 -24)							
	DSO LRP	DSO DMA	DSO XR	DSO PPCI	Reserved	DSI LRP	DSI XR	DSI PPCI
49	LR: LRP0_DS_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR: LRP0_DS_ERR (bit15 -08)							
	DSOA(03:00)				DSOUNC(03:00)			
4B	LR: LRP0_DS_ERR (bit07 -00)							
	DSO ADP	DSO DTP	Reserved	DSI ADP	DSIDTP(03:00)			
4C	LR: LRP0_PCIRX_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR: LRP0_PCIRX_ERR (bit23 -16)							
	Reserved	RADRP	REQIDP	TAGP	WDTP	BEP	WADRP	LENP
4E	LR: LRP0_PCIRX_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	TIMEOUT	OVRUN	PENDP	HDCMP
4F	LR: LRP0_PCIRX_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	STS	DPTY	LAST	CPTY	ECC

SSBLOG05-1080

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(6) Byte28 = 05/45/85 (2/3)

	0	1	2	3	4	5	6	7
50	LR: LRP0_PCITX_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR: LRP0_PCITX_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR: LRP0_PCITX_ERR (bit15 -08)							
	Reserved	LENP	H8P	H423P	PENDCNTP	ENDCNTP	DATAP	ADRP
53	LR: LRP0_PCITX_ERR (bit07 -00)							
	Reserved	Reserved	SEQERR	TIMOUT	STS	DPTY	CPTY	OVRUN
54	LR: LRP0_FCTL_ERR (bit31 -24)							
	CS LRC ERR	CS LDCNT1	CS LDCNT2	CS LDSEQ	CS LDTOP	CS LD TIMEOUT	CS LDXCTP	CS LDBLK CNTP
55	LR: LRP0_FCTL_ERR (bit23 -16)							
	CS LD ADRP	CS LDTP	DXB ADRP	LRP ADRP	CS LDDXBF ERR	Reserved	Reserved	Reserved
56	LR: LRP0_FCTL_ERR (bit15 -08)							
	XRDS DT PERR	XRDS ADR PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR: LRP0_FCTL_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: LRP0_DXLRP_ERR (bit31 -24)							
	ADROP	DTOP	DTIP	SEQ	BEP	Reserved	Reserved	Reserved
59	LR: LRP0_DXLRP_ERR (bit23 -16)							
	TIM2	TIM3R	ROSTS	WOSTS	RACTP	WACTP	RACPP	LAST
5A	LR: LRP0_DXLRP_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: LRP0_DXLRP_ERR (bit07 -00)							
	ADRP	WDTP	WBEP	RDTP	WSEL	RSEL	CMP	SEQ
5C	LR: LRP0_XRPCI_ERR (bit31 -24)							
	ADROP	DTOP	DTIP	SEQ	BEP	TIMOV	STS	Reserved
5D	LR: LRP0_XRPCI_ERR (bit23 -16)							
	NUL	Reserved	Reserved	Reserved	RXCMP	SEQ	RDTP	WDTP
5E	LR: LRP0_XRPCI_ERR (bit15 -08)							
	WBEP	TXSTS	Reserved	TXRCPP	TXTYPP	TXLENP	ADRLOP	ADRHIP
5F	LR: LRP0_XRPCI_ERR (bit07 -00)							
	TOV2	TOV3	ACT	RXECC	RXSTS	RXRDPY	RXLST	RXCPTY

SSBLOG05-1090

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(6) Byte28 = 05/45/85 (3/3)

	0	1	2	3	4	5	6	7
60	LR: LRP0_STATUS (bit31 -24)							
	ACT	ERRSTOP	Reserved	ERR	WARNING	Reserved	DGWAIT	DGPASS
61	LR: LRP0_STATUS (bit23 -16)							
	SEQSTS(07:00)							
62	LR: LRP0_STATUS (bit15 -08)							
	NOWADR(15:08)							
63	LR: LRP0_STATUS (bit07 -00)							
	NOWADR(07:00)							
64	LR: LRP0_ENDCODE (bit31 -24)							
65								
66								
67								
68	LR: LRP0_ENDCODE2 (bit31 -24)							
69								
6A								
6B								
6C	LR: LRP0_SUPPORT (bit31 -24)							
6D								
6E								
6F								
70	LR: LRP0_MAIN_FLAG0 (bit31 -24)							
71								
72								
73								
74	LR: LRP0_MAIN_FLAG1 (bit31 -24)							
75								
76								
77								
78	LR: LRP0_VERSION (bit31 -24)							
79								
7A								
7B								
7C	Reserved							
7D								
7E								
7F								

SSBLOG05-1100

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(7) Byte28 = 06/46/86 (1/4)

	0	1	2	3	4	5	6	7
40	LR: FDMA0_ERR_INFO (bit31 -24)							
	Reserved	Reserved	Reserved	CSCTL SEQ ERR	CSCTL RAM1ECC ADRERR	CSCTL RAM0ECC ADRERR	CSCTL RAM1ECC UNCERR	CSCTL RAM0ECC UNCERR
41	LR: FDMA0_ERR_INFO (bit23 -16)							
	Reserved	Reserved	CSCTL PEDMA CNTERR	CSCTL PEDMA ADRPERR	Reserved	CSCTL ADR PERR	CSCTL RDT PERR	CSCTL RAMDT PERR
42	LR: FDMA0_ERR_INFO (bit15 -08)							
	PAR ERR	MS ERR	ME ERR	PS ERR	PE PERR	HSNTX ERR	PCITX ERR	DXCTL ERR
43	LR: FDMA0_ERR_INFO (bit07 -00)							
	CSDX ERR	Reserved	Reserved	Reserved	ETC ERR	ECC ERR	PERR	FDMA ERR
44	LR: FDMA0_ERR_INFO_0 (bit31 -24)							
	MSDMA RAMECC ADRERR	MEDMA RAMECC ADRERR	PSDMA RAMECC ADRERR	PEDMA RAMECC ADRERR	MSDMA RAMECC UNCRERR	MEDMA RAMECC UNCRERR	PSDMA RAMECC UNCRERR	PEDMA RAMECC UNCRERR
45	LR: FDMA0_ERR_INFO_0 (bit23 -16)							
	Reserved	Reserved	Reserved	MSDMA CNT PERR	PSDMA CNT PERR	ALLDMA DMAPRM PERR	MEDMA BSYCNT PERR	PEDMA BSYCNT PERR
46	LR: FDMA0_ERR_INFO_0 (bit15 -08)							
	MSD HTX DS_DATA LRCERR0	MSD HTX DS_DATA LRCERR1	MSDMA SEQ ERR	MSDMA DSCTL SEQERR	MSDMA DSRDTECC ADRERR0	MSDMA DSRDTECC ADRERR1	MSDMA DSRDTECC UNCERR0	MSDMA DSRDTECC UNCERR1
47	LR: FDMA0_ERR_INFO_0 (bit07 -00)							
	MSDMA PARM ERR	MSDMA RM0WCNT PERR	MSDMA RM1WCNT PERR	MSDMA DSRADR PERR	MSD HTX DS_DATA PERR	MSDMA DSRDT PERR	MSDMA TXPRMDT PERR	MSDMA PARM 16B PERR
48	LR: FDMA0_ERR_INFO_1 (bit31 -24)							
	Reserved	Reserved	MEDMA SEQ ERR	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR: FDMA0_ERR_INFO_1 (bit23 -16)							
	MEDMA PARM ERR	Reserved	Reserved	Reserved	Reserved	Reserved	MEDMA TXPRMDT PERR	MEDMA PARM 16B PERR
4A	LR: FDMA0_ERR_INFO_1 (bit15 -08)							
	Reserved	Reserved	PSDMA SEQ ERR	PSDMA PSDXSEQ ERR	Reserved	Reserved	Reserved	Reserved
4B	LR: FDMA0_ERR_INFO_1 (bit07 -00)							
	PSDMA PARM ERR	Reserved	Reserved	Reserved	PSDMA DXRDT PERR	PSDMA DXWDT PERR	PSDMA TXPRMDT PERR	PSDMA PARM 16B PERR
4C	LR: FDMA0_ERR_INFO_2 (bit31 -24)							
	Reserved	Reserved	PEDMA SEQ ERR	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR: FDMA0_ERR_INFO_2 (bit23 -16)							
	PEDMA PARM ERR	Reserved	Reserved	Reserved	Reserved	Reserved	PEDMA TXPRMDT PERR	PEDMA PARM 16B PERR
4E	LR: FDMA0_ERR_INFO_2 (bit15 -08)							
	HSN1TX MPALSINO ERR	HSN0TX MPALSINO ERR	HSN1TX SEQ ERR	HSN0TX SEQ ERR	HSN1TX HAERR PERR	HSN0TX HAERR PERR	HSN1TX CTL PERR	HSN0TX CTL PERR
4F	LR: FDMA0_ERR_INFO_2 (bit07 -00)							
	HSN1TX BRGQUE PERR	HSN0TX BRGQUE PERR	HSN1TX DATA PERR	HSN0TX DATA PERR	Reserved	Reserved	HSN1TX HD PERR	HSN0TX HD PERR

SSBLOG05-1110

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(7) Byte28 = 06/46/86 (2/4)

	0	1	2	3	4	5	6	7
50	LR: FDMA0_ERR_INFO_3 (bit31 -24)							
	Reserved	Reserved	Reserved	PCITX SEQ ERR	Reserved	Reserved	Reserved	Reserved
51	LR: FDMA0_ERR_INFO_3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	PCITX PAERR PERR	PCITX CTL PERR	PCITX DATA PERR	PCITX HD PERR
52	LR: FDMA0_ERR_INFO_3 (bit15 -08)							
	Reserved	Reserved	Reserved	DXCTL SEQ ERR	Reserved	Reserved	Reserved	DXCTL RDT PERR
53	LR: FDMA0_ERR_INFO_3 (bit07 -00)							
	DXCTL WDT PERR	DXCTL ADR PERR	DXCTL LEN PERR	DXCTL CTL PERR0	DXCTL CTL PERR1	DXCTL CTL PERR2	DXCTL DAWERR PERR	DXCTL DARERR PERR
54	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1120

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(7) Byte28 = 06/46/86 (3/4)

	0	1	2	3	4	5	6	7
60	LR: FDMA1_ERR_INFO (bit31 -24)							
	Reserved	Reserved	Reserved	CSCTL SEQ ERR	CSCTL RAM1ECC ADRERR	CSCTL RAM0ECC ADRERR	CSCTL RAM1ECC UNCERR	CSCTL RAM0ECC UNCERR
61	LR: FDMA1_ERR_INFO (bit23 -16)							
	Reserved	Reserved	CSCTL PEDMA CNTERR	CSCTL PEDMA ADRPERR	Reserved	CSCTL ADR PERR	CSCTL RDT PERR	CSCTL RAMDT PERR
62	LR: FDMA1_ERR_INFO (bit15 -08)							
	PAR ERR	MS ERR	ME ERR	PS ERR	PE PERR	HSNTX ERR	PCITX ERR	DXCTL ERR
63	LR: FDMA1_ERR_INFO (bit07 -00)							
	CSDX ERR	Reserved	Reserved	Reserved	ETC ERR	ECC ERR	PERR	FDMA ERR
64	LR: FDMA1_ERR_INFO_0 (bit31 -24)							
	MSDMA RAMECC ADRERR	MEDMA RAMECC ADRERR	PSDMA RAMECC ADRERR	PEDMA RAMECC ADRERR	MSDMA RAMECC UNCRERR	MEDMA RAMECC UNCRERR	PSDMA RAMECC UNCRERR	PEDMA RAMECC UNCRERR
65	LR: FDMA1_ERR_INFO_0 (bit23 -16)							
	Reserved	Reserved	Reserved	MSDMA CNT PERR	PSDMA CNT PERR	ALLDMA DAPRM PERR	MEDMA BSYCNT PERR	PEDMA BSYCNT PERR
66	LR: FDMA1_ERR_INFO_0 (bit15 -08)							
	MSD HTX DS_DATA LRCERR0	MSD HTX DS_DATA LRCERR1	MSDMA SEQ ERR	MSDMA DSCTL SEQERR	MSDMA DSRDTECC ADRERR0	MSDMA DSRDTECC ADRERR1	MSDMA DSRDTECC UNCERR0	MSDMA DSRDTECC UNCERR1
67	LR: FDMA1_ERR_INFO_0 (bit07 -00)							
	MSDMA PARM ERR	MSDMA RM0WCNT PERR	MSDMA RM1WCNT PERR	MSDMA DSRADR PERR	MSD HTX DS_DATA PERR	MSDMA DSRDT PERR	MSDMA TXPRMDT PERR	MSDMA PARM 16B PERR
68	LR: FDMA1_ERR_INFO_1 (bit31 -24)							
	Reserved	Reserved	MEDMA SEQ ERR	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR: FDMA1_ERR_INFO_1 (bit23 -16)							
	MEDMA PARM ERR	Reserved	Reserved	Reserved	Reserved	Reserved	MEDMA TXPRMDT PERR	MEDMA PARM 16B PERR
6A	LR: FDMA1_ERR_INFO_1 (bit15 -08)							
	Reserved	Reserved	PSDMA SEQ ERR	PSDMA PSDXSEQ ERR	Reserved	Reserved	Reserved	Reserved
6B	LR: FDMA1_ERR_INFO_1 (bit07 -00)							
	PSDMA PARM ERR	Reserved	Reserved	Reserved	PSDMA DXRDT PERR	PSDMA DXWDT PERR	PSDMA TXPRMDT PERR	PSDMA PARM 16B PERR
6C	LR: FDMA1_ERR_INFO_2 (bit31 -24)							
	Reserved	Reserved	PEDMA SEQ ERR	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR: FDMA1_ERR_INFO_2 (bit23 -16)							
	PEDMA PARM ERR	Reserved	Reserved	Reserved	Reserved	Reserved	PEDMA TXPRMDT PERR	PEDMA PARM 16B PERR
6E	LR: FDMA1_ERR_INFO_2 (bit15 -08)							
	HSN1TX MPALSINO ERR	HSN0TX MPALSINO ERR	HSN1TX SEQ ERR	HSN0TX SEQ ERR	HSN1TX HAERR PERR	HSN0TX HAERR PERR	HSN1TX CTL PERR	HSN0TX CTL PERR
6F	LR: FDMA1_ERR_INFO_2 (bit07 -00)							
	HSN1TX BRGQUE PERR	HSN0TX BRGQUE PERR	HSN1TX DATA PERR	HSN0TX DATA PERR	Reserved	Reserved	HSN1TX HD PERR	HSN0TX HD PERR

SSBLOG05-1130

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(7) Byte28 = 06/46/86 (4/4)

	0	1	2	3	4	5	6	7
70	LR: FDMA1_ERR_INFO_3 (bit31 -24)							
	Reserved	Reserved	Reserved	PCITX SEQ ERR	Reserved	Reserved	Reserved	Reserved
71	LR: FDMA1_ERR_INFO_3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	PCITX PAERR PERR	PCITX CTL PERR	PCITX DATA PERR	PCITX HD PERR
72	LR: FDMA1_ERR_INFO_3 (bit15 -08)							
	Reserved	Reserved	Reserved	DXCTL SEQ ERR	Reserved	Reserved	Reserved	DXCTL RDT PERR
73	LR: FDMA1_ERR_INFO_3 (bit07 -00)							
	DXCTL WDT PERR	DXCTL ADR PERR	DXCTL LEN PERR	DXCTL CTL PERR0	DXCTL CTL PERR1	DXCTL CTL PERR2	DXCTL DAWERR PERR	DXCTL DARERR PERR
74	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1140

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(8) Byte28 = 07/47/87 (1/4)

	0	1	2	3	4	5	6	7
40	LR: BRG0_HSN2PCI0_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	PNP DIFF CNTERR	PNP RPTR CNTERR	PNP WPTR CNTERR	PNPBUF PERR
41	LR: BRG0_HSN2PCI0_ERRSTS0 (bit23 -16)							
	Reserved	Reserved	RAM DIFF CNTERR	RAM XFR CNTERR	RAM RADR CNTERR	RAM RPTR PERR	RAM WADR CNTERR	RAM WPTR PERR
42	LR: BRG0_HSN2PCI0_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	RAM ADR ERR	RAM UNC ERR	Reserved	PRMRDT PERR	RAMRHD PERR	RAMWDT PERR
43	LR: BRG0_HSN2PCI0_ERRSTS0 (bit07 -00)							
	LEN ERR	INVCMD ERR	LSTFLG ERR	HRXIF XFR CNTERR	HRXIF RPTR PERR	HRXIF WPTR PERR	HRXDT PERR	HRXHD PERR
44	LR: BRG0_HSN2PCI0_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: BRG0_HSN2PCI0_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: BRG0_HSN2PCI0_ERRSTS1 (bit15 -08)							
	HSN ARB ERR(07:00)							
47	LR: BRG0_HSN2PCI0_ERRSTS1 (bit07 -00)							
	Reserved	Reserved	HAR0 ERR	ERR PERR	PCRC ERR	HCRC ERR	DPTY ERR	CPTY ERR
48	LR: BRG0_HSN2PCI0_ERRSTS2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR: BRG0_HSN2PCI0_ERRSTS2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR: BRG0_HSN2PCI0_ERRSTS2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR: BRG0_HSN2PCI0_ERRSTS2 (bit07 -00)							
	Reserved	Reserved	Reserved	TM0 TOERR	TMR PERR	HRXSEQ ST PERR	HRXSEQ DEC ERR	HRXSEQ ENC ERR
4C	LR: BRG0_HSN2PCI0_ERRSTS3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM4 TOERR
4D	LR: BRG0_HSN2PCI0_ERRSTS3 (bit23 -16)							
	TM3 TOERR	TM2 TOERR	TM1 TOERR	TM0 TOERR	TMR PERR	PNPSEQ ST PERR	PNPSEQ DEC ERR	PNPSEQ ENC ERR
4E	LR: BRG0_HSN2PCI0_ERRSTS3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM10 TOERR	TM03 TOERR
4F	LR: BRG0_HSN2PCI0_ERRSTS3 (bit07 -00)							
	TM02 TOERR	TM01 TOERR	TM00 TOERR	TMR1 PERR	TMR0 PERR	PTXSEQ ST PERR	PTXSEQ DEC ERR	PTXSEQ ENC ERR

SSBLOG05-1150

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(8) Byte28 = 07/47/87 (2/4)

	0	1	2	3	4	5	6	7
50	LR: BRG0_HSN2PCI1_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	PNP DIFF CNTPERR	PNP RPTR CNTPERR	PNP WPTR CNTPERR	PNPBUF PERR
51	LR: BRG0_HSN2PCI1_ERRSTS0 (bit23 -16)							
	Reserved	Reserved	RAM DIFF CNTPERR	RAM XFR CNTPERR	RAM RADR CNTPERR	RAM RPTR PERR	RAM WADR CNTPERR	RAM WPTR PERR
52	LR: BRG0_HSN2PCI1_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	RAM ADR ERR	RAM UNC ERR	Reserved	RAMRDT PERR	RAMRHD PERR	RAMWDT PERR
53	LR: BRG0_HSN2PCI1_ERRSTS0 (bit07 -00)							
	LEN ERR	INVCMD ERR	LSTFLG ERR	HRXIF XFR CNTPERR	HRXIF RPTR PERR	HRXIF WPTR PERR	HRXDT PERR	HRXHD PERR
54	LR: BRG0_HSN2PCI1_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR: BRG0_HSN2PCI1_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR: BRG0_HSN2PCI1_ERRSTS1 (bit15 -08)							
	HSN ARB ERR(07:00)							
57	LR: BRG0_HSN2PCI1_ERRSTS1 (bit07 -00)							
	Reserved	Reserved	HAR1 ERR	ERR PERR	PCRC ERR	HCRC ERR	DPTY ERR	CPTY ERR
58	LR: BRG0_HSN2PCI1_ERRSTS2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR: BRG0_HSN2PCI1_ERRSTS2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR: BRG0_HSN2PCI1_ERRSTS2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: BRG0_HSN2PCI1_ERRSTS2 (bit07 -00)							
	Reserved	Reserved	Reserved	TM0 TOERR	TMR PERR	HRXSEQ ST PERR	HRXSEQ DEC ERR	HRXSEQ ENC ERR
5C	LR: BRG0_HSN2PCI1_ERRSTS3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM4 TOERR
5D	LR: BRG0_HSN2PCI1_ERRSTS3 (bit23 -16)							
	TM3 TOERR	TM2 TOERR	TM1 TOERR	TM0 TOERR	TMR PERR	PNPSEQ ST PERR	PNPSEQ DEC ERR	PNPSEQ ENC ERR
5E	LR: BRG0_HSN2PCI1_ERRSTS3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM10 TOERR	TM03 TOERR
5F	LR: BRG0_HSN2PCI1_ERRSTS3 (bit07 -00)							
	TM02 TOERR	TM01 TOERR	TM00 TOERR	TMR1 PERR	TMR0 PERR	PTXSEQ ST PERR	PTXSEQ DEC ERR	PTXSEQ ENC ERR

SSBLOG05-1160

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(8) Byte28 = 07/47/87 (3/4)

	0	1	2	3	4	5	6	7
60	LR: BRG0_PTXIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	TM0 TOERR	TMR PERR	PARBSEQ ST PERR	PARBSEQ DEC ERR	PARBSEQ ENC ERR
61	LR: BRG0_PTXIF_ERRSTS (bit23 -16)							
	PCI ARB ERR(07:00)							
62	LR: BRG0_PTXIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	CORE ERR	PAT ERR	ERR PERR	CPTY ERR
63	LR: BRG0_PTXIF_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PTXD PERR	PTXHD PERR
64	LR: BRG0_XDCTL_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	TM4 TOERR
65	LR: BRG0_XDCTL_ERRSTS (bit23 -16)							
	TM3 TOERR	TM2 TOERR	TM1 TOERR	TM0 TOERR	TMR PERR	XDSEQ ST PERR	XDSEQ DEC ERR	XDSEQ ENC ERR
66	LR: BRG0_XDCTL_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR: BRG0_XDCTL_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	XDDT PERR	XDHD PERR
68	LR: BRG0_PRXIF_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	TM0 TOERR	TMR PERR	PRXSEQ ST PERR	PRXSEQ DEC ERR	PRXSEQ ENC ERR
69	LR: BRG0_PRXIF_ERRSTS0 (bit23 -16)							
	PCI ARB ERR(07:00)							
6A	LR: BRG0_PRXIF_ERRSTS0 (bit15 -08)							
	CORE ERR	LEN ERR	LSTFLG ERR	PAR ERR	ERR PERR	ECRC ERR	DPTY ERR	CPTY ERR
6B	LR: BRG0_PRXIF_ERRSTS0 (bit07 -00)							
	CMPSTS UR	CMPSTS CRS	CMPSTS CA	PRXIF XFR CNTPERR	PRXIF RPTR PERR	PRXIF WPTR PERR	PRXDT PERR	PRXHD PERR
6C	LR: BRG0_PBUF0_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM12 TOERR	TM11 TOERR	TM10 TOERR
6D	LR: BRG0_PBUF0_ERRSTS0 (bit23 -16)							
	TM02 TOERR	TM01 TOERR	TM00 TOERR	TMR1 PERR	TMR0 PERR	HTXSEQ ST PERR	HTXSEQ DEC ERR	HTXSEQ ENC ERR
6E	LR: BRG0_PBUF0_ERRSTS0 (bit15 -08)							
	Reserved	HDBUF DIFF CNTPERR	RAM DIFF CNTPERR	RAM XFR CNTPERR	RAM RADR CNTPERR	RAM RPTR CNTPERR	RAM WADR CNTPERR	RAM WPTR CNTPERR
6F	LR: BRG0_PBUF0_ERRSTS0 (bit07 -00)							
	Reserved	BACC ERR	RAM ADR ERR	RAM UNC ERR	HDBUF PERR	MPA LSINO CNVERR	RAMRDT PERR	RAMWDT PERR

SSBLOG05-1170

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(8) Byte28 = 07/47/87 (4/4)

	0	1	2	3	4	5	6	7
70	LR: BRG0_PBUF1_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR	TM12 TOERR	TM11 TOERR	TM10 TOERR
71	LR: BRG0_PBUF1_ERRSTS0 (bit23 -16)							
	TM02 TOERR	TM01 TOERR	TM00 TOERR	TMR1 PERR	TMR0 PERR	HTXSEQ ST PERR	HTXSEQ DEC ERR	HTXSEQ ENC ERR
72	LR: BRG0_PBUF1_ERRSTS0 (bit15 -08)							
	Reserved	HDBUF DIFF CNTERR	RAM DIFF CNTERR	RAM XFR CNTERR	RAM RADR CNTERR	RAM RPTR CNTERR	RAM WADR CNTERR	RAM WPTR CNTERR
73	LR: BRG0_PBUF1_ERRSTS0 (bit07 -00)							
	Reserved	BACC ERR	RAM ADR ERR	RAM UNC ERR	HDBUF PERR	MPA LSINO CNVERR	RAMRDT PERR	RAMWDT PERR
74	LR: BRG0_PCOMP0_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR
75	LR: BRG0_PCOMP0_ERRSTS0 (bit23 -16)							
	TM3 TOERR	TM2 TOERR	TM1 TOERR	TM0 TOERR	TMR PERR	CMPSEQ ST PERR	CMPSEQ DEC ERR	CMPSEQ ENC ERR
76	LR: BRG0_PCOMP0_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: BRG0_PCOMP0_ERRSTS0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TAGNO ERR	CMPDT PERR	CMPHD PERR
78	LR: BRG0_PCOMP1_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LOGCNT PERR
79	LR: BRG0_PCOMP1_ERRSTS0 (bit23 -16)							
	TM3 TOERR	TM2 TOERR	TM1 TOERR	TM0 TOERR	TMR PERR	CMPSEQ ST PERR	CMPSEQ DEC ERR	CMPSEQ ENC ERR
7A	LR: BRG0_PCOMP1_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	LR: BRG0_PCOMP1_ERRSTS0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TAGNO ERR	CMPDT PERR	CMPHD PERR
7C	LR: BRG0_HTXIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	TM0 TOERR	TMR PERR	HARBSEQ ST PERR	HARBSEQ DEC ERR	HARBSEQ ENC ERR
7D	LR: BRG0_HTXIF_ERRSTS (bit23 -16)							
	HSN ARB ERR(07:00)							
7E	LR: BRG0_HTXIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	HAT0 ERR	ERR PERR	CPTY ERR
7F	LR: BRG0_HTXIF_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HTXDT PERR	HTXHD PERR

SSBLOG05-1180

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(9) Byte28 = 08/48/88 (1/4)

	0	1	2	3	4	5	6	7
40	LR: XD_ERR_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	TX1	TX0	RX1	RX0
41	LR: XD_ERR_STS (bit23 -16)							
	RXCTL	XR	PCI1	PCI0	DXBF	AES	ERQ	DMAQ
42	LR: XD_ERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR: XD_ERR_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR: XD_HSNTX0_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	MSEQ	TXCNV
45	LR: XD_HSNTX0_ERR (bit23 -16)							
	HSNERR(07:00)							
46	LR: XD_HSNTX0_ERR (bit15 -08)							
	ARB	BSYIFP	SEQ	OUT DATAP	OUT H7PER	OUT H6PER	OUT H5PER	OUT H4PER
47	LR: XD_HSNTX0_ERR (bit07 -00)							
	OUT RDDTP	OUT RQIDP	IN CNTP	OUT CNTP	OUT TAGP	OUT LADRP	DTTOV	RQTOV
48	LR: XD_HSNTX1_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	MSEQ	TXCNV
49	LR: XD_HSNTX1_ERR (bit23 -16)							
	HSNERR(07:00)							
4A	LR: XD_HSNTX1_ERR (bit15 -08)							
	ARB	BSYIFP	SEQ	OUT DATAP	OUT H7PER	OUT H6PER	OUT H5PER	OUT H4PER
4B	LR: XD_HSNTX1_ERR (bit07 -00)							
	OUT RDDTP	OUT RQIDP	IN CNTP	OUT CNTP	OUT TAGP	OUT LADRP	DTTOV	RQTOV
4C	LR: XD_HSNRX0_ERR (bit31 -24)							
	Reserved	Reserved	TRASH	LRC	OUTCNTP	INCNTP	DPTY	RXIF
4D	LR: XD_HSNRX0_ERR (bit23 -16)							
	WRCNT	TRASHP	TIMEOUT	SEQ	TAGOUTP	CMDOUTP	RQIDOP	LENOUTP
4E	LR: XD_HSNRX0_ERR (bit15 -08)							
	BEOUTP	ADROUTP	ECCADR	ECCDT	DTOUTP	RXPCRC	RXHCRC	ACTP
4F	LR: XD_HSNRX0_ERR (bit07 -00)							
	OVERWR	DTINP	HDINP	FUNCP	TAGP	LADRP	REQIDP	LENP

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(9) Byte28 = 08/48/88 (2/4)

	0	1	2	3	4	5	6	7
50	LR: XD_HSNRX1_ERR (bit31 -24)							
	Reserved	Reserved	TRASH	LRC	OUTCNTP	INCNTP	DPTY	RXIF
51	LR: XD_HSNRX1_ERR (bit23 -16)							
	WRCNT	TRASHP	TIMEOUT	SEQ	TAGOUTP	CMDOUTP	RQIDOP	LENOUTP
52	LR: XD_HSNRX1_ERR (bit15 -08)							
	BEOUTP	ADROUTP	ECCADR	ECCDT	DTOUTP	RXPCRC	RXHCRC	ACTP
53	LR: XD_HSNRX1_ERR (bit07 -00)							
	OVERWR	DTINP	HDINP	FUNCP	TAGP	LADRP	REQIDP	LENP
54	LR: XD_RXCTL_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR: XD_RXCTL_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR: XD_RXCTL_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	ERQ32	SEQ	SEL	XPCIDTP
57	LR: XD_RXCTL_ERR (bit07 -00)							
	PCIADP	XRADP	ODTHP	ODTLP	DXBE	TAGP	RQIDP	DXADRP
58	LR: XD_XR_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR: XD_XR_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR: XD_XR_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: XD_XR_ERR (bit07 -00)							
	Reserved	Reserved	ADRP	DTP	SEQ	BEP	TIMEOVER	STS
5C	LR: XD_PCI0_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR: XD_PCI0_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: XD_PCI0_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: XD_PCI0_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	ADRP	DTP	SEQ	ARBERR	TIMEOVER

SSBLOG05-1200

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(9) Byte28 = 08/48/88 (3/4)

	0	1	2	3	4	5	6	7
60	LR: XD_PCI1_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR: XD_PCI1_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: XD_PCI1_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: XD_PCI1_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	ADRP	DTP	SEQ	ARBERR	TIMEOVER
64	LR: XD_DXBF_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR: XD_DXBF_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR: XD_DXBF_ERR (bit15 -08)							
	HSNRQ TOV4	DXDT TOV3	DXRQ TOV2	ADRP	W_DT1P	W_DT0P	R_DTP	SEQ
67	LR: XD_DXBF_ERR (bit07 -00)							
	BEP	LENP	R_STS	W_STS	R_ACTP	W_ACTP	R_ACPP	LENP
68	LR: XD_KEY_ERR (bit31 -24)							
	MF_ALL	TIMEOUT	EXBSY	SEQ	ECCU21	ECCA21	ECCU20	ECCA20
69	LR: XD_KEY_ERR (bit23 -16)							
	EECCU11	DECCU11	EECCA11	DECCA11	EECCU10	DECCU10	EECCA10	DECCA10
6A	LR: XD_KEY_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	EXP4	EXP3	EXP2	EXP1	EXP0
6B	LR: XD_KEY_ERR (bit07 -00)							
	D21P	D20P	D11P	D10P	Reserved	INAP	INDP	ARB
6C	LR: MF_KEY_ERR (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR: MF_KEY_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR: MF_KEY_ERR (bit15 -08)							
	LAPTY	FC_M	CORE(05:00)					
6F	LR: MF_KEY_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	LRC	ARB	INDEXP	SEQ

SSBLOG05-1210

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(9) Byte28 = 08/48/88 (4/4)

0	1	2	3	4	5	6	7
70	LR: ERQ_ERR_STS (bit31 -24)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR: ERQ_ERR_STS (bit23 -16)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	LR: ERQ_ERR_STS (bit15 -08)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: ERQ_ERR_STS (bit07 -00)						
	ERQ(07:00)						
74	LR: DMA_ERR_STS (bit31 -24)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR: DMA_ERR_STS (bit23 -16)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	LR: DMA_ERR_STS (bit15 -08)						
	Reserved	Reserved	Reserved	Reserved	Reserved	DMA ERR(10:8)	
77	LR: DMA_ERR_STS (bit07 -00)						
	DMA ERR(07:00)						
78	LR: INT_ERR_INFO (bit31 -24)						
	INT ERR	Reserved	H0TX ERR(05:00)				
79	LR: INT_ERR_INFO (bit23 -16)						
	DMA0 ERR(01:00)		H0RX ERR(05:00)				
7A	LR: INT_ERR_INFO (bit15 -08)						
	GPTIM ERR(01:00)		H1TX ERR(05:00)				
7B	LR: INT_ERR_INFO (bit07 -00)						
	DMA1 ERR(01:00)		H1RX ERR(05:00)				
7C	LR: FRC_INT (bit31 -24)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR: FRC_INT (bit23 -16)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	LR: FRC_INT (bit15 -08)						
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	LR: FRC_INT (bit07 -00)						
	Reserved	Reserved	Reserved	Reserved	Reserved	FRC CHK4	FRC CHK2 FRC WARN

SSBLOG05-1220

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(10) Byte28 = 09/49/89 (1/4)

	0	1	2	3	4	5	6	7
40	LR: PCDX_PIF_RX_STS (bit31 -24)							
	Reserved	Reserved	Reserved	CS ADR_H PERR	CS ADR_L PERR	CS LEN PERR	CS BYTEACS ERR	CS BLCNT PERR
41	LR: PCDX_PIF_RX_STS (bit23 -16)							
	CNT WRSCMD PERR	PCIARB NULL ERR	PCIARB RERR PERR	PCIARB ECRC ERR	PCIARB CPTY ERR	PCIARB DPTY ERR	PCIARB RXDT PERR	PCIARB RCVCM ERR
42	LR: PCDX_PIF_RX_STS (bit15 -08)							
	PCIARB H00 PERR	PCIARB H40 PERR	PCIARB H42 PERR	PCIARB H8 PERR	PCIARB HC PERR	PCIARB HLEN PERR	TO ERR	TO PERR
43	LR: PCDX_PIF_RX_STS (bit07 -00)							
	TO CMP ERR	SEQDEC ERR	SEQENQ ERR	CNT WWCMD PERR	CNT WRCMD PERR	CNT CMDNUM PERR	CNT BFIWSEL PERR	CNT BFIWADR PERR
44	LR: PCDX_DTX_STS (bit31 -24)							
	TO ACPACTN CMPERR	TO REQACP CMPERR	Reserved	Reserved	Reserved	Reserved	DTX DTTERR ERR	DTX CTLTERR ERR
45	LR: PCDX_DTX_STS (bit23 -16)							
	DTX ERR PCHK	DTX CPTY ERR	DTX WPTY ERR	DTX TXDT PERR	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFI RDDT0 PERR
46	LR: PCDX_DTX_STS (bit15 -08)							
	ECC1 WRN ERR	ECC1 UNC ERR	ECC1 ADR ERR	BFI RDDT1 PERR	TO VALID TOERR	TO VALID TOPERR	TO REQACP ERR	TO REQACP PERR
47	LR: PCDX_DTX_STS (bit07 -00)							
	Reserved	XFRCNT CMP ERR	THRESH CMP ERR	SEQDEC ERR	SEQENQ ERR	CNT RWCMD PERR	CNT BFIRADR PERR	CNT BFIRSEL PERR
48	LR: PCDX_PIF_TX_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR: PCDX_PIF_TX_STS (bit23 -16)							
	TO ACTACTN CMP PER	TO REQACT CMP PERR	BCNT CMPERR	CNT ICRCRAD R PERR	Reserved	PCIARB TERR PERR	PCIARB CPTY ERR	PCIARB TXDT PERR
4A	LR: PCDX_PIF_TX_STS (bit15 -08)							
	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFO RDDT PERR	XFRCNT CMP ERR	THRESH CMP ERR	SEQDEC ERR	SEQENQ ERR
4B	LR: PCDX_PIF_TX_STS (bit07 -00)							
	TO ACTACTN ERR	TO ACTACTN PERR	TO REQACT ERR	TO REQACT PERR	CNT RCMDNUM PERR	CNT WCMDNUM PERR	CNT BFORADR PERR	CNT BFORSEL PERR
4C	LR: PCDX_DRXARB_STS (bit31 -24)							
	DXARB DR PERR(07:00)							
4D	LR: PCDX_DRXARB_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	DXARB DTRERR ERR	DXARB CTLRERR ERR	DXARB LST ERR
4E	LR: PCDX_DRXARB_STS (bit15 -08)							
	DXARB CPTY ERR	DXARB WPTY ERR	DXARB RXDT PERR	DXARB ERR PCHK	TO ACPACTN CMPERR	TO REQACP CMPERR	SEQDEC ERR	SEQENQ ERR
4F	LR: PCDX_DRXARB_STS (bit07 -00)							
	TO ACPACTN ERR	TO ACPACTN PERR	TO REQACT ERR	TO REQACT PERR	CNT BFWADR PERR	CNT BFWSEL PERR	CNT CUSERNO PERR	CNT DRSERNO PERR

SSBLOG05-1230

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(10) Byte28 = 09/49/89 (2/4)

	0	1	2	3	4	5	6	7
50	LR: PCDX_DRX_STS (bit31 -24)							
	Reserved	DRX0 RCMDCNT PERR	DRX0 SEQDEC ERR	DRX0 SEQENC ERR	Reserved	DRX1 RCMDCNT PERR	DRX1 SEQDEC ERR	DRX1 SEQENC ERR
51	LR: PCDX_DRX_STS (bit23 -16)							
	Reserved	DRX2 RCMDCNT PERR	DRX2 SEQDEC ERR	DRX2 SEQENC ERR	Reserved	DRX3 RCMDCNT PERR	DRX3 SEQDEC ERR	DRX3 SEQENC ERR
52	LR: PCDX_DRX_STS (bit15 -08)							
	Reserved	DRX4 RCMDCNT PERR	DRX4 SEQDEC ERR	DRX4 SEQENC ERR	Reserved	DRX5 RCMDCNT PERR	DRX5 SEQDEC ERR	DRX5 SEQENC ERR
53	LR: PCDX_DRX_STS (bit07 -00)							
	Reserved	DRX6 RCMDCNT PERR	DRX6 SEQDEC ERR	DRX6 SEQENC ERR	Reserved	DRX7 RCMDCNT PERR	DRX7 SEQDEC ERR	DRX7 SEQENC ERR
54	LR: PCDX_RCTL_SYN_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RCTL CMP1 ERR	RCTL CMP0 ERR
55	LR: PCDX_RCTL_SYN_STS (bit23 -16)							
	SYNW CNT LP0PERR	SYNW CNT LP1PERR	SYNW CNT LP2PERR	SYNW CNT LP3PERR	SYNW SEQ DEC	SYNW SEQ ENQ	SYNW CNT CMDMONI	SYNW CNT CMDSEL
56	LR: PCDX_RCTL_SYN_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	SYNR RSP_NUM PERR	SYNR CNT WRSRCMD
57	LR: PCDX_RCTL_SYN_STS (bit07 -00)							
	SYNR CNT LP0PERR	SYNR CNT LP1PERR	SYNR CNT LP2PERR	SYNR CNT LP3PERR	SYNR SEQ DEC	SYNR SEQ ENQ	SYNR CNT CMDMONI	SYNR CNT CMDSEL
58	LR: PCDX_PAERR_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR: PCDX_PAERR_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PIF_RX RERR DT	PIF_RX RERR CTL
5A	LR: PCDX_PAERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR: PCDX_PAERR_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PIF_TX TERR DT	PIF_TX TERR CTL
5C	LR: PCDX_LA_STS (bit31 -24)							
	ECC WRN ERR	ECC UNC ERR	ECC ADR ERR	Reserved	Reserved	CNT PERR	DMADT PERR	Reserved
5D	LR: PCDX_LA_STS (bit23 -16)							
	Reserved	XRD LELA PERR	XRD ADD2 PERR	XRD ADD4 PERR	XRD ADD6 PERR	XRD ADD8 PERR	XRD ADD10 PERR	XRD ADD12 PERR
5E	LR: PCDX_LA_STS (bit15 -08)							
	LA ERR	SLRC ERR	CMP BLCNT200 PERR	CMP BLCNT207 PERR	CMP BLCNTLEN PERR	CMP TOALLEN PERR	CMP TOAL203 PERR	CMP TOAL207 PERR
5F	LR: PCDX_LA_STS (bit07 -00)							
	BLKCNT PERR	RTLEN PERR	STR_ADR PERR	SEQDEC ERR	SEQENQ ERR	CNT RCMDW PERR	CNT RCMDR PERR	CNT LOGBUF PERR

SSBLOG05-1240

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(10) Byte28 = 09/49/89 (3/4)

	0	1	2	3	4	5	6	7
60	LR: PCDX_ICRCWR_CHK_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR: PCDX_ICRCWR_CHK_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: PCDX_ICRCWR_CHK_STS (bit15 -08)							
	Reserved	Reserved	Reserved	ECC WRN ERR	ECC UNC ERR	ECC ADR ERR	RAM ADRDT PERR	RAM CRCDT PERR
63	LR: PCDX_ICRCWR_CHK_STS (bit07 -00)							
	Reserved	SEQDEC ERR	SEQENQ ERR	CMP ADR ERR	CNT RAMINIT PCHK	LEN PCK	ADR PCK	DT PCK
64	LR: PCDX_ICRCRD_CHK_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR: PCDX_ICRCRD_CHK_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR: PCDX_ICRCRD_CHK_STS (bit15 -08)							
	Reserved	Reserved	Reserved	ECC WRN ERR	ECC UNC ERR	ECC ADR ERR	RAM ADRDT PERR	RAM CRCDT PERR
67	LR: PCDX_ICRCRD_CHK_STS (bit07 -00)							
	Reserved	SEQDEC ERR	SEQENQ ERR	CMP ADR ERR	CNT RAMINIT PCHK	LEN PCK	ADR PCK	DT PCK
68	LR: PX_WR_SEQ (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR: PX_WR_SEQ (bit23 -16)							
	PIF_RX SEQSTS(07:00)							
6A	LR: PX_WR_SEQ (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR: PX_WR_SEQ (bit07 -00)							
	DTX SEQSTS(07:00)							
6C	LR: PX_RD_SEQ1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR: PX_RD_SEQ1 (bit23 -16)							
	PIF_TX SEQSTS(07:00)							
6E	LR: PX_RD_SEQ1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR: PX_RD_SEQ1 (bit07 -00)							
	DRXARB SEQSTS(07:00)							

SSBLOG05-1250

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(10) Byte28 = 09/49/89 (4/4)

	0	1	2	3	4	5	6	7
70	LR: PX_RD_SEQ2 (bit31 -24)							
	DRX0 SEQSTS(07:00)							
71	LR: PX_RD_SEQ2 (bit23 -16)							
	DRX1 SEQSTS(07:00)							
72	LR: PX_RD_SEQ2 (bit15 -08)							
	DRX2 SEQSTS(07:00)							
73	LR: PX_RD_SEQ2 (bit07 -00)							
	DRX3 SEQSTS(07:00)							
74	LR: PX_RD_SEQ3 (bit31 -24)							
	DRX4 SEQSTS(07:00)							
75	LR: PX_RD_SEQ3 (bit23 -16)							
	DRX5 SEQSTS(07:00)							
76	LR: PX_RD_SEQ3 (bit15 -08)							
	DRX6 SEQSTS(07:00)							
77	LR: PX_RD_SEQ3 (bit07 -00)							
	DRX7 SEQSTS(07:00)							
78	LR: PX_SYN_SEQ3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR: PX_SYN_SEQ3 (bit23 -16)							
	SYNW SEQSTS(07:00)							
7A	LR: PX_SYN_SEQ3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	LR: PX_SYN_SEQ3 (bit07 -00)							
	SYNR SEQSTS(07:00)							
7C	LR: PX_LA_SEQ (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR: PX_LA_SEQ (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	LR: PX_LA_SEQ (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	LR: PX_LA_SEQ (bit07 -00)							
	LA_SEQ SEQSTS(07:00)							

SSBLOG05-1260

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(11) Byte28 = 0a/4a/8a (1/1)

	0	1	2	3	4	5	6	7
40	LR: PPR_MSG_FUNC0_HDR03(31:00)							
41								
42								
43								
44	LR: PPR_MSG_FUNC0_HDR47(31:00)							
45								
46								
47								
48	LR: PPR_MSG_FUNC0_HDR8B(31:00)							
49								
4A								
4B								
4C	LR: PPR_MSG_FUNC0_HDRCF(31:00)							
4D								
4E								
4F								
50	LR: PPR_MSG_FUNC1_HDR03(31:00)							
51								
52								
53								
54	LR: PPR_MSG_FUNC1_HDR47(31:00)							
55								
56								
57								
58	LR: PPR_MSG_FUNC1_HDR8B(31:00)							
59								
5A								
5B								
5C	LR: PPR_MSG_FUNC1_HDRCF(31:00)							
5D								
5E								
5F								
60	LR: PPR_MSG_FUNC2_HDR03(31:00)							
61								
62								
63								
64	LR: PPR_MSG_FUNC2_HDR47(31:00)							
65								
66								
67								
68	LR: PPR_MSG_FUNC2_HDR8B(31:00)							
69								
6A								
6B								
6C	LR: PPR_MSG_FUNC2_HDRCF(31:00)							
6D								
6E								
6F								
70	LR: PPR_MSG_FUNC3_HDR03(31:00)							
71								
72								
73								
74	LR: PPR_MSG_FUNC3_HDR47(31:00)							
75								
76								
77								
78	LR: PPR_MSG_FUNC3_HDR8B(31:00)							
79								
7A								
7B								
7C	LR: PPR_MSG_FUNC3_HDRCF(31:00)							
7D								
7E								
7F								

SSBLOG05-1270

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(12) Byte28 = 0e/4e/8e (1/2)

	0	1	2	3	4	5	6	7
40	LR: SMBM0_MONITOR (31:24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR: SMBM0_MONITOR (23:16)							
	Reserved	Reserved	Reserved	FUNC(04:00)				
42	LR: SMBM0_MONITOR (15:08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
43	LR: SMBM0_MONITOR (07:00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY
44	LR: SMBM1_MONITOR (31:24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: SMBM1_MONITOR (23:16)							
	Reserved	Reserved	Reserved	FUNC(04:00)				
46	LR: SMBM1_MONITOR (15:08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
47	LR: SMBM1_MONITOR (07:00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY
48	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1280

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(12) Byte28 = 0e/4e/8e (2/2)

	0	1	2	3	4	5	6	7
50	Reserved							
51								
52								
53								
54	Reserved							
55								
56								
57								
58	Reserved							
59								
5A								
5B								
5C	Reserved							
5D								
5E								
5F								
60	Reserved							
61								
62								
63								
64	Reserved							
65								
66								
67								
68	Reserved							
69								
6A								
6B								
6C	Reserved							
6D								
6E								
6F								
70	Reserved							
71								
72								
73								
74	Reserved							
75								
76								
77								
78	Reserved							
79								
7A								
7B								
7C	Reserved							
7D								
7E								
7F								

SSBLOG05-1290

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(13) Byte28 = 0f/4f/8f (1/4)

	0	1	2	3	4	5	6	7
40	LR: PABRIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PCIARB ERR(08)
41	LR: PABRIF_ERRSTS (bit23 -16)							
	PCIARB ERR(07:00)							
42	LR: PABRIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ECRC ERR	DPTY ERR	CPTY ERR
43	LR: PABRIF_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	LEN PERR	DT PERR	HD PERR
44	LR: MSG_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: MSG_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: MSG_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RCV ERR MSG	MSGERR HD PERR
47	LR: MSG_ERRSTS (bit07 -00)							
	MSG_HD PERR(07:00)							
48	LR: LOG_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR: LOG_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR: LOG_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR: LOG_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	LOG CNT PERR	LOG DT PERR	LOG HD PERR
4C	LR: RBUF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR: RBUF_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR: RBUF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: RBUF_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	RBUF OUT PERR	Reserved	DCNT PERR	WCNT PERR	RCNT PERR

SSBLOG05-1300

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(13) Byte28 = 0f/4f/8f (2/4)

	0	1	2	3	4	5	6	7
50	LR: REGMIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR: REGMIF_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR: REGMIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	REGARB RDT PERR	REGARB ERR (08)
53	LR: REGMIF_ERRSTS (bit07 -00)							
	REGARB ERR(07:00)							
54	LR: REGTIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR: REGTIF_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR: REGTIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	IDA INVBDR ERR	IDA INVTAG ERR	IDA INVACCS ERR
57	LR: REGTIF_ERRSTS (bit07 -00)							
	Reserved	CMPSTS CA	CMPSTS CRS	CMPSTS UR	Reserved	Reserved	CMP DT PERR	CMP HD PERR
58	LR: PABTIF_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PCIARB ERR (08)
59	LR: PABTIF_ERRSTS (bit23 -16)							
	PCIARB ERR(07:00)							
5A	LR: PABTIF_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	INT PAT ERR	CPTY ERR
5B	LR: PABTIF_ERRSTS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	DT PERR	HD PERR
5C	LR: PCIRSEQ_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR: PCIRSEQ_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: PCIRSEQ_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: PCIRSEQ_ERRSTS (bit07 -00)							
	Reserved	Reserved	PAB MV TOV	TMR PERR	Reserved	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR

SSBLOG05-1310

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(13) Byte28 = 0f/4f/8f (3/4)

	0	1	2	3	4	5	6	7
60	LR: CTLSEQ_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR: CTLSEQ_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: CTLSEQ_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	WR ACC ERR	RD ACC ERR
63	LR: CTLSEQ_ERRSTS (bit07 -00)							
	Reserved	PAB TIF TOV	REGARB TOV	TMR PERR	Reserved	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
64	LR: REGTSEQ_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR: REGTSEQ_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR: REGTSEQ_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR: REGTSEQ_ERRSTS (bit07 -00)							
	Reserved	IDA CMP TOV	IDA PABTIF TOV	TMR PERR	Reserved	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
68	LR: PCITSEQ_ERRSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR: PCITSEQ_ERRSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR: PCITSEQ_ERRSTS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR: PCITSEQ_ERRSTS (bit07 -00)							
	PAB MV TOV	PAB REQ TOV	TXREQ OFF TOV	TMR PERR	Reserved	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
6C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1320

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(13) Byte28 = 0f/4f/8f (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1330

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(14) Byte28 = 90 (1/4)

	0	1	2	3	4	5	6	7
40	LR: CHK4_INT3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FIVE1 MCK	FIVE1 MCK1	FIVE1 MCK0
41	LR: CHK4_INT3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FIVE0 MCK	FIVE0 MCK1	FIVE0 MCK0
42	LR: CHK4_INT3 (bit15 -08)							
	MFCMN5 CHK4	MFCMN4 CHK4	MFCMN3 CHK4	MFCMN2 CHK4	MFCMN1 CHK4	MFCMN0 CHK4	MFPDMA1 CHK4	MFPDMA0 CHK4
43	LR: CHK4_INT3 (bit07 -00)							
	PPR MSG(7)	PPR MSG(6)	PPR MSG(5)	PPR MSG(4)	PPR MSG(3)	PPR MSG(2)	PPR MSG(1)	PPR MSG(0)
44	LR: CCWCMD0_CMD_CHK4 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	XFRSTS DT PERR	PRMQUE QUENUM PERR	CCW PRML PERR	CCW PRMH PERR
45	LR: CCWCMD0_CMD_CHK4 (bit07 -00)							
	CMD SEQ TOV8	CMD SEQ TOV7	CMD SEQ TOV4	CMD SEQ TOV3	TMR PERR2	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
46	LR: CCWSTS0_STSQUE_CMNCHK4STS (bit07 -00)							
	DCNT PERR	WPTR PERR	RPTR PERR	Reserved	TMR PERR	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
47	LR: CCWPRM_PRM_CMNCHK4 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	CCW1 TOV	CCW0 TOV
48	LR: CCWCMD1_CMD_CHK4 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	XFRSTS DT PERR	PRMQUE QUENUM PERR	CCW PRML PERR	CCW PRMH PERR
49	LR: CCWCMD1_CMD_CHK4 (bit07 -00)							
	CMD SEQ TOV8	CMD SEQ TOV7	CMD SEQ TOV4	CMD SEQ TOV3	TMR PERR2	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
4A	LR: CCWSTS1_STSQUE_CMNCHK4STS (bit07 -00)							
	DCNT PERR	WPTR PERR	RPTR PERR	Reserved	TMR PERR	SEQ ST PERR	SEQ DEC ERR	SEQ ENC ERR
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR: WRCTL0_CMNCHK4STS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	WSTXSEQ TMR PERR	WSTXSEQ ST PERR	WSTXSEQ DEC ERR	WSTXSEQ ENC ERR
4D	LR: WRCTL0_CMNCHK4STS0 (bit23 -16)							
	WSTSEQ ST PERR	WSTSEQ DEC ERR	WSTSEQ ENC ERR	WXQASEQ TMR TOERR	WXQASEQ TMR PERR	WXQASEQ ST PERR	WXQASEQ DEC ERR	WXQASEQ ENC ERR
4E	LR: WRCTL0_CMNCHK4STS0 (bit15 -08)							
	WCDSEQ ST02 ERR	PRAM RACNT PERR	WTAG DIFCNT PERR	WCDSEQ TMR0 TOERR	WCDSEQ TMR PERR	WCDSEQ ST PERR	WCDSEQ DEC ERR	WCDSEQ ENC ERR
4F	LR: WRCTL0_CMNCHK4STS0 (bit07 -00)							
	DSTBF DIFCNT PERR	DSTBF RPCNT PERR	DSTBF WPCNT PERR	WXFRQ DIFCNT PERR	WXFRQ RPCNT PERR	WXFRQ WPCNT PERR	TAGBUF RPCNT PERR	TAGBUF WPCNT PERR

SSBLOG05-1340

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(14) Byte28 = 90 (2/4)

	0	1	2	3	4	5	6	7
50	LR: WRCTL1_CMNCHK4STS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	WSTXSEQ TMR PERR	WSTXSEQ ST PERR	WSTXSEQ DEC ERR	WSTXSEQ ENC ERR
51	LR: WRCTL1_CMNCHK4STS0 (bit23 -16)							
	WSTSEQ ST PERR	WSTSEQ DEC ERR	WSTSEQ ENC ERR	WXQASEQ TMR TOERR	WXQASEQ TMR PERR	WXQASEQ ST PERR	WXQASEQ DEC ERR	WXQASEQ ENC ERR
52	LR: WRCTL1_CMNCHK4STS0 (bit15 -08)							
	WCDSEQ ST02 ERR	PRAM RACNT PERR	WTAG DIFCNT PERR	WCDSEQ TMR0 TOERR	WCDSEQ TMR PERR	WCDSEQ ST PERR	WCDSEQ DEC ERR	WCDSEQ ENC ERR
53	LR: WRCTL1_CMNCHK4STS0 (bit07 -00)							
	DSTBF DIFCNT PERR	DSTBF RPCNT PERR	DSTBF WPCNT PERR	WXFRQ DIFCNT PERR	WXFRQ RPCNT PERR	WXFRQ WPCNT PERR	TAGBUF RPCNT PERR	TAGBUF WPCNT PERR
54	LR: RDCTL0_CMNCHK4STS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	RSTXSEQ TMR PERR	RSTXSEQ ST PERR	RSTXSEQ DEC ERR	RSTXSEQ ENC ERR
55	LR: RDCTL0_CMNCHK4STS0 (bit23 -16)							
	RSTSEQ ST PERR	RSTSEQ DEC ERR	RSTSEQ ENC ERR	RXQASEQ TMR TOERR	RXQASEQ TMR PERR	RXQASEQ ST PERR	RXQASEQ DEC ERR	RXQASEQ ENC ERR
56	LR: RDCTL0_CMNCHK4STS0 (bit15 -08)							
	RCDSEQ ST02 ERR	PRAM RACNT PERR	RTAG DIFCNT PERR	RCDSEQ TMR0 TOERR	RCDSEQ TMR PERR	RCDSEQ ST PERR	RCDSEQ DEC ERR	RCDSEQ ENC ERR
57	LR: RDCTL0_CMNCHK4STS0 (bit07 -00)							
	DSTBF DIFCNT PERR	DSTBF RPCNT PERR	DSTBF WPCNT PERR	RXFRQ DIFCNT PERR	RXFRQ RPCNT PERR	RXFRQ WPCNT PERR	TAGBUF RPCNT PERR	TAGBUF WPCNT PERR
58	LR: RDCTL1_CMNCHK4STS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	RSTXSEQ TMR PERR	RSTXSEQ ST PERR	RSTXSEQ DEC ERR	RSTXSEQ ENC ERR
59	LR: RDCTL1_CMNCHK4STS0 (bit23 -16)							
	RSTSEQ ST PERR	RSTSEQ DEC ERR	RSTSEQ ENC ERR	RXQASEQ TMR TOERR	RXQASEQ TMR PERR	RXQASEQ ST PERR	RXQASEQ DEC ERR	RXQASEQ ENC ERR
5A	LR: RDCTL1_CMNCHK4STS0 (bit15 -08)							
	RCDSEQ ST02 ERR	PRAM RACNT PERR	RTAG DIFCNT PERR	RCDSEQ TMR0 TOERR	RCDSEQ TMR PERR	RCDSEQ ST PERR	RCDSEQ DEC ERR	RCDSEQ ENC ERR
5B	LR: RDCTL1_CMNCHK4STS0 (bit07 -00)							
	DSTBF DIFCNT PERR	DSTBF RPCNT PERR	DSTBF WPCNT PERR	RXFRQ DIFCNT PERR	RXFRQ RPCNT PERR	RXFRQ WPCNT PERR	TAGBUF RPCNT PERR	TAGBUF WPCNT PERR
5C	LR: PRM0_CHK4STS1 (bit31 -24)							
	Reserved	Reserved	CSTSSEQ TMR TOV6	CSTSSEQ TMR TOV5	CSTSSEQ TMR TOV4	CSTSSEQ TMR TOV3	CSTSSEQ TMR TOV2	CSTSSEQ TMR TOV1
5D	LR: PRM0_CHK4STS1 (bit23 -16)							
	Reserved	Reserved	Reserved	CSTSSEQ TMR PERR	Reserved	CSTSSEQ ST PERR	CSTSSEQ DEC ERR	CSTSSEQ ENC ERR
5E	LR: PRM0_CHK4STS1 (bit15 -08)							
	Reserved	Reserved	PIF TMR TOV6	PIF TMR TOV5	PIF TMR TOV4	PIF TMR TOV3	PIF TMR TOV2	PIF TMR TOV1
5F	LR: PRM0_CHK4STS1 (bit07 -00)							
	Reserved	Reserved	PIFSEQ TMR PERR2	PIFSEQ TMR PERR1	Reserved	PIFSEQ ST PERR	PIFSEQ DEC ERR	PIFSEQ ENC ERR

SSBLOG05-1350

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(14) Byte28 = 90 (3/4)

	0	1	2	3	4	5	6	7
60	LR: PRM0_CHK4STS2 (bit31 -24)							
	Reserved	PIF CPRM LRCERR	PIF FPRM LRCERR	PIF MPNO ERR	PIF ACCS ERR	PIF CSTSDT PERR	PIF ECCWA PERR	PIF ECCWDT PERR
61	LR: PRM0_CHK4STS2 (bit23 -16)							
	PIF PRMDT3 PERR	PIF PRMDT2 PERR	PIF PRMDT1 PERR	PIF PRMDT0 PERR	PIF LST ERR	PIF HD PERR	PIF WPTR PERR	PIF RPTR PERR
62	LR: PRM0_CHK4STS2 (bit15 -08)							
	Reserved	Reserved	INT HAR ERR	HAR0 PCRC ERR	HAR0 HCRC ERR	HAR0 DPTY ERR	HAR0 CPTY ERR	HAR0 ERR (08)
63	LR: PRM0_CHK4STS2 (bit07 -00)							
	HAR0 ERR(07:00)							
64	LR: PRM0_CHK4STS3 (bit07 -00)							
	Reserved	Reserved	CSTS INVFLG ERR	CSTS INVID ERR	CSTS DT PERR	CSTS DCNT PERR	CSTS WPTR PERR	CSTS RPTR PERR
65	LR: STS0_CHK4STS1 (bit23 -16)							
	Reserved	SIFSEQ ACTOFF TOV	SIFSEQ ACT TOV	SIFSEQ TMR PERR	Reserved	SIFSEQ ST PERR	SIFSEQ DEC ERR	SIFSEQ ENC ERR
66	LR: STS0_CHK4STS1 (bit15 -08)							
	Reserved	SARBSEQ ROFF TOV7	SARBSEQ ROFF TOV6	SARBSEQ ROFF TOV5	SARBSEQ ROFF TOV4	SARBSEQ ROFF TOV3	SARBSEQ ROFF TOV2	SARBSEQ ROFF TOV1
67	LR: STS0_CHK4STS1 (bit07 -00)							
	Reserved	Reserved	Reserved	SARBSEQ TMR PERR	Reserved	SARBSEQ ST PERR	SARBSEQ DEC ERR	SARBSEQ ENC ERR
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR: STS0_CHK4STS2 (bit23 -16)							
	Reserved	Reserved	Reserved	MPA LSINO CNVERR	MFS WPTR PERR	MFS RPTR PERR	MFS DT PERR	MFS HD PERR
6A	LR: STS0_CHK4STS2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	INT HAT ERR	HATO CPTY ERR	HATO ERR (08)
6B	LR: STS0_CHK4STS2 (bit07 -00)							
	HATO ERR(07:00)							
6C	LR: PRM1_CHK4STS1 (bit31 -24)							
	Reserved	Reserved	CSTSSEQ TMR TOV6	CSTSSEQ TMR TOV5	CSTSSEQ TMR TOV4	CSTSSEQ TMR TOV3	CSTSSEQ TMR TOV2	CSTSSEQ TMR TOV1
6D	LR: PRM1_CHK4STS1 (bit23 -16)							
	Reserved	Reserved	Reserved	CSTSSEQ TMR PERR	Reserved	CSTSSEQ ST PERR	CSTSSEQ DEC ERR	CSTSSEQ ENC ERR
6E	LR: PRM1_CHK4STS1 (bit15 -08)							
	Reserved	Reserved	PIF TMR TOV6	PIF TMR TOV5	PIF TMR TOV4	PIF TMR TOV3	PIF TMR TOV2	PIF TMR TOV1
6F	LR: PRM1_CHK4STS1 (bit07 -00)							
	Reserved	Reserved	PIFSEQ TMR PERR2	PIFSEQ TMR PERR1	Reserved	PIFSEQ ST PERR	PIFSEQ DEC ERR	PIFSEQ ENC ERR

SSBLOG05-1360

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(14) Byte28 = 90 (4/4)

	0	1	2	3	4	5	6	7
70	LR: PRM1_CHK4STS2 (bit31 -24)							
	Reserved	PIF CPRM LRCERR	PIF FPRM LRCERR	PIF MPNO ERR	PIF ACCS ERR	PIF CSTSDT PERR	PIF ECCWA PERR	PIF ECCWDT PERR
71	LR: PRM1_CHK4STS2 (bit23 -16)							
	PIF PRMDT3 PERR	PIF PRMDT2 PERR	PIF PRMDT1 PERR	PIF PRMDT0 PERR	PIF LST ERR	PIF HD PERR	PIF WPTR PERR	PIF RPTR PERR
72	LR: PRM1_CHK4STS2 (bit15 -08)							
	Reserved	Reserved	INT HAR ERR	HAR0 PCRC ERR	HAR0 HCRC ERR	HAR0 DPTY ERR	HAR0 CPTY ERR	HAR0 ERR (08)
73	LR: PRM1_CHK4STS2 (bit07 -00)							
	HAR0 ERR(07:00)							
74	LR: PRM1_CHK4STS3 (bit07 -00)							
	Reserved	Reserved	CSTS INVFLG ERR	CSTS INVID ERR	CSTS DT PERR	CSTS DCNT PERR	CSTS WPTR PERR	CSTS RPTR PERR
75	LR: STS1_CHK4STS1 (bit23 -16)							
	Reserved	SIFSEQ ACTOFF TOV	SIFSEQ ACT TOV	SIFSEQ TMR PERR	Reserved	SIFSEQ ST PERR	SIFSEQ DEC ERR	SIFSEQ ENC ERR
76	LR: STS1_CHK4STS1 (bit15 -08)							
	Reserved	SARBSEQ ROFF TOV7	SARBSEQ ROFF TOV6	SARBSEQ ROFF TOV5	SARBSEQ ROFF TOV4	SARBSEQ ROFF TOV3	SARBSEQ ROFF TOV2	SARBSEQ ROFF TOV1
77	LR: STS1_CHK4STS1 (bit07 -00)							
	Reserved	Reserved	Reserved	SARBSEQ TMR PERR	Reserved	SARBSEQ ST PERR	SARBSEQ DEC ERR	SARBSEQ ENC ERR
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR: STS1_CHK4STS2 (bit23 -16)							
	Reserved	Reserved	Reserved	MPA LSINO CNVERR	MFS WPTR PERR	MFS RPTR PERR	MFS DT PERR	MFS HD PERR
7A	LR: STS1_CHK4STS2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	INT HAT ERR	HATO CPTY ERR	HATO ERR (08)
7B	LR: STS1_CHK4STS2 (bit07 -00)							
	HATO ERR(07:00)							
7C	LR: FRM0_CHK4STS1 (bit15 -08)							
	Reserved	Reserved	MFF ADR PERR	MFF WDT PERR	Reserved	FRMCTL DCNT PERR	FRMCTL WPTR PERR	FRMCTL RPTR PERR
7D	LR: FRM0_CHK4STS1 (bit07 -00)							
	Reserved	FRMSEQ TMR TOV2	FRMSEQ TMR TOV1	FRMSEQ TMR PERR	Reserved	FRMSEQ ST PERR	FRMSEQ DEC ERR	FRMSEQ ENC ERR
7E	LR: FRM1_CHK4STS1 (bit15 -08)							
	Reserved	Reserved	MFF ADR PERR	MFF WDT PERR	Reserved	FRMCTL DCNT PERR	FRMCTL WPTR PERR	FRMCTL RPTR PERR
7F	LR: FRM1_CHK4STS1 (bit07 -00)							
	Reserved	FRMSEQ TMR TOV2	FRMSEQ TMR TOV1	FRMSEQ TMR PERR	Reserved	FRMSEQ ST PERR	FRMSEQ DEC ERR	FRMSEQ ENC ERR

SSBLOG05-1370

Byte27 = 84 (CHA LR CHK4/DKA LR CHK4/CHA MHUB CHK4) Byte40-7F Detail

(15) Byte28 = 11/51/91 (1/1)

	0	1	2	3	4	5	6	7
40	LM: CHK4INT_MSG_WD0(31:00)							
41								
42								
43								
44	LM: CHK4INT_MSG_WD1(31:00)							
45								
46								
47								
48	LM: CHK4INT_MSG_WD2(31:00)							
49								
4A								
4B								
4C	LM: CHK4INT_MSG_WD3(31:00)							
4D								
4E								
4F								
50	LM: CHK4INT_MSG_WD4(31:00)							
51								
52								
53								
54	LM: CHK4INT_MSG_WD5(31:00)							
55								
56								
57								
58	LM: CHK4INT_MSG_WD6(31:00)							
59								
5A								
5B								
5C	LM: CHK4INT_MSG_WD7(31:00)							
5D								
5E								
5F								
60	Reserved							
61								
62								
63								
64	Reserved							
65								
66								
67								
68	Reserved							
69								
6A								
6B								
6C	Reserved							
6D								
6E								
6F								
70	Reserved							
71								
72								
73								
74	Reserved							
75								
76								
77								
78	Reserved							
79								
7A								
7B								
7C	Reserved							
7D								
7E								
7F								

SSBLOG05-1380

Byte27 = 89 & Byte0D Bit2 = 0 (FICON CHA CHK2) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'9')			
28	Subcode (See following pages)							
29	Detail Information (See following pages)							
31								
32	Module ID							
33	Routine ID							
34	PCB number(*1)				Message code(x'0')			
35	SSID of self subsystem(low)							
36	Symptom code (x'FF89')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-1381

Byte27 = 89 & Byte0D Bit2 = 0 (FICON CHA CHK2) (2/2)

	0	1	2	3	4	5	6	7
40	Detail Information (See following pages)							
7F								

SSBLOG05-1390

Byte27 = 89 & Byte0D Bit2 = 1 (FIBRE CHA CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Inf valid	Err code						
21	Segment Number							
22	Filemark	ECM	ILI	Reserved	Sense Key			
23	Information Bytes							
24								
25								
26								
27	Format (x'8')				Message(x'9')			
28	Subcode (See following pages)							
29	Detail Information (See following pages)							
31								
32	Reserved							
33								
34	Module ID 1							
35	Routine ID 1							
36	Module ID 2							
37	Routine ID 2							
38	Master Path Number							
39	SCSI Initiator ID							
3A	SCSI Target ID							
3B	Logical Unit Number							
3C	SCSI Command Code							
3D	Processor Number							
3E	LDEV#							
3F								
40	Detail Information (See following pages)							
7F								

SSBLOG05-1400

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(1) Byte28 = 01 & Byte0D Bit2 = 0

	0	1	2	3	4	5	6	7
28	Subcode (x'01' : ESCON I/F CHA – Abnormal Cache Slot Status1, (*1))							
29	Slot Status							
2A								
2B								
2C								
2D	WRITE in Progress	Make GAP	Execute ERASE	Update HA/R0	Update BM	Not used		
2E	SubBlock Number for Ready Response to Transfer							
2F	Start SubBlock Number for Incompletet Recode							
30	End SubBlock Number for Incompletet Recode							
31	Reserved							

*1: Expect for cache ECC/LRC error has been detected in read side.

*2: ① ModuleID = 55 :

After synchronous destage, cache slot with dirty attribute has been detected.

② ModuleID ≠ 55 :

In internal search or orientation process, incomplete subblock has been detected.

SSBLOG05-1410

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(2) Byte28 = 02 & Byte0D Bit2 =0

	0	1	2	3	4	5	6	7
28	Subcode (x'02': ESCON I/F CHA – Abnormal Cache Slot Status2, (*1))							
29	Slot Status							
2A								
2B								
2C								
2D	WRITE in Progress	Make GAP	Execute ERASE	Update HA/R0	Update BM	Not used		
2E	SubBlock Number for Ready Response to Transfer							
2F	Start SubBlock Number for Incompletet Recode							
30	End SubBlock Number for Incompletet Recode							
31	Reserved							

*1: Expect for cache ECC/LRC error has been detected in read side.

SSBLOG05-1420

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(3) Byte28 = 00/01/02/03/10/11/12/13 & Byte0D Bit2 =1

	0	1	2	3	4	5	6	7
28	Subcode (x'00' or x'01' or x'02' or x'03') Subcode (x'10' or x'11' or x'12' or x'13')				Fibre I/F CHA CHK2 DMA Error Fibre I/F CHA CHK2 DMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2 INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1430

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(4) Byte28 = 04/14

	0	1	2	3	4	5	6	7
28	Subcode (x'04') Subcode (x'14')				Fibre I/F CHA CHK2 MDMA Error Fibre I/F CHA CHK2 MDMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2 INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1440

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(5) Byte28 = 08/0c/18/1c

	0	1	2	3	4	5	6	7
28	Subcode (x'08' or x'0c') Subcode (x'18' or x'1c')				Fibre I/F CHA CHK2 DMA Path Error Fibre I/F CHA CHK2 DMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4 INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number DMA0 = 0x00 DMA1 = 0x01 DMA2 = 0x02 DMA3 = 0x03							
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1450

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(6) Byte28 = 09/0d/19/1d

	0	1	2	3	4	5	6	7
28	Subcode (x'09' or x'0d') Subcode (x'19' or x'1d')				Fibre I/F CHA CHK2 MDMA Path Error Fibre I/F CHA CHK2 MDMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4 INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number							
	MDMA = 0x04							
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1460

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(7) Byte28 = 80/81/90/91

	0	1	2	3	4	5	6	7
28	Subcode (x'80' or x'81') Subcode (x'90' or x'91')				FICON I/F CHA CHK2 DMA Error FICON I/F CHA CHK2 DMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2 INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1470

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(8) Byte28 = 85/95

	0	1	2	3	4	5	6	7
28	Subcode (x'85') Subcode (x'95')				FICON I/F CHA CHK2 MFDMA Error FICON I/F CHA CHK2 MFDMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT1(b31 – 24)							
	MFCCW CHK2(15)	MFCCW CHK2(14)	MFCCW CHK2(13)	MFCCW CHK2(12)	MFCCW CHK2(11)	MFCCW CHK2(10)	MFCCW CHK2(9)	MFCCW CHK2(8)
2E	CHK2 INT1(b23 – 16)							
	MFCCW CHK2(7)	MFCCW CHK2(6)	MFCCW CHK2(5)	MFCCW CHK2(4)	MFCCW CHK2(3)	MFCCW CHK2(2)	MFCCW CHK2(1)	MFCCW CHK2(0)
2F	CHK2 INT1(b15 – 08)							
	MFFLD CHK2(15)	MFFLD CHK2(14)	MFFLD CHK2(13)	MFFLD CHK2(12)	MFFLD CHK2(11)	MFFLD CHK2(10)	MFFLD CHK2(9)	MFFLD CHK2(8)
30	CHK2 INT1(b07 – 00)							
	MFFLD CHK2(7)	MFFLD CHK2(6)	MFFLD CHK2(5)	MFFLD CHK2(4)	MFFLD CHK2(3)	MFFLD CHK2(2)	MFFLD CHK2(1)	MFFLD CHK2(0)
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1480

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(9) Byte28 = 88/8c/98/9c

	0	1	2	3	4	5	6	7
28	Subcode (x'88' or x'9c')				FICON I/F CHA CHK2 DMA Path Error			
	Subcode (x'98' or x'9c')				FICON I/F CHA CHK2 DMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2 INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4 INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number							
	DMA0 = 0x00							
	DMA1 = 0x01							
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1490

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(10) Byte28 = 8a/8e/9a/9e

	0	1	2	3	4	5	6	7
28	Subcode (x'8a' or x'8e') Subcode (x'9a' or x'9e')				FICON I/F CHA CHK2 MFDMA Path Error FICON I/F CHA CHK2 MFDMA Timeout			
29	LR: INT STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2 INT1(b15 – 08)							
	MFFLD CHK2(15)	MFFLD CHK2(14)	MFFLD CHK2(13)	MFFLD CHK2(12)	MFFLD CHK2(11)	MFFLD CHK2(10)	MFFLD CHK2(9)	MFFLD CHK2(8)
2E	CHK2 INT1(b07 – 00)							
	MFFLD CHK2(7)	MFFLD CHK2(6)	MFFLD CHK2(5)	MFFLD CHK2(4)	MFFLD CHK2(3)	MFFLD CHK2(2)	MFFLD CHK2(1)	MFFLD CHK2(0)
2F	CHK4 INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number							
	MFDMA0 = 0x10		MFDMA4 = 0x14		MFDMA8 = 0x18		MFDMA12 = 0x1c	
	MFDMA1 = 0x11		MFDMA5 = 0x15		MFDMA9 = 0x19		MFDMA13 = 0x1d	
	MFDMA2 = 0x12		MFDMA6 = 0x16		MFDMA10 = 0x1a		MFDMA14 = 0x1e	
	MFDMA3 = 0x13		MFDMA7 = 0x17		MFDMA11 = 0x1b		MFDMA15 = 0x1f	
31	LR STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1500

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03/10/11/12/13 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: DMACTL DCSQ ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: DMACTL DCSQ ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: DMACTL DCSQ ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: DMACTL DCSQ ERRMPN(bit07 -00)							
	DCSQ ERRMPN EN	Reserved	Reserved	Reserved	DCSQ ERRMPN EN(3)	DCSQ ERRMPN EN(2)	DCSQ ERRMPN EN(1)	DCSQ ERRMPN EN (0)
48	LR: DMACTL ERR ST0(bit31 -24)							
	DMA CSEQ ERR	Reserved	DMA ANQ PIDX PERR	DMA ANQ CIDX PERR	DMA YQ PIDX PERR	DMA YQ CIDX PERR	DMA QSEL PERR	DMA POP PERR
49	LR: DMACTL ERR ST0(bit23 -16)							
	STS0 PERR	STS0 SBADR PERR	STS0 SBTAG PERR	Reserved	STS1 PERR	STS1 SBADR PERR	STS1 SBTAG PERR	Reserved
4A	LR: DMACTL ERR ST0(bit15 -08)							
	STS SBQWP PERR	STS SBQRP PERR	LSNO ERR	DMA PBF0 PERR	DMA PBF1 PERR	DMA QCNT PERR	DMA NQ OVER	DMA YQ OVER
4B	LR: DMACTL ERR ST0(bit07 -00)							
	STSBSEQ ERR	STSPATH ERR	Reserved	Reserved	HTIM PERR	HTIM TO	HTIM STATCNT PERR	Reserved
4C	LR: DMABUF ERR ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGD ZERO ERR
4D	LR: DMABUF ERR ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
4E	LR: DMABUF ERR ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: DMABUF ERR ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1510

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03/10/11/12/13 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF ERR ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
51	LR: DMABUF ERR ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
52	LR: DMABUF ERR ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
53	LR: DMABUF ERR ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF ERR ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
55	LR: DMABUF ERR ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
56	LR: DMABUF ERR ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
57	LR: DMABUF ERR ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
58	LR: DMABUF ERR ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
59	LR: DMABUF ERR ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5A	LR: DMABUF ERR ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	DBH ENCPRM PERR	DBH IVLACNT PERR	DBH KEY34 PERR	DBH FBPRM PERR	DBH DBFDL PERR
5B	LR: DMABUF ERR ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	AES TWVERR (5)	AES TWVERR (4)	AES TWVERR (3)	AES TWVERR (2)	AES TWVERR (1)	AES TWVERR (0)
5C	LR: DMABUF ERR ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
5D	LR: DMABUF ERR ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: DMABUF ERR ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: DMABUF ERR ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1520

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03/10/11/12/13 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF ERR ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
61	LR: DMABUF ERR ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
62	LR: DMABUF ERR ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
63	LR: DMABUF ERR ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
64	LR: DMABUF ERR ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF ERR ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF ERR ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF ERR ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF ERR ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF ERR ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF ERR ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF ERR ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DXBF IF CTL ERR ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
6D	LR: DXBF IF CTL ERR ST0(bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
6E	LR: DXBF IF CTL ERR ST0(bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
6F	LR: DXBF IF CTL ERR ST0(bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved

SSBLOG05-1530

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03/10/11/12/13 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF IF CTL ERR ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
71	LR: DXBF IF CTL ERR ST1 (bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF IF CTL ERR ST1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: DXBF IF CTL ERR ST1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: DXBF IF CTL ERR ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR: DXBF IF CTL ERR ST2 (bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
76	LR: DXBF IF CTL ERR ST2 (bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
77	LR: DXBF IF CTL ERR ST2 (bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1540

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(2) Byte28 = 04/14 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: MDMACTL PFCSQ ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: MDMACTL PFCSQ ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: MDMACTL PFCSQ ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: MDMACTL PFCSQ ERRMPN(bit07 -00)							
	PFCSQ ERRMPN EN	Reserved	Reserved	Reserved	PFCSQ ERRMPN EN(3)	PFCSQ ERRMPN EN(2)	PFCSQ ERRMPN EN(1)	PFCSQ ERRMPN EN (0)
48	LR: DMACTL_TTRDY(bit31 -00)							
49								
4A								
4B								
4C	LR: DMABUF_MULACT(bit31 -00)							
4D								
4E								
4F								

SSBLOG05-1550

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(2) Byte28 = 04/14 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF ERR ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
51	LR: DMABUF ERR ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
52	LR: DMABUF ERR ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: DMABUF ERR ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF ERR ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
55	LR: DMABUF ERR ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
56	LR: DMABUF ERR ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
57	LR: DMABUF ERR ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: DMABUF ERR ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPER	DBF0 RPPER	DBF1 WPPER	DBF1 RPPER	DBF2 WPPER	DBF2 RPPER
59	LR: DMABUF ERR ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
5A	LR: DMABUF ERR ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
5B	LR: DMABUF ERR ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
5C	LR: DMABUF ERR ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
5D	LR: DMABUF ERR ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5E	LR: DMABUF ERR ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	Reserved	Reserved	Reserved	DBH FBPRM PERR	DBH DBFDL PERR
5F	LR: DMABUF ERR ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1560

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(2) Byte28 = 04/14 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF ERR ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
61	LR: DMABUF ERR ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: DMABUF ERR ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: DMABUF ERR ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR: DMABUF ERR ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF ERR ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF ERR ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF ERR ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF ERR ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF ERR ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF ERR ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF ERR ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DMABUF ERR ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
6D	LR: DMABUF ERR ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6E	LR: DMABUF ERR ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6F	LR: DMABUF ERR ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved

SSBLOG05-1570

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(2) Byte28 = 04/14 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF IF CTL ERR ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
71	LR: DXBF IF CTL ERR ST0 (bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF IF CTL ERR ST0 (bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
73	LR: DXBF IF CTL ERR ST0 (bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved
74	LR: DXBF IF CTL ERR ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
75	LR: DXBF IF CTL ERR ST1 (bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
76	LR: DXBF IF CTL ERR ST1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: DXBF IF CTL ERR ST1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: DXBF IF CTL ERR ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR: DXBF IF CTL ERR ST2 (bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
7A	LR: DXBF IF CTL ERR ST2 (bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
7B	LR: DXBF IF CTL ERR ST2 (bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1580

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(3) Byte28 = 08/09/18/19 (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH0 HSNRX ERR ST0(b31 – 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH0 HSNRX ERR ST0 (b23 – 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMA PRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH0 HSNRX ERR ST0 (b15 – 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH0 HSNRX ERR ST0 (b07 – 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH0 HSNTX ERR ST0(b31 – 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH0 HSNTX ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH0 HSNTX ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH0 HSNTX ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH0 HSNTXR ERR ST0(b31 – 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH0 HSNTXR ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH0 HSNTXR ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH0 HSNTXR ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 - 00)							
4D								
4E								
4F								

SSBLOG05-1590

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(3) Byte28 = 08/09/18/19 (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1600

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(3) Byte28 = 08/09/18/19 (3/4)

60	LM: DMA PARM WORD6(bit31 - 00)							
61								
62								
63								
64	LM: DMA PARM WORD7(bit31 - 00)							
65								
66								
67								
68	LM: DMA STS WORD0(bit31 - 24)							
	XFREND	ABORT	OWN ERR	OTHER ERR	ERR2	ERR3	ERR1	Reserved
69	LM: DMA STS WORD0(bit23 - 16)							
	PID(7:0)							
6A	LM: DMA STS WORD0(bit15 - 08)							
	MPN(3:0)				REQID(11:8)			
6B	LM: DMA STS WORD0(bit07 - 00)							
	REQID(7:0)							
6C	LM: DMA STS WORD3(bit31 - 24)							
	LA WRSEQ ERR	LOGICAL PATH ERR	LA CMPERR (RD)	SLRC ERR (RD)	LA CMPERR (WT)	SLRC ERR (WT)	DECODE DATA SLRCERR	T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)							
	T10CRC CRCERR	OCRC ERR	EXTEND WRSEQ ERR	HSN TIMEOUT	DE-DUPLI CATION PTNERR	Reserved	USE P1 PATH	USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)							
	SEGDL(15:08)							
6F	LM: DMA STS WORD3(bit07 - 00)							
	SEGDL(07:00)							

SSBLOG05-1610

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(3) Byte28 = 08/09/18/19 (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1620

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 0c/0d/1c/1d (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH1 HSNRX ERR ST0(b31 – 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH1 HSNRX ERR ST0 (b23 – 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMA PRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH1 HSNRX ERR ST0 (b15 – 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH1 HSNRX ERR ST0 (b07 – 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH1 HSNTX ERR ST0(b31 – 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH1 HSNTX ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH1 HSNTX ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH1 HSNTX ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH1 HSNTXR ERR ST0(b31 – 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH1 HSNTXR ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH1 HSNTXR ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH1 HSNTXR ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 -00)							
4D								
4E								
4F								

SSBLOG05-1630

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 0c/0d/1c/1d (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1640

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 0c/0d/1c/1d (3/4)

(7)	Byte25 - 32/33/34/35 (37:7)							
	0	1	2	3	4	5	6	7
60	LM: DMA PARM WORD6(bit31 - 00)							
61								
62								
63								
64	LM: DMA PARM WORD7(bit31 - 00)							
65								
66								
67								
68	LM: DMA STS WORD0(bit31 - 24)							
	XFREND	ABORT	OWN ERR	OTHER ERR	ERR2	ERR3	ERR1	Reserved
69	LM: DMA STS WORD0(bit23 - 16)							
	PID(7:0)							
6A	LM: DMA STS WORD0(bit15 - 08)							
	MPN(3:0)				REQID(11:8)			
6B	LM: DMA STS WORD0(bit07 - 00)							
	REQID(7:0)							
6C	LM: DMA STS WORD3(bit31 - 24)							
	LA WRSEQ ERR	LOGICAL PATH ERR	LA CMPERR (RD)	SLRC ERR (RD)	LA CMPERR (WT)	SLRC ERR (WT)	DECODE DATA SLRCERR	T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)							
	T10CRC CRCERR	OCRC ERR	EXTEND WRSEQ ERR	HSN TIMEOUT	DE-DUPLI CATION PTNERR	Reserved	USE P1 PATH	USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)							
	SEGDL(15:08)							
6F	LM: DMA STS WORD3(bit07 - 00)							
	SEGDL(07:00)							

SSBLOG05-1650

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 0c/0d/1c/1d (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1660

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 80/81/90/91 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: DMACTL DCSQ ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: DMACTL DCSQ ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: DMACTL DCSQ ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: DMACTL DCSQ ERRMPN(bit07 -00)							
	DCSQ ERRMPN EN	Reserved	Reserved	Reserved	DCSQ ERRMPN EN(3)	DCSQ ERRMPN EN(2)	DCSQ ERRMPN EN(1)	DCSQ ERRMPN EN (0)
48	LR: DMACTL ERR ST0(bit31 -24)							
	DMA CSEQ ERR	Reserved	DMA ANQ PIDX PERR	DMA ANQ CIDX PERR	DMA YQ PIDX PERR	DMA YQ CIDX PERR	DMA QSEL PERR	DMA POP PERR
49	LR: DMACTL ERR ST0(bit23 -16)							
	STS0 PERR	STS0 SBADR PERR	STS0 SBTAG PERR	Reserved	STS1 PERR	STS1 SBADR PERR	STS1 SBTAG PERR	Reserved
4A	LR: DMACTL ERR ST0(bit15 -08)							
	STS SBQWP PERR	STS SBQRP PERR	LSNO ERR	DMA PBF0 PERR	DMA PBF1 PERR	DMA QCNT PERR	DMA NQ OVER	DMA YQ OVER
4B	LR: DMACTL ERR ST0(bit07 -00)							
	STSBSEQ ERR	STSPATH ERR	Reserved	Reserved	HTIM PERR	HTIM TO	HTIM STATCNT PERR	Reserved
4C	LR: DMABUF ERR ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
4D	LR: DMABUF ERR ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
4E	LR: DMABUF ERR ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: DMABUF ERR ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1670

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 80/81/90/91 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF ERR ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
51	LR: DMABUF ERR ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
52	LR: DMABUF ERR ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
53	LR: DMABUF ERR ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF ERR ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
55	LR: DMABUF ERR ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
56	LR: DMABUF ERR ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
57	LR: DMABUF ERR ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
58	LR: DMABUF ERR ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
59	LR: DMABUF ERR ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5A	LR: DMABUF ERR ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	DBH ENCPRM PERR	DBH IVLACNT PERR	DBH KEY34 PERR	DBH FBPRM PERR	DBH DBFDL PERR
5B	LR: DMABUF ERR ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	AES TWVERR (5)	AES TWVERR (4)	AES TWVERR (3)	AES TWVERR (2)	AES TWVERR (1)	AES TWVERR (0)
5C	LR: DMABUF ERR ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
5D	LR: DMABUF ERR ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: DMABUF ERR ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: DMABUF ERR ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1680

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 80/81/90/91 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF ERR ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
61	LR: DMABUF ERR ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
62	LR: DMABUF ERR ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
63	LR: DMABUF ERR ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR: DMABUF ERR ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
65	LR: DMABUF ERR ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
66	LR: DMABUF ERR ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
67	LR: DMABUF ERR ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
68	LR: DMABUF ERR ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
69	LR: DMABUF ERR ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
6A	LR: DMABUF ERR ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	DBH ENCPRM PERR	DBH IVLACNT PERR	DBH KEY34 PERR	DBH FBPRM PERR	DBH DBFDL PERR
6B	LR: DMABUF ERR ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	AES TWVERR (5)	AES TWVERR (4)	AES TWVERR (3)	AES TWVERR (2)	AES TWVERR (1)	AES TWVERR (0)
6C	LR: DMABUF ERR ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
6D	LR: DMABUF ERR ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR: DMABUF ERR ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR: DMABUF ERR ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1690

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 80/81/90/91 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF IF CTL ERR ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
71	LR: DXBF IF CTL ERR ST1 (bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF IF CTL ERR ST1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: DXBF IF CTL ERR ST1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: DXBF IF CTL ERR ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR: DXBF IF CTL ERR ST2 (bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
76	LR: DXBF IF CTL ERR ST2 (bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
77	LR: DXBF IF CTL ERR ST2 (bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1700

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 85/95 (1/3)

	0	1	2	3	4	5	6	7
40	LM: CCWDMA STS WORD0(b31 – 24)							
	CCWEND	CANCEL STATUS	EARLY END	IOSHORT	MPABORT	PN	ASYNC	DUMMY
41	LM: CCWDMA STS WORD0(b23 – 16)							
	CHK2	CHK2 CCW	CHK2 DXBF	CHK2 SYNC	OWN CHK2	OTHER CHK2	FLD_CHK4	FLD_CHK2
42	LM: CCWDMA STS WORD0(b15 – 08)							
	Reserved	SEQID(2:0)			CHID(3:0)			
43	LM: CCWDMA STS WORD0(b07 – 00)							
	REQID(7:0)							
44	LM: CCWDMA STS WORD1(b31 – 24)							
	PRMRAM FULL	MPNO UNMATCH	SEQID UNMATCH	CHID UNMATCH	FCH UNMATCH	XFRPATH UNMATCH	SIZE UNMATCH	SYNCARE A FULL
45	LM: CCWDMA STS WORD1(b23 – 16)							
	QUENUM(3:0)				FRAME ENABLE	PRM REPORT	CD	FC
46	LM: CCWDMA STS WORD1(b15 – 08)							
	RCCNT(15:08)							
47	LM: CCWDMA STS WORD1(b07 – 00)							
	RCCNT(07:00)							
48	LM: CCWDMA STS WORD2(b31 – 24)							
	SP	Reserved	Reserved	TR	Reserved	Reserved	Reserved	Reserved
49	LM: CCWDMA STS WORD2(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LM: CCWDMA STS WORD2(b15 – 08)							
	Reserved	FIVE1 MCK	FIVE1 MCK1	FIVE1 MCK0	FIVECHK1 (3)	FIVECHK1 (2)	FIVECHK1 (1)	FIVECHK1 (0)
4B	LM: CCWDMA STS WORD2(b07 – 00)							
	Reserved	FIVE0 MCK	FIVE0 MCK1	FIVE0 MCK0	FIVECHK0 (3)	FIVECHK0 (2)	FIVECHK0 (1)	FIVECHK0 (0)
4C	LM: CCWDMA STS WORD2(b31 – 24)							
	SBCRC(31:24)							
4D	LM: CCWDMA STS WORD2(b23 – 16)							
	SBCRC(23:16)							
4E	LM: CCWDMA STS WORD2(b15 – 08)							
	SBCRC(15:08)							
4F	LM: CCWDMA STS WORD2(b07 – 00)							
	SBCRC(07:00)							

SSBLOG05-1710

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 85/95 (2/3)

	0	1	2	3	4	5	6	7
50	LM: FLDDMA STS WORD0(b31 – 24)							
	XFREND	CANCEL STATUS	HALT I/O	TRUN CATION	MPABORT	Reserved	Reserved	DUMMY
51	LM: FLDDMA STS WORD0(b23 – 16)							
	CHK2	CHK2 FLD	CHK2 HSN	CHK2 CPATH	OWN CHK2	OTHER CHK2	CCW_CHK4	CCW_CHK 2
52	LM: FLDDMA STS WORD0(b15 – 08)							
	Reserved	SEQID(2:0)			CHID(3:0)			
53	LM: FLDDMA STS WORD0(b07 – 00)							
	REQID(7:0)							
54	LM: FLDDMA STS WORD1(b31 – 24)							
	PRMRAM FULL	MPNO UNMATCH	SEQID UNMATCH	CHID UNMATCH	FCH UNMATCH	XFRPATH UNMATCH	PPS UNMATCH	Reserved
55	LM: FLDDMA STS WORD1(b23 – 16)							
	QUENUM(3:0)				Reserved	Reserved	FF	LF
56	LM: FLDDMA STS WORD1(b15 – 08)							
	RRCNT(15:08)							
57	LM: FLDDMA STS WORD1(b07 – 00)							
	RRCNT(07:00)							
58	LM: FLDDMA STS WORD2(b31 – 24)							
	SP	XP	RW	Reserved	Reserved	Reserved	Reserved	Reserved
59	LM: FLDDMA STS WORD2(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LM: FLDDMA STS WORD2(b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LM: FLDDMA STS WORD2(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C 5D 5E 5F	LM: FLDDMA PARM WORD0(b31 – 00)							

SSBLOG05-1720

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 85/95 (3/3)

	0	1	2	3	4	5	6	7
60	LM: FLDDMA PARM WORD1(b31 – 00)							
61								
62								
63								
64	LM: FLDDMA PARM WORD2(b31 – 00)							
65								
66								
67								
68	LM: FLDDMA PARM WORD3(b31 – 00)							
69								
6A								
6B								
6C	LM: FLDDMA PARM WORD4(b31 – 00)							
6D								
6E								
6F								
70	LM: FLDDMA PARM WORD5(b31 – 00)							
71								
72								
73								
74	LM: FLDDMA PARM WORD6(b31 – 00)							
75								
76								
77								
78	LM: FLDDMA PARM WORD7(b31 – 00)							
79								
7A								
7B								
7C	LM: FLDDMA PARM WORD12(b31 – 00)							
7D								
7E								
7F								

SSBLOG05-1730

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 88/89/98/99 (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH0 HSNRX ERR ST0(b31 – 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH0 HSNRX ERR ST0 (b23 – 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMA PRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH0 HSNRX ERR ST0 (b15 – 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH0 HSNRX ERR ST0 (b07 – 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH0 HSNTX ERR ST0(b31 – 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH0 HSNTX ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH0 HSNTX ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH0 HSNTX ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH0 HSNTXR ERR ST0(b31 – 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH0 HSNTXR ERR ST0 (b23 – 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH0 HSNTXR ERR ST0 (b15 – 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH0 HSNTXR ERR ST0 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 -00)							
4D								
4E								
4F								

SSBLOG05-1740

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 88/89/98/99 (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1750

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 88/89/98/99 (3/4)

(7)	Byte 23	30/31/30/31 (31:1)						
	0	1	2	3	4	5	6	7
60	LM: DMA PARM WORD6(bit31 - 00)							
61								
62								
63								
64	LM: DMA PARM WORD7(bit31 - 00)							
65								
66								
67								
68	LM: DMA STS WORD0(bit31 - 24)							
	XFREND	ABORT	OWN ERR	OTHER ERR	ERR2	ERR3	ERR1	Reserved
69	LM: DMA STS WORD0(bit23 - 16)							
	PID(7:0)							
6A	LM: DMA STS WORD0(bit15 - 08)							
	MPN(3:0)				REQID(11:8)			
6B	LM: DMA STS WORD0(bit07 - 00)							
	REQID(7:0)							
6C	LM: DMA STS WORD3(bit31 - 24)							
	LA WRSEQ ERR	LOGICAL PATH ERR	LA CMPERR (RD)	SLRC ERR (RD)	LA CMPERR (WT)	SLRC ERR (WT)	DECODE DATA SLRCERR	T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)							
	T10CRC CRCERR	OCRC ERR	EXTEND WRSEQ ERR	HSN TIMEOUT	DE-DUPLI CATION PTNERR	Reserved	USE P1 PATH	USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)							
	SEGDL(15:08)							
6F	LM: DMA STS WORD3(bit07 - 00)							
	SEGDL(07:00)							

SSBLOG05-1760

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 88/89/98/99 (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1770

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 8a/9a (1/3)

	0	1	2	3	4	5	6	7
40	LR: WRCTL0 CHK2LOG00 2(b07 – 00)							
	HTX TIMEOUT 7	HTX TIMEOUT 6	HTX TIMEOUT 5	HTX TIMEOUT 4	HTX TIMEOUT 3	HTX TIMEOUT 2	HTX TIMEOUT 1	HTX TIMEOUT 0
41	LR: WRCTL0 CHK2LOG00 1(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HSNSTS ERR	CPATH CNTPERR 8
42	LR: WRCTL0 CHK2LOG00 1(b15 – 08)							
	CPATH CNTPERR 7	CPATH CNTPERR 6	CPATH CNTPERR 5	CPATH CNTPERR 4	CPATH CNTPERR 3	CPATH CNTPERR 2	CPATH CNTPERR 1	CPATH CNTPERR 0
43	LR: WRCTL0 CHK2LOG00 1(b07 – 00)							
	CPATH TIMEOUT 7	CPATH TIMEOUT 6	CPATH TIMEOUT 5	CPATH TIMEOUT 4	CPATH TIMEOUT 3	CPATH TIMEOUT 2	CPATH TIMEOUT 1	CPATH TIMEOUT 0
44	LR: RDCTL0 CHK2LOG00 2(b07 – 00)							
	HTX TIMEOUT 7	HTX TIMEOUT 6	HTX TIMEOUT 5	HTX TIMEOUT 4	HTX TIMEOUT 3	HTX TIMEOUT 2	HTX TIMEOUT 1	HTX TIMEOUT 0
45	LR: RDCTL0 CHK2LOG00 1(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HSNSTS ERR	CPATH CNTPERR 8
46	LR: RDCTL0 CHK2LOG00 1(b15 – 08)							
	CPATH CNTPERR 7	CPATH CNTPERR 6	CPATH CNTPERR 5	CPATH CNTPERR 4	CPATH CNTPERR 3	CPATH CNTPERR 2	CPATH CNTPERR 1	CPATH CNTPERR 0
47	LR: RDCTL0 CHK2LOG00 1(b07 – 00)							
	CPATH TIMEOUT 7	CPATH TIMEOUT 6	CPATH TIMEOUT 5	CPATH TIMEOUT 4	CPATH TIMEOUT 3	CPATH TIMEOUT 2	CPATH TIMEOUT 1	CPATH TIMEOUT 0
48	LM: FLDDMA STS WORD0(b31 – 24)							
	XFREND	CANCEL STATUS	HALT I/O	TRUN CATION	MPABORT	Reserved	Reserved	DUMMY
49	LM: FLDDMA STS WORD0(b23 – 16)							
	CHK2	CHK2 FLD	CHK2 HSN	CHK2 CPATH	OWN CHK2	OTHER CHK2	CCW_CHK4	CCW_CHK 2
4A	LM: FLDDMA STS WORD0(b15 – 08)							
	Reserved	SEQID(2:0)			CHID(3:0)			
4B	LM: FLDDMA STS WORD0(b07 – 00)							
	REQID(7:0)							
4C	LM: FLDDMA STS WORD1(b31 – 24)							
	PRMRAM FULL	MPNO UNMATCH	SEQID UNMATCH	CHID UNMATCH	FCH UNMATCH	XFRPATH UNMATCH	PPS UNMATCH	Reserved
4D	LM: FLDDMA STS WORD1(b23 – 16)							
	QUENUM(3:0)				Reserved	Reserved	FF	LF
4E	LM: FLDDMA STS WORD1(b15 – 08)							
	RRCNT(15:08)							
4F	LM: FLDDMA STS WORD1(b07 – 00)							
	RRCNT(07:00)							

SSBLOG05-1780

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 8a/9a (2/3)

	0	1	2	3	4	5	6	7
50	LM: FLDDMA STS WORD2(b31 – 24)							
	SP	XP	RW	Reserved	Reserved	Reserved	Reserved	Reserved
51	LM: FLDDMA STS WORD2(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LM: FLDDMA STS WORD2(b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LM: FLDDMA STS WORD2(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LM: FLDDMA PARM WORD0(bit31 -00)							
55								
56								
57								
58	LM: FLDDMA PARM WORD1(bit31 -00)							
59								
5A								
5B								
5C	LM: FLDDMA PARM WORD4(bit31 -00)							
5D								
5E								
5F								
60	LM: FLDDMA PARM WORD5(bit31 -00)							
61								
62								
63								
64	LM: FLDDMA PARM WORD6(bit31 -00)							
65								
66								
67								
68	LM: FLDDMA PARM WORD7(bit31 -00)							
69								
6A								
6B								
6C	LM: FLDDMA PARM WORD12(bit31 -00)							
6D								
6E								
6F								

SSBLOG05-1790

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 8a/9a (3/3)

	0	1	2	3	4	5	6	7
70	LR : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LR : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LR : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LR : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LR : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LR : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LR : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LR : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LR : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LR : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1800

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 8e/9e (1/3)

	0	1	2	3	4	5	6	7
40	LR: WRCTL1 CHK2LOG00 2(b07 – 00)							
	HTX TIMEOUT 7	HTX TIMEOUT 6	HTX TIMEOUT 5	HTX TIMEOUT 4	HTX TIMEOUT 3	HTX TIMEOUT 2	HTX TIMEOUT 1	HTX TIMEOUT 0
41	LR: WRCTL1 CHK2LOG00 1(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HSNSTS ERR	CPATH CNTERR 8
42	LR: WRCTL1 CHK2LOG00 1(b15 – 08)							
	CPATH CNTERR 7	CPATH CNTERR 6	CPATH CNTERR 5	CPATH CNTERR 4	CPATH CNTERR 3	CPATH CNTERR 2	CPATH CNTERR 1	CPATH CNTERR 0
43	LR: WRCTL1 CHK2LOG00 1(b07 – 00)							
	CPATH TIMEOUT 7	CPATH TIMEOUT 6	CPATH TIMEOUT 5	CPATH TIMEOUT 4	CPATH TIMEOUT 3	CPATH TIMEOUT 2	CPATH TIMEOUT 1	CPATH TIMEOUT 0
44	LR: RDCTL1 CHK2LOG00 2(b07 – 00)							
	HTX TIMEOUT 7	HTX TIMEOUT 6	HTX TIMEOUT 5	HTX TIMEOUT 4	HTX TIMEOUT 3	HTX TIMEOUT 2	HTX TIMEOUT 1	HTX TIMEOUT 0
45	LR: RDCTL1 CHK2LOG00 1(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HSNSTS ERR	CPATH CNTERR 8
46	LR: RDCTL1 CHK2LOG00 1(b15 – 08)							
	CPATH CNTERR 7	CPATH CNTERR 6	CPATH CNTERR 5	CPATH CNTERR 4	CPATH CNTERR 3	CPATH CNTERR 2	CPATH CNTERR 1	CPATH CNTERR 0
47	LR: RDCTL1 CHK2LOG00 1(b07 – 00)							
	CPATH TIMEOUT 7	CPATH TIMEOUT 6	CPATH TIMEOUT 5	CPATH TIMEOUT 4	CPATH TIMEOUT 3	CPATH TIMEOUT 2	CPATH TIMEOUT 1	CPATH TIMEOUT 0
48	LM: FLDDMA STS WORD0(b31 – 00)							
49								
4A								
4B								
4C	LM: FLDDMA STS WORD1(b31 – 00)							
4D								
4E								
4F								

SSBLOG05-1810

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 8e/9e (2/3)

	0	1	2	3	4	5	6	7
50	LM: FLDDMA STS WORD2(bit31 - 00)							
51								
52								
53								
54	LM: FLDDMA PARM WORD0(bit31 - 00)							
55								
56								
57								
58	LM: FLDDMA PARM WORD1(bit31 - 00)							
59								
5A								
5B								
5C	LM: FLDDMA PARM WORD4(bit31 - 00)							
5D								
5E								
5F								
60	LM: FLDDMA PARM WORD5(bit31 - 00)							
61								
62								
63								
64	LM: FLDDMA PARM WORD6(bit31 - 00)							
65								
66								
67								
68	LM: FLDDMA PARM WORD7(bit31 - 00)							
69								
6A								
6B								
6C	LM: FLDDMA PARM WORD12(bit31 - 00)							
6D								
6E								
6F								

SSBLOG05-1820

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 8e/9e (3/3)

	0	1	2	3	4	5	6	7
70	LR : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LR : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LR : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LR : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LR : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LR : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LR : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LR : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LR : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LR : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1830

Byte27 = 8A (DKA CHK2) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device number				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'A')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number(*1)				Message code(x'0')			
35	SSID of self subsystem(low)							
36	Symptom code (x'FF8A')							
37	(This information is not fixed until DKC reports SSB to HOST.)							

SSBLOG05-1831

Byte27 = 8A (DKA CHK2) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Detail information (See following pages)							
7F								

SSBLOG05-1840

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(1) Byte28 = 40/41/42/43/50/51/52/53

	0	1	2	3	4	5	6	7
28	Subcode (x'40' or x'41' or x'42' or x'43') Subcode (x'50' or x'51' or x'52' or x'53')				DKA CHK2 DMA Error DKA CHK2 DMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2_INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1850

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(2) Byte28 = 44/54

	0	1	2	3	4	5	6	7
28	Subcode (x'44') Subcode (x'54')				DKA CHK2 MDMA Error DKA CHK2 MDMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2_INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1860

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(3) Byte28 = 48/4c/58/5c

	0	1	2	3	4	5	6	7
28	Subcode (x'48' or x'4c') Subcode (x'58' or x'5c')				DKA CHK2 DMA Path Error DKA CHK2 DMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN N	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN N
30	DMA Number							
	DMA0 = 0x00							
	DMA1 = 0x01							
	DMA2 = 0x02							
	DMA3 = 0x03							
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1870

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(4) Byte28 = 49/4d/59/5d

	0	1	2	3	4	5	6	7
28	Subcode (x'49' or x'4d') Subcode (x'59' or x'5d')				DKA CHK2 MDMA Path Error DKA CHK2 MDMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number							
	MDMA = 0x04							
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-1880

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT_STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT_STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT_STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT_STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: DMACTL_DCSQ_ERRMPN(bit31 - 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: DMACTL_DCSQ_ERRMPN(bit23 - 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: DMACTL_DCSQ_ERRMPN(bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: DMACTL_DCSQ_ERRMPN(bit07 - 00)							
	DCSQ ERRMPN EN	Reserved	Reserved	Reserved	DCSQ ERRMPN EN(3)	DCSQ ERRMPN EN(2)	DCSQ ERRMPN EN(1)	DCSQ ERRMPN EN(0)
48	LR: DMACTL_ERR_ST0(bit31 - 24)							
	DMA CSEQ ERR	Reserved	DMA ANQ PIDX PERR	DMA ANQ CIDX PERR	DMA YQ PIDX PERR	DMA YQ CIDX PERR	DMA QSEL PERR	DMA POP PERR
49	LR: DMACTL_ERR_ST0(bit23 - 16)							
	STS0 PERR	STS0 SBADR PERR	STS0 SBTAG PERR	Reserved	STS1 PERR	STS1 SBADR PERR	STS1 SBTAG PERR	Reserved
4A	LR: DMACTL_ERR_ST0(bit15 - 08)							
	STS SBQWP PERR	STS SBQRP PERR	LSNO ERR	DMA PBF0 PERR	DMA PBF1 PERR	DMA QCNT PERR	DMA NQ OVER	DMA YQ OVER
4B	LR: DMACTL_ERR_ST0(bit07 - 00)							
	STSBSEQ ERR	STSPATH ERR	Reserved	Reserved	HTIM PERR	HTIM TO	HTIM STATCNT PERR	Reserved
4C	LR: DMABUF_ERR_ST0(bit31 - 24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
4D	LR: DMABUF_ERR_ST0(bit23 - 16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
4E	LR: DMABUF_ERR_ST0(bit15 - 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: DMABUF_ERR_ST0(bit07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1890

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF_ERR_ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
51	LR: DMABUF_ERR_ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
52	LR: DMABUF_ERR_ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
53	LR: DMABUF_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF_ERR_ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPER	DBF0 RPPER	DBF1 WPPER	DBF1 RPPER	DBF2 WPPER	DBF2 RPPER
55	LR: DMABUF_ERR_ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
56	LR: DMABUF_ERR_ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
57	LR: DMABUF_ERR_ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
58	LR: DMABUF_ERR_ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
59	LR: DMABUF_ERR_ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5A	LR: DMABUF_ERR_ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	DBH ENCPRM PERR	DBH IVLACNT PERR	DBH KEY34 PERR	DBH FBPRM PERR	DBH DBFDL PERR
5B	LR: DMABUF_ERR_ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	AES TWVERR (5)	AES TWVERR (4)	AES TWVERR (3)	AES TWVERR (2)	AES TWVERR (1)	AES TWVERR (0)
5C	LR: DMABUF_ERR_ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
5D	LR: DMABUF_ERR_ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: DMABUF_ERR_ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: DMABUF_ERR_ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF_ERR_ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
61	LR: DMABUF_ERR_ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
62	LR: DMABUF_ERR_ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
63	LR: DMABUF_ERR_ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
64	LR: DMABUF_ERR_ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF_ERR_ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF_ERR_ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF_ERR_ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF_ERR_ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF_ERR_ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF_ERR_ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF_ERR_ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DXBF_IF_CTL_ERR_ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
6D	LR: DXBF_IF_CTL_ERR_ST0(bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
6E	LR: DXBF_IF_CTL_ERR_ST0(bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
6F	LR: DXBF_IF_CTL_ERR_ST0(bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF_IF_CTL_ERR_ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
71	LR: DXBF_IF_CTL_ERR_ST1(bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF_IF_CTL_ERR_ST1(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: DXBF_IF_CTL_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: DXBF_IF_CTL_ERR_ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR: DXBF_IF_CTL_ERR_ST2(bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
76	LR: DXBF_IF_CTL_ERR_ST2(bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
77	LR: DXBF_IF_CTL_ERR_ST2(bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT_STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT_STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT_STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT_STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: MDMACTL_PFC SQ_ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: MDMACTL_PFC SQ_ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: MDMACTL_PFC SQ_ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: MDMACTL_PFC SQ_ERRMPN(bit07 -00)							
	PFC SQ ERRMPN EN	Reserved	Reserved	Reserved	PFC SQ ERRMPN EN(3)	PFC SQ ERRMPN EN(2)	PFC SQ ERRMPN EN(1)	PFC SQ ERRMPN EN (0)
48	LR: DMACTL_TTRDY(bit31 -00)							
49								
4A								
4B								
4C	LR: DMABUF_MULACT(bit31 -00)							
4D								
4E								
4F								

SSBLOG05-1930

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF_ERR_ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
51	LR: DMABUF_ERR_ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
52	LR: DMABUF_ERR_ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: DMABUF_ERR_ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF_ERR_ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
55	LR: DMABUF_ERR_ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
56	LR: DMABUF_ERR_ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
57	LR: DMABUF_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: DMABUF_ERR_ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
59	LR: DMABUF_ERR_ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
5A	LR: DMABUF_ERR_ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
5B	LR: DMABUF_ERR_ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
5C	LR: DMABUF_ERR_ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
5D	LR: DMABUF_ERR_ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5E	LR: DMABUF_ERR_ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	Reserved	Reserved	Reserved	DBH FBPRM PERR	DBH DBFDL PERR
5F	LR: DMABUF_ERR_ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF_ERR_ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
61	LR: DMABUF_ERR_ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: DMABUF_ERR_ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: DMABUF_ERR_ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR: DMABUF_ERR_ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF_ERR_ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF_ERR_ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF_ERR_ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF_ERR_ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF_ERR_ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF_ERR_ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF_ERR_ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DMABUF_ERR_ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
6D	LR: DMABUF_ERR_ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6E	LR: DMABUF_ERR_ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6F	LR: DMABUF_ERR_ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF_IF_CTL_ERR_ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
71	LR: DXBF_IF_CTL_ERR_ST0 (bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF_IF_CTL_ERR_ST0 (bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
73	LR: DXBF_IF_CTL_ERR_ST0 (bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved
74	LR: DXBF_IF_CTL_ERR_ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
75	LR: DXBF_IF_CTL_ERR_ST1 (bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
76	LR: DXBF_IF_CTL_ERR_ST1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: DXBF_IF_CTL_ERR_ST1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: DXBF_IF_CTL_ERR_ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR: DXBF_IF_CTL_ERR_ST2 (bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
7A	LR: DXBF_IF_CTL_ERR_ST2 (bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
7B	LR: DXBF_IF_CTL_ERR_ST2 (bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH0_HSNRX_ERR_ST0(b31 - 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH0_HSNRX_ERR_ST0(b23 - 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMAPRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH0_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH0_HSNRX_ERR_ST0(b07 - 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH0_HSNRX_ERR_ST0(b31 - 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH0_HSNRX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH0_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH0_HSNRX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH0_HSNRX_ERR_ST0(b31 - 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH0_HSNRX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH0_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH0_HSNRX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 - 00)							
4D								
4E								
4F								

SSBLOG05-1970

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1980

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (3/4)

60	LM: DMA PARM WORD6(bit31 - 00)							
61								
62								
63								
64	LM: DMA PARM WORD7(bit31 - 00)							
65								
66								
67								
68	LM: DMA STS WORD0(bit31 - 24)							
	XFREND	ABORT	OWN ERR	OTHER ERR	ERR2	ERR3	ERR1	Reserved
69	LM: DMA STS WORD0(bit23 - 16)							
	PID(7:0)							
6A	LM: DMA STS WORD0(bit15 - 08)							
	MPN(3:0)				REQID(11:8)			
6B	LM: DMA STS WORD0(bit07 - 00)							
	REQID(7:0)							
6C	LM: DMA STS WORD3(bit31 - 24)							
	LA WRSEQ ERR	LOGICAL PATH ERR	LA CMPERR (RD)	SLRC ERR (RD)	LA CMPERR (WT)	SLRC ERR (WT)	DECODE DATA SLRCERR	T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)							
	T10CRC CRCERR	OCRC ERR	EXTEND WRSEQ ERR	HSN TIMEOUT	DE-DUPLI CATION PTNERR	Reserved	USE P1 PATH	USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)							
	SEGDL(15:08)							
6F	LM: DMA STS WORD3(bit07 - 00)							
	SEGDL(07:00)							

SSBLOG05-1990

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-2000

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH1_HSNRX_ERR_ST0(b31 - 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH1_HSNRX_ERR_ST0(b23 - 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMAPRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH1_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH1_HSNRX_ERR_ST0(b07 - 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH1_HSNRX_ERR_ST0(b31 - 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH1_HSNRX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH1_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH1_HSNRX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH1_HSNRX_ERR_ST0(b31 - 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH1_HSNRX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH1_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH1_HSNRX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 -00)							
4D								
4E								
4F								

SSBLOG05-2010

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-2020

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (3/4)

(7)	Byte23	12	11	10	9	8	7	6	5	4	3	2	1	0
60	LM: DMA PARM WORD6(bit31 - 00)													
61														
62														
63														
64	LM: DMA PARM WORD7(bit31 - 00)													
65														
66														
67														
68	LM: DMA STS WORD0(bit31 - 24)													
	XFREND	ABORT		OWN ERR		OTHER ERR		ERR2		ERR3		ERR1		Reserved
69	LM: DMA STS WORD0(bit23 - 16)													
	PID(7:0)													
6A	LM: DMA STS WORD0(bit15 - 08)													
	MPN(3:0)							REQID(11:8)						
6B	LM: DMA STS WORD0(bit07 - 00)													
	REQID(7:0)													
6C	LM: DMA STS WORD3(bit31 - 24)													
	LA WRSEQ ERR	LOGICAL PATH ERR		LA CMPERR (RD)		SLRC ERR (RD)		LA CMPERR (WT)		SLRC ERR (WT)		DECODE DATA SLRCERR		T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)													
	T10CRC CRCERR	OCRC ERR		EXTEND WRSEQ ERR		HSN TIMEOUT		DE-DUPLI CATION PTNERR		Reserved		USE P1 PATH		USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)													
	SEGDL(15:08)													
6F	LM: DMA STS WORD3(bit07 - 00)													
	SEGDL(07:00)													

SSBLOG05-2030

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-2040

Byte27 = 8B (DRR CHK2) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device#				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'B')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
30								
31								
32	Module ID							
33	Routine ID							
34	P/K ID				Message code(x'0')			
35	SSID of self subsystem(low)							
36	Symptom code (x'FF8B')							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-2041

Byte27 = 8B (DRR CHK2) (1/2)

	0	1	2	3	4	5	6	7
40	Detail information (See following pages)							
7F								

SSBLOG05-2050

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/
10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d

	0	1	2	3	4	5	6	7
28	Subcode (x'00' or x'40' or x'80')				FibreCHA/DKA/FICON CHK2 DRR0 Error			
	Subcode (x'01' or x'41' or x'81')				FibreCHA/DKA/FICON CHK2 DRR1 Error			
	Subcode (x'08' or x'48' or x'88')				FibreCHA/DKA/FICON CHK2 DRR0 PATH0 Error			
	Subcode (x'09' or x'49' or x'89')				FibreCHA/DKA/FICON CHK2 DRR1 PATH0 Error			
	Subcode (x'0c' or x'4c' or x'8c')				FibreCHA/DKA/FICON CHK2 DRR0 PATH1 Error			
	Subcode (x'0d' or x'4d' or x'8d')				FibreCHA/DKA/FICON CHK2 DRR1 PATH1 Error			
	Subcode (x'10' or x'50' or x'90')				FibreCHA/DKA/FICON CHK2 DRR0 TIMEOUT			
	Subcode (x'11' or x'51' or x'91')				FibreCHA/DKA/FICON CHK2 DRR1 TIMEOUT			
	Subcode (x'18' or x'58' or x'98')				FibreCHA/DKA/FICON CHK2 DRR0 TIMEOUT			
	Subcode (x'19' or x'59' or x'99')				FibreCHA/DKA/FICON CHK2 DRR1 TIMEOUT			
Subcode (x'1c' or x'5c' or x'9c')				FibreCHA/DKA/FICON CHK2 DRR0 TIMEOUT				
Subcode (x'1d' or x'5d' or x'9d')				FibreCHA/DKA/FICON CHK2 DRR1 TIMEOUT				
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 ERR	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK4_INT1(b31 – 24)							
	(RSV)	(RSV)	XREG1 CHK4	XREG0 CHK4	(RSV)	(RSV)	SMBUS1 CHK4	SMBUS0 CHK4
2E	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
2F	CHK4_INT1(b15 – 08)							
	(RSV)	(RSV)	(RSV)	DA CHK4	(RSV)	(RSV)	PAT CHK4	PAR CHK4
30	CHK4_INT1(b07 – 00)							
	(RSV)	(RSV)	PAT1 CHK4	PAR1 CHK4	(RSV)	(RSV)	PAT0 CHK4	PAR0 CHK4
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

SSBLOG05-2060

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/

10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d (1/4)

	0	1	2	3	4	5	6	7
40	LR: CHK2_00(b31 – 24)							
	DBD1M C1 ADRERR	DBD1M C1 ECCERR	DBD1M C1 UCRCTERR	DBD1M C2 ADRERR	DBD1M C2 ECCERR	DBD1M C2 UCRCTERR	DBD1M DTPER	DBD1N C1 ADRERR
41	LR: CHK2_00(b23 – 16)							
	DBD1N C1 ECCERR	DBD1N C1 UCRCTERR	DBD1N C2 ADRERR	DBD1N C2 ECCERR	DBD1N C2 UCRCTERR	DBD1N DTPER	DBD3X DTPER	Reserved
42	LR: CHK2_00(b15 – 08)							
	DBD4M CRCTCMP ERR_PP	DBD4M CRCTCMP ERR_QQ	DBD4M DTPER	DBD4M PGEN CMPERR	DBD4M PRMISM PER	DBD4M PRMISS PER	DBD4M PRMSMM PER	DBD4M PRMSMS PER
43	LR: CHK2_00(b07 – 00)							
	DBD4N CRCT CMPERR	DBD4N DTPER	DBD4N PRMSNM PER	DBD4N PRMSNS PER	DBD4M DTPER	DBD4N DTPER	Reserved	Reserved
44	LR: CHK2_01(b31 – 24)							
	RLACHK LADTPER	WLACHK LADTPER	RSLRCCHK SLRC CMPERR	RSLRCCHK SLRC PER	WSLRCCHK SLRC CMPERR	WSLRCCHK SLRC PER	Reserved	Reserved
45	LR: CHK2_01(b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: CHK2_01(b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: CHK2_01(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: CHK2_02(b31 – 24)							
	HSNRDBUF RCPERR	HSNRDBUF RDPERR	HSNRDBUF RFUNC CMPERR	HSNRDBUF RTAG CMPERR	HSNRDBUF RID CMPERR	HSNRDBUF RFUNC PERR	HSNRDBUF RTAG PERR	HSNRDBUF RID PERR
49	LR: CHK2_02(b23 – 16)							
	HSNRDBUF RLADR PERR	HSNRDBUF RLEN PERR	HSNRDBUF RH4 PERR	HSNRDBUF RH5 PERR	HSNRDBUF RH6 PERR	HSNRDBUF RH7 PERR	HSNRDBUF ROERR PERR	HSNRDBUF RDAT PERR
4A	LR: CHK2_02(b15 – 08)							
	HSNRDBUF TCPERR	HSNRDBUF TDPERR	HSNRDCNT LENERR	HSNRD TIMER RQTOV	HSNRD TIMER MVTOV	HSNRDBUF HRCERR	HSNRDBUF PCRCERR	Reserved
4B	LR: CHK2_02(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	HAT0 ERRINT	HAR0 ERRINT	HAT1 ERRINT	HAR1 ERRINT
4C	LR: CHK2_03(b31 – 24)							
	HSNWTBUF RCPERR	HSNWTBUF RDPERR	HSNWTBUF RFUNC CMPERR	HSNWTBUF RTAG CMPERR	HSNWTBUF RID CMPERR	HSNWTBUF RFUNC PERR	HSNWTBUF RTAG PERR	HSNWTBUF RID PERR
4D	LR: CHK2_03(b23 – 16)							
	HSNWTBUF RLADR PERR	HSNWTBUF RLEN PERR	HSNWTBUF RH4 PERR	HSNWTBUF RH5 PERR	HSNWTBUF RH6 PERR	HSNWTBUF RH7 PERR	HSNWTBUF ROERR PERR	Reserved
4E	LR: CHK2_03(b15 – 08)							
	HSNWTBUF TCPERR	HSNWTBUF TDPERR	HSNWT TIMER RQTOV	HSNWT TIMER MVTOV	HSNWTBUF HRCERR	HSNWTBUF PCRCERR	Reserved	Reserved
4F	LR: CHK2_03(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2070

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/

10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d (2/4)

	0	1	2	3	4	5	6	7
50	LR: CHK2_04(b31 – 24)							
	HSNRDBUF_TERRST(7:0)							
51	LR: CHK2_04 (b23 – 16)							
	HSNRDBUF_RERRST(7:0)							
52	LR: CHK2_04 (b15 – 08)							
	HSNWTBUF_TERRST(7:0)							
53	LR: CHK2_04(b07 – 00)							
	HSNWTBUF_RERRST(7:0)							
54	LR: CHK2_05(b31 – 24)							
	PRMREG CLDPL ERR	PRMREG CMADPL ERR	PRMREG MDLCRS ERR	DMACTL BADCMD	Reserved	PRMREG INVLDPRM	PRMREG NOTCMA ERR	PRMREG SEGD OVER
55	LR: CHK2_05 (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR: CHK2_05 (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR: CHK2_05 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: CHK2_06(b31 – 24)							
	HSNRDBUF TFUNC PERR	HSNRDBUF TTAG PERR	HSNRDBUF TID PERR	HSNRDBUF TH4 PERR	HSNRDBUF TH5 PERR	HSNRDBUF TH6 PERR	HSNRDBUF TH7 PERR	HSNRDBUF RTDAT PERR
59	LR: CHK2_06 (b23 – 16)							
	HSNRDBUF OVERWT	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR: CHK2_06 (b15 – 08)							
	HSNWTBUF TFUNC PERR	HSNWTBUF TTAG PERR	HSNWTBUF TID PERR	HSNWTBUF TH4 PERR	HSNWTBUF TH5 PERR	HSNWTBUF TH6 PERR	HSNWTBUF TH7 PERR	HSNWTBUF TDAT PERR
5B	LR: CHK2_06 (b07 – 00)							
	HSNWTBUF OUTSTRIP	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR: CHK2_08(b31 – 24)							
	HSNRD TIMER TOV	HSNWT TIMER TOV	HSNRSB STSERR	HSNWSB STSERR	RLACHK LA1ERR	RLACHK LA2ERR	WLACHK LA1ERR	WLACHK LA2ERR
5D	LR: CHK2_08 (b23 – 16)							
	RSLRCCHK SLRCERR	WSLRCCHK SLRCERR	DBD4M VERIFY ERR	DBD4N VERIFY ERR	HSNPATH DMA PDSBL	Reserved	Reserved	Reserved
5E	LR: CHK2_08 (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: CHK2_08 (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2080

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/

10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d (3/4)

	0	1	2	3	4	5	6	7
60	LR: CHK4_00(b31 – 24)							
	PRMCTL DECERR	PRMCTL ENCERR	PRMCTL SQPER	DMACTL DECERR	DMACTL ENCERR	DMACTL SQPER	LMSTSCTL DECERR	LMSTSCTL ENCERR
61	LR: CHK4_00(b23 – 16)							
	LMSTSCTL SQPER	HSNRDCTL DECERR	HSNRDCTL ENCERR	HSNRDCTL SQPER	HSNRDSQ0 DECERR	HSNRDSQ0 ENCERR	HSNRDSQ0 SQPER	HSNRDSQ1 DECERR
62	LR: CHK4_00(b15 – 08)							
	HSNRDSQ1 ENCERR	HSNRDSQ1 SQPER	HSNRDSQ2 DECERR	HSNRDSQ2 ENCERR	HSNRDSQ2 SQPER	HSNRDSQ3 DECERR	HSNRDSQ3 ENCERR	HSNRDSQ3 SQPER
63	LR: CHK4_00(b07 – 00)							
	HSNRDSQ4 DECERR	HSNRDSQ4 ENCERR	HSNRDSQ4 SQPER	HSNRDSQ5 DECERR	HSNRDSQ5 ENCERR	HSNRDSQ5 SQPER	HSNRDSQ6 DECERR	HSNRDSQ6 ENCERR
64	LR: CHK4_01(b31 – 24)							
	HSNRDSQ6 SQPER	HSNRDSQ7 DECERR	HSNRDSQ7 ENCERR	HSNRDSQ7 SQPER	HSNRD TIMSQ DECERR	HSNRD TIMSQ ENCERR	HSNRD TIMSQ SQPER	HSNWTCT L DECERR
65	LR: CHK4_01(b23 – 16)							
	HSNWTCTL ENCERR	HSNWTCTL SQPER	HSNWTQ0 DECERR	HSNWTQ0 ENCERR	HSNWTQ0 SQPER	HSNWTQ1 DECERR	HSNWTQ1 ENCERR	HSNWTQ 1 SQPER
66	LR: CHK4_01(b15 – 08)							
	HSNWTQ2 DECERR	HSNWTQ2 ENCERR	HSNWTQ2 SQPER	HSNWTQ3 DECERR	HSNWTQ3 ENCERR	HSNWTQ3 SQPER	HSNWTQ4 DECERR	HSNWTQ 4 ENCERR
67	LR: CHK4_01(b07 – 00)							
	HSNWTQ4 SQPER	HSNWTQ5 DECERR	HSNWTQ5 ENCERR	HSNWTQ5 SQPER	HSNWTQ6 DECERR	HSNWTQ6 ENCERR	HSNWTQ6 SQPER	HSNWTQ 7 DECERR
68	LR: CHK4_02(b31 – 24)							
	HSNWTQ7 ENCERR	HSNWTQ7 SQPER	HSNWT TIMSQ DECERR	HSNWT TIMSQ ENCERR	HSNWT TIMSQ SQPER	SQD3 DECERR	SQD3 ENCERR	SQD3 PER
69	LR: CHK4_02(b23 – 16)							
	SQD3X DECERR M	SQD3X DECERR S	SQD3X ENCERR M	SQD3X ENCERR S	SQD3X SEQPER M	SQD3X SEQPER S	SQD4M DECERR M	SQD4M DECERR S
6A	LR: CHK4_02(b15 – 08)							
	SQD4M ENCERR M	SQD4M ENCERR S	SQD4M SEQPER M	SQD4M SEQPER S	SQD4N DECERR M	SQD4N DECERR S	SQD4N ENCERR M	SQD4N ENCERR S
6B	LR: CHK4_02(b07 – 00)							
	SQD4N SEQPER M	SQD4N SEQPER S	SQD0M DECERR M	SQD0M DECERR S	SQD0M ENCERR M	SQD0M ENCERR S	SQD0M SEQPER M	SQD0M SEQPER S
6C	LR: CHK4_03(b31 – 24)							
	SQD0N DECERR M	SQD0N DECERR S	SQD0N ENCERR M	SQD0N ENCERR S	SQD0N SEQPER M	SQD0N SEQPER S	SQDE0M DECERR M	SQDE0M DECERR S
6D	LR: CHK4_03(b23 – 16)							
	SQDE0M ENCERR M	SQDE0M ENCERR S	SQDE0M SEQPER M	SQDE0M SEQPER S	SQDE0N DECERR M	SQDE0N DECERR S	SQDE0N ENCERR M	SQDE0N ENCERR S
6E	LR: CHK4_03(b15 – 08)							
	SQDE0N SEQPER M	SQDE0N SEQPER S	SQD1 DECERR	SQD1 ENCERR	SQD1 SQPER	Reserved	Reserved	Reserved
6F	LR: CHK4_03(b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/08/09/0c/0d/40/41/48/49/4c/4d/80/81/88/89/8c/8d/

10/11/18/19/1c/1d/50/51/58/59/5c/5d/90/91/98/99/9c/9d (4/4)

	0	1	2	3	4	5	6	7
70	LR: CHK4_04(b31 – 24)							
	DBD1M XPTPER	DBD1M XXPTPER	DBD1M YPTPER	DBD1M YYPTPER	DBD1N XPTPER	DBD1N XXPTPER	DBD1N YPTPER	DBD1N YYPTPER
71	LR: CHK4_04(b23 – 16)							
	DBD3X XPTPER	DBD3X XXPTPER	DBD3X YPTPER	DBD3X YYPTPER	DBD4M XPTPER	DBD4M XXPTPER	DBD4M YPTPER	DBD4M YYPTPER
72	LR: CHK4_04(b15 – 08)							
	DBD4N XPTPER	DBD4N XXPTPER	DBD4N YPTPER	DBD4N YYPTPER	DBDE1M XPTPER	DBDE1M XXPTPER	DBDE1M YPTPER	DBDE1M YYPTPER
73	LR: CHK4_04(b07 – 00)							
	DBDE1N XPTPER	DBDE1N XXPTPER	DBDE1N YPTPER	DBDE1N YYPTPER	PLCNT5281 CPLCNT PERR	PLCNT5281 NPLCNT PERR	PLCNT5283 CPLCNT PERR	PLCNT5283 NPLCNT PERR
74	LR: CHK4_05(b31 – 24)							
	PLCNTD0M CPLCNT PERR	PLCNTD0M NPLCNT PERR	PLCNTD0N CPLCNT PERR	PLCNTD0N NPLCNT PERR	PLCNTD4M CPLCNT PERR	PLCNTD4M NPLCNT PERR	PLCNTD4N CPLCNT PERR	PLCNTD4N NPLCNT PERR
75	LR: CHK4_05(b23 – 16)							
	SBCNT1 CSBCNT PER	SBCNT1 NSBCNT PER	SBCNT3 CSBCNT PER	SBCNT3 NSBCNT PER	RDADRCNT M CPLCNT PERR	RDADRCNT N NPLCNT PERR	WTADRCNT M CPLCNT PERR	WTADRCNT N NPLCNT PERR
76	LR: CHK4_05(b15 – 08)							
	DBD4M VCNT PERR	DBD4N VCNT PERR	CACNT SL1CNT PERR	CACNT SL2CNT PERR	DRBCNT M PERR	DRBCNT N PERR	DRBCNT RDYCNM PERR	DRBCNT RDYCNM PERR
77	LR: CHK4_05(b07 – 00)							
	HSNPATH MYID PERR	HSNPATH MPEN0 PERR	HSNPATH MPEN1 PERR	HSNPATH CEN0 PERR	HSNPATH CEN1 PERR	HSNPATH XEN0 PERR	HSNPATH XEN1 PERR	DRR STATCNT PERR
78	LR: CHK4_06(b31 – 24)							
	HSNRDSQPN T RDSQRPNT PERR	HSNRDSQPN T RDSQTPNT PERR	HSNRDBUF RPT PERR	HSNRDBUF WPT PERR	HSNRDCNT PCNT PERR	HSNRDCNT PCNT UDF	HSNRDCNT SCNT PERR	HSNRDIME R TIMER PERR
79	LR: CHK4_06(b23 – 16)							
	HSNRDCTL CCNT PERR	HSNRDBUF UNEXPECT0	HSNRDBUF UNEXPECT1	HSNRDIME R MVTIM PERR	HSNRDIME R RQTIM PERR	LMSTSTCTL MVTIM PERR	LMSTSTCTL RQTIM PERR	Reserved
7A	LR: CHK4_06(b15 – 08)							
	HSNWTQPN T RDSQRPNT PERR	HSNWTQPN T RDSQTPNT PERR	HSNWTCTNT PCNT PERR	HSNWTCTNT PCNT UDF	HSNWTCTNT SCNT PERR	HSNWTIME R TIMER PERR	HSNWTBUF TXPT PERR	HSNWTBUF YPT PERR
7B	LR: CHK4_06(b07 – 00)							
	HSNWTCTL CCNT PERR	HSNWTBUF UNEXPECT0	HSNWTBUF UNEXPECT1	HSNWTIME R MVTIM PERR	HSNWTIME R RQTIM PERR	Reserved	Reserved	Reserved
7C	LR: CHK4_07(b31 – 24)							
	HSNPATH STS PDSBL	LMSTSTCTL STSNUL	LMSTSTCTL SINUL	LMSTSTCTL STSWTERR	LMSTSTCTL SIWTERR	LMSTSTCTL MVT0V	LMSTSTCTL RQT0V	Reserved
7D	LR: CHK4_07(b23 – 16)							
	PRMINDEX DCNT PERR	PRMINDEX INDEX PERR	PRMINDEX INDEXCNT PERR	PRMINDEX INDEX OVF	DXTIMER RQT0V	DXTIMER MVT0V	DXTIMER RQTIM PERR	DXTIMER MVTIM PERR
7E	LR: CHK4_07(b15 – 08)							
	PRMBUF DMACHNPT PERR	PRMBUF CHNPT PERR	PRMBUF DADR PERR	PRMBUF RDAT PERR	PRMBUF CO PERR	PRMBUF RO PERR	PRMBUF WO PERR	PRMBUF ROERR PERR
7F	LR: CHK4_07(b07 – 00)							
	PRMBUF WOERR PERR	PRMREG PRB0003 PERR	PRMREG PRB0407 PERR	PRMREG PRB0811 PERR	PRMREG PRB1215 PERR	Reserved	LMSTSGEN STSADR 32BERR	LMSTSGEN SIADR 32BERR

SSBLOG05-2100

Byte27 = 8C (SVP Failure : SVP(PC)) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'C')			
28	Error information (See following pages)							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (*1)							
31								
32								
33	Error information (See following pages)							
34								
35								
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address (x'0000')							
3E								
3F	Head address							

SSBLOG05-2110

Byte27 = 8C (SVP Failure : SVP(PC)) (2/2)

	0	1	2	3	4	5	6	7
40	Error information (See following pages)							
7F								

*1: Error type code

- x'0000' : Logical inconsistency
- x'0001' : HEAP related error
- x'0002' : File related error
- x'0003' : LAN failure
- x'0004' : SSVP failure
- x'0005' : Windows failure
- x'0006' : CUDG detected error
- x'0009' : BOOT detected error
- x'000A' : BOOT detected error
- x'000B' : Router failure
- x'000C' : Disable SVP reboot
- x'000D' : Stop SSB overwrite
- x'000E' : Stop SSBS overwrite
- x'000F' : Stop SSBS overwrite
- x'0015' : Battery failure
- x'0030' : Delay of time
- x'0031' : Warning of a stack
- x'0032' : Single task error
- x'0033' : SVP launcher error
- x'0034' : SVP reboot
- x'0035' : SVP reboot (WSAENOBUFF)
- x'0036' : Standby SVP fail
- x'0037' : SVP fail over
- x'0038' : SVP change refusal
- x'0039' : Ip address setting error
- x'003A' : Subnet mask setting error
- x'003B' : Virus detection error
- x'003C' : Virus detection error
- x'003D' : Virus detection error
- x'003E' : SVP environment error
- x'003F' : Situation observation error
- x'0040' : Dummy PS/Off starting (normal)

SSBLOG05-2120

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

- (1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure /
 Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure /
 Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error /
 SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over /
 SVP change refusal / Ip address setting error / Subnet mask setting error /
 Virus detection error / SVP environment error / Situation observation error /
 Dummy PS/Off starting (normal)

	0	1	2	3	4	5	6	7
28	Failed AP code (*1)							
29								
2A	Reserved							
2F								
30	Error type code (*2)							
31								
32	Error code							
35								
40	Failed Window class							
47								
48	Failed function name							
5F								
60	Failed line number							
61								
62	Error code							
65								
66	Reserved							
7F								

*1: Failed AP code

x'0000' : Dump task

x'0001' : DKU inline

x'0002' : Microprogram change (online)

x'0003' : Information

x'0004' : LOGOUT

x'0005' : PATH inline

x'0006' : Back Ground

x'0007' : DIAG

x'0008' : Install

x'0009' : Online

x'000A' : Micro-program change (off-line)

x'000B' : Special package change

x'000C' : Common function for communication

x'00FF' : Others

***2: Error type code**

x'0000' : Logical inconsistency
x'0001' : HEAP related error
x'0002' : File related error
x'0003' : LAN failure
x'0005' : Windows failure
x'000A' : SVP message
x'000B' : Router failure
x'000C' : Disable SVP reboot
x'000D' : Stop SSB overwrite
x'000E' : Stop SSBS overwrite
x'000F' : Flash memory failure
x'0015' : Battery failure
x'0030' : Delay of time
x'0031' : Warning of a stack
x'0032' : Single task error
x'0033' : SVP launcher error
x'0034' : SVP reboot
x'0035' : SVP rebot(WSAENOBUF)
x'0036' : Standby SVP fail
x'0037' : SVP fail over
x'0038' : SVP change refusal
x'0039' : Ip address setting error
x'003A' : Subnet mask setting error
x'003B' : Virus detection error
x'003C' : Virus detection error
x'003D' : Virus detection error
x'003E' : Virus detection error
x'003F' : Situation observation error
x'0040' : Dummy PS/Off starting (normal)

SSBLOG05-2140

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(2) SSVP Failure

	0	1	2	3	4	5	6	7
28	Failed AP code (x'00FF')							
29								
2A	Reserved							
2B								
2F	Error type code (x'0004': SSVP Failure)							
30								
31	Reserved							
32								
33	SSVP error code							
34								
35								
40	SSVP IMPL Sequence							
41	Detect Code							
42	Part Code							
43								
44								
45								
46								
47								
48								
49	Detail Information							
4A								
4B								
4C								
4D								
4E								
4F								
50	Reserved							
51								
52								
53								
54								
55								
7F								

SSBLOG05-2150

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(3) CUDG detected error

	0	1	2	3	4	5	6	7
28	'CUDG ERR' (x'43 55 44 47 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0006: CUDG detected error)							
31								
32	Error code (x'00000000')							
33								
34								
35								
40	Error occurred time (year)							
41	Error occurred time (month)							
42	Error occurred time (day)							
43	Error occurred time (hour)							
44	Error occurred time (minute)							
45	Error occurred time (second)							
46	Report MPID							
47	Test object							
48	Test type (x'01' : CUDG3, x'10' : FCDG3)							
49	Failed LCP number							
4A	Test contents							
4B	Test contents							
4C	Error code							
4D	Error code							
4E	Not used							
4F	Not used							
50	Cache Module Group No. (slave 1)							
51	Cache Module Group No. (slave 1)							
52	Cache Module Group No. (slave 1)							
53	Cache Module Group No. (slave 1)							
54	Cache Module Group No. (slave 2)							
55	Cache Module Group No. (slave 2)							
56	Cache Module Group No. (slave 2)							
57	Cache Module Group No. (slave 2)							
58	PCB Location (Main Platter)							
59	PCB Location (Main Platter)							
5A	PCB Location (Main Platter)							
5B	PCB Location (Cache Platter 1)							
5C	PCB Location (Cache Platter 1)							
5D	PCB Location (Cache Platter 2)							
5E	PCB Location (Cache Platter 2)							
5F	RUN Option							
60	Additional information (32byte) (*1)							
7F								

*1: Additional information (for FCDG3 only)

	0	1	2	3	4	5	6	7
60	P-Path Map(0x00 FF=P-Path0, 0xFF 00=P-Path1)							
61								
62	Reserved							
63								
64	Record type (x'10' : HTP IMPL diagnostic)							
65	HTP Map(0x01=HTP0, 0x02=HTP1, 0x04=HTP2, 0x08=HTP3)							
66	LR number (x'00'~x'1F', x'FF' : indefinite)							
67	LCP ID (LCP#A : x'80'~LCP#H : x'01')							
68	Error code (*2)							
69	Process type (*3)							
6A	Error status1 (Record type = x'10': See (*4))							
6B								
6C								
6E								
6F								
70	Error status2							
71								
72								
73								
74								
7F								

*2: Error code

Code	Meaning	Function	Remarks
0A	FCDG3' load error	FCDG3'	Make status in loading process
10	Memory clear and test error	FCDG3'	
18	FCDG3 MP detected error	FCDG3	Detail information in error status 1
1A	FCDG3 load error	FCDG3	Make status in loading process
1F	Error before LCP loading	Main loading	
20	SCAN detected error	All	MP detected error
2A	LCP main load error	Main loading	Make status in loading process

*3: Process type

Code	Process	Remarks
01	FCDG3'	Set loading error status when a loading error occurred.
02	FCDG3	Set loading error status when a loading error occurred.
03	Loading	
04	Set of LRP support function	

*4: Error status1 (Record type (Byte 64) = x'10' : FCDG3)

Code	Meaning
0101	FCDG3' Waiting for end of FIVE-EX diagnosis timeout
0104	FCDG3' MCHK Detected in FIVE-EX diagnosis (except Soft MCHK)
0105	FCDG3' CHK1B Detected
010f	FCDG3' Invalid Parameter(s) Received
0110	FCDG3' FIVE-EX diagnosis result, Error detected in preliminary diagnosis
0111	FCDG3' FIVE-EX diagnosis result, Error detected in Memory diagnosis (Fixed data)
0112	FCDG3' Error detected in Memory diagnosis (Sequential access)
0113	FCDG3' FIVE-EX diagnosis result, Error detected in Memory diagnosis (Inc data)
0114	FCDG3' FIVE-EX diagnosis result, Error detected in Memory diagnosis (random data)
0115	FCDG3' FIVE-EX diagnosis result, Failure detected in CS access diagnosis
0120	FCDG3' Time-Out error in FIVE-EX inside memory initialization
0170	FCDG3' Invalid Parameter(s) received in FIVE-EX inside memory initialization
0171	FCDG3' Invalid PCB Indicated
0181	FCDG3' Error of except MCHK detected in FIVE-EX diagnosis
01A1	FCDG3' CHK4 Detected
01C0	FCDG3' FIVE-EX diagnosis result, Correctable error detected
01D0	FCDG3' Register read error
01D1	FCDG3' Register write error
08xx	FCDG3 FIVE-EX processor diagnosis
12xx	FCDG3 LINK diagnosis
13xx	FCDG3 Frame diagnosis
14xx	FCDG3 SFP information acquisition
15xx	FCDG3 CAM function diagnosis
21xx	FCDG3 FIVE-EX data transport diagnosis (MS request)
22xx	FCDG3 FIVE-EX data transport diagnosis (burst)
23xx	FCDG3 WS direct space diagnosis
24xx	Indirect access Write/Read Test
25xx	Scan space Write/Read Test
30xx	FCDG3 diagnosis
32xx	FCDG3' Load area diagnosis
33xx	Set of MHUB LRP support function

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xx:	02	MPCK Detected	70	Invalid Parameter(s) Received
	03	FIVE-EX diagnosis result is Others Error	71	Invalid PCB Received
	04	Hard MCHK Detected	72	Invalid Package Speed Detected
	20	Time-out	76	Invalid SFP Speed
	21	Frame transmission Time-out	77	SFP4G/PK8G Error
	22	Frame reception Time-out	78	SFP8G/PK4G Error
	24	Loop Count Over	80	FIVE-EX MCK Detected
	30	CHK1B Detected	81	FIVE-EX CHK1B or CHK4 Detected
	40	Compare Error	A1	CHK4 Detected
	61	Store burst (STB) transmission Error	C0	Correctable Error
	62	Fetch burst(FTB) transmission Error	D0	Register read error
	63	Store (ST) transmission Error Detected	D1	Register write error
	64	Fetch(FT) transmission Error Detected	D2	MCH_DMA error
			E0	MailBOX error
			E1	MailBOX_Status error

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(4) BOOT detected error

	0	1	2	3	4	5	6	7
28	'BOOT ERR' (x'42 4F 4F 54 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F	Error type code (x'0009' : BOOT detected error)							
30								
31	Error code (x'00000000' X: Processor number)							
32								
33								
34								
35								
40	Error detection point							
41								
42								
43								
4F	Reserved							
50								
51								
52								
53	Error code for loading error							
54								
55								
56								
57	Error code of internal interface							
58								
59								
5A								
5B	Error code of initialization function (*1)							
5C								
5D								
5E								
5F	Reserved							
60								
61								
7F								

*1: Error code of initialization function.

x'31000041': Forcible start with jumper pin.

x'40000041': Load module ID in FM is incorrect.

(FM writing was failed or load module is incorrect)

x'42000041': Sum check error of FM.

(Failure occurred at FM writing or input / output error of FM occurred)

x'48000041': This MP has the micro-programs which type is different from the PCB name.

(In the MP-Install procedure, the package may have been set which type is different from one that is specified in the define configuration.)

x'56000041': Sum check error of the load module.

(SVP Failure occurred)

others : Loading for the load module impossible.

(The load module was incorrect)

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Byte27 = 8D (Power Failure) Common information (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'D')			
28	Hardware information (See following pages)							
31								
32	Not used							
33								
34	PCB							
35	SSID of self subsystem (low order)							
36	Symptom code (x'FF8D' : LDEV type = DKU87I/x'EF8D' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-2210

Byte27 = 8D (Power Failure) Common information (2/2)

	0	1	2	3	4	5	6	7
40	Micro control information							
7F								

SSBLOG05-2220

Byte27 = 8D (Power Failure) Byte28-31 detail (1/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'40' : DKC Power Failure)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Power Failure Hardware Information							
	ACDCALARM value							
2B	Micro Information							
	Power Failure Status Management Information							
2C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

SSBLOG05-2230

Byte27 = 8D (Power Failure) Byte28-31 detail (2/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'80' : DKU/HDU Power Failure)							
29	Detected from Error Occurred PDEV 0x00 ~ 0x4F : Detected from Error Occurred RDEV# 0xFF : Detected from Polling							
2A	Micro Information							
	DKU17 Power Failure Status	DKU16 Power Failure Status	DKU15 Power Failure Status	DKU14 Power Failure Status	DKU13 Power Failure Status	DKU12 Power Failure Status	DKU11 Power Failure Status	DKU10 Power Failure Status
2B	Micro Information							
	DKU07 Power Failure Status	DKU06 Power Failure Status	DKU05 Power Failure Status	DKU04 Power Failure Status	DKU03 Power Failure Status	DKU02 Power Failure Status	DKU01 Power Failure Status	DKU00 Power Failure Status
2C	Micro Information							
	Power Failure Process Trace Information							
2D	Micro Information							
	Power Failure Process Trace Information							
2E	Micro Information							
	Power Failure Process Trace Information							
2F	Micro Information							
	Power Failure Process Trace Information							
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

SSBLOG05-2240

Byte27 = 8D (Power Failure) Byte28-31 detail (3/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'90' : DKA detected DKU/HDU Power Recover)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Micro Information							
	DKU17 Power Failure Status	DKU16 Power Failure Status	DKU15 Power Failure Status	DKU14 Power Failure Status	DKU13 Power Failure Status	DKU12 Power Failure Status	DKU11 Power Failure Status	DKU10 Power Failure Status
2B	Micro Information							
	DKU07 Power Failure Status	DKU06 Power Failure Status	DKU05 Power Failure Status	DKU04 Power Failure Status	DKU03 Power Failure Status	DKU02 Power Failure Status	DKU01 Power Failure Status	DKU00 Power Failure Status
2C	Micro Information							
	Power Failure Process Trace Information							
2D	Micro Information							
	Power Failure Process Trace Information							
2E	Micro Information							
	Power Failure Process Trace Information							
2F	Micro Information							
	Power Failure Process Trace Information							
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

SSBLOG05-2250

Byte27 = 8E (CHK1)

(1) Byte28 = 20 or 21 or 22 or 23 or 24 or 25 or FE

	0	1	2	3	4	5	6	7
28	Subcode (x'20' or x'21' or x'22' or x'23' or x'24' or x'25' or x'FE')							
29	MP_Number							
	MP_Number							
2A	NMI_ISTAT1							
	NMI_ISTAT 17	NMI_ISTAT 16	NMI_ISTAT 15	NMI_ISTAT 14	NMI_ISTAT 13	NMI_ISTAT 12	NMI_ISTAT 11	NMI_ISTAT 10
2B	NMI_ISTAT0							
	NMI_ISTAT 07	NMI_ISTAT 06	NMI_ISTAT 05	NMI_ISTAT 04	NMI_ISTAT 03	NMI_ISTAT 02	NMI_ISTAT 01	NMI_ISTAT 00
2C	OTHER_ISTAT1							
	OTHER_ISTAT17	OTHER_ISTAT16	OTHER_ISTAT15	OTHER_ISTAT14	OTHER_ISTAT13	OTHER_ISTAT12	OTHER_ISTAT11	OTHER_ISTAT10
2D	OTHER_ISTAT0							
	OTHER_ISTAT07	OTHER_ISTAT06	OTHER_ISTAT05	OTHER_ISTAT04	OTHER_ISTAT03	OTHER_ISTAT02	OTHER_ISTAT01	OTHER_ISTAT00
2E	FERR_GLOBAL:B3							
	G_FER _31	G_FER _30	G_FER _29	G_FER _28	G_FER _27	G_FER _26	G_FER _25	G_FER _24
2F	FERR_GLOBAL:B2							
	G_FER _23	G_FER _22	G_FER _21	G_FER _20	G_FER _19	G_FER _18	G_FER _17	G_FER _16
30	FERR_GLOBAL:B1							
	G_FER _15	G_FER _14	G_FER _13	G_FER _12	G_FER _11	G_FER _10	G_FER _09	G_FER _08
31	FERR_GLOBAL:B0							
	G_FER _07	G_FER _06	G_FER _05	G_FER _04	G_FER _03	G_FER _02	G_FER _01	G_FER _00

SSBLOG05-2260

Byte27 = 8E (CHK1)

(2) Byte28 = 30

	0	1	2	3	4	5	6	7
28	Subcode (x'30')							
29	MP_Number							
	MP_Number							
2A	FERR_GLOBAL:B3							
	G_FER _31	G_FER _30	G_FER _29	G_FER _28	G_FER _27	G_FER _26	G_FER _25	G_FER _24
2B	FERR_GLOBAL:B2							
	G_FER _23	G_FER _22	G_FER _21	G_FER _20	G_FER _19	G_FER _18	G_FER _17	G_FER _16
2C	FERR_GLOBAL:B1							
	G_FER _15	G_FER _14	G_FER _13	G_FER _12	G_FER _11	G_FER _10	G_FER _09	G_FER _08
2D	FERR_GLOBAL:B0							
	G_FER _07	G_FER _06	G_FER _05	G_FER _04	G_FER _03	G_FER _02	G_FER _01	G_FER _00
2E	NERR_GLOBAL:B3							
	G_NER _31	G_NER _30	G_NER _29	G_NER _28	G_NER _27	G_NER _26	G_NER _25	G_NER _24
2F	NERR_GLOBAL:B2							
	G_NER _23	G_NER _22	G_NER _21	G_NER _20	G_NER _19	G_NER _18	G_NER _17	G_NER _16
30	NERR_GLOBAL:B1							
	G_NER _15	G_NER _14	G_NER _13	G_NER _12	G_NER _11	G_NER _10	G_NER _09	G_NER _08
31	NERR_GLOBAL:B0							
	G_NER _07	G_NER _06	G_NER _05	G_NER _04	G_NER _03	G_NER _02	G_NER _01	G_NER _00

SSBLOG05-2270

Byte27 = 8E (CHK1)

(3) Byte28 = 40 or 41 or 42 or 43 or 44 or 45 or 50 or FF

	0	1	2	3	4	5	6	7
28	Subcode (x'40' or x'41' or x'42' or x'43' or x'44' or x'45' or x'50' or x'FF')							
29	MP_Number							
	MP_Number							
2A	COREn_ISTAT1							
	I STATE _17	I STATE _16	I STATE _15	I STATE _14	I STATE _13	I STATE _12	I STATE _11	I STATE _10
2B	COREn_ISTAT0							
	I STATE _07	I STATE _06	I STATE _05	I STATE _04	I STATE _03	I STATE _02	I STATE _01	I STATE _00
2C	IA32 THERM STATUS							
	Therm Thres _1 Log	Therm Thres _1 Status	Out of Spec Status_Log	Out of Spec Status	PROC# or FRCEPR# Log	PROC# or FRCEPR# Event	Thermal _Status Log	Thermal _Status
2D	ESR							
	Illegal Reg Adr	Recvd Illegal Vector	Send Illegal Vector	Reserved	Receive Acpt Error	Send Acpt Error	Receive Chk SUM Error	Send Chk SUM Error
2E	FERR_GLOBAL:B3							
	G_FER _31	G_FER _30	G_FER _29	G_FER _28	G_FER _27	G_FER _26	G_FER _25	G_FER _24
2F	FERR_GLOBAL:B2							
	G_FER _23	G_FER _22	G_FER _21	G_FER _20	G_FER _19	G_FER _18	G_FER _17	G_FER _16
30	FERR_GLOBAL:B1							
	G_FER _15	G_FER _14	G_FER _13	G_FER _12	G_FER _11	G_FER _10	G_FER _09	G_FER _08
31	FERR_GLOBAL:B0							
	G_FER _07	G_FER _06	G_FER _05	G_FER _04	G_FER _03	G_FER _02	G_FER _01	G_FER _00

Byte27 = 8E (CHK1)

(4) Byte28 = 47

	0	1	2	3	4	5	6	7
28								
29	MP_Number							
	MP_Number							
2A	P4_PEXLNKSTS:B1							
	Reserved	Reserved	Reserved	SCCON	LNKTRG	TERR	NLNKWD	NLNKWD
2B	P4_PEXLNKSTS:B0							
	NLNKWD	NLNKWD	NLNKWD	NLNKWD	LNKSPD	LNKSPD	LNKSPD	LNKSPD
2C	P5_PEXLNKSTS:B1							
	Reserved	Reserved	Reserved	SCCON	LNKTRG	TERR	NLNKWD	NLNKWD
2D	P5_PEXLNKSTS:B0							
	NLNKWD	NLNKWD	NLNKWD	NLNKWD	LNKSPD	LNKSPD	LNKSPD	LNKSPD
2E	P6_PEXLNKSTS:B1							
	Reserved	Reserved	Reserved	SCCON	LNKTRG	TERR	NLNKWD	NLNKWD
2F	P6_PEXLNKSTS:B0							
	NLNKWD	NLNKWD	NLNKWD	NLNKWD	LNKSPD	LNKSPD	LNKSPD	LNKSPD
30	P7_PEXLNKSTS:B1							
	Reserved	Reserved	Reserved	SCCON	LNKTRG	TERR	NLNKWD	NLNKWD
31	P7_PEXLNKSTS:B0							
	NLNKWD	NLNKWD	NLNKWD	NLNKWD	LNKSPD	LNKSPD	LNKSPD	LNKSPD

SSBLOG05-2290

Byte27 = 8E (CHK1)

(5) Byte28 = 51

	0	1	2	3	4	5	6	7
28	Subcode (x'51')							
29	MP_Number							
	MP_Number							
2A	P4_SECSTS:B1							
	SDPE	SRSE	SRMAS	SRTAS	SSTAS	SDEVT	SDEVT	SMDPERR
2B	P4_SECSTS:B0							
	SFB2BTC	Reserved	S66MHCAP	Reserved	Reserved	Reserved	Reserved	Reserved
2C	P5_SECSTS:B1							
	SDPE	SRSE	SRMAS	SRTAS	SSTAS	SDEVT	SDEVT	SMDPERR
2D	P5_SECSTS:B0							
	SFB2BTC	Reserved	S66MHCAP	Reserved	Reserved	Reserved	Reserved	Reserved
2E	P6_SECSTS:B1							
	SDPE	SRSE	SRMAS	SRTAS	SSTAS	SDEVT	SDEVT	SMDPERR
2F	P6_SECSTS:B0							
	SFB2BTC	Reserved	S66MHCAP	Reserved	Reserved	Reserved	Reserved	Reserved
30	P7_SECSTS:B1							
	SDPE	SRSE	SRMAS	SRTAS	SSTAS	SDEVT	SDEVT	SMDPERR
31	P7_SECSTS:B0							
	SFB2BTC	Reserved	S66MHCAP	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2300

Byte27 = 8E (CHK1)

(6) Byte28 = 60 or 61 or 62 or 63 or 64 or 65 or 66 or 67 or 68

	0	1	2	3	4	5	6	7
28	Subcode (x'60' or x'61' or x'62' or x'63' or x'64' or x'65' or x'66' or x'67' or x'68')							
29	MPA_LSI_REV							
	MPA LSI Revision Number							
2A	MPA_ERRSTS_MPx:B5							
	Not used	Not used	DBG_BLK Warning	R_BLK Warning	D3_BLK Warning	D2_BLK Warning	D1_BLK Warning	D0_BLK Warning
2B	MPA_ERRSTS_MPx:B4							
	M3_BLK Warning	M2_BLK Warning	M1_BLK Warning	M0_BLK Warning	E3_BLK Warning	E2_BLK Warning	E1_BLK Warning	E0_BLK Warning
2C	MPA_ERRSTS_MPx:B3							
	Not used	Not used	DBG_BLK CHK3	R_BLK CHK3	D3_BLK CHK3	D2_BLK CHK3	D1_BLK CHK3	D0_BLK CHK3
2D	MPA_ERRSTS_MPx:B2							
	M3_BLK CHK3	M2_BLK CHK3	M1_BLK CHK3	M0_BLK CHK3	E3_BLK CHK3	E2_BLK CHK3	E1_BLK CHK3	E0_BLK CHK3
2E	MPA_ERRSTS_MPx:B1							
	Not used	Not used	DBG_BLK CHK1B	R_BLK CHK1B	D3_BLK CHK1B	D2_BLK CHK1B	D1_BLK CHK1B	D0_BLK CHK1B
2F	MPA_ERRSTS_MPx:B0							
	M3_BLK CHK1B	M2_BLK CHK1B	M1_BLK CHK1B	M0_BLK CHK1B	E3_BLK CHK1B	E2_BLK CHK1B	E1_BLK CHK1B	E0_BLK CHK1B
30	MPA_MDL_NUM							
	MPA Module Number							
31	MPA_LSI_NUM							
	MPA LSI Number							

SSBLOG05-2310

Byte27 = 8E (CHK1)

(7) Byte28 = 80 or 81

	0	1	2	3	4	5	6	7
28	Subcode (x'80' or x'81')							
29	MP_Number							
	MP_Number							
2A								
	0	0	0	0	0	0	0	0
2B	IA32 MCi STATUS:B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
2C	IA32 MCi STATUS:B1 MCA Error Code							
2D	IA32 MCi STATUS:B0 MCA Error Code							
2E	FERR GLOBAL:B3							
	G_FER _31	G_FER _30	G_FER _29	G_FER _28	G_FER _27	G_FER _26	G_FER _25	G_FER _24
2F	FERR GLOBAL:B2							
	G_FER _23	G_FER _22	G_FER _21	G_FER _20	G_FER _19	G_FER _18	G_FER _17	G_FER _16
30	FERR GLOBAL:B1							
	G_FER _15	G_FER _14	G_FER _13	G_FER _12	G_FER _11	G_FER _10	G_FER _09	G_FER _08
31	FERR GLOBAL:B0							
	G_FER _07	G_FER _06	G_FER _05	G_FER _04	G_FER _03	G_FER _02	G_FER _01	G_FER _00

SSBLOG05-2320

Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'02'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	FERR_NF_FBD :B3							
	Reserved	Reserved	FBD Chan Index	FBD Chan Index	Reserved	Reserved	Reserved	M28 Err
4A	FERR_NF_FBD :B2							
	M27 Err	Unused	Unused	Unused	Unused	M22 Err	M21 Err	M20 Err
4B	FERR_NF_FBD :B1							
	M19 Err	M18 Err	M17 Err	M16 Err	M15 Err	M14 Err	M13 Err	M12 Err
4C	FERR_NF_FBD :B0							
	M11 Err	M10 Err	M9 Err	M8 Err	M7 Err	M6 Err	M5 Err	M4 Err
4D	NERR_NF_FBD :B3							
	Reserved	Reserved	FBD Chan Index	FBD Chan Index	Reserved	Reserved	Reserved	M28 Err
4E	NERR_NF_FBD :B2							
	M27 Err	Unused	Unused	Unused	Unused	M22 Err	M21 Err	M20 Err
4F	NERR_NF_FBD :B1							
	M19 Err	M18 Err	M17 Err	M16 Err	M15 Err	M14 Err	M13 Err	M12 Err
	NERR_NF_FBD :B0							
	M11 Err	M10 Err	M9 Err	M8 Err	M7 Err	M6 Err	M5 Err	M4 Err

SSBLOG05-2330

Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 (2/3)

	0	1	2	3	4	5	6	7
50	VALIDLOG0:B3							
	0	0	0	0	0	0	0	0
51	VALIDLOG0:B2							
	0	0	0	0	0	0	0	0
52	VALIDLOG0:B1							
	0	0	0	0	0	0	0	0
53	VALIDLOG0:B0							
	0	0	0	0	0	REDMEM VALID	RECMEM VALID	NRECMEM VALID
54	VALIDLOG1:B3							
	0	0	0	0	0	0	0	0
55	VALIDLOG1:B2							
	0	0	0	0	0	0	0	0
56	VALIDLOG1:B1							
	0	0	0	0	0	0	0	0
57	VALIDLOG1:B0							
	0	0	0	0	0	REDMEM VALID	RECMEM VALID	NRECMEM VALID
58	RECMEMA0:B3							
	0	0	0	0	0	0	0	0
59	RECMEMA0:B2							
	0	0	0	MERR	MERR	MERR	MERR	MERR
5A	RECMEMA0:B1							
	MERR	BANK	BANK	BANK	0	RANK	RANK	RANK
5B	RECMEMA0:B0							
	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D
5C	RECMEMA1:B3							
	0	0	0	0	0	0	0	0
5D	RECMEMA1:B2							
	0	0	0	MERR	MERR	MERR	MERR	MERR
5E	RECMEMA1:B1							
	MERR	BANK	BANK	BANK	0	RANK	RANK	RANK
5F	RECMEMA1:B0							
	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D
60	RECMEMB0:B3							
	RDWR	0	0	CAS	CAS	CAS	CAS	CAS
61	RECMEMB0:B2							
	CAS	CAS	CAS	CAS	CAS	CAS	CAS	CAS
62	RECMEMB0:B1							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
63	RECMEMB0:B0							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
64	RECMEMB1:B3							
	RDWR	0	0	CAS	CAS	CAS	RDWR	0
65	RECMEMB1:B2							
	CAS	CAS	CAS	CAS	CAS	CAS	CAS	CAS
66	RECMEMB1:B1							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
67	RECMEMB1:B0							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
68	REDMEMA0:B3							
	SYNDROME							
69	REDMEMA0:B2							
	SYNDROME							
6A	REDMEMA0:B1							
	SYNDROME							
6B	REDMEMA0:B0							
	SYNDROME							

SSBLOG05-2340

Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 (3/3)

	0	1	2	3	4	5	6	7
6C	REDMEMA1:B3							
	SYNDROME							
6D	REDMEMA1:B2							
	SYNDROME							
6E	REDMEMA1:B1							
	SYNDROME							
6F	REDMEMA1:B0							
	SYNDROME							
70	REDMEMB0:B3							
	0	0	0	0	0	0	0	0
71	RECMEMB0:B2							
	0	0	0	0	0	0	ECC_Locator	ECC_Locator
72	RECMEMB0:B1							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
73	RECMEMB0:B0							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
74	REDMEMB1:B3							
	0	0	0	0	0	0	0	0
75	RECMEMB1:B2							
	0	0	0	0	0	0	ECC_Locator	ECC_Locator
76	RECMEMB1:B1							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
77	RECMEMB1:B0							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2350

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 or 22 or 23 or 24 or 25 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'03'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	FERR FAT INT							
	Reserved	Reserved	Reserved	Reserved	B4 Err	B3 Err	Reserved	B1 Err
4A	FERR NF INT							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	B5 Err
4B	NERR FAT INT							
	Reserved	Reserved	Reserved	Reserved	B4 Err	B3 Err	Reserved	B1 Err
4C	NERR NF INT							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	B5 Err
4D	P0_PEXFATFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	P0_PEXFATFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	P0_PEXFATFERR :B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
4F	P0_PEXFATFERR :B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2360

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 or 22 or 23 or 24 or 25 (2/3)

	0	1	2	3	4	5	6	7
50	P0_PEXFATNERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	P0_PEXFATNERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	P0_PEXFATNERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
53	P0_PEXFATNERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
54	P0_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	P0_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
56	P0_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
57	P0_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
58	P0_PEXNFCORNERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	P0_PEXNFCORNERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
5A	P0_PEXNFCORNERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
5B	P0_PEXNFCORNERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
5C	D31:F0 PCISTS:B1							
	Detected Parity Err	Signaled System Err	Master Abort Status	Received Target Abort	Signaled Target Abort	DEVSEL Timing STS	DEVSEL Timing STS	Data Parity Err Detected
5D	D31:F0 PCISTS:B0							
	Fast B to B Capable	Reserved	66MHz CAP	Capabilities List	Interrupt Status	Reserved	Reserved	Reserved
5E	D31:F3 PCISTS:B1							
	Detected Parity Err	Signaled System Err	Received Master Abort	Received Target Abort	Signaled Target Abort	DEVSEL Timing STS	DEVSEL Timing STS	Data Parity Err Detected
5F	D31:F3 PCISTS:B0							
	Fast B to B Capable	User Definable Features	66MHz CAP	Capabilities List	Interrupt Status	Reserved	Reserved	Reserved

SSBLOG05-2370

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 or 22 or 23 or 24 or 25 (3/3)

	0	1	2	3	4	5	6	7
60	PLDERR 0							
	PLDERR 07	PLDERR 06	PLDERR 05	PLDERR 04	PLDERR 03	PLDERR 02	PLDERR 01	PLDERR 00
61	PLDERR 1							
	PLDERR 17	PLDERR 16	PLDERR 15	PLDERR 14	PLDERR 13	PLDERR 12	PLDERR 11	PLDERR 10
62	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
70	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2380

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 30 (1/4)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'07'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	P0_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	P0_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
4B	P0_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
4C	P0_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
4D	P2_PEXFATFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	P2_PEXFATFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	P2_PEXFATFERR :B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
	P2_PEXFATFERR :B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2390

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 30 (2/4)

	0	1	2	3	4	5	6	7
50	P2_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	P2_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
52	P2_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
53	P2_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
54	P3_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	P3_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	P3_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
57	P3_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
58	P3_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	P3_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
5A	P3_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
5B	P3_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
5C	P4_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	P4_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	P4_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
5F	P4_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2400

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 30 (3/4)

	0	1	2	3	4	5	6	7
60	P4_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	P4_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
62	P4_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
63	P4_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
64	P5_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	P5_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	P5_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
67	P5_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
68	P5_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	P5_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
6A	P5_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
6B	P5_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
6C	P6_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	P6_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	P6_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
6F	P6_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2410

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 30 (4/4)

	0	1	2	3	4	5	6	7
70	P6_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	P6_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
72	P6_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
73	P6_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
74	P7_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	P7_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	P7_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
77	P7_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
78	P7_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	P7_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
7A	P7_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
7B	P7_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
7C	FERR_CHANERR:B1							
	F_Unsffliated Err DMA15	F_Soft Err DMA14	F_INTConf Err DMA13	F_CompAdr Err DMA12	F_Desc Leng Err DMA11	F_DescCtl Err DMA10	F_Wr DT Err DMA9	F_RdDT Err DMA8
7D	FERR_CHANERR:B0							
	F_DMA DT PErr DMA7	F_CS DT PErr DMA6	F_ChanCMD Err DMA5	F_ChainADR VErr DMA4	F_Desc Err DMA3	F_NDescADR Err DMA2	F_DMA TDest AErr DMA1	F_TSorce AErr DMA0
7E	NERR_CHANERR:B1							
	N_Unsffliated Err DMA15	N_Soft Err DMA14	N_INTConf Err DMA13	N_CompAdr Err DMA12	N_Desc Leng Err DMA11	N_DescCtl Err DMA10	N_Wr DT Err DMA9	N_RdDT Err DMA8
7F	NERR_CHANERR:B0							
	N_DMA DT PErr DMA7	N_CS DT PErr DMA6	N_ChanCMD Err DMA5	N_ChainADR VErr DMA4	N_Desc Err DMA3	N_NDescADR Err DMA2	NDMA TDest AErr DMA1	N_TSorce AErr DMA0

SSBLOG05-2420

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 40 (1/4)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'05'							
	0	0	0	0	0	0	0	0
42	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
45	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
46	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
47	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
48	FERR_NF_FBD :B3							
	Reserved	Reserved	FBD Chan Index	FBD Chan Index	Reserved	Reserved	Reserved	M28 Err
49	FERR_NF_FBD :B2							
	M27 Err	Unused	Unused	Unused	Unused	M22 Err	M21 Err	M20 Err
4A	FERR_NF_FBD :B1							
	M19 Err	M18 Err	M17 Err	M16 Err	M15 Err	M14 Err	M13 Err	M12 Err
4B	FERR_NF_FBD :B0							
	M11 Err	M10 Err	M9 Err	M8 Err	M7 Err	M6 Err	M5 Err	M4 Err
4C	NERR_NF_FBD :B3							
	Reserved	Reserved	FBD Chan Index	FBD Chan Index	Reserved	Reserved	Reserved	M28 Err
4D	NERR_NF_FBD :B2							
	M27 Err	Unused	Unused	Unused	Unused	M22 Err	M21 Err	M20 Err
4E	NERR_NF_FBD :B1							
	M19 Err	M18 Err	M17 Err	M16 Err	M15 Err	M14 Err	M13 Err	M12 Err
4F	NERR_NF_FBD :B0							
	M11 Err	M10 Err	M9 Err	M8 Err	M7 Err	M6 Err	M5 Err	M4 Err

SSBLOG05-2430

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 40 (2/4)

	0	1	2	3	4	5	6	7
50	VALIDLOG0:B3							
	0	0	0	0	0	0	0	0
51	VALIDLOG0:B2							
	0	0	0	0	0	0	0	0
52	VALIDLOG0:B1							
	0	0	0	0	0	0	0	0
53	VALIDLOG0:B0							
	0	0	0	0	0	REDMEM VALID	RECMEM VALID	NRECMEM VALID
54	VALIDLOG1:B3							
	0	0	0	0	0	0	0	0
55	VALIDLOG1:B2							
	0	0	0	0	0	0	0	0
56	VALIDLOG1:B1							
	0	0	0	0	0	0	0	0
57	VALIDLOG1:B0							
	0	0	0	0	0	REDMEM VALID	RECMEM VALID	NRECMEM VALID
58	RECMEMA0:B3							
	0	0	0	0	0	0	0	0
59	RECMEMA0:B2							
	0	0	0	MERR	MERR	MERR	MERR	MERR
5A	RECMEMA0:B1							
	MERR	BANK	BANK	BANK	0	RANK	RANK	RANK
5B	RECMEMA0:B0							
	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D
5C	RECMEMA1:B3							
	0	0	0	0	0	0	0	0
5D	RECMEMA1:B2							
	0	0	0	MERR	MERR	MERR	MERR	MERR
5E	RECMEMA1:B1							
	MERR	BANK	BANK	BANK	0	RANK	RANK	RANK
5F	RECMEMA1:B0							
	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D	DM_BUF_I D

SSBLOG05-2440

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 40 (3/4)

	0	1	2	3	4	5	6	7
60	RECMEMB0:B3							
	RDWR	0	0	CAS	CAS	CAS	CAS	CAS
61	RECMEMB0:B2							
	CAS	CAS	CAS	CAS	CAS	CAS	CAS	CAS
62	RECMEMB0:B1							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
63	RECMEMB0:B0							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
64	RECMEMB1:B3							
	RDWR	0	0	CAS	CAS	CAS	RDWR	0
65	RECMEMB1:B2							
	CAS	CAS	CAS	CAS	CAS	CAS	CAS	CAS
66	RECMEMB1:B1							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
67	RECMEMB1:B0							
	RAS	RAS	RAS	RAS	RAS	RAS	RAS	RAS
68	REDMEMA0:B3							
	SYNDROME							
69	REDMEMA0:B2							
	SYNDROME							
6A	REDMEMA0:B1							
	SYNDROME							
6B	REDMEMA0:B0							
	SYNDROME							
6C	REDMEMA1:B3							
	SYNDROME							
6D	REDMEMA1:B2							
	SYNDROME							
6E	REDMEMA1:B1							
	SYNDROME							
6F	REDMEMA1:B0							
	SYNDROME							

SSBLOG05-2450

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 40 (4/4)

	0	1	2	3	4	5	6	7
70	REDMEMB0:B3							
	0	0	0	0	0	0	0	0
71	RECMEMB0:B2							
	0	0	0	0	0	0	ECC_Locator	ECC_Locator
72	RECMEMB0:B1							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
73	RECMEMB0:B0							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
74	REDMEMB1:B3							
	0	0	0	0	0	0	0	0
75	RECMEMB1:B2							
	0	0	0	0	0	0	ECC_Locator	ECC_Locator
76	RECMEMB1:B1							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
77	RECMEMB1:B0							
	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator	ECC_Locator
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2460

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 (1/4)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'06'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	P0_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	P0_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
4B	P0_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
4C	P0_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
4D	P0_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	P0_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
4F	P0_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
	P0_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0

SSBLOG05-2470

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 (2/4)

	0	1	2	3	4	5	6	7
50	P2_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	P2_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
52	P2_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
53	P2_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
54	P2_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	P2_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
56	P2_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
57	P2_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
58	P3_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	P3_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
5A	P3_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
5B	P3_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
5C	P3_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	P3_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
5E	P3_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
5F	P3_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0

SSBLOG05-2480

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 (3/4)

	0	1	2	3	4	5	6	7
60	P4_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	P4_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
62	P4_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
63	P4_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
64	P4_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	P4_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
66	P4_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
67	P4_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
68	P5_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	P5_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
6A	P5_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
6B	P5_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
6C	P5_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	P5_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
6E	P5_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
6F	P5_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0

SSBLOG05-2490

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 (4/4)

	0	1	2	3	4	5	6	7
70	P6_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	P6_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
72	P6_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
73	P6_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
74	P6_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	P6_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
76	P6_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
77	P6_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
78	P7_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	P7_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
7A	P7_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
7B	P7_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
7C	P7_PEXNFCORNERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	P7_PEXNFCORNERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
7E	P7_PEXNFCORNERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
7F	P7_PEXNFCORNERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0

SSBLOG05-2500

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 43 or 44 or 45 or 46 or 47 or 50 or 51 (1/4)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'07'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	P0_PEXNFCORFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	P0_PEXNFCORFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
4B	P0_PEXNFCORFERR :B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
4C	P0_PEXNFCORFERR :B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
4D	P2_PEXFATFERR :B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	P2_PEXFATFERR :B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	P2_PEXFATFERR :B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
	P2_PEXFATFERR :B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2510

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 43 or 44 or 45 or 46 or 47 or 50 or 51 (2/4)

	0	1	2	3	4	5	6	7
50	P2_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	P2_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
52	P2_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
53	P2_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
54	P3_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	P3_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	P3_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
57	P3_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
58	P3_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	P3_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
5A	P3_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
5B	P3_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
5C	P4_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	P4_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	P4_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
5F	P4_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2520

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 43 or 44 or 45 or 46 or 47 or 50 or 51 (3/4)

	0	1	2	3	4	5	6	7
60	P4_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	P4_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
62	P4_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
63	P4_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
64	P5_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	P5_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	P5_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
67	P5_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
68	P5_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	P5_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
6A	P5_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
6B	P5_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
6C	P6_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	P6_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	P6_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
6F	P6_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0

SSBLOG05-2530

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 43 or 44 or 45 or 46 or 47 or 50 or 51 (4/4)

	0	1	2	3	4	5	6	7
70	P6_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	P6_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
72	P6_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
73	P6_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
74	P7_PEXFATFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	P7_PEXFATFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	P7_PEXFATFERR:B1							
	Reserved	Reserved	Reserved	F_FAT Err 19	F_FAT Err 18	F_FAT Err 9	F_FAT Err 10	F_FAT Err 8
77	P7_PEXFATFERR:B0							
	F_FAT Err 7	F_FAT Err 6	F_FAT Err 5	F_FAT Err 4	F_FAT Err 3	F_FAT Err 2	F_FAT Err 1	F_FAT Err 0
78	P7_PEXNFCORFERR:B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	P7_PEXNFCORFERR:B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	F_NFAT CORErr 19	F_NFAT CORErr 17	F_NFAT CORErr 16
7A	P7_PEXNFCORFERR:B1							
	F_NFAT CORErr 15	F_NFAT CORErr 14	F_NFAT CORErr 13	F_NFAT CORErr 12	F_NFAT CORErr 11	F_NFAT CORErr 10	F_NFAT CORErr 9	F_NFAT CORErr 8
7B	P7_PEXNFCORFERR:B0							
	F_NFAT CORErr 7	F_NFAT CORErr 6	F_NFAT CORErr 5	F_NFAT CORErr 4	F_NFAT CORErr 3	F_NFAT CORErr 2	F_NFAT CORErr 1	F_NFAT CORErr 0
7C	FERR_CHANERR:B1							
	F_Unsffliated Err DMA15	F_Soft Err DMA14	F_INTConf Err DMA13	F_CompAdr Err DMA12	F_Desc Leng Err DMA11	F_DescCtl Err DMA10	F_Wr DT Err DMA9	F_RdDT Err DMA8
7D	FERR_CHANERR:B0							
	F_DMA DT PErr DMA7	F_CS DT PErr DMA6	F_ChanCMD Err DMA5	F_ChainADR VErr DMA4	F_Desc Err DMA3	F_NDescADR Err DMA2	F_DMA TDest AErr DMA1	F_TSorce AErr DMA0
7E	NERR_CHANERR:B1							
	N_Unsffliated Err DMA15	N_Soft Err DMA14	N_INTConf Err DMA13	N_CompAdr Err DMA12	N_Desc Leng Err DMA11	N_DescCtl Err DMA10	N_Wr DT Err DMA9	N_RdDT Err DMA8
7F	NERR_CHANERR:B0							
	N_DMA DT PErr DMA7	N_CS DT PErr DMA6	N_ChanCM D Err DMA5	N_ChainAD R VErr DMA4	N_Desc Err DMA3	N_NDescADR Err DMA2	NDMA TDest AErr DMA1	N_TSorce AErr DMA0

SSBLOG05-2540

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 60 or 61 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::E_ERRSTS_MPxB1							
	E_BLK PCI_250M CHK1B	E_BLK PCI_125M CHK1B	Not used	Not used	Not used	E_BLK Error10 CHK1B	E_BLK Error9 CHK1B	E_BLK Error8 CHK1B
41	MPA::E_ERRSTS_MPxB0							
	E_BLK Error7 CHK1B	E_BLK Error6 CHK1B	E_BLK Error5 CHK1B	E_BLK Error4 CHK1B	E_BLK Error3 CHK1B	E_BLK Error2 CHK1B	E_BLK Error1 CHK1B	E_BLK Error0 CHK1B
42	MPA::E_ERRSTS_MPxB3							
	Not used	Not used	Not used	Not used	Not used	E_BLK Error10 CHK3	E_BLK Error9 CHK3	E_BLK Error8 CHK3
43	MPA::E_ERRSTS_MPxB2							
	E_BLK Error7 CHK3	E_BLK Error6 CHK3	E_BLK Error5 CHK3	E_BLK Error4 CHK3	E_BLK Error3 CHK3	E_BLK Error2 CHK3	E_BLK Error1 CHK3	E_BLK Error0 CHK3
44	MPA::E_ERRSTS_MPxB5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
45	MPA::E_ERRSTS_MPxB4							
	E_BLK Error7 Warning	E_BLK Error6 Warning	E_BLK Error5 Warning	E_BLK Error4 Warning	E_BLK Error3 Warning	E_BLK Error2 Warning	E_BLK Error1 Warning	E_BLK Error0 Warning
46	MPA::E_ERRDTL00_CHK1B:B6							
	Not used	Not used	Not used	ESEQ04 Grant Error_P	Not used	Not used	Not used	ESEQ04 Grant Error_P
47	MPA::E_ERRDTL00_CHK1B:B5							
	Not used	ESEQ04 Decode Error_P	ESEQ04 Encode Error_P	ESEQ04 Parity Error_P	ESEQ04 Compare Error	ESEQ04 Decode Error_D	ESEQ04 Encode Error_D	ESEQ04 Parity Error_D
48	MPA::E_ERRDTL00_CHK1B:B4							
	Not used	ESEQ03 Decode Error_P	ESEQ03 Encode Error_P	ESEQ03 Parity Error_P	ESEQ03 Compare Error	ESEQ03 Decode Error_D	ESEQ03 Encode Error_D	ESEQ03 Parity Error_D
49	MPA::E_ERRDTL00_CHK1B:B3							
	ESEQ02 Path Select Paradox	ESEQ02 Decode Error_P	ESEQ02 Encode Error_P	ESEQ02 Parity Error_P	ESEQ02 Compare Error	ESEQ02 Decode Error_D	ESEQ02 Encode Error_D	ESEQ02 Parity Error_D
4A	MPA::E_ERRDTL00_CHK1B:B2							
	ESEQ01 State Error	ESEQ01 Decode Error_P	ESEQ01 Encode Error_P	ESEQ01 Parity Error_P	ESEQ01 Compare Error	ESEQ01 Decode Error_D	ESEQ01 Encode Error_D	ESEQ01 Parity Error_D
4B	MPA::E_ERRDTL00_CHK1B:B1							
	Not used	ESEQ00 Decode Error_P	ESEQ00 Encode Error_P	ESEQ00 Parity Error_P	ESEQ00 Compare Error	ESEQ00 Decode Error_D	ESEQ00 Encode Error_D	ESEQ00 Parity Error_D
4C	MPA::E_ERRDTL01_CHK1B:B3							
	Not used	Not used	E_Timer04 Parity Error	E_Timer03 Parity Error	E_Timer02 Parity Error	E_Timer01 Parity Error	E_Timer00 Packet Full Parity	E_Timer00 Parity Error
4D	MPA::E_ERRDTL01_CHK1B:B2							
	Not used	Not used	ECNT04 MSI Timer Parity	ECNT04 Data Counter Parity Error	ECNT03 Data Counter Parity Error	ECNT02 Data Counter Parity Error	ECNT01 Data Counter Parity Error	ECNT00 Data Counter Parity Error
4E	MPA::E_ERRDTL01_CHK1B:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	EARB Comapare Error
4F	MPA::E_ERRDTL01_CHK1B:B0							
	EARB Grant Error_P	EARB Decode Error_P	EARB Encode Error_P	EARB Parity Error_P	EARB Grant Error_D	EARB Decode Error_D	EARB Encode Error_D	EARB Parity Error_D

SSBLOG05-2550

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 60 or 61 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::E_ERRDTL02_CHK1B:B1							
	Not used	Not used	Not used	Not used	Write FIFO Internal CRC Error	Write FIFO Parity Error	Read FIFO Internal CRC Error	Read FIFO Parity Error
51	MPA::E_ERRDTL02_CHK1B:B0							
	Not used	EREG02 Data Parity Error	EREG01 Packet Flag Parity Error	EREG01 Data Parity Error	EREG00 NXT2DT Parity Error	EREG00 NXTDT Parity Error	EREG00 Before FF Parity Error	EREG00 Data Parity Error
52	MPA::E_ERRDTL03_CHK1B:B1							
	Not used	ETXTCNT Read Parity Error	ERXTCNT Read Parity Error	ETMR04 Read Parity Error	ETMR03 Read Parity Error	ETMR02 Read Parity Error	ETMR01 Read Parity Error	ETMR00 Read Parity Error
53	MPA::E_ERRDTL03_CHK1B:B0							
	ETBL Read Parity Error	ECNT04 Read Parity Error	ECNT03 Read Parity Error	ECNT02 Read Parity Error	ECNT01 Read Parity Error	ECNT00 Read Parity Error	ECNVREG Read Parity Error	L_LOG Read Parity Error
54	MPA::E_ERRDTL04_CHK1B:B4							
	Not used	Not used	Not used	PXTB125 Read Parity Error	Not used	Not used	Not used	PXRB125 Read Parity Error
55	MPA::E_ERRDTL04_CHK1B:B3							
	Not used	SEQT0M Read Parity Error	SEQT0S Read Parity Error	SEQT0C Read Parity Error	Not used	SEQR0M Read Parity Error	SEQR0S Read Parity Error	SEQR0C Read Parity Error
56	MPA::E_ERRDTL04_CHK1B:B2							
	Not used	PXTB Read Parity Error	PXTC Read Parity Error	PXTH Read Parity Error	Not used	PXRB Read Parity Error	PXRC Read Parity Error	PXRH Read Parity Error
57	MPA::E_ERRDTL04_CHK1B:B1							
	Not used	KADEC03 Decode Error_P	KADEC03 Encode Error_P	KADEC03 Parity Error_P	KADEC03 Compare Error	KADEC03 Decode Error_D	KADEC03 Encode Error_D	KADEC03 Parity Error_D
58	MPA::E_ERRDTL04_CHK1B:B0							
	Not used	KADEC02 Decode Error_P	KADEC02 Encode Error_P	KADEC02 Parity Error_P	KADEC02 Compare Error	KADEC02 Decode Error_D	KADEC02 Encode Error_D	KADEC02 Parity Error_D
59	MPA::E_ERRDTL05_CHK1B:B3							
	ESEQ04 R_END Error_P	ESEQ04 R_STP Error_P	ESEQ04 W_END Error_P	ESEQ04 W_STP Error_P	ESEQ04 R_END Error_D	ESEQ04 R_STP Error_D	ESEQ04 W_END Error_D	ESEQ04 W_STP Error_D
5A	MPA::E_ERRDTL05_CHK1B:B2							
	Not used	Not used	ESEQ03 END Error_P	ESEQ03 STP Error_P	Not used	Not used	ESEQ03 END Error_D	ESEQ03 STP Error_D
5B	MPA::E_ERRDTL05_CHK1B:B1							
	Not used	Not used	ESEQ02 END Error_P	ESEQ02 STP Error_P	Not used	Not used	ESEQ02 END Error_D	ESEQ02 STP Error_D
5C	MPA::E_ERRDTL05_CHK1B:B0							
	Not used	ESEQ01 Grant Error_P	ESEQ01 END Error_P	ESEQ01 STP Error_P	Not used	ESEQ01 Grant Error_D	ESEQ01 END Error_D	ESEQ01 STP Error_D
5D	MPA::E_ERRDTL06_CHK1B:B7							
	Not used	Write FIFO WFCNT Parity Error	Write FIFO Packet Flag RD_PT Parity	Write FIFO Packet Flag WT_PT Parity	Write FIFO RD_PT Parity Error	Write FIFO WT_PT Parity Error	Write FIFO DIFF Counter Parity Error	Write FIFO Read Parity Error
5E	MPA::E_ERRDTL06_CHK1B:B6							
	Not used	Not used	Not used	Not used	Read FIFO RD_PT Parity Error	Read FIFO WT_PT Parity Error	Read FIFO DIFF Counter Parity Error	Read FIFO Read Parity Error
5F	MPA::E_ERRDTL06_CHK1B:B5							
	Not used	Not used	EREGX DIFF Counter Parity Error	EREGX Read Parity Error	Not used	Not used	EREG04 Read Parity Error	EREG04 DIFF Counter Parity Error

SSBLOG05-2560

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 60 or 61 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::E_ERRDTL06_CHK1B:B4							
	Not used	Not used	ERE02 Packet Flag RD_PT Parity	ERE02 Packet Flag WT_PT Parity	ERE02 RD_PT Parity Error	ERE02 WT_PT Parity Error	ERE02 DIFF Counter Parity Error	ERE02 Read Parity Error
61	MPA::E_ERRDTL06_CHK1B:B3							
	Not used	Not used	ERE01 Packet Flag RD_PT Parity	ERE01 Packet Flag WT_PT Parity	ERE01 RD_PT Parity Error	ERE01 WT_PT Parity Error	ERE01 DIFF Counter Parity Error	ERE01 Read Parity Error
62	MPA::E_ERRDTL06_CHK1B:B2							
	Not used	Not used	ERE00 Packet Flag RD_PT Parity	ERE00 Packet Flag WT_PT Parity	ERE00 RD_PT Parity Error	ERE00 WT_PT Parity Error	ERE00 DIFF Counter Parity Error	ERE00 Read Parity Error
63	MPA::E_ERRDTL06_CHK1B:B1							
	Not used	Not used	Not used	ERE02 Packet Flag Error	Not used	Not used	Not used	ERE01 HSN_CMD Error
64	MPA::E_ERRDTL06_CHK1B:B0							
	Not used	Not used	ERE00 Payload Length Over	ERE00 Payload Length Parity	ERE00 ENDOFF Error	ERE00 ENDON Error	ERE00 STPOFF Error	ERE00 STPON Error
65	MPA::E_ERRDTL07_CHK1B:B7							
	Not used	Not used	ERE01 No Read Completion	ERE01 Invalid Packet Error	ERE01 Invalid DST MP3	ERE01 Invalid DST MP2	ERE01 Invalid DST MP1	ERE01 Invalid DST MP0
66	MPA::E_ERRDTL09_CHK1B:B2							
	Not used	ETMR04 Read Ready Time Out	ETMR04 Write Ready Time Out	ETMR02 Read Ready Time Out	Not used	Not used	Not used	ETMR02 WT_RDY TOV D_BLK
67	MPA::E_ERRDTL09_CHK1B:B1							
	ETMR02 WT_RDY TOV E3_BLK	ETMR02 WT_RDY TOV E2_BLK	ETMR02 WT_RDY TOV E1_BLK	ETMR02 WT_RDY TOV E0_BLK	ETMR02 WT_RDY TOV M3BLK	ETMR02 WT_RDY TOV M2BLK	ETMR02 WT_RDY TOV M1BLK	ETMR02 WT_RDY TOV M0BLK
68	MPA::E_ERRDTL09_CHK1B:B0							
	Not used	Not used	Not used	ETMR00 Read Ready Time Out	Not used	ETMR03 Sequencer Time Out	ETMR01 Sequencer Time Out	ETMR00 Packet Full Time Out
69	MPA::E_ERRDTL10_CHK1B:B0							
	Not used	ETBL01 Information Parity Error	ETBL01 Timer Parity Error	ETBL01 Time Out	Not used	ETBL00 Information Parity Error	ETBL00 Timer Parity Error	ETBL00 Time Out
6A	MPA::E_ERRDTL_PCIE125:B2							
	Not used	Not used	Not used	Not used	Not used	MPXTB Read Parity Error	MPXTB Error	MPXTB HWERR Error
6B	MPA::E_ERRDTL_PCIE125:B1							
	Not used	Not used	Not used	Not used	Not used	MPXRB Read Parity Error	MPXRB Error	MPXRB HWERR Error
6C	MPA::E_ERRDTL_PCIE125:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MPXRB FATAL Error
6D	MPA::E_ERRDTL_PCIE250:B7							
	ESEQY Read Parity Error	MPXTB Read Parity Error	MPXTC Read Parity Error	MPXTH Read Parity Error	ESEQX Read Parity Error	MPXRB Read Parity Error	MPXRC Read Parity Error	MPXRHL Read Parity Error
6E	MPA::E_ERRDTL_PCIE250:B6							
	Not used	Not used	ESEQY_C Error	ESEQY_M Error	ESEQY_S Error	MPXTB Error	MPXTC Error	MPXTH Error
6F	MPA::E_ERRDTL_PCIE250:B5							
	Not used	Not used	ESEQX_C Error	ESEQX_M Error	ESEQX_S Error	MPXRB Error	MPXRC Error	MPXRH Error

SSBLOG05-2570

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 60 or 61 (4/4)

	0	1	2	3	4	5	6	7
70	MPA::E_ERRDTL_PCIE250:B4							
	Not used	Not used	ESEQY_C HWERR Error	ESEQY_M HWERR Error	ESEQY_S HWERR Error	MPXTB HWERR Error	MPXTC HWERR Error	Not used
71	MPA::E_ERRDTL_PCIE250:B3							
	Not used	Not used	ESEQX_C HWERR Error	ESEQX_M HWERR Error	ESEQX_S HWERR Error	MPXRB HWERR Error	MPXRC HWERR Error	Not used
72	MPA::E_ERRDTL_PCIE250:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	MPXTC MICRO Error	MPXRC Path Error
73	MPA::E_ERRDTL_PCIE250:B1							
	Not used	Not used	MPXTHL TXVC Error_B	MPXTHL RXVC Error_B	MPXTHL DLPL Error_B	MPXRHL TXVC Error_B	MPXRHL RXVC Error_B	MPXRHL DLPL Error_B
74	MPA::E_ERRDTL_PCIE250:B0							
	Not used	Not used	MPXTHL TXVC Error_A	MPXTHL RXVC Error_A	MPXTHL DLPL Error_A	MPXRHL TXVC Error_A	MPXRHL RXVC Error_A	MPXRHL DLPL Error_A
75	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2580

Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 62 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::D_ERRSTS_MPx:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 CHK1B	D_BLK Error8 CHK1B
41	MPA::D_ERRSTS_MPx:B0							
	D_BLK Error7 CHK1B	D_BLK Error6 CHK1B	D_BLK Error5 CHK1B	D_BLK Error4 CHK1B	D_BLK Error3 CHK1B	D_BLK Error2 CHK1B	D_BLK Error1 CHK1B	D_BLK Error0 CHK1B
42	MPA::M_ERRSTS_MPx:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 CHK3	D_BLK Error8 CHK3
43	MPA::D_ERRSTS_MPx:B2							
	D_BLK Error7 CHK3	D_BLK Error6 CHK3	D_BLK Error5 CHK3	D_BLK Error4 CHK3	D_BLK Error3 CHK3	D_BLK Error2 CHK3	D_BLK Error1 CHK3	D_BLK Error0 CHK3
44	MPA::D_ERRSTS_MPx:B5							
	Not used	Not used	Not used	Not used	Not used	Not used	D_BLK Error9 Warning	D_BLK Error8 Warning
45	MPA::D_ERRSTS_MPx:B4							
	D_BLK Error7 Warning	D_BLK Error6 Warning	D_BLK Error5 Warning	D_BLK Error4 Warning	D_BLK Error3 Warning	D_BLK Error2 Warning	D_BLK Error1 Warning	D_BLK Error0 Warning
46	MPA::D_ERRDTL00_CHK1B:B7							
	DSEQ02 TLP END Error_P	DSEQ02 TLP STP Error_P	DSEQ02 TLP END Error_D	DSEQ02 TLP STP Error_D	DSEQ02 Path Select Paradox	DSEQ02 Decode Error_P	DSEQ02 Encode Error_P	DSEQ02 Parity Error_P
47	MPA::D_ERRDTL00_CHK1B:B6							
	DSEQ02 Compare Error	DSEQ02 Decode Error_D	DSEQ02 Encode Error_D	DSEQ02 Parity Error_D	Not used	Not used	DSEQ01 Grant Error_P	DSEQ01 Grant Error_D
48	MPA::D_ERRDTL00_CHK1B:B5							
	Not used	DSEQ01 Decode Error_P	DSEQ01 Encode Error_P	DSEQ01 Parity Error_P	DSEQ01 Compare Error	DSEQ01 Decode Error_D	DSEQ01 Encode Error_D	DSEQ01 Parity Error_D
49	MPA::D_ERRDTL00_CHK1B:B4							
	Not used	DSEQ00 Decode Error_P	DSEQ00 Encode Error_P	DSEQ00 Parity Error_P	DSEQ00 Compare Error	DSEQ00 Decode Error_D	DSEQ00 Encode Error_D	DSEQ00 Parity Error_D
4A	MPA::D_ERRDTL00_CHK1B:B3							
	Not used	DSEQ0x_MP3 Decode Error_P	DSEQ0x_MP3 Encode Error_P	DSEQ0x_MP3 Parity Error_P	DSEQ0x_MP3 Compare Error	DSEQ0x_MP3 Decode Error_D	DSEQ0x_MP3 Encode Error_D	DSEQ0x_MP3 Parity Error_D
4B	MPA::D_ERRDTL00_CHK1B:B2							
	Not used	DSEQ0x_MP2 Decode Error_P	DSEQ0x_MP2 Encode Error_P	DSEQ0x_MP2 Parity Error_P	DSEQ0x_MP2 Compare Error	DSEQ0x_MP2 Decode Error_D	DSEQ0x_MP2 Encode Error_D	DSEQ0x_MP2 Parity Error_D
4C	MPA::D_ERRDTL00_CHK1B:B1							
	Not used	DSEQ0x_MP1 Decode Error_P	DSEQ0x_MP1 Encode Error_P	DSEQ0x_MP1 Parity Error_P	DSEQ0x_MP1 Compare Error	DSEQ0x_MP1 Decode Error_D	DSEQ0x_MP1 Encode Error_D	DSEQ0x_MP1 Parity Error_D
4D	MPA::D_ERRDTL00_CHK1B:B0							
	Not used	DSEQ0x_MP0 Decode Error_P	DSEQ0x_MP0 Encode Error_P	DSEQ0x_MP0 Parity Error_P	DSEQ0x_MP0 Compare Error	DSEQ0x_MP0 Decode Error_D	DSEQ0x_MP0 Encode Error_D	DSEQ0x_MP0 Parity Error_D
4E	MPA::D_ERRDTL01_CHK1B:B2							
	DCNT02 Data Count Parity Error	DCNT01 LM Write Count Parity Error	DCNT01 Data Count Parity Error	DCNT00 Data Count Parity Error	DTMR02 Timer Parity Error	DTMR01 RX Timer Parity Error	DTMR01 Timer Parity Error	DTMR00 Timer Parity Error
4F	MPA::D_ERRDTL01_CHK1B:B1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	DARB Compare Error

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Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 62 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::D_ERRDTL01_CHK1B:B0							
	DARB Grant Error_P	DARB Decode Error_P	DARB Encode Error_P	DARB Parity Error_P	DARB Grant Error_D	DARB Decode Error_D	DARB Encode Error_D	DARB Parity Error_D
51	MPA::D_ERRDTL02_CHK1B:B7							
	Not used	DREG0X STWT_ADR Low MP3	DREG0X STWT_ADR Up MP3	DREG0X STWT_ADR BoundaryMP3	Not used	DREG0X Base ADR Low MP3	DREG0X Base ADR Up MP3	DREG0X Base ADR BoundaryMP3
52	MPA::D_ERRDTL02_CHK1B:B6							
	Not used	DREG0X STWT_ADR Low MP2	DREG0X STWT_ADR Up MP2	DREG0X STWT_ADR BoundaryMP2	Not used	DREG0X Base ADR Low MP2	DREG0X Base ADR Up MP2	DREG0X Base ADR BoundaryMP2
53	MPA::D_ERRDTL02_CHK1B:B5							
	Not used	DREG0X STWT_ADR Low MP1	DREG0X STWT_ADR Up MP1	DREG0X STWT_ADR BoundaryMP1	Not used	DREG0X Base ADR Low MP1	DREG0X Base ADR Up MP1	DREG0X Base ADR BoundaryMP1
54	MPA::D_ERRDTL02_CHK1B:B4							
	Not used	DREG0X STWT_ADR Low MP0	DREG0X STWT_ADR Up MP0	DREG0X STWT_ADR BoundaryMP0	Not used	DREG0X Base ADR Low MP0	DREG0X Base ADR Up MP0	DREG0X Base ADR BoundaryMP0
55	MPA::D_ERRDTL02_CHK1B:B3							
	DREG0X DIF Counter Parity_P MP3	DREG0X DIF Counter Parity_D MP3	DREG0X PI Write Count Full MP3	DREG0X PI Write Count Parity MP3	DREG0X PI Parity Error MP3	DREG0X CI Parity Error MP3	DREG0X PI Inclement Error MP3	DREG0X PI Pass Error MP3
56	MPA::D_ERRDTL02_CHK1B:B2							
	DREG0X DIF Counter Parity_P MP2	DREG0X DIF Counter Parity_D MP2	DREG0X PI Write Count Full MP2	DREG0X PI Write Count Parity MP2	DREG0X PI Parity Error MP2	DREG0X CI Parity Error MP2	DREG0X PI Inclement Error MP2	DREG0X PI Pass Error MP2
57	MPA::D_ERRDTL02_CHK1B:B1							
	DREG0X DIF Counter Parity_P MP1	DREG0X DIF Counter Parity_D MP1	DREG0X PI Write Count Full MP1	DREG0X PI Write Count Parity MP1	DREG0X PI Parity Error MP1	DREG0X CI Parity Error MP1	DREG0X PI Inclement Error MP1	DREG0X PI Pass Error MP1
58	MPA::D_ERRDTL02_CHK1B:B0							
	DREG0X DIF Counter Parity_P MP0	DREG0X DIF Counter Parity_D MP0	DREG0X PI Write Count Full MP0	DREG0X PI Write Count Parity MP0	DREG0X PI Parity Error MP0	DREG0X CI Parity Error MP0	DREG0X PI Inclement Error MP0	DREG0X PI Pass Error MP0
59	MPA::D_ERRDTL03_CHK1B:B7							
	Not used	Not used	Not used	DADEC Read Parity Error	Not used	Not used	Not used	DCNT02 Read Parity Error
5A	MPA::D_ERRDTL03_CHK1B:B6							
	Not used	Not used	Not used	DCNT01 Read Parity Error	Not used	Not used	Not used	DCNT00 Read Parity Error
5B	MPA::D_ERRDTL03_CHK1B:B5							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	DTMR02 Read Parity Error
5C	MPA::D_ERRDTL03_CHK1B:B4							
	Not used	Not used	Not used	DTMR01 Read Parity Error	Not used	Not used	Not used	DTMR00 Read Parity Error
5D	MPA::D_ERRDTL03_CHK1B:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	DREG02 Read Parity Error
5E	MPA::D_ERRDTL03_CHK1B:B2							
	Not used	Not used	Not used	DREG01 Read Parity Error	Not used	Not used	Not used	DREG00 Read Parity Error
5F	MPA::D_ERRDTL03_CHK1B:B1							
	Not used	Not used	Not used	DREG0XMP3 Read Parity Error	Not used	Not used	Not used	DREG0XMP2 Read Parity Error

SSBLOG05-2600

Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 62 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::D_ERRDTL03_CHK1B:B0							
	Not used	Not used	Not used	DREG0XMP1 Read Parity Error	Not used	Not used	Not used	DREG0XMP 0 Read Parity Error
61	MPA::D_ERRDTL04_CHK1B:B3							
	Not used	Not used	DREG02 Packet Flag RD_PT Parity	DREG02 Packet Flag WT_PT Parity	DREG02 RD_PT Parity Error	DREG02 WT_PT Parity Error	DREG02 DIFF Counter Parity Error	DREG02 Read Parity Error
62	MPA::D_ERRDTL04_CHK1B:B2							
	Not used	DREG01 Status Pointer Parity Error	DREG01 Packet Flag RD_PT Parity	DREG01 Packet Flag WT_PT Parity	DREG01 RD_PT Parity Error	DREG01 WT_PT Parity Error	DREG01 DIFF Counter Parity Error	DREG01 Read Parity Error
63	MPA::D_ERRDTL04_CHK1B:B1							
	Not used	DREG00 RD_PT Parity Error	DREG00 WT_PT Parity Error	DREG00 Data Parity Error	DREG01 Multi Completion	DREG00 Function# Unmatch	DREG00 TAG# Unmatch	DREG00 TAG Count Parity Error
64	MPA::D_ERRDTL04_CHK1B:B0							
	P6P7 Parity Error	P4P5 Parity Error	P2P3 Parity Error	P0P1 Parity Error	DREG00 Flag Error	DREG00 PCRC Error	DREG00 END Error	DREG00 STP Error
65	MPA::D_ERRDTL08_CHK1B:B1							
	DTMR02 WTRDY Timeout E3	DTMR02 WTRDY Timeout E2	DTMR02 WTRDY Timeout E1	DTMR02 WTRDY Timeout E0	DTMR02 WTRDY Timeout M3	DTMR02 WTRDY Timeout M2	DTMR02 WTRDY Timeout M1	DTMR02 WTRDY Timeout M0
66	MPA::D_ERRDTL08_CHK1B:B0							
	Not used	Not used	DTMR01 RXWAIT Time Out	DTMR01 WTRDY Time Out	Not used	Not used	Not used	DTMR00 RDRDY Time Out
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	MPA::DSX0_CST:B4							
	DSEQ00_P Current State							
69	MPA::DSX0_CST:B0							
	DSEQ00_D Current State							
6A	MPA::DSX1_CST:B4							
	DSEQ01_P Current State							
6B	MPA::DSX1_CST:B0							
	DSEQ01_D Current State							
6C	MPA::DSX2_CST:B4							
	DSEQ02_P Current State							
6D	MPA::DSX2_CST:B0							
	DSEQ02_D Current State							
6E	MPA::DSX3_CST:B4							
	DSEQ0X_MP0_P Current State							
6F	MPA::DSX3_CST:B0							
	DSEQ0X_MP0_D Current State							

SSBLOG05-2610

Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 62 (4/4)

	0	1	2	3	4	5	6	7
70	MPA::DS0_CST:B4							
	DSEQ0X_MP1_P Current State							
71	MPA::DS0_CST:B0							
	DSEQ0X_MP1_D Current State							
72	MPA::DS1_CST:B4							
	DSEQ0X_MP2_P Current State							
73	MPA::DS1_CST:B0							
	DSEQ0X_MP2_D Current State							
74	MPA::DS2_CST:B4							
	DSEQ0X_MP3_P Current State							
75	MPA::DS2_CST:B0							
	DSEQ0X_MP3_D Current State							
76	MPA::DA_CST:B4							
	DARB_P Current State							
77	MPA::DA_CST:B0							
	DARB_D Current State							
78	MPA::DR1_DMAPROG:B3							
	Not used	Not used	Not used	Not used	Not used	DMA Progress Status Write End	DMA Progress Status Write Transmit	DMA Progress Status Write Start
79	MPA::DR1_DMAPROG:B2							
	DMA Progress LM Write All End	DMA Progress LM Write End	DMA Progress LM Write Transmit	DMA Progress LM Write Start	DMA Progress CM Read All End	DMA Progress CM Read Receive	DMA Progress CM Read Transmit	DMA Progress CM Read Start
7A	MPA::DR1_DMAPROG:B1							
	DMA Progress LR Write All End	DMA Progress LR Write End	DMA Progress LR Write Transmit	DMA Progress LR Write Start	DMA Progress CM Write All End	DMA Progress CMWT Res. Receive	DMA Progress CM Write Transmit	DMA Progress CM Write Start
7B	MPA::DR1_DMAPROG:B0							
	DMA Progress LM Read All End	DMA Progress LM Read Receive	DMA Progress LM Read Transmit	DMA Progress LM Read Start	DMA Progress Param. Read All End	DMA Progress Param. Read Receive	DMA Progress Param. Read Transmit	DMA Progress Param. Read Start
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2620

Byte27 = 8E (CHK1) Byte40-7F Detail

(9) Byte28 = 63 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::R_ERRSTS_MPX:B0							
	Not used	Not used	Not used	R_BLK SIP_Error CHK1B	R_BLK Error3 CHK1B	R_BLK Error2 CHK1B	R_BLK Error1 CHK1B	R_BLK Error0 CHK1B
41	MPA::R_ERRSTS_MPX:B2							
	Not used	Not used	Not used	R_BLK SIP_Error CHK3	R_BLK Error3 CHK3	R_BLK Error2 CHK3	R_BLK Error1 CHK3	R_BLK Error0 CHK3
42	MPA::R_ERRSTS_MPX:B4							
	Not used	Not used	Not used	R_BLK SIP_Error Warning	R_BLK Error3 Warning	R_BLK Error2 Warning	R_BLK Error1 Warning	R_BLK Error0 Warning
43	MPA::R_ERRDTL00_CHK1B:B7							
	Not used	Not used	SYSTM 36M Timer Parity Error	SYSTM 125M Timer Parity Error	Not used	Side Band Scan Timer1 Parity Error	Side Band Scan Timer0 Parity Error	Side Band Timer Parity Error
44	MPA::R_ERRDTL00_CHK1B:B6							
	Not used	Not used	Not used	RTMR02 Parity Error	Not used	Not used	Not used	RTMR01 Parity Error
45	MPA::R_ERRDTL00_CHK1B:B5							
	Not used	Not used	Not used	Not used	MP Serial 3 Parity Error	MP Serial 2 Parity Error	MP Serial 1 Parity Error	MP Serial 0 Parity Error
46	MPA::R_ERRDTL00_CHK1B:B4							
	SBSEQ Path Select Paradox	SBSEQ Decode Error_P	SBSEQ Endcode Error_P	SBSEQ Parity Error_P	SBSEQ Compare Error	SBSEQ Decode Error_D	SBSEQ Endcode Error_D	SBSEQ Parity Error_D
47	MPA::R_ERRDTL00_CHK1B:B3							
	RSEQ02 Path Select Paradox	RSEQ02 Decode Error_P	RSEQ02 Endcode Error_P	RSEQ02 Parity Error_P	RSEQ02 Compare Error	RSEQ02 Decode Error_D	RSEQ02 Endcode Error_D	RSEQ02 Parity Error_D
48	MPA::R_ERRDTL00_CHK1B:B2							
	RSEQ01 Path Select Paradox	RSEQ01 Decode Error_P	RSEQ01 Endcode Error_P	RSEQ01 Parity Error_P	RSEQ01 Compare Error	RSEQ01 Decode Error_D	RSEQ01 Endcode Error_D	RSEQ01 Parity Error_D
49	MPA::R_ERRDTL00_CHK1B:B1							
	Not used	Not used	Not used	Not used	Not used	SBSEQ Grant Error	Not used	RARB Compare Error
4A	MPA::R_ERRDTL00_CHK1B:B0							
	RARB Grant Error_P	RARB Decode Error_P	RARB Endcode Error_P	RARB Parity Error_P	RARB Grant Error_D	RARB Decode Error_D	RARB Endcode Error_D	RARB Parity Error_D
4B	MPA::R_ERRDTL01_CHK1B:B7							
	Not used	Not used	Not used	Not used	Not used	SBTMR Scan Poiling Time Out	SBTMR Scan Read Time Out	SBTMR Scan Write Time Out
4C	MPA::R_ERRDTL01_CHK1B:B6							
	Not used	Not used	Not used	RTMR02 WTRDY Timeout DBG	RTMR02 WTRDY Timeout E3	RTMR02 WTRDY Timeout E2	RTMR02 WTRDY Timeout E1	RTMR02 WTRDY Timeout E0
4D	MPA::R_ERRDTL01_CHK1B:B5							
	Not used	Not used	RTMR01 ROSC Time Out	RTMR01 Side Band Time Out	RTMR01 WTRDY Time Out	RTMR01 System Timer Time Out	RTMR01 I2C Time Out	RTMR01 Core I/F Time Out
4E	MPA::R_ERRDTL01_CHK1B:B4							
	Not used	Not used	Not used	RCNT02 Counter Parity Error	Not used	Not used	Not used	Register Read Path Data Parity Error
4F	MPA::R_ERRDTL01_CHK1B:B3							
	RREG02 Write Pointer Parity Error	RREG02 Internal CRC Error	RREG02 FIFO5 Parity Error	RREG02 FIFO4 Parity Error	RREG02 FIFO3 Parity Error	RREG02 FIFO2 Parity Error	RREG02 FIFO1 Parity Error	RREG02 FIFO0 Parity Error

Byte27 = 8E (CHK1) Byte40-7F Detail

(9) Byte28 = 63 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::R_ERRDTL01_CHK1B:B2							
	Not used	Not used	RREG01 Packet Flag RD_PT Parity	RREG01 Packet Flag WT_PT Parity	RREG01 RD_PT Parity Error	RREG01 WT_PT Parity Error	RREG01 DIFF Counter Parity Error	RREG01 Read Parity Error
51	MPA::R_ERRDTL01_CHK1B:B1							
	Not used	Not used	Not used	Not used	RSEQ02 TLP END Error_P	RSEQ02 TLP STP Error_P	RSEQ02 TLP END Error_D	RSEQ02 TLP STP Error_D
52	MPA::R_ERRDTL01_CHK1B:B0							
	Not used	Not used	Not used	Not used	RSEQ01 TLP END Error_P	RSEQ01 TLP STP Error_P	RSEQ01 TLP END Error_D	RSEQ01 TLP STP Error_D
53	MPA::R_ERRDTL02_CHK1B:B7							
	Not used	Not used	Not used	SYSTM Read Parity Error	Not used	Not used	Not used	SBTMR Read Parity Error
54	MPA::R_ERRDTL02_CHK1B:B6							
	Not used	Not used	Not used	RTMR02 Read Parity Error	Not used	Not used	Not used	RTMR01 Read Parity Error
55	MPA::R_ERRDTL02_CHK1B:B5							
	Not used	Not used	KRGBUF Read Parity Error	CRGBUF Read Parity Error	MPSER3 Read Parity Error	MPSER2 Read Parity Error	MPSER1 Read Parity Error	MPSER0 Read Parity Error
56	MPA::R_ERRDTL02_CHK1B:B4							
	Not used	Not used	Not used	RCNT02 Read Parity Error	Not used	Not used	Not used	Not used
57	MPA::R_ERRDTL02_CHK1B:B3							
	Illegal Address MP15	Illegal Address MP14	Illegal Address MP13	Illegal Address MP12	Illegal Address MP11	Illegal Address MP10	Illegal Address MP09	Illegal Address MP08
58	MPA::R_ERRDTL02_CHK1B:B2							
	Illegal Address MP07	Illegal Address MP06	Illegal Address MP05	Illegal Address MP04	Illegal Address MP03	Illegal Address MP02	Illegal Address MP01	Illegal Address MP00
59	MPA::R_ERRDTL02_CHK1B:B1							
	Not used	Not used	Not used	RREG02 Read Parity Error	Not used	Not used	Not used	RREG01 Read Parity Error
5A	MPA::R_ERRDTL02_CHK1B:B0							
	Not used	Not used	Not used	MSELDEC Read Parity Error	Not used	Not used	Not used	RADEC Read Parity Error
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	MPA::RS1_CST:B5							
	RSEQ01_P Previous State							
5D	MPA::RS1_CST:B4							
	RSEQ01_P Current State							
5E	MPA::RS1_CST:B1							
	RSEQ01_D Previous State							
5F	MPA::RS1_CST:B0							
	RSEQ01_D Current State							

SSBLOG05-2640

Byte27 = 8E (CHK1) Byte40-7F Detail

(9) Byte28 = 63 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::RS2_CST:B5							
	RSEQ02_P Previous State							
61	MPA::RS2_CST:B4							
	RSEQ02_P Current State							
62	MPA::RS2_CST:B1							
	RSEQ02_D Previous State							
63	MPA::RS2_CST:B0							
	RSEQ02_D Current State							
64	MPA::SBS_CST:B5							
	SBSEQ_P Previous State							
65	MPA::SBS_CST:B4							
	SBSEQ_P Current State							
66	MPA::SBS_CST:B1							
	SBSEQ_D Previous State							
67	MPA::SBS_CST:B0							
	SBSEQ_D Current State							
68	MPA::RA_CST:B5							
	RARB_P Previous State							
69	MPA::RA_CST:B4							
	RARB_P Current State							
6A	MPA::RA_CST:B1							
	RARB_D Previous State							
6B	MPA::RA_CST:B0							
	RARB_D Current State							
6C	MPA::SPMSEQ_CST:B5							
	SPMSEQ_P Previous State							
6D	MPA::SPMSEQ_CST:B4							
	SPMSEQ_P Current State							
6E	MPA::SPMSEQ_CST:B1							
	SPMSEQ_D Previous State							
6F	MPA::SPMSEQ_CST:B0							
	SPMSEQ_D Current State							

SSBLOG05-2650

Byte27 = 8E (CHK1) Byte40-7F Detail

(9) Byte28 = 63 (4/4)

	0	1	2	3	4	5	6	7
70	MPA::SB0_RXSEQ_CST:B5							
	SB0_RXSEQ Previous State							
71	MPA::SB0_RXSEQ_CST:B4							
	SB0_RXSEQ Current State							
72	MPA::SB0_RXSEQ_CST:B1							
	SB0_SEQ Previous State							
73	MPA::SB0_RXSEQ_CST:B0							
	SB0_SEQ Current State							
74	MPA::SB1_RXSEQ_CST:B5							
	SB1_RXSEQ Previous State							
75	MPA::SB1_RXSEQ_CST:B4							
	SB1_RXSEQ Current State							
76	MPA::SB1_RXSEQ_CST:B1							
	SB1_SEQ Previous State							
77	MPA::SB1_RXSEQ_CST:B0							
	SB1_SEQ Current State							
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2660

Byte27 = 8E (CHK1) Byte40-7F Detail

(10) Byte28 = 64 or 65 or 66 or 67 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::M_ERRSTS_MPx:B0							
	M_BLK Error7 CHK1B	M_BLK Error6 CHK1B	M_BLK Error5 CHK1B	M_BLK Error4 CHK1B	M_BLK Error3 CHK1B	M_BLK Error2 CHK1B	M_BLK Error7 CHK1B	M_BLK Error6 CHK1B
41	MPA::M_ERRSTS_MPx:B3							
	M_BLK PCI_250M CHK3	M_BLK PCI_125M CHK3	Not used	Not used	Not used	Not used	Not used	Not used
42	MPA::M_ERRSTS_MPx:B2							
	M_BLK Error7 CHK3	M_BLK Error6 CHK3	M_BLK Error5 CHK3	M_BLK Error4 CHK3	M_BLK Error3 CHK3	M_BLK Error2 CHK3	M_BLK Error1 CHK3	M_BLK Error0 CHK3
43	MPA::M_ERRSTS_MPx:B5							
	M_BLK PCI_250M Warning2	M_BLK PCI_250M Warning1	M_BLK PCI_250M Warning0	M_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
44	MPA::M_ERRSTS_MPx:B4							
	M_BLK Error7 Warning	M_BLK Error6 Warning	M_BLK Error5 Warning	M_BLK Error4 Warning	M_BLK Error3 Warning	M_BLK Error2 Warning	M_BLK Error7 Warning	M_BLK Error6 Warning
45	MPA::M_ERRDTL00_CHK1B:B4							
	Not used	Not used	MSEQ02 TLP END Error_P	MSEQ02 TLP STP Error_P	Not used	Not used	MSEQ02 TLP END Error_D	MSEQ02 TLP STP Error_D
46	MPA::M_ERRDTL00_CHK1B:B3							
	Not used	MSEQ03 Decode Error_P	MSEQ03 Encode Error_P	MSEQ03 Parity Error_P	MSEQ03 Compare Error	MSEQ03 Decode Error_D	MSEQ03 Encode Error_D	MSEQ03 Parity Error_D
47	MPA::M_ERRDTL00_CHK1B:B2							
	MSEQ02 Path Select Paradox	MSEQ02 Decode Error_P	MSEQ02 Encode Error_P	MSEQ02 Parity Error_P	MSEQ02 Compare Error	MSEQ02 Decode Error_D	MSEQ02 Encode Error_D	MSEQ02 Parity Error_D
48	MPA::M_ERRDTL00_CHK1B:B1							
	Not used	Not used	Not used	MSEQ01 Grant Error_P	Not used	Not used	Not used	MSEQ01 Grant Error_D
49	MPA::M_ERRDTL00_CHK1B:B0							
	Not used	MSEQ01 Decode Error_P	MSEQ01 Encode Error_P	MSEQ01 Parity Error_P	MSEQ01 Compare Error	MSEQ01 Decode Error_D	MSEQ01 Encode Error_D	MSEQ01 Parity Error_D
4A	MPA::M_ERRDTL01_CHK1B:B1							
	Not used	MCNT03 Counter Parity Error	MCNT02 Counter Parity Error	MCNT01 Counter Parity Error	MTMR03 Timer Parity Error	MTMR02 Timer Parity Error	MTMR01 Timer Parity Error	MARB Compare Error
4B	MPA::M_ERRDTL01_CHK1B:B0							
	MARB Grant Error_P	MARB Decode Error_P	MARB Encode Error_P	MARB Parity Error_P	MARB Grant Error_D	MARB Decode Error_D	MARB Encode Error_D	MARB Parity Error_D
4C	MPA::M_ERRDTL02_CHK1B:B7							
	Not used	MREG03 SEQ# Parity MDL0 CMA1	MREG03 SEQ# Parity MDL0 CMA0	MREG03 SEQ# Parity MDL0 LR4	MREG03 SEQ# Parity MDL0 LR3	MREG03 SEQ# Parity MDL0 LR2	MREG03 SEQ# Parity MDL0 LR1	MREG03 SEQ# Parity MDL0 LR0
4D	MPA::M_ERRDTL02_CHK1B:B6							
	MREG03 Internal CRC Error	MREG03 Payload Length	Not used	Not used	MREG03 End Off Error	MREG03 End On Error	MREG03 STP Off Error	Not used
4E	MPA::M_ERRDTL02_CHK1B:B5							
	Not used	Not used	MREG02 Packet Flag RD_PT Parity	MREG02 Packet Flag WT_PT Parity	MREG02 Read Pointer Parity	MREG02 Write Pointer Parity	MREG02 Diff. Counter Parity Error	MREG02 Data Parity Error
4F	MPA::M_ERRDTL02_CHK1B:B4							
	Not used	Not used	Not used	MREG01 SEQ# Parity MDL0 LR4	MREG01 SEQ# Parity MDL0 LR3	MREG01 SEQ# Parity MDL0 LR2	MREG01 SEQ# Parity MDL0 LR1	MREG01 SEQ# Parity MDL0 LR0

SSBLOG05-2670

Byte27 = 8E (CHK1) Byte40-7F Detail

(10) Byte28 = 64 or 65 or 66 or 67 (2/4)

	0	1	2	3	4	5	6	7
50	MPA::M_ERRDTL02_CHK1B:B3							
	Not used	Not used	Not used	MREG01 SEQ# MDL0 LR4	MREG01 SEQ# MDL0 LR3	MREG01 SEQ# MDL0 LR2	MREG01 SEQ# MDL0 LR1	MREG01 SEQ# MDL0 LR0
51	MPA::M_ERRDTL02_CHK1B:B2							
	MREG01 Table Data ACS Paradox	MREG01 Round Robin Illegal	MREG01 Before FF Parity Error	MREG01 Paloal Length Error	MREG01 Core I/F Flag Error	MREG01 No Wait ACK Error	MREG01 HSN Command	MREG01 REQ and DST Unmatch
52	MPA::M_ERRDTL02_CHK1B:B1							
	MREG01 HCRC Error	MREG01 PCRC Error	MREG01 Packet Flag RD_PT Parity	MREG01 Packet Flag WT_PT Parity	MREG01 Read Pointer Parity	MREG01 Write Pointer Parity	MREG01 Diff. Counter Parity Error	MREG01 Data Parity Error
53	MPA::M_ERRDTL02_CHK1B:B0							
	MREG01 End Off Error	MREG01 End On Error	MREG01 STP Off Error	MREG01 STP On Error	Not used	Not used	Not used	MREGX Diff. Counter Parity Error
54	MPA::M_ERRDTL03_CHK1B:B7							
	MREG03 SEQ# Parity MDL1 LM	MREG03 SEQ# Parity MDL1 CMA1	MREG03 SEQ# Parity MDL1 CMA0	MREG03 SEQ# Parity MDL1 LR4	MREG03 SEQ# Parity MDL1 LR3	MREG03 SEQ# Parity MDL1 LR2	MREG03 SEQ# Parity MDL1 LR1	MREG03 SEQ# Parity MDL1 LR0
55	MPA::M_ERRDTL03_CHK1B:B4							
	Not used	Not used	MREG01 SEQ# Parity MDL1 LM	MREG01 SEQ# Parity MDL1 LR4	MREG01 SEQ# Parity MDL1 LR3	MREG01 SEQ# Parity MDL1 LR2	MREG01 SEQ# Parity MDL1 LR1	MREG01 SEQ# Parity MDL1 LR0
56	MPA::M_ERRDTL03_CHK1B:B3							
	Not used	Not used	MREG01 SEQ# MDL1 LM	MREG01 SEQ# MDL1 LR4	MREG01 SEQ# MDL1 LR3	MREG01 SEQ# MDL1 LR2	MREG01 SEQ# MDL1 LR1	MREG01 SEQ# MDL1 LR0
57	MPA::M_ERRDTL03_CHK1B:B1							
	Not used	Not used	MTX Stat. Counter Read Parity	MRX Stat. Counter Read Parity	MCNT03 Read Parity Error	MCNT02 Read Parity Error	MCNT01 Read Parity Error	MTMR03 Read Parity Error
58	MPA::M_ERRDTL03_CHK1B:B0							
	MTMR02 Read Parity Error	MTMR01 Read Parity Error	MTBL03 Read Parity Error	MREGX Read Parity Error	MREG03 Read Parity Error	MREG02 Read Parity Error	MREG01 Read Parity Error	MADEC Read Parity Error
59	MPA::M_ERRDTL05_CHK1B:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	MTMR03 RDRDY Time Out	MTMR03 WTRDY Time Out
5A	MPA::M_ERRDTL05_CHK1B:B1							
	MTMR02 WTRDY Time Out E3	MTMR02 WTRDY Time Out E2	MTMR02 WTRDY Time Out E1	MTMR02 WTRDY Time Out E0	MTMR02 WTRDY Time Out D3	MTMR02 WTRDY Time Out D2	MTMR02 WTRDY Time Out D1	MTMR02 WTRDY Time Out D0
5B	MPA::M_ERRDTL05_CHK1B:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	MTMR01 RDRDY Time Out	MTMR01 WTRDY Time Out
5C	MPA::M_ERRDTL06_CHK1B:B7							
	Not used	MTBL03 Time Out TBL_1E	MTBL03 Time Out TBL_1D	MTBL03 Time Out TBL_1C	MTBL03 Time Out TBL_1B	MTBL03 Time Out TBL_1A	MTBL03 Time Out TBL_19	MTBL03 Time Out TBL_18
5D	MPA::M_ERRDTL06_CHK1B:B6							
	MTBL03 Time Out TBL_17	MTBL03 Time Out TBL_16	MTBL03 Time Out TBL_15	MTBL03 Time Out TBL_14	Not used	Not used	Not used	Not used
5E	MPA::M_ERRDTL06_CHK1B:B5							
	MTBL03 Time Out TBL_0F	MTBL03 Time Out TBL_0E	MTBL03 Time Out TBL_0D	MTBL03 Time Out TBL_0C	MTBL03 Time Out TBL_0B	MTBL03 Time Out TBL_0A	MTBL03 Time Out TBL_09	MTBL03 Time Out TBL_08
5F	MPA::M_ERRDTL06_CHK1B:B4							
	MTBL03 Time Out TBL_07	MTBL03 Time Out TBL_06	MTBL03 Time Out TBL_05	MTBL03 Time Out TBL_04	MTBL03 Time Out TBL_03	MTBL03 Time Out TBL_02	MTBL03 Time Out TBL_01	MTBL03 Time Out TBL_00

SSBLOG05-2680

Byte27 = 8E (CHK1) Byte40-7F Detail

(10) Byte28 = 64 or 65 or 66 or 67 (3/4)

	0	1	2	3	4	5	6	7
60	MPA::M_ERRDTL06_CHK1B:B3							
	Not used	MTBL03 Timer Parity TBL_1E	MTBL03 Time Parity TBL_1D	MTBL03 Time Parity TBL_1C	MTBL03 Time Parity TBL_1B	MTBL03 Time Parity TBL_1A	MTBL03 Time Parity TBL_19	MTBL03 Time Parity TBL_18
61	MPA::M_ERRDTL06_CHK1B:B2							
	MTBL03 Time Parity TBL_17	MTBL03 Time Parity TBL_16	MTBL03 Time Parity TBL_15	MTBL03 Time Parity TBL_14	Not used	Not used	Not used	Not used
62	MPA::M_ERRDTL06_CHK1B:B1							
	MTBL03 Time Parity TBL_0F	MTBL03 Timer Parity TBL_0E	MTBL03 Time Parity TBL_0D	MTBL03 Time Parity TBL_0C	MTBL03 Time Parity TBL_0B	MTBL03 Time Parity TBL_0A	MTBL03 Time Parity TBL_09	MTBL03 Time Parity TBL_08
63	MPA::M_ERRDTL06_CHK1B:B0							
	MTBL03 Time Parity TBL_07	MTBL03 Timer Parity TBL_06	MTBL03 Time Parity TBL_05	MTBL03 Time Parity TBL_04	MTBL03 Time Parity TBL_03	MTBL03 Time Parity TBL_02	MTBL03 Time Parity TBL_01	MTBL03 Time Parity TBL_00
64	MPA::HRX_CST:B5							
	SEQR0 Previous State Monitor 1							
65	MPA::HRX_CST:B4							
	SEQR0 Current State Monitor 1							
66	MPA::HRX_CST:B1							
	SEQR0 Previous State Monitor 0							
67	MPA::HRX_CST:B0							
	SEQR0 Current State Monitor 0							
68	MPA::MS1_CST:B5							
	MSEQ1_P Previous State							
69	MPA::MS1_CST:B4							
	MSEQ1_P Current State							
6A	MPA::MS1_CST:B1							
	MSEQ1_D Previous State							
6B	MPA::MS1_CST:B0							
	MSEQ1_D Current State							
6C	MPA::MS2_CST:B5							
	MSEQ2_P Previous State							
6D	MPA::MS2_CST:B4							
	MSEQ2_P Current State							
6E	MPA::MS2_CST:B1							
	MSEQ2_P Previous State							
6F	MPA::MS2_CST:B0							
	MSEQ2_P Current State							

SSBLOG05-2690

Byte27 = 8E (CHK1) Byte40-7F Detail

(10) Byte28 = 64 or 65 or 66 or 67 (4/4)

	0	1	2	3	4	5	6	7
70	MPA::MS3_CST:B5							
	MSEQ3_P Previous State							
71	MPA::MS3_CST:B4							
	MSEQ3_P Current State							
72	MPA::MS3_CST:B1							
	MSEQ3_D Previous State							
73	MPA::MS3_CST:B0							
	MSEQ3_D Current State							
74	MPA::HTX_CST:B5							
	SEQT0 Previous State Monitor 1							
75	MPA::HTX_CST:B4							
	SEQT0 Current State Monitor 1							
76	MPA::HTX_CST:B1							
	SEQT0 Previous State Monitor 0							
77	MPA::HTX_CST:B0							
	SEQT0 Current State Monitor 0							
78	MPA::MA_CST:B5							
	MARB_P Previous State							
79	MPA::MA_CST:B4							
	MARB_P Current State							
7A	MPA::MA_CST:B1							
	MARB_D Previous State							
7B	MPA::MA_CST:B0							
	MARB_D Current State							
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2700

Byte27 = 8E (CHK1) Byte40-7F Detail

(11) Byte28 = 80 or 81 (1/2)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT NUMBER							
41	'08'							
42	0	0	0	0	0	0	0	0
43	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
44	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
45	NERR_GLOBAL :B3							
	G_NER_ 31	G_NER_ 30	G_NER_ 29	G_NER_ 28	G_NER_ 27	G_NER_ 26	G_NER_ 25	G_NER_ 24
46	NERR_GLOBAL :B2							
	G_NER_ 23	G_NER_ 22	G_NER_ 21	G_NER_ 20	G_NER_ 19	G_NER_ 18	G_NER_ 17	G_NER_ 16
47	NERR_GLOBAL :B1							
	G_NER_ 15	G_NER_ 14	G_NER_ 13	G_NER_ 12	G_NER_ 11	G_NER_ 10	G_NER_ 09	G_NER_ 08
48	NERR_GLOBAL :B0							
	G_NER_ 07	G_NER_ 06	G_NER_ 05	G_NER_ 04	G_NER_ 03	G_NER_ 02	G_NER_ 01	G_NER_ 00
49	IA32_MC0_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
50	IA32_MC0_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
4A	IA32_MC0_STATUS :B5 Other Information							
4B	IA32_MC0_STATUS :B4 Other Information							
4C	IA32_MC0_STATUS :B3 Model Specific Error Code							
4D	IA32_MC0_STATUS :B2 Model Specific Error Code							
4E	IA32_MC0_STATUS :B1 MCA Error Code							
4F	IA32_MC0_STATUS :B0 MCA Error Code							
51	IA32_MC1_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
52	IA32_MC1_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
53	IA32_MC1_STATUS :B5 Other Information							
54	IA32_MC1_STATUS :B4 Other Information							
55	IA32_MC1_STATUS :B3 Model Specific Error Code							
56	IA32_MC1_STATUS :B2 Model Specific Error Code							
57	IA32_MC1_STATUS :B1 MCA Error Code							
58	IA32_MC1_STATUS :B0 MCA Error Code							
59	IA32_MC2_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
60	IA32_MC2_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
61	IA32_MC2_STATUS :B5 Other Information							
62	IA32_MC2_STATUS :B4 Other Information							
63	IA32_MC2_STATUS :B3 Model Specific Error Code							
64	IA32_MC2_STATUS :B2 Model Specific Error Code							
65	IA32_MC2_STATUS :B1 MCA Error Code							
66	IA32_MC2_STATUS :B0 MCA Error Code							

SSBLOG05-2710

Byte27 = 8E (CHK1) Byte40-7F Detail

(11) Byte28 = 80 or 81 (2/2)

	0	1	2	3	4	5	6	7
60	IA32_MC3_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
61	IA32_MC3_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
62	IA32_MC3_STATUS :B5 Other Information							
63	IA32_MC3_STATUS :B4 Other Information							
64	IA32_MC3_STATUS :B3 Model Specific Error Code							
65	IA32_MC3_STATUS :B2 Model Specific Error Code							
66	IA32_MC3_STATUS :B1 MCA Error Code							
67	IA32_MC3_STATUS :B0 MCA Error Code							
68	IA32_MC4_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
69	IA32_MC4_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
6A	IA32_MC4_STATUS :B5 Other Information							
6B	IA32_MC4_STATUS :B4 Other Information							
6C	IA32_MC4_STATUS :B3 Model Specific Error Code							
6D	IA32_MC4_STATUS :B2 Model Specific Error Code							
6E	IA32_MC4_STATUS :B1 MCA Error Code							
6F	IA32_MC4_STATUS :B0 MCA Error Code							
70	IA32_MC5_STATUS :B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Reserved
71	IA32_MC5_STATUS :B6							
	Reserved	Reserved	Threshold-b Err status	Threshold-b Err status	Other Information			
72	IA32_MC5_STATUS :B5 Other Information							
73	IA32_MC5_STATUS :B4 Other Information							
74	IA32_MC5_STATUS :B3 Model Specific Error Code							
75	IA32_MC5_STATUS :B2 Model Specific Error Code							
76	IA32_MC5_STATUS :B1 MCA Error Code							
77	IA32_MC5_STATUS :B0 MCA Error Code							
78	FERR_FAT_FSB0							
	Reserved	Reserved	F9 Error	Reserved	F2 Error	Reserved	Reserved	F1 Error
79	FERR_NF_FSB0							
	Reserved	Reserved	Reserved	Reserved	Reserved	F7Err	F8Err	F6Err
7A	FERR_FAT_FSB0							
	Reserved	Reserved	F9 Error	Reserved	F2 Error	Reserved	Reserved	F1 Error
7B	NERR_NF_FSB0							
	Reserved	Reserved	Reserved	Reserved	Reserved	F7Err	F8Err	F6Err
7C	NERR_FAT_FSB1							
	Reserved	Reserved	F9 Error	Reserved	F2 Error	Reserved	Reserved	F1 Error
7D	FERR_NF_FSB1							
	Reserved	Reserved	Reserved	Reserved	Reserved	F7Err	F8Err	F6Err
7E	NERR_FAT_FSB1							
	Reserved	Reserved	F9 Error	Reserved	F2 Error	Reserved	Reserved	F1 Error
7F	NERR_NF_FSB1							
	Reserved	Reserved	Reserved	Reserved	Reserved	F7Err	F8Err	F6Err

SSBLOG05-2720

Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PKinformation VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	Controller No.	DKU physical device No.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

*1: Failed processor number for WCHK1

SSBLOG05-2721

Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-2730

Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (1/3)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	PCB type		Processor ID					
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21 22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used(x'0')							
29	DCN status (ldcnstsls)							
	Force device busy	Channel connect request	Command chain execute	Path reserve	Stack status	Sense pending	Long Busy (Command process)	Processor connect
2A	DCN status (ldcnstsls)							
	Single path mode	Garant path mode	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	Garant path MP
2B	DCN flag 1 (ldcnflglc)							
	RST NTF/ RST ALG SSB pending	Pin Data pending	(RSV)	(RSV)	ODE pending	RSQ pending	SCI/SCE pending	PCH pending
2C	LCT initiation cause (lctsfcs)							
	0:Ready 1:Not ready	0:Disable 1:Enable	0:No 64SNS 1:64SNS pending	0: Mount 1:No mount	0:No RST ALG LPN 1:Exist	0:No SYS ALG LPN 1:Exist	0:No Stack 1:Exist	0:No Wait SNS 1:Exist
2D	LCT initiation cause (lctsfcs)							
	0:No Wait-RI 1:Exist	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	0:Others 1:DEV BSY JOB

SSBLOG05-2740**Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (2/3)**

	0	1	2	3	4	5	6	7
2E	Command interface flag (lctfigc)							
	Pending exist	SCE(RSP)	(RSV)	(RSV)	ODE pending	RSG pending	SCI pending	PCH pending
2F	Command code							
30	Internal SSB log number							
31								
32	Related reset detailed log number							
33								
34	PCB number				Message code (x'1')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT(DKA x'00'))							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								
60								
61	wchklmpbitmap (from LCT)							
62								
63								

SSBLOG05-2750

Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (3/3)

	0	1	2	3	4	5	6	7
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	EXITFLAG AREA							
6D								
6E								
6F								
70	DCN (wldcn1l)							
71								
72								
73								
74	DCN (wldcn2l)							
75								
76								
77								
78	RIW							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

SSBLOG05-2760

Byte27 = 8E & Byte34:bit4-7=3 (wait SENSE Timeout) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used(x'00')							
29	LDEV#							
2A	24 Byte type	Basic SSB (*1)			32 Byte type	Basic SSB (*1)		
2B								
2C						Exception code (*1)		
2D		F/M (*1)						
2E		System error code (*1)				System error code (*1)		
2F								
30	Reset internal SSB number							
31								
32	Detail log number for RESET							
33								
34	PCB number				Message code (x'3')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							

*1: Timeout SSB information

SSBLOG05-2770

Byte27 = 8E & Byte34:bit4-7=3 (wait SENSE Timeout) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used (x'00')							
41								
42								
43								
44								
45								
46								
47								
48								
4F								
50								
51								
52								
53								
54								
55								
56	LPN							
57								
58	Not used (x'00')							
5F								
60	Timeout SSB							
61								
62								
63								
64								
65								
66								
67								
68								
6F								
70								
7F								

SSBLOG05-2780

Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset) (1/3)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Related internal SSB log number (for error support)							
29								
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (Interruption source address)							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Related RESET detail log number							
33								
34	PCB number				Message code (x'5')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							

SSBLOG05-2790

Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2791

Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	DKCMAIN program counter							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

SSBLOG05-2800

Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset) (1/3)

	0	1	2	3	4	5	6	7
00 	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW(Interruption source address)							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'6')			
35	SSID (Low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							

SSBLOG05-2810**Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset) (2/3)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2811

Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
74								
77	DKCMAIN program counter							
78								
7B	SLV-DCN (slvdcnr)							
7C								
7F								

SSBLOG05-2820

Byte27 = 8E & Byte34:bit4-7=8 (Force Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (Interruption source address)							
2D								
2E								
2F								

SSBLOG05-2830**Byte27 = 8E & Byte34:bit4-7=8 (Force Reset) (2/3)**

	0	1	2	3	4	5	6	7
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'8')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2831

Byte27 = 8E & Byte34:bit4-7=8 (Force Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchkmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	DKCMAIN program counter							
7B	SLV-DCN (slvdcnr)							
7C								
7F								

SSBLOG05-2840

Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09	JCB#							
0A								
0B	REQ ID							
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13	CDEV#							
14								
15	RDEV#							
16	SSB log number							
17	Related failure log number							
18								
19	Related SIM log number							
1A								
1B	Related SSB log number							
1C								
1D	Reserved							
1E								
1F	SCB (x'37')							
20	Basic SSB							
21								
22	Count							
23								
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29								
2A								
2B	Old PSW (Interruption source address)							
2C								
2D								
2E								
2F								

SSBLOG05-2850**Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset) (2/3)**

	0	1	2	3	4	5	6	7
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'9')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2851

Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchkmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	DKCMAIN program counter							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

SSBLOG05-2860

Byte27 = 8F (Logical Inconsistency) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PKinformation VLD	Reserved
0F	SEMI-SYNC	RCU SIM	reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'F')			
28	Module ID							
29	Routine ID							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								

SSBLOG05-2870**Byte27 = 8F (Logical Inconsistency) (2/2)**

	0	1	2	3	4	5	6	7
34	PCB number				Message code (x'00')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type =DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-2880

Byte27 = 9F (Trace data/Log data) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PKinformation VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'9')				Message (x'F')			
28	Module ID (*2)							
29	Routine ID (*2)							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number				Message code (x'00')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF9F')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					

Byte27 = 9F (Trace data/Log data) (2/2)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

*1:

- (1) The pair of system error code x'AE5A' (Drive not Ready) and x'A772' are reported even when one of the following processes has completed successfully. These indicate the PDEV spin down executed correctly and do not mean the error condition. Please ignore them.
 - PDEV Replace
 - DKU Inline execution at Installation or at PDEV expansion.
- (2) System error code x'AE5B' (Unit Attention) is reported even after one of the following processes has completed successfully. It indicates the PDEV was reset correctly and does not mean the error condition. Please ignore it.
 - DKA Replace
 - DKA expansion
 - PDEV expansion
 - MP PCB micro-program exchange
 - PDEVmicro-program exchange

*2: The details information of every Module ID & Routine ID

- Module ID & Routine ID=0xC3A5: The Specified R-VOL of TC-MF is still Online from the HOST.
(Add Pair operation from the host was rejected)

Check the SSB.LOG on SVP of the specified MCU. SSB containing EC=C3A5 on the specified R-VOL number should be logged.

(1) Analyze the SSB.LOG

Refer to the case of Byte27=0X(X≠8/A) for the way of tracing an on-line host.

But, only the Byte position of BitMap information is different. Refer to Byte100(0x64)-103(0x67) of SSB for the BitMap information.

*3: System error code x'507F' is LOG which shows that the function of MODE277 is not effective. PS OFF/ON is required in order to confirm the function of MODE277.

*4: In case of Mode 449 ON at the primary subsystem of UR, if the secondary subsystem is powered off or the path between primary and secondary subsystem is blocked, error code DB00 might be displayed on primary subsystem.

These error codes, which are generated when the communication between primary and secondary subsystem fails, doesn't indicate any failures.

Please ignore them.

SSBLOG05-2900

Byte27 = EC (Environment Monitor Detected DKC Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PKinformation VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'E')				Message (x'C')			
28	Error detection code (*1)							
29	Error detection code (*2)							
2A	Error reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number (don't care)				PCB number (don't care)			
35	SSID (low) of self subsystem (don't care)							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

SSBLOG05-2910

Byte27 = EC (Environment Monitor Detected DKC Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
7F								

*1: Error Detection Code

Code	Detection
10	DKCMN1 (DKC-0)
20	DKCMN2 (DKC-0)
11	DKCMN1 (DKC-1)
21	DKCMN2 (DKC-1)
30	SSVP
00	Others

*2: Error Component Code

Code	Component
10	Logic 1 (DKC-0)
11	Logic 2 (DKC-0)
12	Logic 1 (DKC-1)
13	Logic 2 (DKC-1)
A0	DKCMN1 (DKC-0)
A1	DKCMN2 (DKC-0)
A2	SSVP
A4	DKCMN1 (DKC-1)
A5	DKCMN2 (DKC-1)
A3	others

*3: Error Reason Code (1/2)

Code	Reason
10	Outside (intake) temperature alarm (high temperature) is detected.
11	Outside (intake) temperature warning (high temperature) is detected.
12	Outside (intake) temperature warning (low temperature) is detected.
13	Internal temperature alarm is detected.
14	Internal temperature warning 2 is detected.
15	Internal temperature warning 1 is detected.
20	Logic voltage alarm is detected.
21	Sub voltage alarm is detected.
40	DKCPS-x0 abnormality is detected. (x: DKC Module#)
41	DKCPS-x1 abnormality is detected. (x: DKC Module#)
42	DKCPS-x2 abnormality is detected. (x: DKC Module#)
43	DKCPS-x3 abnormality is detected. (x: DKC Module#)

*3: Error Reason Code (2/2)

Code	Reason
70	Low rotation speed of DKCFAN-x0 is detected. (x: Cluster#)
71	Low rotation speed of DKCFAN-x1 is detected. (x: Cluster#)
72	Low rotation speed of DKCFAN-x2 is detected. (x: Cluster#)
73	Low rotation speed of DKCFAN-x3 is detected. (x: Cluster#)
74	Low rotation speed of DKCFAN-x4 is detected. (x: Cluster#)
75	Low rotation speed of DKCFAN-x5 is detected. (x: Cluster#)
76	Low rotation speed of DKCFAN-x6 is detected. (x: Cluster#)
85	JP1 for SVP (BASIC) remains.
86	JP1 for SVP (OPTION) remains.
90	Environment monitor register access error.
9A	DKCMN Ready off.
9B	DKCMN version disagreement.
A0	Logic voltage alarm disagreement is detected.
A1	Sub voltage alarm disagreement is detected.
A2	Abnormal temperature disagreement of the DKC outside is detected.
A3	Temperature alarm disagreement in DKC is detected.
A4	Temperature warning disagreement in DKC is detected.
A5	PSOFFREQ control signal disagreement is detected.
A6	PSOFFOK control signal disagreement is detected.
A7	SYSON control signal disagreement is detected.
A8	DKCPS-x0 control signal disagreement is detected. (x: DKC Module#)
A9	DKCPS-x1 control signal disagreement is detected. (x: DKC Module#)
AA	DKCPS-x2 control signal disagreement is detected. (x: DKC Module#)
AB	DKCPS-x3 control signal disagreement is detected. (x: DKC Module#)
AC	MN communication abnormality is detected.
AD	DKCPANEL connection abnormality is detected.
AE	PCIADP connection abnormality is detected.
AF	MN abnormality is detected.
B0	MN status disagreement is detected.
B1	Communication abnormality between MN is detected.
B2	PS Status disagreement is detected.
B4	EPO unmatched.
B5	Connection abnormality between the DKC module is detected.
E3	Duplex SVP Setup fail.

SSBLOG05-2930

Byte27 = EE (Environment Monitor Detected DKU Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Time stamp							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD		
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'E')				Message (x'E')			
28	Error Detection code (*1)							
29	Error Component code (*2)							
2A	Error Reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number (don't care)				Message code (don't care)			
35	SSID (low) of self subsystem (don't care)							
36	Symptom code (x'FFEE')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

SSBLOG05-2940

Byte27 = EE (Environment Monitor Detected DKU Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
44								
45								
7F								

***1: Error Detection Code**

Code	Detection
80	SSWxx0-HP0
82	SSWxx0-LP1
90	SSWxx1-HP0
92	SSWxx1-LP1
A0	SSWxx2-HP0
A2	SSWxx2-LP1
B0	SSWxx3-HP0
B2	SSWxy3-LP1

***2: Error Component Code**

Code	Detection	Code	Component	Code	Component
00	HDU-000/004	30	HDU-030/034	60	HDU-060/064
01	HDU-001/005	31	HDU-031/035	61	HDU-061/065
02	HDU-002/006	32	HDU-032/036	62	HDU-062/066
03	HDU-003/007	33	HDU-033/037	63	HDU-063/067
04	HDU-100/104	34	HDU-130/134	64	HDU-160/164
05	HDU-101/105	35	HDU-131/135	65	HDU-161/165
06	HDU-102/106	36	HDU-132/136	66	HDU-162/166
07	HDU-103/107	37	HDU-133/137	67	HDU-163/167
10	HDU-010/014	40	HDU-040/044	70	HDU-070/074
11	HDU-011/015	41	HDU-041/045	71	HDU-071/075
12	HDU-012/016	42	HDU-042/046	72	HDU-072/076
13	HDU-013/017	43	HDU-043/047	73	HDU-073/077
14	HDU-110/114	44	HDU-140/144	74	HDU-170/174
15	HDU-111/115	45	HDU-141/145	75	HDU-171/175
16	HDU-112/116	46	HDU-142/146	76	HDU-172/176
17	HDU-113/117	47	HDU-143/147	77	HDU-173/177
20	HDU-020/024	50	HDU-050/054		
21	HDU-021/025	51	HDU-051/055		
22	HDU-022/026	52	HDU-052/056		
23	HDU-023/027	53	HDU-053/057		
24	HDU-120/124	54	HDU-150/154		
25	HDU-121/125	55	HDU-151/155		
26	HDU-122/126	56	HDU-152/156		
27	HDU-123/127	57	HDU-153/157		

***3: Error Reason Code**

Code	Reason
50	DKU PS_0 abnormality has been detected.
51	DKU PS_1 abnormality has been detected.
52	DKU PS_2 abnormality has been detected.
53	DKU PS_3 abnormality has been detected.
C0	DKU FAN_0 abnormality has been detected.
C1	DKU FAN_1 abnormality has been detected.
C4	DKU FAN_0 fuse disconnection has been detected.
C5	DKU FAN_0 fuse disconnection has been detected.
D0	HDD PWR_0 abnormality has been detected.
D1	HDD PWR_1 abnormality has been detected.
D2	HDD PWR_2 abnormality has been detected.
D3	HDD PWR_3 abnormality has been detected.
F0	SSW alarm disagreement.
F1	SSW access error abnormality (Cluster 1 Standard) has been detected.
F2	SSW access error abnormality (Cluster 2 Standard) has been detected.
F3	SSW access error abnormality (Cluster 1 High performance) has been detected.
F4	SSW access error abnormality (Cluster 2 High performance) has been detected.

SSBLOG05-2960

Byte27 = F0 (Operation Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'0')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (*1)							
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF0')							
37								

SSBLOG05-2970**Byte27 = F0 (Operation Terminated) (2/2)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'1D')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

(*1) Hardware level

Bit 0 : Serial channel hardware level	
For bit0 = 0	For bit0 = 1
Bit 1 : Not used	Bit 1 : Not used
Bit 2 ~ 3 : SP number	Bit 2 ~ 3 : SP number
Bit 4 ~ 5 : CHLs per Cluster	Bit 4 ~ 5 : CHLs per Cluster
00 : 4	0000 : Parallel = 4, Serial = 0
01 : 8	0001 : Parallel = 8, Serial = 0
10 : Not used	0010 : Parallel = 4, Serial = 2
11 : Not used	0100 : Parallel = 4, Serial = 4
Bit 6 : NVS	0110 : Parallel = 0, Serial = 2
0 : Without NVS	1000 : Parallel = 0, Serial = 4
1 : With NVS	1010 : Parallel = 0, Serial = 8
Bit 7 : Not used	1100 : Parallel = 0, Serial = 8
Bit 8 ~ 10 : Cache size	Bit 8 : DUAL FRAME
000 : Non cache	0 : DUAL FRAME
001 : 256MB	1 : MODULAR POWER
010 : 512MB	Bit 9 ~ 11 : Cache size
011 : 768MB	000 : Non cache
100 : 1024MB	001 : 256MB
101 : 1280MB	010 : 512MB
110 : 1536MB	011 : 768MB
111 : Over 1537MB	100 : 1024MB
Bit 11 ~ 13 : Cluster hardware level	101 : 1280MB
Bit 14 ~ 15 : Cache/NVS hardware level	110 : 1536MB
	111 : Over 1536MB
	Bit 12 ~ 13 : Cluster hardware level
	Bit 14 ~ 15 : Cache/NVS hardware level

SSBLOG05-2980

Byte27 = F1 (Micro-program Detected Cache Failure) (1/2)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	PCB type		Processor ID (Note)					
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16 17	SSB log number							
18 19	Related failure log number							
1A 1B	Related SIM log number							
1C 1D	Related SSB log number							
1E	Reserved							
1F	SCB							
20	Basic SSB							
21 22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'1')			
28	Queue transition in progress (Side A)	Queue transition in progress (Side B)	Free SGCB queue operation in progress	Free SLCB queue operation in progress	Free GRPT queue operation in progress	Not used		
29	Not used							
	VDEV number							
2A	Queue type							
	VDEV number		Slot number					
2B	Queue number							
	Slot number							
2C	Queue number							
	Slot number							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

*1: When byte 28, bit2-4 = 000, upper value is valid.

When byte 28, bit2-4 ≠ 000, lower value is valid.

SSBLOG05-2990**Byte27 = F1 (Micro-program Detected Cache Failure) (2/2)**

	0	1	2	3	4	5	6	7
30	Not used							
31	Module ID							
33	Routine ID							
32	PCB number				Error code			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF1')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Not used (x'00') Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						
40	Error information							
7F								

SSBLOG05-3000**Byte27 = F2 (CACHE ERR) (1/2)**

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'2')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D	SSID for Other Subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

SSBLOG05-3001**Byte27 = F2 (CACHE ERR) (2/2)**

	0	1	2	3	4	5	6	7
30	Hardware information (See following pages)							
31	Module ID							
32	Routine ID							
33	PKID				Message code(x'0')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF2')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B		Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B		Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B		Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B		Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Hardware information (See following pages)							
7F								

Byte27 = F2 (CACHE ERR) Byte28-2C,30 Detail (1/1)

	0	1	2	3	4	5	6	7
28	Subcode							
	x'1'=C_path error(SW detection) (CHK2) x'2'=C_path error(CMA detection) (CHK2) x'3'=CACHE board error(CHK2) x'4'=CACHE memory error(CHK2) x'5'=SW C_path Port error(CHK3) x'6'=C_path error(SW detection) (CHK3) x'7'=C_path error(CMA detection) (CHK3) x'8'=CACHE board error (CHK3) x'9'=CACHE memory error (CHK3)				CMA or SW BOARD error	CMA or SW ANY error	SW register LOG error	CMA register LOG error
29	Transfer information							
	Transfer P/K Type x'1'=ESW x'2'=CHA x'3'=FICON x'4'=DKA x'5'=MPPK				Act mode x'1'=Normal write x'2'=Read x'3'=2 Cache write x'4'=CtoC Cache Copy x'5'=Atomic write			
2A	Transfer Master P/K board number							
	ESW: x'00'~x'07' CHA/FICON: x'00'~x'1F' DKA: x'00'~x'07'							
2B	Using path code0(CHK2)							
	Not used'0'	Not used'0'	Command1 Using P_Path#1	Command1 Using P_Path#0	Command1 Access CACHE board number			
2C	Using path code1(CHK2)							
	Not used'0'	Not used'0'	Command2 Using P_Path#1	Command2 Using P_Path#0	Command2 Access CACHE board number			
30	Transfer Master Path							
	x'0'=Not used'0' x'1'=MCH0, x'2'=MCH1, x'3'=MCH2, x'4'=MCH3 x'5'=MPA_DMA0, x'6'=MPA_DMA1, x'7'=MPA_DMA2, x'8'=MPA_DMA3, x'9'=LR_DMA0, x'A'=LR_DMA1, x'B'=LR_DMA2, x'C'=LR_DMA3, x'D'=DMA, x'E'=DRR0, x'F'=DRR1				x'0'=MFDMA0, x'1'=MFDMA1, x'2'=MFDMA2, x'3'=MFDMA3, x'4'=MFDMA4, x'5'=MFDMA5, x'6'=MFDMA6, x'7'=MFDMA7, x'8'=MFDMA8, x'9'=MFDMA9, x'A'=MFDMA10, x'B'=MFDMA11, x'C'=MFDMA12, x'D'=MFDMA13, x'E'=MFDMA14, x'F'=MFDMA15			

SSBLOG05-3020

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(1) Byte28 = x'1', x'2', x'5', x'6', x'7' : C_path error (1/4)

	0	1	2	3	4	5	6	7
40	Command1 HSN Status byte0							
	ERROR	WARNING	PATH ERROR	Not used	Module ID			
41	Command1 HSN Status byte1							
	LSI ID							Not used
42	Command1 HSN Status byte2							
	Not used	Not used	Not used	PATH #				
43	Command1 HSN Status byte3							
	PCIBLK TX ERR	PCIBLK RX ERR	REGBLK ERR	Not used	MEMBLK BNK0 ERR	MEMBLK BNK1 ERR	RAMBLK ERR	FMBPCI BLK ERR
44	Command1 HSN Status byte4							
	Command1 HSN Status byte4							
45	Command1 HSN Status byte5							
	Command1 HSN Status byte5							
46	Command1 HSN Status byte6							
	Command1 HSN Status byte6							
47	Command1 HSN Status byte7							
	Command1 HSN Status byte7							
48	Command2 HSN Status byte0							
	ERROR	WARNING	PATH ERROR	Not used	Module ID			
49	Command2 HSN Status byte1							
	LSI ID							Not used
4A	Command2 HSN Status byte2							
	Not used	Not used	Not used	PATH #				
4B	Command2 HSN Status byte3							
	PCIBLK TX ERR	PCIBLK RX ERR	REGBLK ERR	Not used	MEMBLK BNK0 ERR	MEMBLK BNK1 ERR	RAMBLK ERR	FMBPCI BLK ERR
4C	Command2 HSN Status byte4							
	Command1 HSN Status byte4							
4D	Command2 HSN Status byte5							
	Command1 HSN Status byte5							
4E	Command2 HSN Status byte6							
	Command1 HSN Status byte6							
4F	Command2 HSN Status byte7							
	Command1 HSN Status byte7							

SSBLOG05-3030

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(1) Byte28 = x'1', x'2', x'5', x'6', x'7' : C_path error (2/4)

	0	1	2	3	4	5	6	7
50	SW::ERRINT_LOG Byte0							
	Not used	Not used	Not used	P12_ERRINT_LOG	P11_ERRINT_LOG	P10_ERRINT_LOG	P09_ERRINT_LOG	P08_ERRINT_LOG
51	SW::ERRINT_LOG Byte1							
	P07_ERRINT_LOG	P06_ERRINT_LOG	P05_ERRINT_LOG	P04_ERRINT_LOG	P03_ERRINT_LOG	P02_ERRINT_LOG	P01_ERRINT_LOG	Not used
52	SW::RX_LOG_ERR Byte0							
	Not used	Not used	Not used	Not used	Not used	PERR	SEQR1_TOV	HCRC_ERR
53	SW::RX_LOG_ERR Byte1							
	PKTLEN_ERR	ROUTING_ERR	PCRC_FLG_ERR	PKTEND_ERR	UNDERRUN_ERR	HCRCFLG_ERR	STP_ERR	PCRC_ERR
54	SW::BF0_ERR Byte0							
	Not used	Not used	Not used	BFCTL1_RDPT_ERR	BFCTL1_RGFDC_ERR	BFCTL1_PRM_ERR	BFCTL1_SEQCNT_ERR	BFCTL1_SEQTM_ERR
55	SW::BF0_ERR Byte1							
	BFCTL1_SEQCMP_ERR	BFCTL1_SEQS_ERR	BFCTL1_SEQM_ERR	Not used	Not used	Not used	Not used	BFCTL0_RDPT_ERR
56	SW::BF0_ERR Byte2							
	BFCTL0_RGFDC_ERR	BFCTL0_PRM_ERR	BFCTL0_SEQCNT_ERR	BFCTL0_SEQTM_ERR	BFCTL0_SEQCMP_ERR	BFCTL0_SEQS_ERR	BFCTL0_SEQM_ERR	Not used
57	SW::BF0_ERR Byte3							
	BF_RGF_ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG_ERR	BF_WTPT_ERR
58	SW::BF0_LOG_ERR Byte0							
	Not used	BFCTL1_PADTOV_ERR	BFCTL1_PERR	BFCTL1_TOV_ERR	BFCTL1_UNDERRUN_ERR	BFCTL1_CPLTOV_ERR	BFCTL1_PKTEND_ERR	BFCTL1_PKTSTP_ERR
59	SW::BF0_LOG_ERR Byte1							
	Not used	BFCTL0_PADTOV_ERR	BFCTL0_PERR	BFCTL0_TOV_ERR	BFCTL0_UNDERRUN_ERR	BFCTL0_CPLTOV_ERR	BFCTL0_PKTEND_ERR	BFCTL0_PKTSTP_ERR
5A	Not used (x'00')							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5B	Not used (x'00')							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5C	SW::BF1_ERR Byte3							
	Not used	Not used	Not used	BFCTL1_RDPT_ERR	BFCTL1_RGFDC_ERR	BFCTL1_PRM_ERR	BFCTL1_SEQCNT_ERR	BFCTL1_SEQTM_ERR
5D	SW::BF1_ERR Byte2							
	BFCTL1_SEQCMP_ERR	BFCTL1_SEQS_ERR	BFCTL1_SEQM_ERR	Not used	Not used	Not used	Not used	BFCTL0_RDPT_ERR
5E	SW::BF1_ERR Byte1							
	BFCTL0_RGFDC_ERR	BFCTL0_PRM_ERR	BFCTL0_SEQCNT_ERR	BFCTL0_SEQTM_ERR	BFCTL0_SEQCMP_ERR	BFCTL0_SEQS_ERR	BFCTL0_SEQM_ERR	Not used
5F	SW::BF1_ERR Byte0							
	BF_RGF_ERR	Not used	Not used	Not used	Not used	Not used	BF_LOG_ERR	BF_WTPT_ERR

SSBLOG05-3040

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(1) Byte28 = x'1', x'2', x'5', x'6', x'7' : C_path error (3/4)

	0	1	2	3	4	5	6	7
60	SW::BF1_LOG_ERR Byte2							
	Not used	BFCTL1 PADTOV ERR	BFCTL1 PERR	BFCTL1 TOV ERR	BFCTL1 UNDERRUN ERR	BFCTL1 CPLTOV ERR	BFCTL1 PKTEND ERR	BFCTL1 PKTSTP ERR
61	SW::BF1_LOG_ERR Byte0							
	Not used	BFCTL0 PADTOV ERR	BFCTL0 PERR	BFCTL0 TOV ERR	BFCTL0 UNDERRUN ERR	BFCTL0 CPLTOV ERR	BFCTL0 PKTEND ERR	BFCTL0 PKTSTP ERR
62	SW::PCIE_ERR_STS Byte1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
63	SW::PCIE_ERR_STS Byte0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL_UP ERR
64	SW::PCIE_MFTLP_CODE_LOG							
	TLM MFTLP CODE(7)	TLM MFTLP CODE(6)	TLM MFTLP CODE(5)	TLM MFTLP CODE(4)	TLM MFTLP CODE(3)	TLM MFTLP CODE(2)	TLM MFTLP CODE(1)	TLM MFTLP CODE(0)
65	SW::PCIE_PORT_STATUS Byte1							
	Not used	Not used	Not used	PLM RXEIDL	PLM LTSSM STATE(3)	PLM LTSSM STATE(2)	PLM LTSSM STATE(1)	PLM LTSSM STATE(0)
66	SW::PCIE_PORT_STATUS Byte0							
	PLM LINK TRAINING	PLM LINK WIDTH(5)	PLM LINK WIDTH(4)	PLM LINK WIDTH(3)	PLM LINK WIDTH(2)	PLM LINK WIDTH(1)	PLM LINK WIDTH(0)	DLM DL_UP
67	SW::TXVC_MFTLP_CODE_LOG							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR
68	CMA::Board error Byte0							
	PCIBLK250 0 BODERR	PCIBLK250 1 BODERR	PCIBLK250 2 BODERR	PCIBLK250 3 BODERR	PCIBLK125 0 BODERR	PCIBLK125 1 BODERR	PCIBLK125 2 BODERR	PCIBLK125 3 BODERR
69	CMA::Board error Byte1							
	Not used	GCMN REG BODERR	PCM N REG BODERR	MCMN REG BODERR	MEMBLK 0 BODERR	MEMBLK 1 BODERR	DSCBLK 0 BODERR	DSCBLK 1 BODERR
6A	CMA::Board error Byte2							
	SWBLK 0 BODERR	SWBLK 1 BODERR	SWBLK 2 BODERR	SWBLK 3 BODERR	SWBLK 4 BODERR	SWBLK 5 BODERR	SWBLK 6 BODERR	SWBLK 7 BODERR
6B	CMA::Board error Byte3							
	SWBLK 8 BODERR	SWBLK 9 BODERR	SWBLK A BODERR	FMB250 PCIBLK BODERR	FMB125 PCIBLK BODERR	RAMBLK BODERR	MSTBLK BODERR	MPIBLK BODERR
6C	CMA::ANY error Byte0							
	PCIBLK250 0 ANYERR	PCIBLK250 1 ANYERR	PCIBLK250 2 ANYERR	PCIBLK250 3 ANYERR	PCIBLK125 0 ANYERR	PCIBLK125 1 ANYERR	PCIBLK125 2 ANYERR	PCIBLK125 3 ANYERR
6D	CMA::ANY error Byte1							
	SIPBLK ANYERR	GCMN REG ANYERR	PCM N REG ANYERR	MCMN REG ANYERR	MEMBLK 0 ANYERR	MEMBLK 1 ANYERR	DSCBLK 0 ANYERR	DSCBLK 1 ANYERR
6E	CMA::ANY error Byte2							
	SWBLK 0 ANYERR	SWBLK 1 ANYERR	SWBLK 2 ANYERR	SWBLK 3 ANYERR	SWBLK 4 ANYERR	SWBLK 5 ANYERR	SWBLK 6 ANYERR	SWBLK 7 ANYERR
6F	CMA::ANY error Byte3							
	SWBLK 8 ANYERR	SWBLK 9 ANYERR	SWBLK A ANYERR	FMB250 PCIBLK ANYERR	FMB125 PCIBLK ANYERR	RAMBLK ANYERR	MSTBLK ANYERR	MPIBLK ANYERR

SSBLOG05-3050

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(1) Byte28 = x'1', x'2', x'5', x'6', x'7' : C_path error (4/4)

	0	1	2	3	4	5	6	7
70	CMA::ANY warning Byte0							
	PCIBLK250 0 ANYWRN	PCIBLK250 1 ANYWRN	PCIBLK250 2 ANYWRN	PCIBLK250 3 ANYWRN	Not used	Not used	Not used	Not used
71	CMA::ANY warning Byte1							
	Not used	Not used	Not used	Not used	MEMBLK 0 ANYWRN	MEMBLK 1 ANYWRN	Not used	Not used
72	CMA::ANY warning Byte2							
	Not used	Not used	Not used	FMB250 PCIBLK ANYWRN	FMB125 PCIBLK ANYWRN	RAMBLK ANYWRN	Not used	Not used
73	CMA::SLV error Byte0							
	PCIBLK250 0 SLVERR	PCIBLK250 1 SLVERR	PCIBLK250 2 SLVERR	PCIBLK250 3 SLVERR	FMB250 PCIBLK SLVERR	FMB125 PCIBLK SLVERR	Not used	Not used
74	Access mode Byte0							
75	Access mode Byte1							
76	Access mode Byte2							
77	Access mode Byte3							
78	Access Address Byte0							
79	Access Address Byte1							
7A	Access Address Byte2							
7B	Access Address Byte3							
7C	Access Address Byte4							
7D	Access Address Byte5							
7E	Access Address Byte6							
7F	Access Address Byte7							

SSBLOG05-3060

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(2) Byte28 = x'3', x'4', x'8', x'9' : CACHE error (1/4)

	0	1	2	3	4	5	6	7
40	Command1 HSN Status byte0							
	ERROR	WARNING	PATH ERROR	Not used	Module ID			
41	Command1 HSN Status byte1							
	LSI ID							Not used
42	Command1 HSN Status byte2							
	Not used	Not used	Not used	PATH #				
43	Command1 HSN Status byte3							
	PCIBLK TX ERR	PCIBLK RX ERR	REGBLK ERR	Not used	MEMBLK BNK0 ERR	MEMBLK BNK1 ERR	RAMBLK ERR	FMBPCI BLK ERR
44	Command1 HSN Status byte4							
	Command1 HSN Status byte4							
45	Command1 HSN Status byte5							
	Command1 HSN Status byte5							
46	Command1 HSN Status byte6							
	Command1 HSN Status byte6							
47	Command1 HSN Status byte7							
	Command1 HSN Status byte7							
48	Command2 HSN Status byte0							
	ERROR	WARNING	PATH ERROR	Not used	Module ID			
49	Command2 HSN Status byte1							
	LSI ID							Not used
4A	Command2 HSN Status byte2							
	Not used	Not used	Not used	PATH #				
4B	Command2 HSN Status byte3							
	PCIBLK TX ERR	PCIBLK RX ERR	REGBLK ERR	Not used	MEMBLK BNK0 ERR	MEMBLK BNK1 ERR	RAMBLK ERR	FMBPCI BLK ERR
4C	Command2 HSN Status byte4							
	Command1 HSN Status byte4							
4D	Command2 HSN Status byte5							
	Command1 HSN Status byte5							
4E	Command2 HSN Status byte6							
	Command1 HSN Status byte6							
4F	Command2 HSN Status byte7							
	Command1 HSN Status byte7							

SSBLOG05-3070

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(2) Byte28 = x'3', x'4', x'8', x'9' : CACHE error (2/4)

	0	1	2	3	4	5	6	7
50	CMA::Board error Byte0							
	PCIBLK250 0 BODERR	PCIBLK250 1 BODERR	PCIBLK250 2 BODERR	PCIBLK250 3 BODERR	PCIBLK125 0 BODERR	PCIBLK125 1 BODERR	PCIBLK125 2 BODERR	PCIBLK125 3 BODERR
51	CMA::Board error Byte1							
	Not used	GCMN REG BODERR	PCMN REG BODERR	MCMN REG BODERR	MEMBLK 0 BODERR	MEMBLK 1 BODERR	DSCBLK 0 BODERR	DSCBLK 1 BODERR
52	CMA::Board error Byte2							
	SWBLK 0 BODERR	SWBLK 1 BODERR	SWBLK 2 BODERR	SWBLK 3 BODERR	SWBLK 4 BODERR	SWBLK 5 BODERR	SWBLK 6 BODERR	SWBLK 7 BODERR
53	CMA::Board error Byte3							
	SWBLK 8 BODERR	SWBLK 9 BODERR	SWBLK A BODERR	FMB250 PCIBLK BODERR	FMB125 PCIBLK BODERR	RAMBLK BODERR	MSTBLK BODERR	MPIBLK BODERR
54	CMA::ANY error Byte0							
	PCIBLK250 0 ANYERR	PCIBLK250 1 ANYERR	PCIBLK250 2 ANYERR	PCIBLK250 3 ANYERR	PCIBLK125 0 ANYERR	PCIBLK125 1 ANYERR	PCIBLK125 2 ANYERR	PCIBLK125 3 ANYERR
55	CMA::ANY error Byte1							
	SIPBLK ANYERR	GCMN REG ANYERR	PCMN REG ANYERR	MCMN REG ANYERR	MEMBLK 0 ANYERR	MEMBLK 1 ANYERR	DSCBLK 0 ANYERR	DSCBLK 1 ANYERR
56	CMA::ANY error Byte2							
	SWBLK 0 ANYERR	SWBLK 1 ANYERR	SWBLK 2 ANYERR	SWBLK 3 ANYERR	SWBLK 4 ANYERR	SWBLK 5 ANYERR	SWBLK 6 ANYERR	SWBLK 7 ANYERR
57	CMA::ANY error Byte3							
	SWBLK 8 ANYERR	SWBLK 9 ANYERR	SWBLK A ANYERR	FMB250 PCIBLK ANYERR	FMB125 PCIBLK ANYERR	RAMBLK ANYERR	MSTBLK ANYERR	MPIBLK ANYERR
58	CMA::ANY warning Byte0							
	PCIBLK250 0 ANYWRN	PCIBLK250 1 ANYWRN	PCIBLK250 2 ANYWRN	PCIBLK250 3 ANYWRN	Not used	Not used	Not used	Not used
59	CMA::ANY warning Byte1							
	Not used	Not used	Not used	Not used	MEMBLK 0 ANYWRN	MEMBLK 1 ANYWRN	Not used	Not used
5A	CMA::ANY warning Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5B	CMA::ANY warning Byte3							
	Not used	Not used	Not used	FMB250 PCIBLK ANYWRN	FMB125 PCIBLK ANYWRN	RAMBLK ANYWRN	Not used	Not used
5C	CMA::SLV error Byte0							
	PCIBLK250 0 SLVERR	PCIBLK250 1 SLVERR	PCIBLK250 2 SLVERR	PCIBLK250 3 SLVERR	Not used	Not used	Not used	Not used
5D	CMA::SLV error Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5E	CMA::SLV error Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5F	CMA::SLV error Byte3							
	Not used	Not used	Not used	FMB250 PCIBLK SLVERR	FMB125 PCIBLK SLVERR	Not used	Not used	Not used

SSBLOG05-3080

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(2) Byte28 = x'3', x'4', x'8', x'9' : CACHE error (3/4)

	0	1	2	3	4	5	6	7
60	CMA::MEMBLK0 DIMM configuration Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
61	CMA::MEMBLK0 DIMM configuration Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
62	CMA::MEMBLK0 DIMM configuration Byte2							
	Stack SEL	Not used	Not used	Not used	8GB DIMM	4GB DIMM	2GB DIMM	MG2 enable
63	CMA::MEMBLK0 DIMM configuration Byte3							
	Stack SEL	Not used	Not used	Not used	8GB DIMM	4GB DIMM	2GB DIMM	MG0 enable
64	CMA::MEMBLK0 MEMCTL MEMCTLSIG ERR							
	Not used	Not used	Not used	Not used	CM21 error	CM20 error	CM01 error	CM00 error
65	CMA::MEMBLK0 DIMM check enable							
	Not used	Not used	Not used	Not used	CM21 enable	CM20 enable	CM01 enable	CM00 enable
66	CMA::MEMBLK0 MRDCTL uncorrectable error location							
	CM21 Rank1 UNCORR	CM21 Rank0 UNCORR	CM20 Rank1 UNCORR	CM20 Rank0 UNCORR	CM01 Rank1 UNCORR	CM01 Rank0 UNCORR	CM00 Rank1 UNCORR	CM00 Rank0 UNCORR
67	CMA::MEMBLK0 MRDCTL correctable error location							
	CM21 Rank1 CORR	CM21 Rank0 CORR	CM20 Rank1 CORR	CM20 Rank0 CORR	CM01 Rank1 CORR	CM01 Rank0 CORR	CM00 Rank1 CORR	CM00 Rank0 CORR
68	CMA::MEMBLK0 MRDCTL DQS count error location							
	CM21 Rank1 DQSCNT	CM21 Rank0 DQSCNT	CM20 Rank1 DQSCNT	CM20 Rank0 DQSCNT	CM01 Rank1 DQSCNT	CM01 Rank0 DQSCNT	CM00 Rank1 DQSCNT	CM00 Rank0 DQSCNT
69	CMA::MEMBLK0 RTNCTL ANYERR Status							
	Not used	Not used	Not used	RDSLRC ERR	Not used	Not used	REGADR Parity error	REGDAT Parity error
6A	CMA::MEMBLK1 DIMM configuration Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6B	CMA::MEMBLK1 DIMM configuration Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
6C	CMA::MEMBLK1 DIMM configuration Byte2							
	Stack SEL	Not used	Not used	Not used	8GB DIMM	4GB DIMM	2GB DIMM	MG3 enable
6D	CMA::MEMBLK1 DIMM configuration Byte3							
	Stack SEL	Not used	Not used	Not used	8GB DIMM	4GB DIMM	2GB DIMM	MG1 enable
6E	CMA::MEMBLK1 MEMCTL MEMCTLSIG ERR							
	Not used	Not used	Not used	Not used	CM31 error	CM30 error	CM11 error	CM10 error
6F	CMA::MEMBLK1 DIMM check enable							
	Not used	Not used	Not used	Not used	CM31 enable	CM30 enable	CM11 enable	CM10 enable

SSBLOG05-3090

Byte27 = F2 (CACHE ERR) Byte40-7F Detail

(2) Byte28 = x'3', x'4', x'8', x'9' : CACHE error (4/4)

	0	1	2	3	4	5	6	7
70	CMA::MEMBLK1 MRDCTL uncorrectable error location							
	CM31 Rank1 UNCORR	CM31 Rank0 UNCORR	CM30 Rank1 UNCORR	CM30 Rank0 UNCORR	CM11 Rank1 UNCORR	CM11 Rank0 UNCORR	CM10 Rank1 UNCORR	CM10 Rank0 UNCORR
71	CMA::MEMBLK1 MRDCTL correctable error location							
	CM31 Rank1 CORR	CM31 Rank0 CORR	CM30 Rank1 CORR	CM30 Rank0 CORR	CM11 Rank1 CORR	CM11 Rank0 CORR	CM10 Rank1 CORR	CM10 Rank0 CORR
72	CMA::MEMBLK1 MRDCTL DQS count error location							
	CM31 Rank1 DQSCNT	CM31 Rank0 DQSCNT	CM30 Rank1 DQSCNT	CM30 Rank0 DQSCNT	CM11 Rank1 DQSCNT	CM11 Rank0 DQSCNT	CM10 Rank1 DQSCNT	CM10 Rank0 DQSCNT
73	CMA::MEMBLK1 RTNCTL ANYERR Status							
	Not used	Not used	Not used	RDSLRC ERR	Not used	Not used	REGADR Parity error	REGDAT Parity error
74	Access mode Byte0							
75	Access mode Byte1							
76	Access mode Byte2							
77	Access mode Byte3							
78	Access Address Byte0							
79	Access Address Byte1							
7A	Access Address Byte2							
7B	Access Address Byte3							
7C	Access Address Byte4							
7D	Access Address Byte5							
7E	Access Address Byte6							
7F	Access Address Byte7							

SSBLOG05-3100

Byte27 = F5 (Remote Copy Paths Removed/Restored) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type							
07	Processor ID (Note)							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'5')			
28	Reason code (*1)							
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C	RCU Sequence No.							
2D								
2E								
2F	MCU Manufacture code/Factory code							
30								
31	MCU Sequence No.							
32								
33								
34	SSID of self subsystem							
35								
36	Symptom code (x'38')							
37	Symptom code (x'87')							

*1: Reason code

x'01' : One or more remote copy paths removed unexpectedly

x'02' : One or more remote copy paths restored

x'03' : All remote copy paths removed unexpectedly

SSBLOG05-3110**Byte27 = F5 (Remote Copy Paths Removed/Restored) (2/2)**

	0	1	2	3	4	5	6	7
38	Not used (x'00')							
39	Not used (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
	Not used							
7F								

SSBLOG05-3120

Byte27 = F6 (CFW Access not Authorized) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'6')			
28	Reason code (x'01': CFW ID unmatched)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (Invalid)			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF6')							
37								

SSBLOG05-3130

Byte27 = F6 (CFW Access not Authorized) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
7F	Not used							

SSBLOG05-3140

Byte27 = FA (NVS Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'A')			
28	Reason code (x'02': NVS failure)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (x'F')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFFA')							
37								

SSBLOG05-3150**Byte27 = FA (NVS Terminated) (2/2)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Key code (for reconnection)							
41	Format/Message (for reconnection)							
42	Module ID (for reconnection)							
43	Routine ID (for reconnection)							
44								
	Not used							
7F								

SSBLOG05-3160

Byte27 = FB (TrueCopy for Mainframe Pair suspend) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'B')			
28	R-VOL Suspended	R-VOL failed	Reason code (*1)					
29		RCU device address						
2A	RCU Manufacture code/Factory code							
2B								
2C								
2D	RCU Sequence No.							
2E								
2F	MCU Manufacture code/Factory code							
30								
31								
32	MCU Sequence No.							
33								
34	SSID of self subsystem							
35								

***1: Reason code**

x'04'-x'2F' : Reserved

x'30' : Pair suspended. MCU device write error.

x'31' : Pair suspended. RCU subsystem error/or MCU subsystem error.

x'32' : Pair suspended. RCU device communication error.

x'33' : Pair suspended (Critical device status). All write command is rejected until the pair is re-established.

x'34' : Pair suspended. RCU device is not ready (intervention required).

SSBLOG05-3170

Byte27 = FB (TrueCopy for Mainframe Pair suspend) (2/2)

	0	1	2	3	4	5	6	7
36	Symptom code (x'FE')							
37	Symptom code (same byte #28)							
38	Not used (x'00')							
39	Not used (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-3180

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'F')				Message (x'F')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Module ID							
33	Routine ID							
34	PKID				Message code(x'0')			
35	SSID of self subsystem							

SSBLOG05-3181

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) (2/2)

	0	1	2	3	4	5	6	7
36	Symptom code (x'FF8B')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Hardware information (See following pages)							
7F								

SSBLOG05-3190

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 : CHF_DXBF correctable error (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'01' : CHF_DXBF correctable error							
29	MPID (Reported SSB)							
2A	Module ID, LR_LSI number							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Warning 0							
	ECC WARNING3	ECC WARNING2	ECC WARNING1	ECC WARNING0	Not used '0'	Not used '0'	Not used '0'	Not used '0'
41	Warning 1							
	WBUFFA 1BERR Warning	WBUFFB 1BERR Warning	RBUFFA 1BERR Warning	RBUFFB 1BERR Warning	Not used '0'	Not used '0'	Not used '0'	Not used '0'
42	Not used (x'00')							
43	Not used (x'00')							
44	DXBF ECC monitor0							
45	DXBF ECC monitor1							
46	DXBF ECC monitor2							
47	DXBF ECC monitor3							
48	DXBF ECC monitor4							
49	DXBF ECC monitor5							
4A	DXBF ECC monitor6							
4B	DXBF ECC monitor7							
4C	DXBF LOG address0							
4D	DXBF LOG address1							
4E	DXBF LOG address2							
4F	DXBF LOG address3							
50	DXBF LOG address4							
51	DXBF LOG address5							
52	DXBF LOG address6							
53	DXBF LOG address7							
54	DXBF LOG address8							
55	DXBF LOG address9							
56	DXBF LOG address10							
57	DXBF LOG address11							
58	DXBF LOG address12							
59	DXBF LOG address13							
5A	DXBF LOG address14							
5B	DXBF LOG address15							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3200

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(2) Byte28 = 04 : DKF_DXBF correctable error (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'04' : DKF_DXBF correctable error							
29	MPID (Reported SSB)							
2A	Module ID, LR_LSI number							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Warning 0							
	ECC WARNING3	ECC WARNING2	ECC WARNING1	ECC WARNING0	Not used '0'	Not used '0'	Not used '0'	Not used '0'
41	Warning 1							
	WBUFFA 1BERR Warning	WBUFFB 1BERR Warning	RBUFFA 1BERR Warning	RBUFFB 1BERR Warning	Not used '0'	Not used '0'	Not used '0'	Not used '0'
42	Not used (x'00')							
43	Not used (x'00')							
44	DXBF ECC monitor0							
45	DXBF ECC monitor1							
46	DXBF ECC monitor2							
47	DXBF ECC monitor3							
48	DXBF ECC monitor4							
49	DXBF ECC monitor5							
4A	DXBF ECC monitor6							
4B	DXBF ECC monitor7							
4C	DXBF LOG address0							
4D	DXBF LOG address1							
4E	DXBF LOG address2							
4F	DXBF LOG address3							
50	DXBF LOG address4							
51	DXBF LOG address5							
52	DXBF LOG address6							
53	DXBF LOG address7							
54	DXBF LOG address8							
55	DXBF LOG address9							
56	DXBF LOG address10							
57	DXBF LOG address11							
58	DXBF LOG address12							
59	DXBF LOG address13							
5A	DXBF LOG address14							
5B	DXBF LOG address15							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3210

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 05 : CHM_DXBF correctable error (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'05' : CHM_DXBF correctable error							
29	MPID (Reported SSB)							
2A	Module ID, LR_LSI number							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	Warning 0							
	ECC WARNING3	ECC WARNING2	ECC WARNING1	ECC WARNING0	Not used '0'	Not used '0'	Not used '0'	Not used '0'
41	Warning 1							
	WBUFFA 1BERR Warning	WBUFFB 1BERR Warning	RBUFFA 1BERR Warning	RBUFFB 1BERR Warning	Not used '0'	Not used '0'	Not used '0'	Not used '0'
42	Not used (x'00')							
43	Not used (x'00')							
44	DXBF ECC monitor0							
45	DXBF ECC monitor1							
46	DXBF ECC monitor2							
47	DXBF ECC monitor3							
48	DXBF ECC monitor4							
49	DXBF ECC monitor5							
4A	DXBF ECC monitor6							
4B	DXBF ECC monitor7							
4C	DXBF LOG address0							
4D	DXBF LOG address1							
4E	DXBF LOG address2							
4F	DXBF LOG address3							
50	DXBF LOG address4							
51	DXBF LOG address5							
52	DXBF LOG address6							
53	DXBF LOG address7							
54	DXBF LOG address8							
55	DXBF LOG address9							
56	DXBF LOG address10							
57	DXBF LOG address11							
58	DXBF LOG address12							
59	DXBF LOG address13							
5A	DXBF LOG address14							
5B	DXBF LOG address15							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3220

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 02 : Cache Memory correctable error (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'02' : Cache Memory correctable error							
29	MPID (Reported SSB)							
2A	Module ID							
2B	PK ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	CMA threshold counter for the warning (MG0 CM00)							
41	CMA threshold counter for the warning (MG0 CM01)							
42	CMA threshold counter for the warning (MG1 CM10)							
43	CMA threshold counter for the warning (MG1 CM11)							
44	CMA threshold counter for the warning (MG2 CM20)							
45	CMA threshold counter for the warning (MG2 CM21)							
46	CMA threshold counter for the warning (MG3 CM30)							
47	CMA threshold counter for the warning (MG3 CM31)							
48	Error DIMM number (MEMBLK0)							
	CM21 Rank1	CM21 Rank0	CM20 Rank1	CM20 Rank0	CM01 Rank1	CM01 Rank0	CM00 Rank1	CM00 Rank0
49	Error DIMM number (MEMBLK1)							
	CM31 Rank1	CM31 Rank0	CM30 Rank1	CM30 Rank0	CM11 Rank1	CM11 Rank0	CM10 Rank1	CM10 Rank0
4A	Data LOG Buffer status (MEMBLK0)							
	uncorrectable error1 DIMM0/2	uncorrectable error0 DIMM0/2	correctable error1 DIMM0/2	correctable error0 DIMM0/2	Not used	Not used	LOG INH1 DIMM0/2	LOG INH0 DIMM0/2
4B	Data LOG Buffer status (MEMBLK1)							
	uncorrectable error1 DIMM1/3	uncorrectable error0 DIMM1/3	correctable error1 DIMM1/3	correctable error0 DIMM1/3	Not used	Not used	LOG INH1 DIMM1/3	LOG INH0 DIMM1/3
4C	MRDCTL correctable error location0 (MEMBLK0, port0)							
4D	MRDCTL correctable error location1 (MEMBLK0, port0)							
4E	MRDCTL correctable error location2 (MEMBLK0, port0)							
4F	MRDCTL correctable error location3 (MEMBLK0, port0)							
50	MRDCTL correctable error location0 (MEMBLK0, port1)							
51	MRDCTL correctable error location1 (MEMBLK0, port1)							
52	MRDCTL correctable error location2 (MEMBLK0, port1)							
53	MRDCTL correctable error location3 (MEMBLK0, port1)							
54	MRDCTL correctable error location0 (MEMBLK1, port0)							
55	MRDCTL correctable error location1 (MEMBLK1, port0)							
56	MRDCTL correctable error location2 (MEMBLK1, port0)							
57	MRDCTL correctable error location3 (MEMBLK1, port0)							
58	MRDCTL correctable error location0 (MEMBLK1, port1)							
59	MRDCTL correctable error location1 (MEMBLK1, port1)							
5A	MRDCTL correctable error location2 (MEMBLK1, port1)							
5B	MRDCTL correctable error location3 (MEMBLK1, port1)							

SSBLOG05-3230

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(4) Byte28 = 02 : Cache Memory correctable error (2/2)

	0	1	2	3	4	5	6	7
5C	DATALOGBUF Header LOG4_0							
5D	DATALOGBUF Header LOG4_1							
5E	DATALOGBUF Header LOG4_2							
5F	DATALOGBUF Header LOG4_3							
60	DATALOGBUF Header LOG5_0							
61	DATALOGBUF Header LOG5_1							
62	DATALOGBUF Header LOG5_2							
63	DATALOGBUF Header LOG5_3							
64	DATALOGBUF Header LOG6_0							
65	DATALOGBUF Header LOG6_1							
66	DATALOGBUF Header LOG6_2							
67	DATALOGBUF Header LOG6_3							
68	DATALOGBUF Header LOG7_0							
69	DATALOGBUF Header LOG7_1							
6A	DATALOGBUF Header LOG7_2							
6B	DATALOGBUF Header LOG7_3							
6C	DATALOGBUF ERROR word information0							
6D	DATALOGBUF ERROR word information1							
6E	DATALOGBUF ERROR word information2							
6F	DATALOGBUF ERROR word information3							
70	ERROR LOC. Information0							
71	ERROR LOC. Information1							
72	ERROR LOC. Information2							
73	ERROR LOC. Information3							
74	Uncorrected data LOG0							
75	Uncorrected data LOG1							
76	Uncorrected data LOG2							
77	Uncorrected data LOG3							
78	Corrected data LOG0							
79	Corrected data LOG1							
7A	Corrected data LOG2							
7B	Corrected data LOG3							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3240

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(5) Byte28 = 84 : SW correctable error (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'84' : SW correctable error							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	SW::Correctable warning:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	SW::Correctable warning:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PORT16 COR WNG
42	SW::Correctable warning:B1							
	PORT15 COR WNG	PORT14 COR WNG	PORT13 COR WNG	PORT12 COR WNG	PORT11 COR WNG	PORT10 COR WNG	PORT09 COR WNG	PORT08 COR WNG
43	SW::Correctable warning:B0							
	PORT07 COR WNG	PORT06 COR WNG	PORT05 COR WNG	PORT04 COR WNG	PORT03 COR WNG	PORT02 COR WNG	PORT01 COR WNG	Not used
44	SW::RX Correctable warning:B3							
	Not used	Not used	Not used	Not used	RX STATUS6 WNG	RX STATUS0 WNG	HCRC WNG	PCRC WNG
45	SW::RX Correctable warning:B2							
	Not used	Not used	ECRC WNG	Poisoned TLP WNG	Replay Timeout WNG	Bad DLLP WNG	Bad TLP WNG	Receiver Error WNG
46	SW::RX Correctable warning:B1							
	Not used	Not used	Not used	Not used	BF1 RGF ECCWNG 0	BF1 RGF ECCWNG 1	BF0 RGF ECCWNG 0	BF0 RGF ECCWNG 1
47	SW::RX Correctable warning:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	TX SIZE CHK WNG
48	SW::PXR_B_ERR error LOG status:B1							
	Not used	RX STATUS LOG(6)	RX STATUS LOG(5)	RX STATUS LOG(4)	RX STATUS LOG(3)	RX STATUS LOG(2)	RX STATUS LOG(1)	RX STATUS LOG(0)
49	SW::PXR_B_ERR error LOG status:B0							
	RX ERR LOG	RX DERR LOG	Not used	STP ERR LOG	HCRC ERR LOG	PCRC ERR LOG	LEAVE OFF LOG	PADDED LOG

SSBLOG05-3250

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(5) Byte28 = 84 : SW correctable error (2/2)

	0	1	2	3	4	5	6	7
4A	SW::PCI EXPRESS error status:B1							
	RECOV ST ERR	TLM UR ERR	TLM ECRC ERR	TLM MFTLP ERR	TLM PTLP ERR	TLM ROVF ERR	DLM FCPTC ERR	DLM REPLAY TO_ERR
4B	SW::PCI EXPRESS error status:B0							
	DLM REPLAY RO_ERR	DLM DLLP ERR	DLM TLP ERR	DLM PTC ERR	PLM RECEIVER ERR	DLM SURPRISE DW_ERR	LINK WIDTH ERR	DL_UP ERR
4C	SW::BF0_RGF ECC correctable error							
	Not used	Not used	RGFRD1 MUL ECC ERR	RGFRD0 MUL ECC ERR	Not used	Not used	RGFRD1 SGL ECC ERR	RGFRD0 SGL ECC ERR
4D	SW::BF1_RGF ECC correctable error							
	Not used	Not used	RGFRD1 MUL ECC ERR	RGFRD0 MUL ECC ERR	Not used	Not used	RGFRD1 SGL ECC ERR	RGFRD0 SGL ECC ERR
4E	SW::PCI EXPRESS TXVC error status							
	Undefined type TLP ERR	Configuration I/O ERR	TC# Violation On Msg	Uninitialized VC used	Address Boundary ERR	Byte Enable ERR	Length ERR	Max Payload Size ERR
4F	Not used (x'00')							
50	SW::PKT DEC HEADERID:B3							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
51	SW::PKT DEC HEADERID:B2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
52	SW::PKT DEC HEADERID:B1							
	LSI ID7	LSI ID6	LSI ID5	LSI ID4	LSI ID3	LSI ID2	LSI ID1	LSI ID0
53	SW::PKT DEC HEADERID:B0							
	MOD ID7	MOD ID6	MOD ID5	MOD ID4	MOD ID3	MOD ID2	MOD ID1	MOD ID0
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3260

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(6) Byte28 = 81 : LR correctable error (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'81' : LR correctable error							
29	MPID (Reported SSB)							
2A	Module ID, LR LSI number							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

SSBLOG05-3270

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(6) Byte28 = 81 : LR correctable error (2/5)

	0	1	2	3	4	5	6	7
40	Warning INTO_0							
	INT FOP1 WRN	INT FOP0 WRN	Reserved	Reserved	INT XD WRN	Reserved	INT FDMA1 WRN	INT FDMA0 WRN
41	Warning INTO_1							
	INT DCUP	INT DCDOWN	INT ENCEN	INT DA WRN	INT HA1 WRN	INT HA0 WRN	INT PA1 WRN	INT PA0 WRN
42	Warning INTO_2							
	Reserved	INT PPR WRN	INT BRG1 WRN	INT BRG0 WRN	Reserved	Reserved	INT DRR1 WRN	INT DRR0 WRN
43	Warning INTO_3							
	INT PCDX WRN	Reserved	Reserved	INT DMAA WRN	INT DMA3 WRN	INT DMA2 WRN	INT DMA1 WRN	INT DMA0 WRN
44	LRP0_Warning0							
	PCINUL	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LRP0_Warning1							
	Reserved	Reserved	Reserved	CSCR	DSCRW3	DSCRW2	DSCRW1	DSCRW0
46	LRP1_Warning0							
	PCINUL	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LRP1_Warning1							
	Reserved	Reserved	Reserved	CSCR	DSCRW3	DSCRW2	DSCRW1	DSCRW0
48	XDERQ Warning status							
	ERQ7	ERQ6	ERQ5	ERQ4	ERQ3	ERQ2	ERQ1	ERQ0
49	XDKEY Warning status							
	Reserved	Reserved	ECC21	ECC20	EECC11	DECC11	EECC10	DECC10
4A	XDHSN_RX0 Warning status							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	ECC
4B	XDHSN_RX1 Warning status							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	ECC
4C	FDMA0 Warning0							
	PAR ECK WARN (7)	PAR ECK WARN (6)	PAR ECK WARN (5)	PAR ECK WARN (4)	PAR ECK WARN (3)	PAR ECK WARN (2)	PAR ECK WARN (1)	PAR ECK WARN (0)
4D	FDMA0 Warning1							
	MS ECK WARN (7)	MS ECK WARN (6)	MS ECK WARN (5)	MS ECK WARN (4)	MS ECK WARN (3)	MS ECK WARN (2)	MS ECK WARN (1)	MS ECK WARN (0)
4E	FDMA0 Warning2							
	CS ECK WARN (7)	CS ECK WARN (6)	CS ECK WARN (5)	CS ECK WARN (4)	CS ECK WARN (3)	CS ECK WARN (2)	CS ECK WARN (1)	CS ECK WARN (0)
4F	FDMA0 Warning3							
	PAR WARN	MS WARN	CS WARN	DMA DISCARD WARN	Reserved	Reserved	RAM WARN	FDMA WARN

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(6) Byte28 = 81 : LR correctable error (3/5)

	0	1	2	3	4	5	6	7
50	FDMA1 Warning0							
	PAR ECK WARN (7)	PAR ECK WARN (6)	PAR ECK WARN (5)	PAR ECK WARN (4)	PAR ECK WARN (3)	PAR ECK WARN (2)	PAR ECK WARN (1)	PAR ECK WARN (0)
51	FDMA1 Warning1							
	MS ECK WARN (7)	MS ECK WARN (6)	MS ECK WARN (5)	MS ECK WARN (4)	MS ECK WARN (3)	MS ECK WARN (2)	MS ECK WARN (1)	MS ECK WARN (0)
52	FDMA1 Warning2							
	CS ECK WARN (7)	CS ECK WARN (6)	CS ECK WARN (5)	CS ECK WARN (4)	CS ECK WARN (3)	CS ECK WARN (2)	CS ECK WARN (1)	CS ECK WARN (0)
53	FDMA1 Warning3							
	PAR WARN	MS WARN	CS WARN	DMA DISCARD WARN	Reserved	Reserved	RAM WARN	FDMA WARN
54	DMA0 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
55	DMA0 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
56	DMA1 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
57	DMA1 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
58	DMA2 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
59	DMA2 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
5A	DMA3 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
5B	DMA3 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
5C	MD DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
5D	MD DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
5E	MD DMA_BUFF Warning status2							
	HPBF CRCT ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	DA_WARNING							
	WBUFFA 1BERR	WBUFFB 1BERR	RBUFFA 1BERR	RBUFFB 1BERR	Reserved	Reserved	Reserved	Reserved

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(6) Byte28 = 81 : LR correctable error (4/5)

	0	1	2	3	4	5	6	7
60	PA0 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
61	PA0 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
62	PA0 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
63	PA0 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
64	PA1 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
65	PA1 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
66	PA1 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
67	PA1 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
68	HA0 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
69	HA0 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
6A	HA0 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
6B	HA0 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
6C	HA1 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
6D	HA1 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
6E	HA1 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
6F	HA1 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)

SSBLOG05-3300

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(6) Byte28 = 81 : LR correctable error (5/5)

	0	1	2	3	4	5	6	7
70	PCDX_DTX_Status0							
	DTX ERR PCHK	DTX CPTY ERR	DTX WPTY ERR	DTX TXDT PERR	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFI RDDT0 PERR
71	PCDX_DTX_Status1							
	ECC1 WRN ERR	ECC1 UNC ERR	ECC1 ADR ERR	BFI RDDT1 PERR	TO VALID TOERR	TO VALID TOPERR	TO REQACP ERR	TO REQACP PERR
72	PCDX_PIF_TX_Status							
	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFO RDDT PERR	XFRcnt CMP PERR	THRESH CMP PERR	SEQDEC ERR	SEQENQ ERR
73	PCDX_LA_Status							
	ECC WRN ERR	ECC UNC ERR	ECC ADR ERR	Reserved	Reserved	CNT PERR	DMADT PERR	Reserved
74	DRR0_Warning							
	WARNING (31)	WARNING (30)	WARNING (29)	WARNING (28)	WARNING (27)	WARNING (26)	WARNING (25)	WARNING (24)
75	DRR1_Warning							
	WARNING (31)	WARNING (30)	WARNING (29)	WARNING (28)	WARNING (27)	WARNING (26)	WARNING (25)	WARNING (24)
76	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	BRG0_Warning							
	Reserved	Reserved	Reserved	Reserved	PBUF1 COR ERR	PBUF0 COR ERR	HBUF1 COR ERR	HBUF0 COR ERR
79	BRG1_Warning							
	Reserved	Reserved	Reserved	Reserved	PBUF1 COR ERR	PBUF0 COR ERR	HBUF1 COR ERR	HBUF0 COR ERR
7A	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-3310

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(7) Byte28 = 82 : MHUB correctable error (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'82' : MHUB correctable error							
29	MPID (Reported SSB)							
2A	Module ID, LR LSI number							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

SSBLOG05-3320

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(7) Byte28 = 82 : MHUB correctable error (2/5)

	0	1	2	3	4	5	6	7
40	Warning INT0_0							
	INT FOP1 WRN	INT FOP0 WRN	Reserved	Reserved	INT XD WRN	Reserved	INT FDMA1 WRN	INT FDMA0 WRN
41	Warning INT0_1							
	INT DCUP	INT DCDOWN	INT ENCEN	INT DA WRN	INT HA1 WRN	INT HA0 WRN	INT PA1 WRN	INT PA0 WRN
42	Warning INT0_2							
	Reserved	INT PPR WRN	INT BRG1 WRN	INT BRG0 WRN	Reserved	Reserved	INT DRR1 WRN	INT DRR0 WRN
43	Warning INT0_3							
	INT PCDX WRN	Reserved	Reserved	INT DMAA WRN	INT DMA3 WRN	INT DMA2 WRN	INT DMA1 WRN	INT DMA0 WRN
44	LRP0_Warning0							
	PCINUL	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LRP0_Warning1							
	Reserved	Reserved	Reserved	CSCR	DSCRW3	DSCRW2	DSCRW1	DSCRW0
46	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	Not used (x'00')							
	ERQ7	ERQ6	ERQ5	ERQ4	ERQ3	ERQ2	ERQ1	ERQ0
49	XDKEY Warning status							
	Reserved	Reserved	ECC21	ECC20	EECC11	DECC11	EECC10	DECC10
4A	XDHSN_RX0 Warning status							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	ECC
4B	XDHSN_RX1 Warning status							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	ECC
4C	FDMA0 Warning0							
	PAR ECK WARN (7)	PAR ECK WARN (6)	PAR ECK WARN (5)	PAR ECK WARN (4)	PAR ECK WARN (3)	PAR ECK WARN (2)	PAR ECK WARN (1)	PAR ECK WARN (0)
4D	FDMA0 Warning1							
	MS ECK WARN (7)	MS ECK WARN (6)	MS ECK WARN (5)	MS ECK WARN (4)	MS ECK WARN (3)	MS ECK WARN (2)	MS ECK WARN (1)	MS ECK WARN (0)
4E	FDMA0 Warning2							
	CS ECK WARN (7)	CS ECK WARN (6)	CS ECK WARN (5)	CS ECK WARN (4)	CS ECK WARN (3)	CS ECK WARN (2)	CS ECK WARN (1)	CS ECK WARN (0)
4F	FDMA0 Warning3							
	PAR WARN	MS WARN	CS WARN	DMA DISCARD WARN	Reserved	Reserved	RAM WARN	FDMA WARN

SSBLOG05-3330

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(7) Byte28 = 82 : MHUB correctable error (3/5)

	0	1	2	3	4	5	6	7
50	HSNTX0 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	WRAM COR ERR	IBF COR ERR
51	HSNTX1 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	WRAM COR ERR	IBF COR ERR
52	HSNRX0 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RRAM COR ERR
53	HSNRX1 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RRAM COR ERR
54	DMA0 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
55	DMA0 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
56	DMA1 DMA_BUFF Warning status0							
	DBF0 CRCT ERR0	DBF0 CRCT ERR1	DBF0 CRCT ERR2	DBF0 CRCT ERR3	DBF1 CRCT ERR0	DBF1 CRCT ERR1	DBF1 CRCT ERR2	DBF1 CRCT ERR3
57	DMA1 DMA_BUFF Warning status1							
	DBF2 CRCT ERR0	DBF2 CRCT ERR1	DBF2 CRCT ERR2	DBF2 CRCT ERR3	Reserved	Reserved	Reserved	Reserved
58	WRCTL0 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PRAM COR ERR
59	WRCTL1 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PRAM COR ERR
5A	RDCTL0 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PRAM COR ERR
5B	RDCTL1 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PRAM COR ERR
5C	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	DA_WARNING							
	WBUFFA 1BERR	WBUFFB 1BERR	RBUFFA 1BERR	RBUFFB 1BERR	Reserved	Reserved	Reserved	Reserved

SSBLOG05-3340

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(7) Byte28 = 82 : MHUB correctable error (4/5)

	0	1	2	3	4	5	6	7
60	PA0 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
61	PA0 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
62	PA0 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
63	PA0 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
64	PA1 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
65	PA1 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
66	PA1 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
67	PA1 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
68	HA0 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
69	HA0 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
6A	HA0 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
6B	HA0 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)
6C	HA1 Warning0							
	ARB TXBUF 1BERR	Reserved	Reserved	Reserved	ARB RXBUF 1BERR	Reserved	Reserved	Reserved
6D	HA1 Warning1							
	VC0TX ECC 1BERR	RETRY ECC 1BERR	Reserved	Reserved	VC0RX ECC 1BERR	Reserved	Reserved	Reserved
6E	HA1 Warning2							
	TRERR	DLPERR	RBOVR	MALFTLP	Reserved	Reserved	Reserved	LKTR STATE
6F	HA1 Warning3							
	BADTLP	RNPROVR	ACKTO	PLPERR	BADDLLP	NULLTLP	RX1_ERR (DIS=1)	RX0_ERR (DIS=1)

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(7) Byte28 = 82 : MHUB correctable error (5/5)

	0	1	2	3	4	5	6	7
70	PCDX_DTX_Status0							
	DTX ERR PCHK	DTX CPTY ERR	DTX WPTY ERR	DTX TXDT PERR	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFI RDDT0 PERR
71	PCDX_DTX_Status1							
	ECC1 WRN ERR	ECC1 UNC ERR	ECC1 ADR ERR	BFI RDDT1 PERR	TO VALID TOERR	TO VALID TOPERR	TO REQACP ERR	TO REQACP PERR
72	PCDX_PIF_TX_Status							
	ECC0 WRN ERR	ECC0 UNC ERR	ECC0 ADR ERR	BFO RDDT PERR	XFRcnt CMP PERR	THRESH CMP PERR	SEQDEC ERR	SEQENQ ERR
73	PCDX_LA_Status							
	ECC WRN ERR	ECC UNC ERR	ECC ADR ERR	Reserved	Reserved	CNT PERR	DMADT PERR	Reserved
74	DRR0_Warning							
	WARNING (31)	WARNING (30)	WARNING (29)	WARNING (28)	WARNING (27)	WARNING (26)	WARNING (25)	WARNING (24)
75	DRR1_Warning							
	WARNING (31)	WARNING (30)	WARNING (29)	WARNING (28)	WARNING (27)	WARNING (26)	WARNING (25)	WARNING (24)
76	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	BRG0_Warning							
	Reserved	Reserved	Reserved	Reserved	PBUF1 COR ERR	PBUF0 COR ERR	HBUF1 COR ERR	HBUF0 COR ERR
79	BRG1_Warning							
	Reserved	Reserved	Reserved	Reserved	PBUF1 COR ERR	PBUF0 COR ERR	HBUF1 COR ERR	HBUF0 COR ERR
7A	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	CCWXFR0 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	IRX ECCL CORERR	IRX ECCH CORERR
7D	CCWXFR1 ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	IRX ECCL CORERR	IRX ECCH CORERR
7E	CCWPRM ECC correctable error							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	ECC COR ERR
7F	Not used (x'00')							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-3360

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(8) Byte28 = 83 : CMA correctable error (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : CMA correctable error							
29	MPID (Reported SSB)							
2A	Module ID							
2B	PK ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	ANYWRN0							
	PCIBLK250 0 ANYWRN	PCIBLK250 1 ANYWRN	PCIBLK250 2 ANYWRN	PCIBLK250 3 ANYWRN	Not used	Not used	Not used	Not used
41	ANYWRN1							
	Not used	Not used	Not used	Not used	MEMBLK 0 ANYWRN	MEMBLK 1 ANYWRN	Not used	Not used
42	ANYWRN2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
43	ANYWRN3							
	Not used	Not used	Not used	FMB250 PCIBLK ANYWRN	FMB125 PCIBLK ANYWRN	RAMBLK ANYWRN	Not used	Not used
44	RRDCTL Warning error status							
45	Not used (x'00')							
46	RAM Correctable LOG CC0							
47	RAM Correctable LOG CC1							
48	RAM Correctable LOG DATA0							
49	RAM Correctable LOG DATA1							
4A	RAM Correctable LOG DATA2							
4B	RAM Correctable LOG DATA3							
4C	RAM Correctable LOG DATA4							
4D	RAM Correctable LOG DATA5							
4E	RAM Correctable LOG DATA6							
4F	RAM Correctable LOG DATA7							
50	RAM Correctable LOG DATA8							
51	RAM Correctable LOG DATA9							
52	RAM Correctable LOG DATA10							
53	RAM Correctable LOG DATA11							
54	RAM Correctable LOG DATA12							
55	RAM Correctable LOG DATA13							
56	RAM Correctable LOG DATA14							
57	RAM Correctable LOG DATA15							
58	RAM Correctable LOG DATA16							
59	RAM Correctable LOG DATA17							
5A	RAM Correctable LOG DATA18							
5B	RAM Correctable LOG DATA19							
5C	RAM Correctable LOG DATA20							
5D	RAM Correctable LOG DATA21							
5E	RAM Correctable LOG DATA22							
5F	RAM Correctable LOG DATA23							

SSBLOG05-3370

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(8) Byte28 = 83 : CMA correctable error (2/2)

	0	1	2	3	4	5	6	7
60	RAM Correctable LOG DATA24							
61	RAM Correctable LOG DATA25							
62	RAM Correctable LOG DATA26							
63	RAM Correctable LOG DATA27							
64	RAM Correctable LOG DATA28							
65	RAM Correctable LOG DATA29							
66	RAM Correctable LOG DATA30							
67	RAM Correctable LOG DATA31							
68	RAM Correctable error address0_0							
69	RAM Correctable error address0_1							
6A	RAM Correctable error address1_0							
6B	RAM Correctable error address1_1							
6C	RX4BTO32B Path any error status C0							
6D	RX4BTO32B Path any error status C1							
6E	RX4BTO32B Path any error status C2							
6F	RX4BTO32B Path any error status C3							
70	PCI Express core any warning status C0_0							
71	PCI Express core any warning status C0_1							
72	PCI Express core any warning status C0_2							
73	PCI Express core any warning status C0_3							
74	PCI Express core any warning status C1_0							
75	PCI Express core any warning status C1_1							
76	PCI Express core any warning status C1_2							
77	PCI Express core any warning status C1_3							
78	PCI Express core any warning status C2_0							
79	PCI Express core any warning status C2_1							
7A	PCI Express core any warning status C2_2							
7B	PCI Express core any warning status C2_3							
7C	PCI Express core any warning status C3_0							
7D	PCI Express core any warning status C3_1							
7E	PCI Express core any warning status C3_2							
7F	PCI Express core any warning status C3_3							

SSBLOG05-3380

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(9) Byte28 = 11 : MPA correctable error (1/5)

	0	1	2	3	4	5	6	7
28	Sub Code : x'11' : MPA correctable error							
29	MP Number (Reported SSB)							
	MP Number							
2A	MPA:: Module Number							
	Module Number							
2B	MPA:: LSI Number							
	LSI Number							
2C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-3390

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = 11 : MPA correctable error (2/5)

	0	1	2	3	4	5	6	7
40	MPA_ERRSTS_MPx:B5							
	Not used	Not used	DBG_BLK Warning	R_BLK Warning	D3_BLK Warning	D2_BLK Warning	D1_BLK Warning	D0_BLK Warning
41	MPA_ERRSTS_MPx:B4							
	M3_BLK Warning	M2_BLK Warning	M1_BLK Warning	M0_BLK Warning	E3_BLK Warning	E2_BLK Warning	E1_BLK Warning	E0_BLK Warning
42	MPA::E0_ERRSTS_MPx:B5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
43	MPA::E1_ERRSTS_MPx:B5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
44	MPA::E2_ERRSTS_MPx:B5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
45	MPA::E3_ERRSTS_MPx:B5							
	E_CORE_IF 250M Warning2	E_CORE_IF 250M Warning1	E_CORE_IF 250M Warning0	E_CORE_IF 125M Warning	Not used	E_BLK Error10 Warning	E_BLK Error9 Warning	E_BLK Error8 Warning
46	MPA::R_ERRSTS_MPx:B4							
	Not used	Not used	Not used	R_BLK SIP_Error Warning	R_BLK Error3 Warning	R_BLK Error2 Warning	R_BLK Error1 Warning	R_BLK Error0 Warning
47	MPA::HSNSTS_WARNING							
	Reserved	Reserved	Reserved	Reserved	M3 Path HSNSTS Warning	M2 Path HSNSTS Warning	M1 Path HSNSTS Warning	M0 Path HSNSTS Warning
48	MPA::M0_ERRSTS_MPx:B5							
	M0_BLK PCI_250M Warning2	M0_BLK PCI_250M Warning1	M0_BLK PCI_250M Warning0	M0_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
49	MPA::M0_ERRSTS_MPx:B4							
	M0_BLK Error7 Warning	M0_BLK Error6 Warning	M0_BLK Error5 Warning	M0_BLK Error4 Warning	M0_BLK Error3 Warning	M0_BLK Error2 Warning	M0_BLK Error1 Warning	M0_BLK Error0 Warning
4A	MPA::M1_ERRSTS_MPx:B5							
	M1_BLK PCI_250M Warning2	M1_BLK PCI_250M Warning1	M1_BLK PCI_250M Warning0	M1_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
4B	MPA::M1_ERRSTS_MPx:B4							
	M1_BLK Error7 Warning	M1_BLK Error6 Warning	M1_BLK Error5 Warning	M1_BLK Error4 Warning	M1_BLK Error3 Warning	M1_BLK Error2 Warning	M1_BLK Error1 Warning	M1_BLK Error0 Warning
4C	MPA::M2_ERRSTS_MPx:B5							
	M2_BLK PCI_250M Warning2	M2_BLK PCI_250M Warning1	M2_BLK PCI_250M Warning0	M2_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
4D	MPA::M2_ERRSTS_MPx:B4							
	M2_BLK Error7 Warning	M2_BLK Error6 Warning	M2_BLK Error5 Warning	M2_BLK Error4 Warning	M2_BLK Error3 Warning	M2_BLK Error2 Warning	M2_BLK Error1 Warning	M2_BLK Error0 Warning
4E	MPA::M3_ERRSTS_MPx:B5							
	M3_BLK PCI_250M Warning2	M3_BLK PCI_250M Warning1	M3_BLK PCI_250M Warning0	M3_BLK PCI_125M Warning	Not used	Not used	Not used	Not used
4F	MPA::M3_ERRSTS_MPx:B4							
	M3_BLK Error7 Warning	M3_BLK Error6 Warning	M3_BLK Error5 Warning	M3_BLK Error4 Warning	M3_BLK Error3 Warning	M3_BLK Error2 Warning	M3_BLK Error1 Warning	M3_BLK Error0 Warning

SSBLOG05-3400

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = 11 : MPA correctable error (3/5)

	0	1	2	3	4	5	6	7
50	MPA::E0_SR0_M_ERR:B0							
	E0 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	E0 BLK SEQ0_R0 ENC Error	E0 BLK SEQ0_R0 DEC Error
51	MPA::E0_PXRB_250_ERR:B3							
	Not used	E0 BLK TLM_RX STS Error6	E0 BLK TLM_RX STS Error5	E0 BLK TLM_RX STS Error4	E0 BLK TLM_RX STS Error3	E0 BLK TLM_RX STS Error2	E0 BLK TLM_RX STS Error1	E0 BLK TLM_RX STS Error0
52	MPA::E0_PXRHL_ERRSTS:B1							
	Not used	E0 BLK Unsupported Request	E0 BLK ECRC Error	E0 BLK Malformed TLP	E0 BLK Poisoned TLP	E0 BLK Receiver Overflow	E0 BLK Flow Control Protocol Error	E0 BLK Replay Timeout
53	MPA::E0_PXRHL_ERRSTS:B0							
	E0 BLK Replay Rollover	E0 BLK Bad DLLP	E0 BLK Bad TLP	E0 BLK DL Protocol Error	E0 BLK Receiver Error	E0 BLK Surprise Down	E0 BLK Link Width Error	E0 BLK DL UP Fail
54	MPA::E0_PXTHL_TXVC_ERRSTS:B0							
	E0 BLK TX DATAP1 CHK	E0 BLK TX DATAP0 CHK	E0 BLK TX CTLP CHK	Not used	Not used	E0 BLK TX PRCL CHK	E0 BLK TX SIZE CHK	E0 BLK TX MFTLP CHK
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	MPA::E1_SR0_M_ERR:B0							
	E1 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	E1 BLK SEQ0_R0 ENC Error	E1 BLK SEQ0_R0 DEC Error
57	MPA::E1_PXRB_250_ERR:B3							
	Not used	E1 BLK TLM_RX STS Error6	E1 BLK TLM_RX STS Error5	E1 BLK TLM_RX STS Error4	E1 BLK TLM_RX STS Error3	E1 BLK TLM_RX STS Error2	E1 BLK TLM_RX STS Error1	E1 BLK TLM_RX STS Error0
58	MPA::E1_PXRHL_ERRSTS:B1							
	Not used	E1 BLK Unsupported Request	E1 BLK ECRC Error	E1 BLK Malformed TLP	E1 BLK Poisoned TLP	E1 BLK Receiver Overflow	E1 BLK Flow Control Protocol Error	E1 BLK Replay Timeout
59	MPA::E1_PXRHL_ERRSTS:B0							
	E1 BLK Replay Rollover	E1 BLK Bad DLLP	E1 BLK Bad TLP	E1 BLK DL Protocol Error	E1 BLK Receiver Error	E1 BLK Surprise Down	E1 BLK Link Width Error	E1 BLK DL UP Fail
5A	MPA::E1_PXTHL_TXVC_ERRSTS:B0							
	E1 BLK TX DATAP1 CHK	E1 BLK TX DATAP0 CHK	E1 BLK TX CTLP CHK	Not used	Not used	E1 BLK TX PRCL CHK	E1 BLK TX SIZE CHK	E1 BLK TX MFTLP CHK
5B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	MPA::E2_SR0_M_ERR:B0							
	E2 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	E2 BLK SEQ0_R0 ENC Error	E2 BLK SEQ0_R0 DEC Error
5D	MPA::E2_PXRB_250_ERR:B3							
	Not used	E2 BLK TLM_RX STS Error6	E2 BLK TLM_RX STS Error5	E2 BLK TLM_RX STS Error4	E2 BLK TLM_RX STS Error3	E2 BLK TLM_RX STS Error2	E2 BLK TLM_RX STS Error1	E2 BLK TLM_RX STS Error0
5E	MPA::E2_PXRHL_ERRSTS:B1							
	Not used	E2 BLK Unsupported Request	E2 BLK ECRC Error	E2 BLK Malformed TLP	E2 BLK Poisoned TLP	E2 BLK Receiver Overflow	E2 BLK Flow Control Protocol Error	E2 BLK Replay Timeout
5F	MPA::E2_PXRHL_ERRSTS:B0							
	E2 BLK Replay Rollover	E2 BLK Bad DLLP	E2 BLK Bad TLP	E2 BLK DL Protocol Error	E2 BLK Receiver Error	E2 BLK Surprise Down	E2 BLK Link Width Error	E2 BLK DL UP Fail

SSBLOG05-3410

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = 11 : MPA correctable error (4/5)

	0	1	2	3	4	5	6	7
60	MPA::E2_PXTHL_TXVC_ERRSTS:B0							
	E2 BLK TX DATAP1 CHK	E2 BLK TX DATAP0 CHK	E2 BLK TX CTLP CHK	Not used	Not used	E2 BLK TX PRCL CHK	E2 BLK TX SIZE CHK	E2 BLK TX MFTLP CHK
61	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	MPA::E3_SR0_M_ERR:B0							
	E3 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	E3 BLK SEQ0_R0 ENC Error	E3 BLK SEQ0_R0 DEC Error
63	MPA::E3_PXRB_250_ERR:B3							
	Not used	E3 BLK TLM_RX STS Error6	E3 BLK TLM_RX STS Error5	E3 BLK TLM_RX STS Error4	E3 BLK TLM_RX STS Error3	E3 BLK TLM_RX STS Error2	E3 BLK TLM_RX STS Error1	E3 BLK TLM_RX STS Error0
64	MPA::E3_PXRHL_ERRSTS:B1							
	Not used	E3 BLK Unsupported Request	E3 BLK ECRC Error	E3 BLK Malformed TLP	E3 BLK Poisoned TLP	E3 BLK Receiver Overflow	E3 BLK Flow Control Protocol Error	E3 BLK Replay Timeout
65	MPA::E3_PXRHL_ERRSTS:B0							
	E3 BLK Replay Rollover	E3 BLK Bad DLLP	E3 BLK Bad TLP	E3 BLK DL Protocol Error	E3 BLK Receiver Error	E3 BLK Surprise Down	E3 BLK Link Width Error	E3 BLK DL UP Fail
66	MPA::E3_PXTHL_TXVC_ERRSTS:B0							
	E3 BLK TX DATAP1 CHK	E3 BLK TX DATAP0 CHK	E3 BLK TX CTLP CHK	Not used	Not used	E3 BLK TX PRCL CHK	E3 BLK TX SIZE CHK	E3 BLK TX MFTLP CHK
67	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	MPA::M0_SR0_M_ERR:B0							
	M0 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	M0 BLK SEQ0_R0 ENC Error	M0 BLK SEQ0_R0 DEC Error
69	MPA::M0_PXRB_250_ERR:B3							
	Not used	M0 BLK TLM_RX STS Error6	M0 BLK TLM_RX STS Error5	M0 BLK TLM_RX STS Error4	M0 BLK TLM_RX STS Error3	M0 BLK TLM_RX STS Error2	M0 BLK TLM_RX STS Error1	M0 BLK TLM_RX STS Error0
6A	MPA::M0_PXRHL_ERRSTS:B1							
	Not used	M0 BLK Unsupported Request	M0 BLK ECRC Error	M0 BLK Malformed TLP	M0 BLK Poisoned TLP	M0 BLK Receiver Overflow	M0 BLK Flow Control Protocol Error	M0 BLK Replay Timeout
6B	MPA::M0_PXRHL_ERRSTS:B0							
	M0 BLK Replay Rollover	M0 BLK Bad DLLP	M0 BLK Bad TLP	M0 BLK DL Protocol Error	M0 BLK Receiver Error	M0 BLK Surprise Down	M0 BLK Link Width Error	M0 BLK DL UP Fail
6C	MPA::M0_PXTHL_TXVC_ERRSTS:B0							
	M0 BLK TX DATAP1 CHK	M0 BLK TX DATAP0 CHK	M0 BLK TX CTLP CHK	Not used	Not used	M0 BLK TX PRCL CHK	M0 BLK TX SIZE CHK	M0 BLK TX MFTLP CHK
6D	MPA::M1_SR0_M_ERR:B0							
	M1 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	M1 BLK SEQ0_R0 ENC Error	M1 BLK SEQ0_R0 DEC Error
6E	MPA::M1_PXRB_250_ERR:B3							
	Not used	M1 BLK TLM_RX STS Error6	M1 BLK TLM_RX STS Error5	M1 BLK TLM_RX STS Error4	M1 BLK TLM_RX STS Error3	M1 BLK TLM_RX STS Error2	M1 BLK TLM_RX STS Error1	M1 BLK TLM_RX STS Error0
6F	MPA::M1_PXRHL_ERRSTS:B1							
	Not used	M1 BLK Unsupported Request	M1 BLK ECRC Error	M1 BLK Malformed TLP	M1 BLK Poisoned TLP	M1 BLK Receiver Overflow	M1 BLK Flow Control Protocol Error	M1 BLK Replay Timeout

SSBLOG05-3420

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = 11 : MPA correctable error (5/5)

	0	1	2	3	4	5	6	7
70	MPA::M1_PXRHL_ERRSTS:B0							
	M1 BLK Replay Rollover	M1 BLK Bad DLLP	M1 BLK Bad TLP	M1 BLK DL Protocol Error	M1 BLK Receiver Error	M1 BLK Surprise Down	M1 BLK Link Width Error	M1 BLK DL UP Fail
71	MPA::M1_PXTHL_TXVC_ERRSTS:B0							
	M1 BLK TX DATAP1 CHK	M1 BLK TX DATAP0 CHK	M1 BLK TX CTLP CHK	Not used	Not used	M1 BLK TX PRCL CHK	M1 BLK TX SIZE CHK	M1 BLK TX MFTLP CHK
72	MPA::M2_SR0_M_ERR:B0							
	M2 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	M2 BLK SEQ0_R0 ENC Error	M2 BLK SEQ0_R0 DEC Error
73	MPA::M2_PXRB_250_ERR:B3							
	Not used	M2 BLK TLM_RX STS Error6	M2 BLK TLM_RX STS Error5	M2 BLK TLM_RX STS Error4	M2 BLK TLM_RX STS Error3	M2 BLK TLM_RX STS Error2	M2 BLK TLM_RX STS Error1	M2 BLK TLM_RX STS Error0
74	MPA::M2_PXRHL_ERRSTS:B1							
	Not used	M2 BLK Unsupported Requet	M2 BLK ECRC Error	M2 BLK Malformed TLP	M2 BLK Poisoned TLP	M2 BLK Receiver Overflow	M2 BLK Flow Control Protocol Error	M2 BLK Replay Timeout
75	MPA::M2_PXRHL_ERRSTS:B0							
	M2 BLK Replay Rollover	M2 BLK Bad DLLP	M2 BLK Bad TLP	M2 BLK DL Protocol Error	M2 BLK Receiver Error	M2 BLK Surprise Down	M2 BLK Link Width Error	M2 BLK DL UP Fail
76	MPA::M2_PXTHL_TXVC_ERRSTS:B0							
	M2 BLK TX DATAP1 CHK	M2 BLK TX DATAP0 CHK	M2 BLK TX CTLP CHK	Not used	Not used	M2 BLK TX PRCL CHK	M2 BLK TX SIZE CHK	M2 BLK TX MFTLP CHK
77	MPA::M3_SR0_M_ERR:B0							
	M3 BLK SEQ0_R0 STP Error	Not used	Not used	Not used	Not used	Not used	M3 BLK SEQ0_R0 ENC Error	M3 BLK SEQ0_R0 DEC Error
78	MPA::M3_PXRB_250_ERR:B3							
	Not used	M3 BLK TLM_RX STS Error6	M3 BLK TLM_RX STS Error5	M3 BLK TLM_RX STS Error4	M3 BLK TLM_RX STS Error3	M3 BLK TLM_RX STS Error2	M3 BLK TLM_RX STS Error1	M3 BLK TLM_RX STS Error0
79	MPA::M3_PXRHL_ERRSTS:B1							
	Not used	M3 BLK Unsupported Requet	M3 BLK ECRC Error	M3 BLK Malformed TLP	M3 BLK Poisoned TLP	M3 BLK Receiver Overflow	M3 BLK Flow Control Protocol Error	M3 BLK Replay Timeout
7A	MPA::M3_PXRHL_ERRSTS:B0							
	M3 BLK Replay Rollover	M3 BLK Bad DLLP	M3 BLK Bad TLP	M3 BLK DL Protocol Error	M3 BLK Receiver Error	M3 BLK Surprise Down	M3 BLK Link Width Error	M3 BLK DL UP Fail
7B	MPA::M3_PXTHL_TXVC_ERRSTS:B0							
	M3 BLK TX DATAP1 CHK	M3 BLK TX DATAP0 CHK	M3 BLK TX CTLP CHK	Not used	Not used	M3 BLK TX PRCL CHK	M3 BLK TX SIZE CHK	M3 BLK TX MFTLP CHK
7C	MPA::R_ERRDTL00_Warning:B5							
	Not used	Not used	Not used	Not used	MP Serial3 Data Parity Error	MP Serial2 Data Parity Error	MP Serial1 Data Parity Error	MP Serial0 Data Parity Error
7D	MPA::R_ERRDTL01_Warning:B7							
	Not used	Not used	Not used	Not used	Not used	SBTMR Scan Polling Read Timeout	SBTMR Scan Read Timeout	SBTMR Scan Write Timeout
7E	MPA::R_ERRDTL03_Warning:B0							
	Not used	Not used	Not used	Not used	Not used	Not used	SCANM1 Byte Enable Parity Error	SCANM0 Byte Enable Parity Error
7F	[8B]SIPERRDTL_warning							
	Not used	Not used	Not used	Not used	Not used	SPROM Register Parity Error	Not used	SPROM I2C Error

SSBLOG05-3430

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(10) Byte28 = 40 : Abnormal DC Voltage control warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'40' : Abnormal DC Voltage control warning							
29	MPID (Reported SSB)							
2A	Register access result BYTE (00:No error, 01:Error detected)							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Threshold counter for the warning (Module1, CHA-1EU)				Threshold counter for the warning (Module0, CHA-1EU)			
41	Threshold counter for the warning (Module1, CHA-1EL)				Threshold counter for the warning (Module0, CHA-1EL)			
42	Threshold counter for the warning (Module1, CHA-1FU)				Threshold counter for the warning (Module0, CHA-1FU)			
43	Threshold counter for the warning (Module1, CHA-1FL)				Threshold counter for the warning (Module0, CHA-1FL)			
44	Threshold counter for the warning (Module1, DKA-1AU)				Threshold counter for the warning (Module0, DKA-1AU)			
45	Threshold counter for the warning (Module1, DKA-1AL)				Threshold counter for the warning (Module0, DKA-1AL)			
46	Threshold counter for the warning (Module1, ESW-1SA)				Threshold counter for the warning (Module0, ESW-1SA)			
47	Threshold counter for the warning (Module1, ESW-1SB)				Threshold counter for the warning (Module0, ESW-1SB)			
48	Threshold counter for the warning (Module1, CM-1CA)				Threshold counter for the warning (Module0, CM-1CA)			
49	Threshold counter for the warning (Module1, CM-1CB)				Threshold counter for the warning (Module0, CM-1CB)			
4A	Threshold counter for the warning (Module1, CM-1CE)				Threshold counter for the warning (Module0, CM-1CE)			
4B	Threshold counter for the warning (Module1, CM-1CF)				Threshold counter for the warning (Module0, CM-1CF)			
4C	Threshold counter for the warning (Module1, MP-1MA)				Threshold counter for the warning (Module0, MP-1MA)			
4D	Threshold counter for the warning (Module1, MP-1MB)				Threshold counter for the warning (Module0, MP-1MB)			
4E	Not used (x'00')				Not used (x'00')			
4F	Not used (x'00')				Not used (x'00')			
50	Threshold counter for the warning (Module1, CHA-2EU)				Threshold counter for the warning (Module0, CHA-2EU)			
51	Threshold counter for the warning (Module1, CHA-2EL)				Threshold counter for the warning (Module0, CHA-2EL)			
52	Threshold counter for the warning (Module1, CHA-2FU)				Threshold counter for the warning (Module0, CHA-2FU)			
53	Threshold counter for the warning (Module1, CHA-2FL)				Threshold counter for the warning (Module0, CHA-2FL)			
54	Threshold counter for the warning (Module1, DKA-2AU)				Threshold counter for the warning (Module0, DKA-2AU)			
55	Threshold counter for the warning (Module1, DKA-2AL)				Threshold counter for the warning (Module0, DKA-2AL)			
56	Threshold counter for the warning (Module1, ESW-2SA)				Threshold counter for the warning (Module0, ESW-2SA)			
57	Threshold counter for the warning (Module1, ESW-2SB)				Threshold counter for the warning (Module0, ESW-2SB)			
58	Threshold counter for the warning (Module1, CM-2CA)				Threshold counter for the warning (Module0, CM-2CA)			
59	Threshold counter for the warning (Module1, CM-2CB)				Threshold counter for the warning (Module0, CM-2CB)			
5A	Threshold counter for the warning (Module1, CM-2CE)				Threshold counter for the warning (Module0, CM-2CE)			
5B	Threshold counter for the warning (Module1, CM-2CF)				Threshold counter for the warning (Module0, CM-2CF)			
5C	Threshold counter for the warning (Module1, MP-2MA)				Threshold counter for the warning (Module0, MP-2MA)			
5D	Threshold counter for the warning (Module1, MP-2MB)				Threshold counter for the warning (Module0, MP-2MB)			
5E	Not used (x'00')				Not used (x'00')			
5F	Not used (x'00')				Not used (x'00')			

SSBLOG05-3440

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(10) Byte28 = 40 : Abnormal DC Voltage control warning (2/3)

	0	1	2	3	4	5	6	7
60	Voltage control register information (Module1, CHA-1EU)				Voltage control register information (Module0, CHA-1EU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
61	Voltage control register information (Module1, CHA-1EL)				Voltage control register information (Module0, CHA-1EL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
62	Voltage control register information (Module1, CHA-1FU)				Voltage control register information (Module0, CHA-1FU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
63	Voltage control register information (Module1, CHA-1FL)				Voltage control register information (Module0, CHA-1FL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
64	Voltage control register information (Module1, DKA-1AU)				Voltage control register information (Module0, DKA-1AU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
65	Voltage control register information (Module1, DKA-1AL)				Voltage control register information (Module0, DKA-1AL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
66	Voltage control register information (Module1, ESW-1SA)				Voltage control register information (Module0, ESW-1SA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
67	Voltage control register information (Module1, ESW-1SB)				Voltage control register information (Module0, ESW-1SB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
68	Voltage control register information (Module1, CM-1CA)				Voltage control register information (Module0, CM-1CA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
69	Voltage control register information (Module1, CM-1CB)				Voltage control register information (Module0, CM-1CB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
6A	Voltage control register information (Module1, CM-1CE)				Voltage control register information (Module0, CM-1CE)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
6B	Voltage control register information (Module1, CM-1CF)				Voltage control register information (Module0, CM-1CF)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
6C	Voltage control register information (Module1, MP-1MA)				Voltage control register information (Module0, MP-1MA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
6D	Voltage control register information (Module1, MP-1MB)				Voltage control register information (Module0, MP-1MB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
6E	Not used (x'00')				Not used (x'00')			
6F	Not used (x'00')				Not used (x'00')			

SSBLOG05-3450

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(10) Byte28 = 40 : Abnormal DC Voltage control warning (3/3)

	0	1	2	3	4	5	6	7
70	Voltage control register information (Module1, CHA-2EU)				Voltage control register information (Module0, CHA-2EU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
71	Voltage control register information (Module1, CHA-2EL)				Voltage control register information (Module0, CHA-2EL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
72	Voltage control register information (Module1, CHA-2FU)				Voltage control register information (Module0, CHA-2FU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
73	Voltage control register information (Module1, CHA-2FL)				Voltage control register information (Module0, CHA-2FL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
74	Voltage control register information (Module1, DKA-2AU)				Voltage control register information (Module0, DKA-2AU)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
75	Voltage control register information (Module1, DKA-2AL)				Voltage control register information (Module0, DKA-2AL)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
76	Voltage control register information (Module1, ESW-2SA)				Voltage control register information (Module0, ESW-2SA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
77	Voltage control register information (Module1, ESW-2SB)				Voltage control register information (Module0, ESW-2SB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
78	Voltage control register information (Module1, CM-2CA)				Voltage control register information (Module0, CM-2CA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
79	Voltage control register information (Module1, CM-2CB)				Voltage control register information (Module0, CM-2CB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
7A	Voltage control register information (Module1, CM-2CE)				Voltage control register information (Module0, CM-2CE)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
7B	Voltage control register information (Module1, CM-2CF)				Voltage control register information (Module0, CM-2CF)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
7C	Voltage control register information (Module1, MP-2MA)				Voltage control register information (Module0, MP-2MA)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
7D	Voltage control register information (Module1, MP-2MB)				Voltage control register information (Module0, MP-2MB)			
	DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)		DC_CTL_EN	DC_UP (+5%)	DC_DWN (-5%)	
7E	Not used (x'00')				Not used (x'00')			
7F	Not used (x'00')				Not used (x'00')			

SSBLOG05-3460

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(11) Byte28 = 41 : Abnormal CEMODE warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'41' : Abnormal CEMODE warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	PK status							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
41	UPDOWN							
	Reserved	Reserved	Down(Moni)	Up(Moni)	Reserved	Reserved	Down(set)	Up(set)
42	Not used (x'00')							
43	Not used (x'00')							
44	Not used (x'00')							
45	Not used (x'00')							
46	Not used (x'00')							
47	Not used (x'00')							
48	Not used (x'00')							
49	Not used (x'00')							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60								
7F	Not used (x'00')							

SSBLOG05-3470

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(12) Byte28 = 42 : Abnormal CEDT warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'42' : Abnormal CEDT warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	PK status							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
41	Not used (x'00')							
42	Not used (x'00')							
43	Not used (x'00')							
44	Not used (x'00')							
45	Not used (x'00')							
46	Not used (x'00')							
47	Not used (x'00')							
48	Not used (x'00')							
49	Not used (x'00')							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3480

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(13) Byte28 = C0 : SFP (Tachyon) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C0' : SFP (Tachyon) warning							
29	MPID (Reported SSB)							
2A	Module ID, Tx_Fault detection LR_LSI number							
2B	Tx_Fault detection port number							
2C	LR TX_FAULT warning register							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR Tx_Fault detection port number0 (0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning0
41	LR Tx_Fault detection port number0 (1)							
	Not used	Not used	Not used	Not used	Port3 Tx_Fault	Port2 Tx_Fault	Port1 Tx_Fault	Port0 Tx_Fault
42	LR Tx_Fault detection port number1 (0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning1
43	LR Tx_Fault detection port number1 (1)							
	Not used	Not used	Not used	Not used	Port7 Tx_Fault	Port6 Tx_Fault	Port5 Tx_Fault	Port4 Tx_Fault
44	Device address "A0", Data address "6"							
45	Device address "A0", Data address "10"							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5D	Device address "A0", Data address "73"							
5E	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

SSBLOG05-3490

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(13) Byte28 = C0 : SFP (Tachyon) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3500

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(14) Byte28 = C2 : SFP (FIVE) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C2' : SFP (FIVE) warning							
29	MPID (Reported SSB)							
2A	Module ID, Tx_Fault detection LR_LSI number							
2B	Tx_Fault detection port number							
2C	LR TX_FAULT warning register							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR Tx_Fault detection port number0 (0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning0
41	Not used (x'00')							
42	Not used (x'00')							
43	Not used (x'00')							
44	Device address "A0", Data address "6"							
45	Device address "A0", Data address "10"							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5E	Device address "A0", Data address "73"							
5D	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

SSBLOG05-3510

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(14) Byte28 = C2 : SFP (FIVE) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3520

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(15) Byte28 = 45 : Battery warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'45' : Battery warning							
29	MPID (Reported SSB)							
2A	Module ID							
2B	PK ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	Parts status0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	Parts status1							
	Not used	Not used	Not used	Not used	Not used	Not used	CM Backup warning	Battery Warning
42	Parts status2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	FAN Control Warning
43	Parts status3							
	BRG_P warning	BRG_S warning	SH Micon warning	SSD1 warning	SSD0 warning	Battery Controller warning	Battery warning	Battery Charge
44	Battery status0							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
45	Battery status1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
46	Battery status2							
	battery Full	Not used	Not used	Not used	Not used	Not used	Not used	Battery Half
47	Battery status3							
	Not used	Not used	Not used	Not used	Not used	Not used	Battery empty	Not used
48	Parts firm exchange status0							
	SPROM Erase fail	SPROM Erase completion	Not used	Not used	SSD1 Erase fail	SSD0 Erase fail	SSD1 Erase completion	SSD0 Erase Completion
49	Parts firm exchange status1							
	Not used	Not used	Not used	Not used	BATC_FIRM Exchange fail	SSD1_FIRM Exchange fail	SSD0_FIRM Exchange fail	SH_FIRM Exchange fail
4A	Parts firm exchange status2							
	BATC_FIRM Exchange Done	SSD1_FIRM Exchange done	SSD0_FIRM Exchange done	SH_FIRM Exchange done	BATC_FIRM Exchange busy	SSD1_FIRM Exchange busy	SSD0_FIRM Exchange busy	SH_FIRM Exchange Busy
4B	Parts firm exchange status3							
	Not used	Not used	Not used	Not used	Not used	Battery Exchange fail	Battery Exchange done	Battery Exchange ready

SSBLOG05-3530

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(15) Byte28 = 45 : Battery warning (2/3)

	0	1	2	3	4	5	6	7
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Battery controller LOG00							
51	Battery controller LOG01							
52	Battery controller LOG02							
53	Battery controller LOG03							
54	Battery controller LOG04							
55	Battery controller LOG05							
56	Battery controller LOG06							
57	Battery controller LOG07							
58	Battery controller LOG08							
59	Battery controller LOG09							
5A	Battery controller LOG10							
5B	Battery controller LOG11							
5C	Battery controller LOG12							
5D	Battery controller LOG13							
5E	Battery controller LOG14							
5F	Battery controller LOG15							

SSBLOG05-3540

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(15) Byte28 = 45 : Battery warning (3/3)

	0	1	2	3	4	5	6	7
60	Battery controller LOG16							
61	Battery controller LOG17							
62	Battery controller LOG18							
63	Battery controller LOG19							
64	Battery controller LOG20							
65	Battery controller LOG21							
66	Battery controller LOG22							
67	Battery controller LOG23							
68	Battery controller LOG24							
69	Battery controller LOG25							
6A	Battery controller LOG26							
6B	Battery controller LOG27							
6C	Battery controller LOG28							
6D	Battery controller LOG29							
6E	Battery controller LOG30							
6F	Battery controller LOG31							
70	Battery controller LOG32							
71	Battery controller LOG33							
72	Battery controller LOG34							
73	Battery controller LOG35							
74	Battery controller LOG36							
75	Battery controller LOG37							
76	Battery controller LOG38							
77	Battery controller LOG39							
78	Battery controller LOG40							
79	Battery controller LOG41							
7A	Battery controller LOG42							
7B	Battery controller LOG43							
7C	Battery controller LOG44							
7D	Battery controller LOG45							
7E	Battery controller LOG46							
7F	Battery controller LOG47							

SSBLOG05-3550

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(16) Byte28 = 46 : SSD warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'46' : SSD warning							
29	MPID (Reported SSB)							
2A	Module ID							
2B	PK ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Store / Restore status0							
	DIAG restore done	DIAG store done	Not used	Not used	SSD Inline done	SSD CUDG5 done	SSD CUDG4 done	SSD CUDG3 done
41	Store / Restore status1							
	Not used	Not used	Not used	Internal DIAG running	Not used	Not used	Not used	Invalid Command error
42	Store / Restore status2							
	Not used	Not used	Config done	Config fail	Restore done	Restore busy	Restore fail	Restore success
43	Store / Restore status3							
	Restore volatile	Not used	Not used	Not used	Store done	Store busy	Store fail	Store Success
44	PARTS status0							
	Not used	Not used	Not used	Not used	Not used	Not used	CM Backup warning	Battery Warning
45	PARTS status1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	FAN Control Warning
46	PARTS status2							
	BRG_P warning	BRG_S warning	SH Micon warning	SSD1 warning	SSD0 warning	Battery Controller warning	Battery warning	Battery Charge
47	SH bus error status(GPIO)							
	Not used	Not used	Not used	Not used	SH Bus warning	SH Bus(micro) warning	PK LED test	PK Thermistor alarm
48	Parts Firm exchange status0							
	SPROM Erase fail	SPROM Erase completion	Not used	Not used	SSD1 Erase fail	SSD0 Erase fail	SSD1 Erase completion	SSD0 Erase Completion
49	Parts Firm exchange status1							
	Not used	Not used	Not used	Not used	BATC_FIRM Exchange fail	SSD1_FIRM Exchange fail	SSD0_FIRM Exchange fail	SH_FIRM Exchange fail
4A	Parts Firm exchange status2							
	BATC_FIRM Exchange Done	SSD1_FIRM Exchange done	SSD0_FIRM Exchange done	SH_FIRM Exchange done	BATC_FIRM Exchange busy	SSD1_FIRM Exchange busy	SSD0_FIRM Exchange busy	SH_FIRM Exchange Busy
4B	Parts Firm exchange status3							
	Not used	Not used	Not used	Not used	Not used	Battery Exchange fail	Battery Exchange done	Battery Exchange ready

SSBLOG05-3560

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(16) Byte28 = 46 : SSD warning (2/3)

	0	1	2	3	4	5	6	7
4C	SSD CONFIG0							
	Not used	Not used	Not used	Not used	Not used	SSD1 Capa 64GB	SSD1 Capa 32GB	Not used
4D	SSD CONFIG1							
	Not used	Not used	Not used	Not used	Not used	SSD0 Capa 64GB	SSD0 Capa 32GB	Not used
4E	Store error LOG							
	Not used	Not used	Not used	Not used	Restore Error_log MG3	Restore Error_log MG2	Restore Error_log MG1	Restore Error_log MG0
4F	Restore error LOG							
	Not used	Not used	Not used	Not used	store Error_log MG3	store Error_log MG2	store Error_log MG1	store Error_log MG0
50	SH_micro Firm revision0							
51	SH_micro Firm revision1							
52	SH_micro Firm revision2							
53	SH_micro Firm revision3							
54	Bridge_Chip error status (PCI express) 0							
55	Bridge_Chip error status (PCI express) 1							
56	Bridge_Chip error status (PCI express) 2							
57	Bridge_Chip error status (PCI express) 3							
58	Bridge_Chip error status (SATA_P0) 0							
59	Bridge_Chip error status (SATA_P0) 1							
5A	Bridge_Chip error status (SATA_P0) 2							
5B	Bridge_Chip error status (SATA_P0) 3							
5C	Bridge_Chip error status (SATA_P1) 0							
5D	Bridge_Chip error status (SATA_P1) 1							
5E	Bridge_Chip error status (SATA_P1) 2							
5F	Bridge_Chip error status (SATA_P1) 3							

SSBLOG05-3570

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(16) Byte28 = 46 : SSD warning (3/3)

	0	1	2	3	4	5	6	7
60	SSD0 SATA error status0							
61	SSD0 SATA error status1							
62	SSD0 RFIS error status0							
63	SSD0 RFIS error status1							
64	SSD1 SATA error status0							
65	SSD1 SATA error status1							
66	SSD1 RFIS error status0							
67	SSD1 RFIS error status1							
68	SSD model number0							
69	SSD model number1							
6A	SSD model number2							
6B	SSD model number3							
6C	SSD model number4							
6D	SSD model number5							
6E	SSD model number6							
6F	SSD model number7							
70	SSD SMART0							
71	SSD SMART1							
72	SSD SMART2							
73	SSD SMART3							
74	SSD SMART4							
75	SSD SMART5							
76	SSD SMART6							
77	SSD SMART7							
78	SSD SMART8							
79	SSD SMART9							
7A	SSD SMART10							
7B	SSD SMART11							
7C	SSD SMART12							
7D	SSD SMART13							
7E	SSD SMART14							
7F	SSD SMART15							

SSBLOG05-3580

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(17) Byte28 = 48 : MPPK Abnormal temperature warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'48' : MPPK Abnormal temperature warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	IA32 Thermistor status0 :B7							
	Reserved							
41	IA32 Thermistor status1 :B6							
	Reserved							
42	IA32 Thermistor status2 :B5							
	Reserved							
43	IA32 Thermistor status3 :B4							
	Reserved							
44	IA32 Thermistor status4 :B3							
Reading Valid	Resolution in Degrees Celsius					Reserved	Reserved	Reserved
45	IA32 Thermistor status5 :B2							
Reserved	Digital Readout							
46	IA32 Thermistor status6 :B1							
Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Thmal Thres #2 Log	Thmal Thres #2 Status
47	IA32 Thermistor status7 :B0							
Thmal Thres #1 Log	Thmal Thres #1 Status	Out of Spec Status Log	Out of Spec Status	PRCHOT# or FRCEPR# Log	PRCHOT# or FRCEPR# Eve	Thmal Status Log	Thmal Status	
48	LM84(0) Local temperature							
	Local Temp(0)							
49	LM84(1) Local temperature							
	Local Temp(1)							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60								
7F	Not used (x'00')							

SSBLOG05-3590

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (1/6)

	0	1	2	3	4	5	6	7
28	Subcode : x'49' : PK Abnormal temperature warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR/MHUB Abnormal temperature register (Module0, LR#0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
41	LR/MHUB Abnormal temperature register (Module0, LR#1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
42	LR/MHUB Abnormal temperature register (Module0, LR#2)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
43	LR/MHUB Abnormal temperature register (Module0, LR#3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
44	LR/MHUB Abnormal temperature register (Module0, LR#4)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
45	LR/MHUB Abnormal temperature register (Module0, LR#5)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
46	LR/MHUB Abnormal temperature register (Module0, LR#6)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
47	LR/MHUB Abnormal temperature register (Module0, LR#7)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
48	LR/MHUB Abnormal temperature register (Module0, LR#8)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
49	LR/MHUB Abnormal temperature register (Module0, LR#9)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
4A	LR/MHUB Abnormal temperature register (Module0, LR#a)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
4B	LR/MHUB Abnormal temperature register (Module0, LR#b)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (2/6)

	0	1	2	3	4	5	6	7
4C	LR/MHUB Abnormal temperature register (Module0, LR#c)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
4D	LR/MHUB Abnormal temperature register (Module0, LR#d)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
4E	LR/MHUB Abnormal temperature register (Module0, LR#e)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
4F	LR/MHUB Abnormal temperature register (Module0, LR#f)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
50	LR/MHUB Abnormal temperature register (Module1, LR#10)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
51	LR/MHUB Abnormal temperature register (Module1, LR#11)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
52	LR/MHUB Abnormal temperature register (Module1, LR#12)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
53	LR/MHUB Abnormal temperature register (Module1, LR#13)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
54	LR/MHUB Abnormal temperature register (Module1, LR#14)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
55	LR/MHUB Abnormal temperature register (Module1, LR#15)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
56	LR/MHUB Abnormal temperature register (Module1, LR#16)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
57	LR/MHUB Abnormal temperature register (Module1, LR#17)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
58	LR/MHUB Abnormal temperature register (Module1, LR#18)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	
59	LR/MHUB Abnormal temperature register (Module1, LR#19)							PK temperature warning
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	

SSBLOG05-3610

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (3/6)

	0	1	2	3	4	5	6	7
5A	LR/MHUB Abnormal temperature register (Module1, LR#1a)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
5B	LR/MHUB Abnormal temperature register (Module1, LR#1b)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
5C	LR/MHUB Abnormal temperature register (Module1, LR#1c)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
5D	LR/MHUB Abnormal temperature register (Module1, LR#1d)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
5E	LR/MHUB Abnormal temperature register (Module1, LR#1e)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
5F	LR/MHUB Abnormal temperature register (Module1, LR#1f)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning

SSBLOG05-3620

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (4/6)

	0	1	2	3	4	5	6	7
60	MPA Abnormal temperature register (Module0, MPA#0)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
61	MPA Abnormal temperature register (Module0, MPA#1)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
62	MPA Abnormal temperature register (Module0, MPA#2)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
63	MPA Abnormal temperature register (Module0, MPA#3)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
64	MPA Abnormal temperature register (Module1, MPA#4)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
65	MPA Abnormal temperature register (Module1, MPA#5)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
66	MPA Abnormal temperature register (Module1, MPA#6)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
67	MPA Abnormal temperature register (Module1, MPA#7)							
	MPPK ERR3	MPPK ERR2	MPPK ERR1	MPPK ERR0	Not used	Not used	ESW Abnormal Temperature Alarm	CHK1B and MPERR Signal OR
68	CMA Abnormal temperature register (Module0, CMA#0)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
69	CMA Abnormal temperature register (Module0, CMA#1)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm

SSBLOG05-3630

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (5/6)

	0	1	2	3	4	5	6	7
6A	CMA Abnormal temperature register (Module0, CMA#2)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
6B	CMA Abnormal temperature register (Module0, CMA#3) z							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
6C	CMA Abnormal temperature register (Module0, CMA#4)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
6D	CMA Abnormal temperature register (Module0, CMA#5)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
6E	CMA Abnormal temperature register (Module0, CMA#6)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
6F	CMA Abnormal temperature register (Module0, CMA#7)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm

SSBLOG05-3640

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(18) Byte28 = 49 : PK Abnormal temperature warning (6/6)

	0	1	2	3	4	5	6	7
70	CMA Abnormal temperature register (Module1, CMA#8)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
71	CMA Abnormal temperature register (Module1, CMA#9)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
72	CMA Abnormal temperature register (Module1, CMA#a)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
73	CMA Abnormal temperature register (Module1, CMA#b)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
74	CMA Abnormal temperature register (Module1, CMA#c)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
75	CMA Abnormal temperature register (Module1, CMA#d)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
76	CMA Abnormal temperature register (Module1, CMA#e)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
77	CMA Abnormal temperature register (Module1, CMA#f)							
	Not used	Not used	Not used	Not used	SHBUS Warning	SHBUS Warning _micro	PK LED Test	PK Thermistor Alarm
78	MP Abnormal temperature register (Module0, MPPK#0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
79	MP Abnormal temperature register (Module0, MPPK#1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7A	MP Abnormal temperature register (Module0, MPPK#2)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7B	MP Abnormal temperature register (Module0, MPPK#3)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7C	MP Abnormal temperature register (Module1, MPPK#4)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7D	MP Abnormal temperature register (Module1, MPPK#5)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7E	MP Abnormal temperature register (Module1, MPPK#6)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7F	MP Abnormal temperature register (Module1, MPPK#7)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0

SSBLOG05-3650

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(19) Byte28 = 43 : Forced volatile reboot warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'43' : Forced volatile reboot warning							
29	MPID (Reported SSB)							
2A	Module ID							
2B	PK ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	Volatile PIN(Module0, CM-1CA)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
41	Volatile PIN(Module0, CM-1CB)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
42	Volatile PIN(Module0, CM-1CE)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
43	Volatile PIN(Module0, CM-1CF)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
44	Volatile PIN(Module0, CM-2CC)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
45	Volatile PIN(Module0, CM-2CD)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
46	Volatile PIN(Module0, CM-2CG)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
47	Volatile PIN(Module0, CM-2CH)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
48	Volatile PIN(Module1, CM-1CJ)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
49	Volatile PIN(Module1, CM-1CK)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used

SSBLOG05-3660

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(19) Byte28 = 43 : Forced volatile reboot warning (2/2)

	0	1	2	3	4	5	6	7
4A	Volatile PIN(Module1, CM-1CN)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
4B	Volatile PIN(Module1, CM-1CP)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
4C	Volatile PIN(Module1, CM-2CL)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
4D	Volatile PIN(Module1, CM-2CM)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
4E	Volatile PIN(Module1, CM-2CQ)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
4F	Volatile PIN(Module1, CM-2CR)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	ENV alarm	Not used
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

SSBLOG05-3661

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(20) Byte28 = C5 : SFP (CHG) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C5' : SFP (CHG) warning							
29	MPID (Reported SSB)							
2A	Module ID, Tx_Fault detection LR_LSI number							
2B	Tx_Fault detection port number							
2C	LR TX_FAULT warning register							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR Tx_Fault detection port number0 (0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning0
41	LR Tx_Fault detection port number0 (1)							
	Not used	Not used	Not used	Not used	Port3 Tx_Fault	Port2 Tx_Fault	Port1 Tx_Fault	Port0 Tx_Fault
42	LR Tx_Fault detection port number1 (0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning1
43	LR Tx_Fault detection port number1 (1)							
	Not used	Not used	Not used	Not used	Port7 Tx_Fault	Port6 Tx_Fault	Port5 Tx_Fault	Port4 Tx_Fault
44	Device address "A0", Data address "3"							
45	Not used (x'00')							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5D	Device address "A0", Data address "73"							
5E	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

SSBLOG05-3662

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(20) Byte28 = C5 : SFP (CHG) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3663

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(21) Byte28 = 47 : SSD Config warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'47' : SSD Config warning							
29	MPID (Reported SSB)							
2A	Module ID							
2B	P/K ID							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Store / Restore status0							
	DIAG restore done	DIAG store done	Not used	Not used	SSD Inline done	SSD CUDG5 done	SSD CUDG4 done	SSD CUDG3 done
41	Store / Restore status1							
	Not used	Not used	Not used	Internal DIAG running	Not used	Not used	Not used	Invalid Command error
42	Store / Restore status2							
	Not used	Not used	Config done	Config fail	Restore done	Restore busy	Restore fail	Restore success
43	Store / Restore status3							
	Restore volatile	Not used	Not used	Not used	Store done	Store busy	Store fail	Store Success
44	PARTS status0							
	Not used	Not used	Not used	Not used	Not used	Not used	CM Backup warning	Battery Warning
45	PARTS status1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	FAN Control Warning
46	PARTS status2							
	BRG_P warning	BRG_S warning	SH Micon warning	SSD1 warning	SSD0 warning	Battery Controller warning	Battery warning	Battery Charge
47	SH bus error status(GPIO)							
	Not used	Not used	Not used	Not used	SH Bus warning	SH Bus(micro) warning	PK LED test	PK Thermistor alarm
48	Parts Firm exchange status0							
	SPROM Erase fail	SPROM Erase completion	Not used	Not used	SSD1 Erase fail	SSD0 Erase fail	SSD1 Erase completion	SSD0 Erase Completion
49	Parts Firm exchange status1							
	Not used	Not used	Not used	Not used	BATC_FIRM Exchange fail	SSD1_FIRM Exchange fail	SSD0_FIRM Exchange fail	SH_FIRM Exchange fail
4A	Parts Firm exchange status2							
	BATC_FIRM Exchange Done	SSD1_FIRM Exchange done	SSD0_FIRM Exchange done	SH_FIRM Exchange done	BATC_FIRM Exchange busy	SSD1_FIRM Exchange busy	SSD0_FIRM Exchange busy	SH_FIRM Exchange Busy
4B	Parts Firm exchange status3							
	Not used	Not used	Not used	Not used	Not used	Battery Exchange fail	Battery Exchange done	Battery Exchange ready

SSBLOG05-3664

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(21) Byte28 = 47 : SSD Config warning (2/3)

	0	1	2	3	4	5	6	7
4C	SSD CONFIG0							
	Not used	Not used	Not used	Not used	Not used	SSD1 Capa 64GB	SSD1 Capa 32GB	Not used
4D	SSD CONFIG1							
	Not used	Not used	Not used	Not used	Not used	SSD0 Capa 64GB	SSD0 Capa 32GB	Not used
4E	Store error LOG							
	Not used	Not used	Not used	Not used	Restore Error_log MG3	Restore Error_log MG2	Restore Error_log MG1	Restore Error_log MG0
4F	Restore error LOG							
	Not used	Not used	Not used	Not used	store Error_log MG3	store Error_log MG2	store Error_log MG1	store Error_log MG0
50	SH_micro Firm revision0							
51	SH_micro Firm revision1							
52	SH_micro Firm revision2							
53	SH_micro Firm revision3							
54	Bridge_Chip error status (PCI express) 0							
55	Bridge_Chip error status (PCI express) 1							
56	Bridge_Chip error status (PCI express) 2							
57	Bridge_Chip error status (PCI express) 3							
58	Bridge_Chip error status (SATA_P0) 0							
59	Bridge_Chip error status (SATA_P0) 1							
5A	Bridge_Chip error status (SATA_P0) 2							
5B	Bridge_Chip error status (SATA_P0) 3							
5C	Bridge_Chip error status (SATA_P1) 0							
5D	Bridge_Chip error status (SATA_P1) 1							
5E	Bridge_Chip error status (SATA_P1) 2							
5F	Bridge_Chip error status (SATA_P1) 3							

SSBLOG05-3665

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(21) Byte28 = 47 : SSD Config warning (3/3)

	0	1	2	3	4	5	6	7
60	SSD0 SATA error status0							
61	SSD0 SATA error status1							
62	SSD0 RFIS error status0							
63	SSD0 RFIS error status1							
64	SSD1 SATA error status0							
65	SSD1 SATA error status1							
66	SSD1 RFIS error status0							
67	SSD1 RFIS error status1							
68	SSD model number0							
69	SSD model number1							
6A	SSD model number2							
6B	SSD model number3							
6C	SSD model number4							
6D	SSD model number5							
6E	SSD model number6							
6F	SSD model number7							
70	SSD SMART0							
71	SSD SMART1							
72	SSD SMART2							
73	SSD SMART3							
74	SSD SMART4							
75	SSD SMART5							
76	SSD SMART6							
77	SSD SMART7							
78	SSD SMART8							
79	SSD SMART9							
7A	SSD SMART10							
7B	SSD SMART11							
7C	SSD SMART12							
7D	SSD SMART13							
7E	SSD SMART14							
7F	SSD SMART15							

SSBLOG05-3670

Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'3')			
27	Operation byte of LR/LRE command parameter							
28	Auxiliary byte of LR/LRE command parameter							
29	Byte 2 for LR/LRE command							
2A	Reserved							
2B								
2C	Search parameter of LR command (CCHHR) or Record ID of re-execution command (R)							
2D								
2E								
2F								

SSBLOG05-3680

Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (2/2)

	0	1	2	3	4	5	6	7
30	Sector number to re-execute the LR command							
31	TLF for LR/LRE command							
32								
33	Operation byte = 0x3F : LRE External operation byte Others : Reserved							
34	SSID (Low) of self subsystem							
35								
36	Symptom code (x'08C1')							
37								
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used							
7F								

SSBLOG05-3690

Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06								
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'4')			
27	Operation byte of LR/LRE command parameter							
28	Extension operation byte of LR/LRE command parameter							
29								
2A	Head address of extent							
2B	(DX command parameter)							
2C								
2D								
2E								
2F	Path group ID							
30	(byte 1 to 7 of the ID by the last Set Path Group ID command)							
31								
32								
33								
34	SSID (Low) of self subsystem							
35								
36	Reference code (x'0212')							
37								

SSBLOG05-3700

Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code (x'01')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Not used (x'00')							
55								
56	Module ID (detected error)							
57	Routine ID (detected error)							
58	Not used (x'0 ~ 0')							
7F								

SSBLOG05-3710

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): Common information (1/1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'1')			
27	Sector number							
28	Cylinder address							
29								
2A	Head address							
2B								
2C	Record number							
2D	Error displacement							
2E								
2F								
30	Drive serial number (x'0C24' + DKU sequence number)							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Reference code (x'4XY') X: 0: Data check in HA field or RO field, 1: Data check in C field, 2: Data check in K field,							
37	3: Data check in D field, 5: PA error YY: x'80': Correctable (Recovered), x'C0': Uncorrectable							
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3720

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 40/41/42/43 (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44								
45	VDEV number, slot number							
46								
47								
48	Slot status							
49								
4A								
4B	Staging bit map/Dirty bit map							
4C								
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56	Not used (x'00')							
7F								

***1: EXIT FLAG**

x'08':	CHL I/F CHK2 time over	x'40':	'Staging' requested
x'0D':	EOF	x'41':	'Waiting free segment' requested
x'0E':	DSB = 0E requested	x'42':	'Time over of waiting for staging' requested
x'0F':	Logical error	x'43':	'Staging for PA mismatch' requested
x'1E':	Make sense only	x'45':	'Destaging' requested
x'1F':	Logical error (make sense only)	x'46':	'High priority destaging' requested(30 slot)
x'30':	Selective reset requested	x'47':	'Write through process' requested
		x'4B':	'DSB = 4E retry' requested (data transfer)
		x'4E':	'DSB = 4E retry' requested (DSB = 4E retry)
		x'4F':	STG requested (Read or STG in progress)

***2: Slot failure**

x'80':	Sector write error	x'08':	No decision
x'40':	Sector read error		
x'20':	ECC/LRC error		
x'10':	Incomplete data		

***3: Obtained information flag**

bit 0-1: Block type	bit2-3: Side	bit4: Segment	b5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

SSBLOG05-3730

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception):For Byte36 = 45 (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44								
45	VDEV number, slot number							
46								
47								
48								
49	Slot status							
4A								
4B								
4C								
4D	Staging bit map/Dirty bit map							
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	PACC in CBUF							
55								
56	PAHF in CBUF							
57								
58	Current CC in JCB							
59								
5A	Current HH in JCB							
5B								
5C								
6	Not used (x'00')							
7F								

***1: EXIT FLAG**

x'08':	CHL I/F CHK2 time over	x'40':	'Staging' requested
x'0D':	EOF	x'41':	'Waiting free segment' requested
x'0E':	DSB = 0E requested	x'42':	'Time over of waiting for staging' requested
x'0F':	Logical error	x'43':	'Staging for PA mismatch' requested
x'1E':	Make sense only	x'45':	'Destaging' requested
x'1F':	Logical error (make sense only)	x'46':	'High priority destaging' requested (30 slot)
x'30':	Selective reset requested	x'47':	'Write through process' requested
		x'4B':	'DSB = 4E retry' requested (data transfer)
		x'4E':	'DSB = 4E retry' requested (DSB = 4E retry)
		x'4F':	STG requested (Read or STG in progress)

***2: Slot failure**

x'80':	Sector write error	x'08':	No decision
x'40':	Sector read error		
x'20':	ECC/LRC error		
x'10':	Incomplete data		

***3: Obtained information flag**

bit 0-1: Block type	bit 2-3: Side	bit 4: Segment	bit5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

SSBLOG05-3740

Byte36 = 6F (Subsystem Information) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Command overrun threshold exceeded							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'1')			
27	Data overrun threshold exceeded							
28	Seek count							
29								
2A	Drive serial number			(x'0C')				
2B				(x'24')				
2C				(DKU sequence number)				
2D								
2E								
2F	DKC serial number			(x'0C')				
30				(x'24')				
31				(x'00')				
32				(DKC sequence number : high)				
33				(DKC sequence number : low)				
34	SSID of self subsystem							
35								
36	Symptom code (x'6FX' Y: 0: Statistics/RRBL command, 2: Channel data overrun							
37	X: For serial CHL : LCP#, For parallel CHL : CHL#)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Read/search byte count							
3D								
3E								
3F								

SSBLOG05-3750

Byte36 = 6F (Subsystem Information) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3760

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 0 (SPC Detected Failure : DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('111' : SPC Detected Failure)			0	0	0	0	0
28	Not used							
29	Not used							
2A	SCSI command (See SSBLOG05-3800)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'D0YY') (YY: RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code	Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit)	Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit)	Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0: Head address	Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					

SSBLOG05-3770

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 0 (SPC Detected Failure : DSP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	MSG setting							
42	CDB information							
43								
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD# or TAG#							
4F								
50	LIST#							
51								
52	ID Number							
53								
54	I/O Status							
55								
56	DKA Port#							
57	RAD Port#							
58	Request information							
59								
63								
64	Message send time							
65								
67								
68	SAS Error Code							
69								
6A	Number of Script / TOV Count							
6B	Value of LA							
6C								
6D								
6E								
6F								
70	Reserved							
71	EDEV#							
72	VDEV#							
73								
74	CDEV Slot#							
75								
76								
77								
78	Legacy VDEV Slot#							
79								
7A								
7B								
7C	Value of MIC							
7D								
7E								
7F								

SSBLOG05-3780

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1
(SPC Detected Failure: SAS) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('111': SPC Detected Failure)			0	0	0	0	0
28	Internal Failure Part							
29								
2A	SCSI command (See SSBLOG05-3800)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') (Y: CDEV number, ZZ: RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code						
		Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit)						
		Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit)						
		Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address						
		Bit3=1: Bit 0-3 Cylinder address Bit 8-11.						
		Bit 4-7 Head address						

SSBLOG05-3790

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1
 (SPC Detected Failure: SAS) (2/3)

	0	1	2	3	4	5	6	7
40	The fixed value (x'00')							
41								
42								
43								
44	Flg							
45	Reserved (x'00')							
46	CDEV#							
47	RDEV#							
48	CDB[0]-[3]							
49								
4A								
4B								
4C	CDB[4]-[7]							
4D								
4E								
4F								
50	DataLengs							
51								
52								
53								
54	Command Sub Status#							
55								
56								
57								
58	SCSI Status							
59								
5A								
5B								
5C	Request ID							
5D								
5E								
5F								
60	Error Post							
61								
62	Error Code							
63								
64	SPC Status (PDEV Information Tabale)							
65								
66								
67								
68	HDD Status (PDEV Information Table)							
69								
6A								
6B								
6C	Device_ID							
6D								
6E								
6F								

SSBLOG05-3800

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1
 (SPC Detected Failure: SAS) (3/3)

	0	1	2	3	4	5	6	7
70	SAS_Control#							
71								
72	Command Block #							
73								
74	Outbound Que Error Status							
75								
76								
77								
78	Reserved							
79								
7A								
7B								
7C	Reserved							
7D								
7E								
7F								

*1: SCSI command code

Code	Command Name	Code	Command Name
00	TEST UNIT READY	1C	RECEIVE DIAGNOSTIC RESULT
03	REQUEST SENSE	1D	SEND DIAGNOSTIC
04	FORMAT UNIT	28	READ (EXTEND)
07	REASSIGN BLOCKS	2A	WRITE (EXTEND)
12	INQUIRY	2E	WRITE AND VERIFY
15	MODE SELECT	3B	WRITE BUFFER
1A	MODE SENSE	FF	Command is non-decision
1B	START/STOP UNIT		

*2: 2TB SATA SCSI command code

Code	Command Name
0A	VERIFY
88	READ
8A	WRITE
8E	WRITE AND VERIFY
9E	READ CAPACITY

SSBLOG05-3810

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure: DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('000': Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'D0YY') (YY: RDEV RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code						
		Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit)						
		Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit)						
		Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address						
		Bit3=1: Bit 0-3 Cylinder address Bit 8-11.						
		Bit 4-7 Head address						

SSBLOG05-3820

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure: DSP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	MSG setting							
42	CDB information							
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD# or TAG#							
4F								
50	LIST#							
51								
52	ID Number							
53								
54	I/O Status							
55								
56	DKA Port#							
57	RAD Port#							
58	Request information							
63								
64	Message send time							
67								
68	SAS Error Code							
69								
6A	Number of Script / TOV Count							
6B	Value of LA							
6C								
6D								
6E								
6F								
70	Reserved							
71	EDEV#							
72	VDEV#							
73								
74	CDEV Slot#							
75								
76								
77								
78	Legacy VDEV Slot#							
79								
7A								
7B								
7C	Value of MIC							
7D								
7E								
7F								

SSBLOG05-3830

Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure: DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('000': Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command							
2B	Not used							
2C	Module ID (≠ x'5X/6X')							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + (DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'E0YY') (YY: RDEV RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						

SSBLOG05-3840

Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure: DSP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	MSG setting							
42	CDB information							
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD#							
4F								
50	LIST#							
51								
52	ID Number							
53								
54	SENSE DATA							
73								
74	Error Block#							
7B								
7C	Physical address							
7F								

SSBLOG05-3850

Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
(1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'0')			
27	Type code (b'000')			Not used (0)	Not used ('0')			
28	READY	ENABLE	SSB PEND	Not used (00000)				
29	Not used							
2A	M.M. RSV	PIN VOL	WR INH	Not used (00000)				
2B	Not used							
2C	Module ID (*1)							
2D	Routine ID (*1)							
2E	Not used							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'E000')							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						

SSBLOG05-3860

Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
(2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Module ID•Routine ID

x'5011/501C/5202'

: Module ID•Routine ID

x'501A/501B'

: Pin VOL detected

x'6605/6697/6705/6784/6805/6853/6909/6953' : Write inhibited

SSBLOG05-3861

Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'1F')							
23	Count							
24	Device address							
25	Device type (x'3C')							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'0')			
27	Not used							
28								
29								
2A								
2B								
2C								
2D								
2E								
2F	Drive serial number (x'0CE5' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'EF07')							
37								
38	Log and message control (x'1A')							
39	Program action code (x'01')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code	Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit)	Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit)	Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0: Head address	Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					

SSBLOG05-3862

Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3870

Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'2')			
27	Type code (b'000')			Not used (0)	Not used			
28	READY	ENABLE	SSB PEND	0	0	0	0	0
29	x'00'							
2A	M.M. RSV	PIN VOL	0	0	0	0	0	0
2B	HOST TYPE	DKC TYPE	0	0	DKU TYPE			
2C	Module ID · Routine ID (x'501C/5202': LDEV NOT READY)							
2D								
2E	Command code							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	DKU sequence number							
35								
36	Exception code (x'E210')							
37								
38	Log and message control (x'01')							
39	Program action code (x'02')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code (x'01')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3880

Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3890

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Basic SSB (x'009010')							
21								
22								
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Range affected by repair (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)		'0000000'					
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C2200' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*15)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'F1')							
3D	Affected SSID							
3E								
3F	Not used							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (2/3)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- bit0 : Serial number valid (0:Invalid, 1:Valid)
- bit1 : Device address valid (0:Invalid, 1:Valid)
- bit2 : Track address valid (0:Invalid, 1:Valid)
- bit3 : Not used (0)

*2: Range subject to the error

- x'0': Not used
- x'1': Error range not known
- x'2': SP error
- x'3': CHL-I error
- x'4': Not affect performance
- x'5 ~ F': Not used

*3: Range affected by repair

- x'0': Not used
- x'1': Repair range not known
- x'2': Repair does not affect performance
- x'3': SP disabled during repair
- x'4': SSID disabled during repair
- x'5 ~ F': Not used

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Failure SPs

- 0: 1SP
- 1: 2SP

*6: SPs in SSID

- 0: 2SP
- 1: 4SP

*7: SPs in SSID

- 0: 1SSID
- 1: 2SSID

*8: Affected SSIDs

- 0: 1SSID
- 1: 2SSID

*9: Status

- 0: Reduction
- 1: Stop

*10: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (3/3)

*11: Hardware level

Bit0: Serial channel hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs PER CLUSTER

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 4

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 4

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 6

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache / NVS hardware level ('000')

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00': Byte 36 and 37 are a symptom code.

Byte 2D = x'00': Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*13: SSB ID

0: SVP

1: SP

*14: SVP ID

0: SVP1

1: SVP2

*15: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00: Don't log

01: Unconditional log

10: Log only once

11: Log only if persistent

Bit 6-7 : Operator message

00: No message

01: Unconditional message

10: Send only once

11: Send only if persistent

SSBLOG05-3920

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'009010')							
21								
22								
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27								
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Failure leve; (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)	'0000000'						
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C2200' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*15)							
39	Program action code (x'10')							
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'F2')							
3D	Affected SSID							
3E								
3F	Not used							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (2/3)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- Bit 0: Serial number valid (0:Invalid, 1:Valid)
- Bit 1: Device address valid (0:Invalid, 1:Valid)
- Bit 2: Track address valid (0:Invalid, 1:Valid)
- Bit 3: Not used (0)

*2: Range subject to the error

- x'0': Not used
- x'1': Error range not known
- x'2': SP error
- x'3': CHL-I error
- x'4': Not affect performance
- x'5 ~ F': Not used

*3: Range affected by repair

- x'0': Not used
- x'1': Repair range not known
- x'2': Repair does not affect performance
- x'3': SP disabled during repair
- x'4': SSID disabled during repair
- x'5 ~ F': Not used

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Failure SPs

- 0: 1SP
- 1: 2SP

*6: SPs in SSID

- 0: 2SP
- 1: 4SP

*7: Failure SSIDs

- 0: 1SSID
- 1: 2SSID

*8: Affected SSIDs

- 0: 1SSID
- 1: 2SSID

*9: Status

- 0: Reduction
- 1: Stop

*10: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (3/3)

*11: Hardware level

Bit0: Serial channel hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs PER CLUSTER

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 2

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 2

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 8

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache / NVS hardware level ('000')

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00': Byte 36 and 37 are a symptom code.

Byte 2D = x'00': Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*13: SSB ID

0: SVP

1: SP

*14: SVP ID

0: SVP1

1: SVP2

*15: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00: Don't log

01: Unconditional log

10: Log only once

11: Log only if persistent

Bit 6-7 : Operator message

00: No message

01: Unconditional message

10: Send only once

11: Send only if persistent

SSBLOG05-3950

Byte26: b4-7 = F & Byte3C = FE (Device SIM) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'0000')							
21								
22	Storage control type (x'06')							
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number					SVP ID (*3)	
28	Device exception (x'1')				Service message (x'1': Action, x'C': No action)			
29	Failure level (*4)		'000000'					
2A	Resent SIM (*5)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (x'05')							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*6)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FE')							
3D	Not used							
3E								
3F								

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = FE (Device SIM) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- bit0 : Serial number valid (0:Invalid, 1:Valid)
- bit1 : Device address valid (0:Invalid, 1:Valid)
- bit2 : Track address valid (0:Invalid, 1:Valid)
- bit3 : Not used (0)

*2: SSB ID

- 0: SVP
- 1: SP

*3: SVP ID

- 0: SVP1
- 1: SVP2

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

*6: Log and message control

- Bit 0-2 : Not used
- Bit 3 : Log mode(0)
- Bit 4-5 : Logging action
 - 00: Don't log
 - 01: Unconditional log
 - 10: Log only once
 - 11: Log only if persistent
- Bit 6-7 : Operator message
 - 00: No message
 - 01: Unconditional message
 - 10: Send only once
 - 11: Send only if persistent

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Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM) (1/2)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	PCB type		Processor ID					
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'0000')							
21								
22	Storage control type (x'06')							
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number					SVP ID (*3)	
28	Device exception (x'1')				Service message (x'1')			
29	Failure level (*4)		Media Maintenance Procedure Number (Correctable : x'07', Uncorrectable : x'05')					
2A	Resent SIM (*5)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (Correctable : x'87', Uncorrectable : x'85')							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*6)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB(0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'FF')							
3D	Cylinder address (Invalid)							
3E								
3F	Head address (Invalid)							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- Bit 0: Serial number valid (0:Invalid, 1:Valid)
- Bit 1: Device address valid (0:Invalid, 1:Valid)
- Bit 2: Track address valid (0:Invalid, 1:Valid)
- Bit 3: Not used (0)

*2: SSB ID

- 0: SVP
- 1: SP

*3: SVP ID

- 0: SVP1
- 1: SVP2

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

*6: Log and message control

- Bit 0-2 : Not used
- Bit 3 : Log mode (0)
- Bit 4-5 : Logging action
 - 00: Don't log
 - 01: Unconditional log
 - 10: Log only once
 - 11: Log only if persistent
- Bit 6-7 : Operator message
 - 00: No message
 - 01: Unconditional message
 - 10: Send only once
 - 11: Send only if persistent