

SSBLOG SECTION

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SSBLOG05-320	• Byte27 = 81 (CHK3)
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SSBLOG05-330	(1) Byte28 = [00 to 07] or [10 to 17] or [20 to 2B] or [30 to 35] or [40] or [50] or [F0]
SSBLOG05-340	Byte27 = 81 (CHK3) Byte40-7F Detail
SSBLOG05-340	(1) Byte28 = [00 to 07]
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SSBLOG05-460	(4) Byte28 = [30 to 35]
SSBLOG05-500	(5) Byte28 = [40]
SSBLOG05-540	(6) Byte28 = [50]
SSBLOG05-580	(7) Byte28 = [F0]

SSBLOG00-20

SSBLOG05-620	• Byte27 = 84 (CHK4)
SSBLOG05-630	Byte27 = 84 (CHK4) Byte28-31
SSBLOG05-630	(1) FC IF CHK4
SSBLOG05-640	Byte27 = 84 (CHK4) Byte40-7F Detail
SSBLOG05-640	(1) Byte28 = 01
SSBLOG05-680	(2) Byte28 = 02
SSBLOG05-720	(3) Byte28 = 03
SSBLOG05-760	(4) Byte28 = 04
SSBLOG05-800	(5) Byte28 = 05
SSBLOG05-840	(6) Byte28 = 06
SSBLOG05-880	(7) Byte28 = 07
SSBLOG05-920	(8) Byte28 = 08
SSBLOG05-960	(9) Byte28 = 09
SSBLOG05-1000	(10) Byte28 = 0A
SSBLOG05-1040	(11) Byte28 = 0B
SSBLOG05-1080	(12) Byte28 = 0C
SSBLOG05-1120	(13) Byte28 = 0D
SSBLOG05-1160	(14) Byte28 = 0E
SSBLOG05-1200	(15) Byte28 = 0F
SSBLOG05-1240	(16) Byte28 = 10
SSBLOG05-1280	(17) Byte28 = 11
SSBLOG05-1320	(18) Byte28 = 12
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SSBLOG05-1400	(20) Byte28 = 14
SSBLOG05-1440	(21) Byte28 = 15
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SSBLOG05-1520	• Byte27 = 89 & Byte0D Bit2 = 1 (Main-PK CHK2)
SSBLOG05-1530	Byte27 = 89 (Main-PK CHK2) Byte28-31 Detail
SSBLOG05-1530	(1) Byte28 = X0/X1/X2/X3/X4/X5
SSBLOG05-1540	(2) Byte28 = X8/XC
SSBLOG05-1550	Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail
SSBLOG05-1550	(1) Byte28 = 00/01/02/03
SSBLOG05-1590	(2) Byte28 = 04/05/0C
SSBLOG05-1630	(3) Byte28 = 08

SSBLOG00-30

SSBLOG05-1670	• Byte27 = 8A (DKA CHK2)
SSBLOG05-1690	Byte27 = 8A (DKA CHK2) Byte28-31 Detail
SSBLOG05-1690	(1) Byte28 = 40/41/42/43/50/51/52/53
SSBLOG05-1700	(2) Byte28 = 44/54
SSBLOG05-1710	(3) Byte28 = 48/4c/58/5c
SSBLOG05-1720	(4) Byte28 = 49/4d/59/5d
SSBLOG05-1730	Byte27 = 8A (DKA CHK2) Byte40-7F Detail
SSBLOG05-1730	(1) Byte28 = 40/41/42/43/50/51/52/53
SSBLOG05-1770	(2) Byte28 = 44/54
SSBLOG05-1810	(3) Byte28 = 48/49/58/59
SSBLOG05-1850	(4) Byte28 = 4c/4d/5c/5d
SSBLOG05-1890	• Byte27 = 8B (DRR CHK2)
SSBLOG05-1900	Byte27 = 8B (DRR CHK2) Byte28-31 Detail
SSBLOG05-1900	(1) Byte28 = 00/01
SSBLOG05-1910	(2) Byte28 = 08
SSBLOG05-1920	Byte27 = 8B (DRR CHK2) Byte40-7F Detail
SSBLOG05-1920	(1) Byte28 = 00/01
SSBLOG05-1960	(2) Byte28 = 08
SSBLOG05-2000	• Byte27 = 8C (SVP Failure : SVP(PC))
SSBLOG05-2020	Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail
SSBLOG05-2020	(1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure / Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error / SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over / SVP change refusal / Ip address setting error / Subnet mask setting error / Virus detection error / SVP environment error / Situation observation error / Dummy PS/Off starting (normal)
SSBLOG05-2040	(2) SSVP Failure
SSBLOG05-2050	(3) CUDG detected error
SSBLOG05-2090	(4) BOOT detected error
SSBLOG05-2100	• Byte27 = 8D (Power Failure) Common information
SSBLOG05-2120	Byte27 = 8D (Power Failure) Byte28-31 detail

SSBLOG00-40

- SSBLOG05-2150 • Byte27 = 8E & Byte34 = X0 & Byte28 ≠ F0 (CHK1)
- SSBLOG05-2160 (1) Byte28 = 20
- SSBLOG05-2170 (2) Byte28 = 21
- SSBLOG05-2180 (3) Byte28 = 22 or 23 or 24 or 25
- SSBLOG05-2190 (4) Byte28 = 30
- SSBLOG05-2200 (5) Byte28 = 40
- SSBLOG05-2210 (6) Byte28 = 41 or 42 or 43
- SSBLOG05-2220 (7) Byte28 = 44 or 45
- SSBLOG05-2230 (8) Byte28 = 47
- SSBLOG05-2240 (9) Byte28 = 50 or 51 or 52
- SSBLOG05-2250 (10) Byte28 = 80 or 81
- SSBLOG05-2260 Byte27 = 8E (CHK1) Byte40-7F Detail
- SSBLOG05-2260 (1) Byte28 = 20 or 40
- SSBLOG05-2290 (2) Byte28 = 21
- SSBLOG05-2320 (3) Byte28 = 22 or 23 or 24 or 25
- SSBLOG05-2350 (4) Byte28 = 30
- SSBLOG05-2380 (5) Byte28 = 41 or 43 or 47 or 51 or 52
- SSBLOG05-2410 (6) Byte28 = 42 or 50
- SSBLOG05-2440 (7) Byte28 = 44 or 45
- SSBLOG05-2470 (8) Byte28 = 80 or 81
- SSBLOG05-2500 • Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)
- SSBLOG05-2510 • Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset)
- SSBLOG05-2540 • Byte27 = 8E & Byte34:bit4-7=3 (wait SENSE Timeout)
- SSBLOG05-2560 • Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset)
- SSBLOG05-2590 • Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset)
- SSBLOG05-2620 • Byte27 = 8E & Byte34:bit4-7=8 (Force Reset)
- SSBLOG05-2650 • Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset)
- SSBLOG05-2680 • Byte27 = 8F (Logical Inconsistency)
- SSBLOG05-2700 • Byte27 = 9F (Trace data/Log data)
- SSBLOG05-2720 • Byte27 = EC (Environment Monitor Detected DKC Failure)
- SSBLOG05-2750 • Byte27 = EE (Environment Monitor Detected DKU Failure)
- SSBLOG05-2780 • Byte27 = F0 (Operation Terminated)
- SSBLOG05-2800 • Byte27 = F1 (Micro-program Detected Cache Failure)

SSBLOG00-50

SSBLOG05-2820	• Byte27 = F2 (CACHE ERR)
SSBLOG05-2830	Byte27 = F2 (CACHE ERR) Byte28 - 2C, 30 Detail
SSBLOG05-2840	Byte27 = F2 (CACHE ERR) Byte40 - 4F Detail
SSBLOG05-2850	Byte27 = F2 (CACHE ERR) Byte50 - 63 Detail
SSBLOG05-2860	Byte27 = F2 (CACHE ERR) Byte64 - 6F Detail
SSBLOG05-2860	(1) If MSWSTS : (b4 = '1') then CM0 or CM4 error
SSBLOG05-2860	(2) If MSWSTS : (b5 = '1') then CM1 or CM5 error
SSBLOG05-2860	(3) If MSWSTS : (b6 = '1') then CM2 or CM6 error
SSBLOG05-2860	(4) If MSWSTS : (b7 = '1') then CM3 or CM7 error
SSBLOG05-2870	Byte27 = F2 (CACHE ERR) Byte70 - 7F Detail
SSBLOG05-2880	• Byte27 = F5 (Remote Copy/global-active device Paths Removed/Restored)
SSBLOG05-2900	• Byte27 = F6 (CFW Access not Authorized)
SSBLOG05-2920	• Byte27 = FA (NVS Terminated)
SSBLOG05-2940	• Byte27 = FB (TrueCopy for Mainframe Pair suspend)
SSBLOG05-2960	• Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support)
SSBLOG05-2970	• Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information
SSBLOG05-2970	(1) Byte28 = 01 or 04 : DXBF correctable error
SSBLOG05-3010	(2) Byte28 = 02 : Cache Memory correctable error
SSBLOG05-3040	(3) Byte28 = 81 : LR correctable error
SSBLOG05-3090	(4) Byte28 = 83 : MSW correctable error
SSBLOG05-3090	(4-1) Byte7F = 01 : MSW correctable error LOG1
SSBLOG05-3140	(4-2) Byte7F = 02 : MSW correctable error LOG2
SSBLOG05-3180	(4-3) Byte7F = 03 : MSW correctable error LOG3
SSBLOG05-3230	(4-4) Byte7F = 04 : MSW correctable error LOG4
SSBLOG05-3280	(5) Byte28 = 40 : Abnormal DC Voltage control warning
SSBLOG05-3310	(6) Byte28 = 41 : Abnormal CEMODE warning
SSBLOG05-3320	(7) Byte28 = 42 : Abnormal CEDT warning
SSBLOG05-3330	(8) Byte28 = C0 : SFP (8/16G Fibre) warning
SSBLOG05-3350	(9) Byte28 = C2 : SFP (8/16G CHM) warning
SSBLOG05-3370	(10) Byte28 = C5 : SFP (10G Either) warning
SSBLOG05-3390	(11) Byte28 = 45 : Battery warning
SSBLOG05-3390	(11-1) Byte4F = 00 : Battery warning LOG1
SSBLOG05-3410	(11-2) Byte4F = 01 : Battery warning LOG2
SSBLOG05-3430	(12) Byte28 = 46 : CMBK warning
SSBLOG05-3460	(13) Byte28 = 48 : MPPK Abnormal temperature warning
SSBLOG05-3470	(14) Byte28 = 49 : PK Abnormal temperature warning
SSBLOG05-3510	(15) Byte28 = 43 : Forced volatile reboot warning
SSBLOG05-3520	(16) Byte28 = 47 : SSD Config warning
SSBLOG05-3550	(17) Byte28 = 11 : MSW PLDIF warning

SSBLOG00-60

SSBLOG05-3560	• Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception)
SSBLOG05-3580	• Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception)
SSBLOG05-3600	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): Common information
SSBLOG05-3610	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 40/41/42/43
SSBLOG05-3620	• Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 45
SSBLOG05-3630	• Byte36 = 6F (Storage system Information)
SSBLOG05-3650	• Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1 (SPC Detected Failure)
SSBLOG05-3680	• Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure)
SSBLOG05-3700	• Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure)
SSBLOG05-3720	• Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
SSBLOG05-3740	• Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity)
SSBLOG05-3760	• Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready)
SSBLOG05-3780	• Byte26: b4-7 = F & Byte3C = F1 (DKC SIM)
SSBLOG05-3810	• Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM)
SSBLOG05-3840	• Byte26: b4-7 = F & Byte3C = FE (Device SIM)
SSBLOG05-3860	• Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM)

1. Introduction

The internal SSB (SSB Log) information is presented as either 24-byte compatibility SSB format or ECKD 32-byte SSB format. The 24-byte SSB is used for DKC and the 32-byte SSB is used for DKU.

The internal SSB is divided into the following sections:

- Internal SSB common information
This information is used for SSB management, and same for all formats.
- External SSB information presented to host.
This is 32 bytes sense information presented to host as SSB.
- Format-dependent information
This is auxiliary information for error analysis.

See Fig. 1-1 and Fig. 1-2.

NOTE: The byte offset in SSB logs is described in hexadecimal number.
This section is corresponding to SSB log on SVP.
If you want to know about SSB which was reported to host, you should see SSB SECTION.

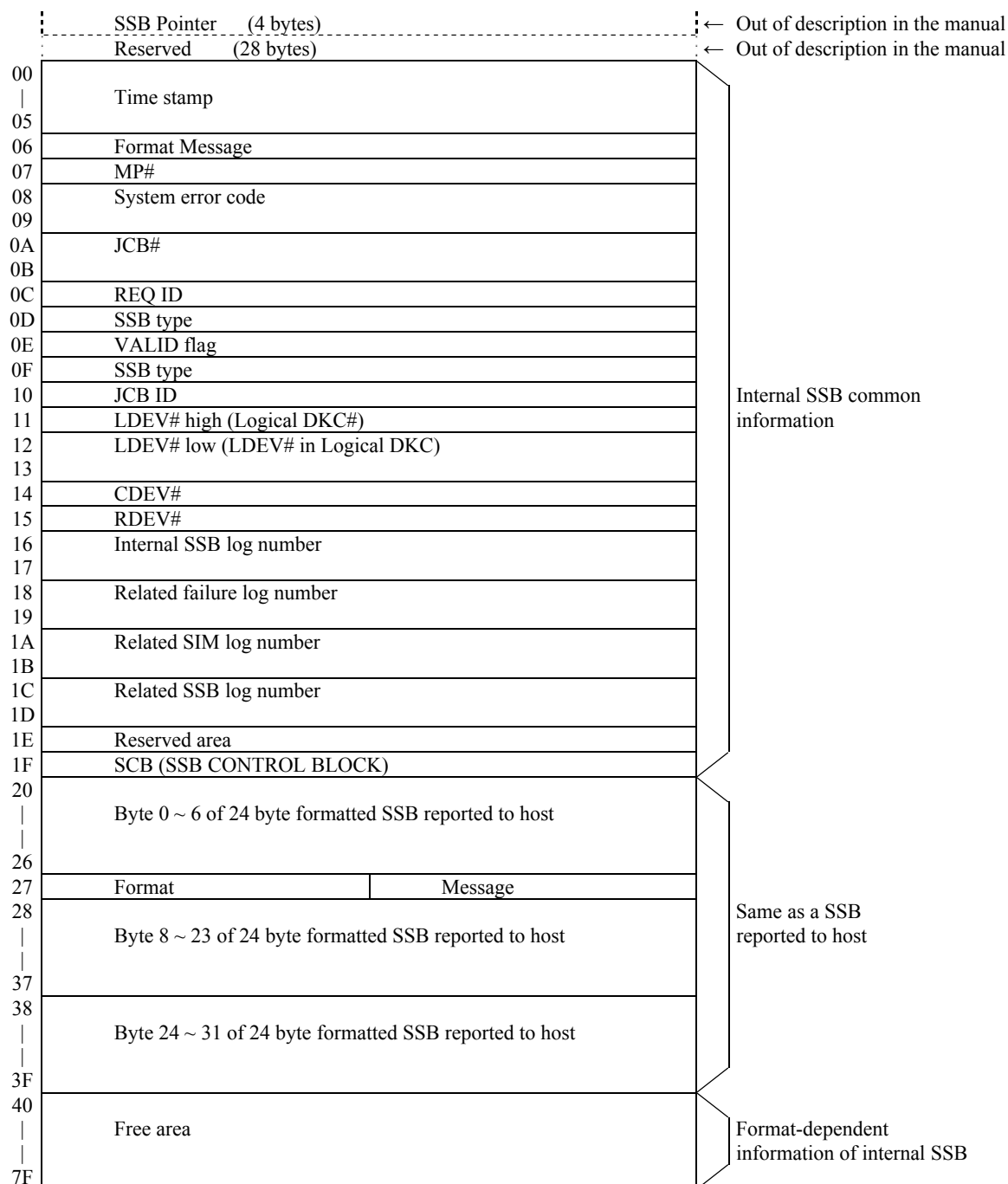


Fig. 1-1 Outline of 24-byte Format Internal SSB

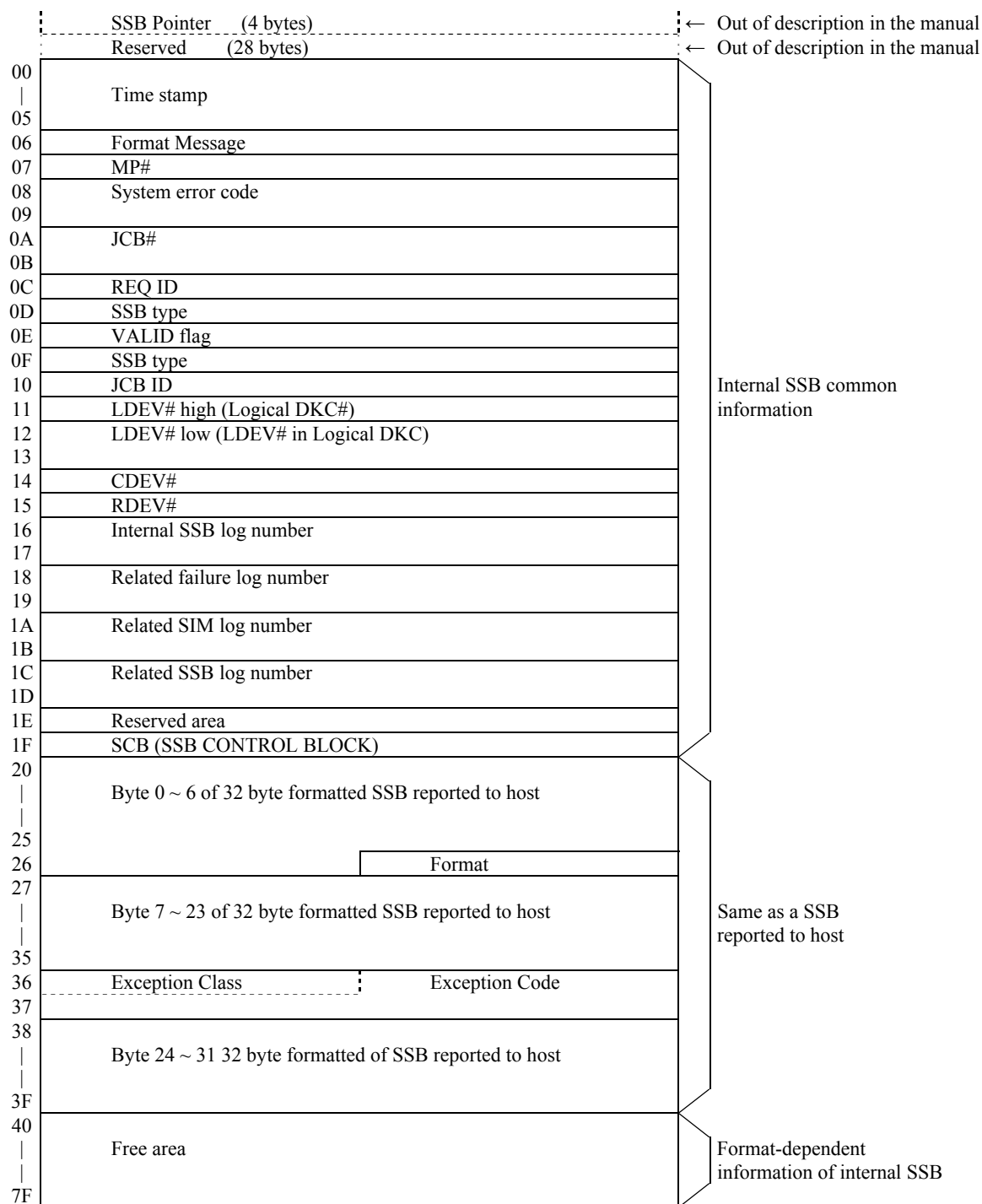


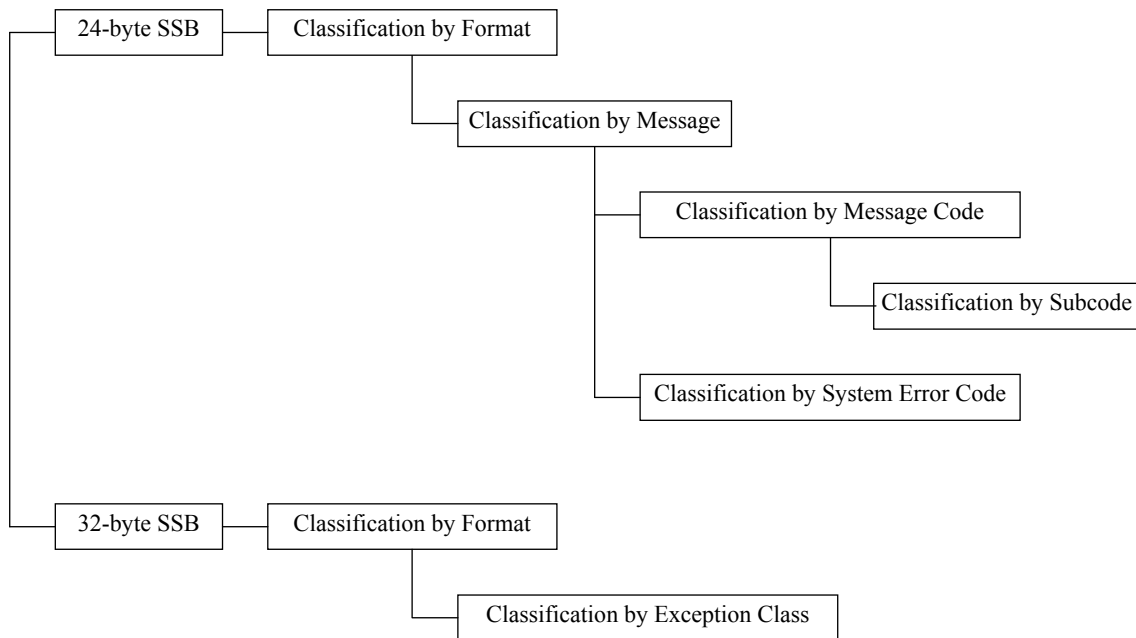
Fig. 1-2 Outline of 32-byte Format Internal SSB

	SSB Pointer (4 bytes)	← Out of description in the manual	
	Reserved (28 bytes)	← Out of description in the manual	
00	Time stamp	Internal SSB common information	
05			
06			Format Message
07			MP#
08			System error code
09			
0A			JCB#
0B			
0C			REQ ID
0D			SSB type
0E			VALID flag
0F			SSB type
10			JCB ID
11			LDEV# high (Logical DKC#)
12			LDEV# low (LDEV# in Logical DKC)
13			
14			CDEV#
15			RDEV#
16			Internal SSB log number
17			
18			Related failure log number
19			
1A	Related SIM log number		
1B			
1C	Related SSB log number		
1D			
1E	Reserved area		
1F	SCB (SSB CONTROL BLOCK)		
20	Byte 0 to 31 of SCSI host reported format SSB		
3F			
40			
7F	Byte 32 to 95 of SCSI host reported format SSB		

Fig. 1-3 Overview of SCSI Host Report Format Internal SSB

2. Classification of Internal SSB

The internal SSB is classified as the following figure, and Fig. 2-1 for detail.



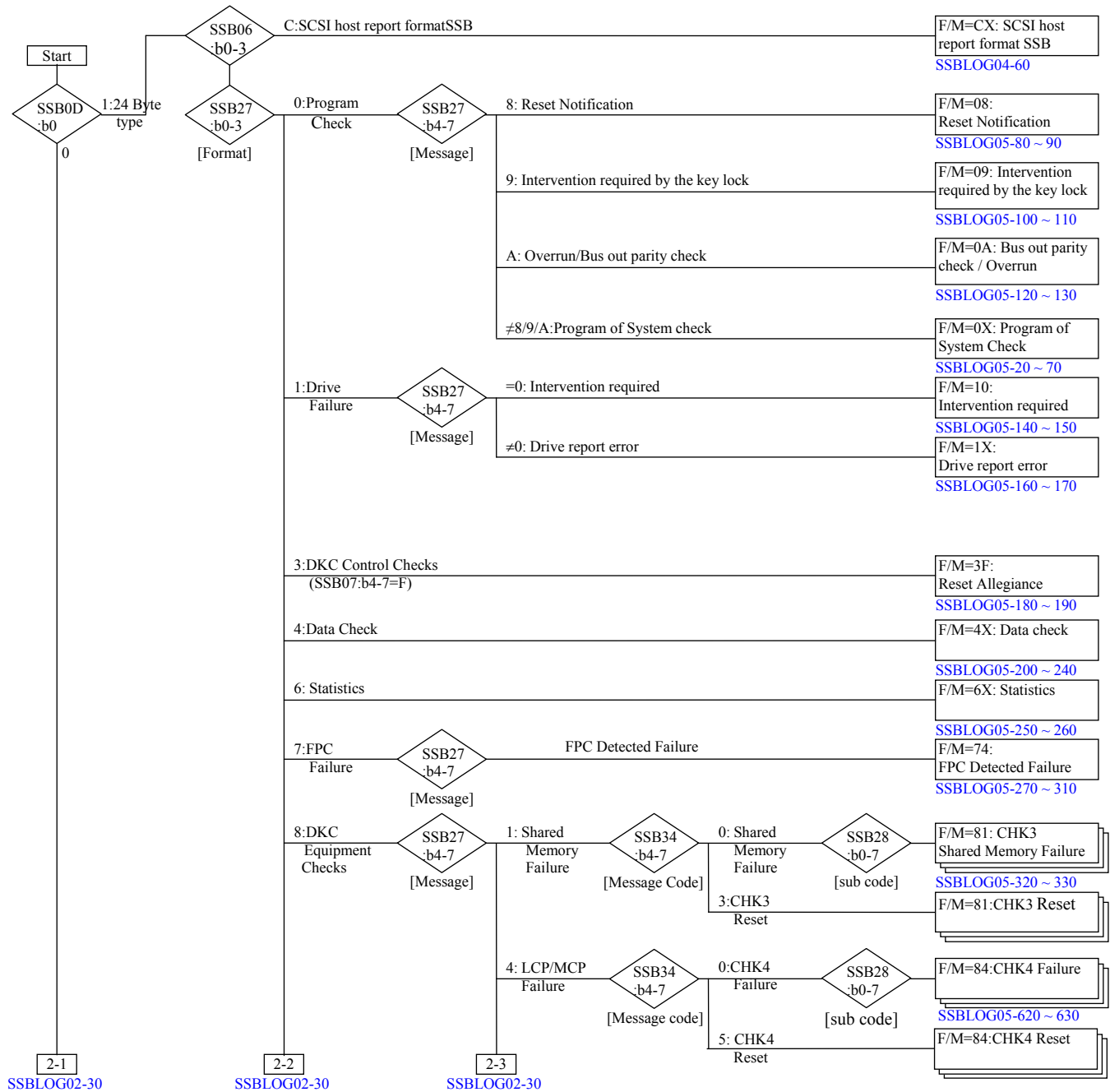


Fig. 2-1 Search method for SSB tables (1/5)

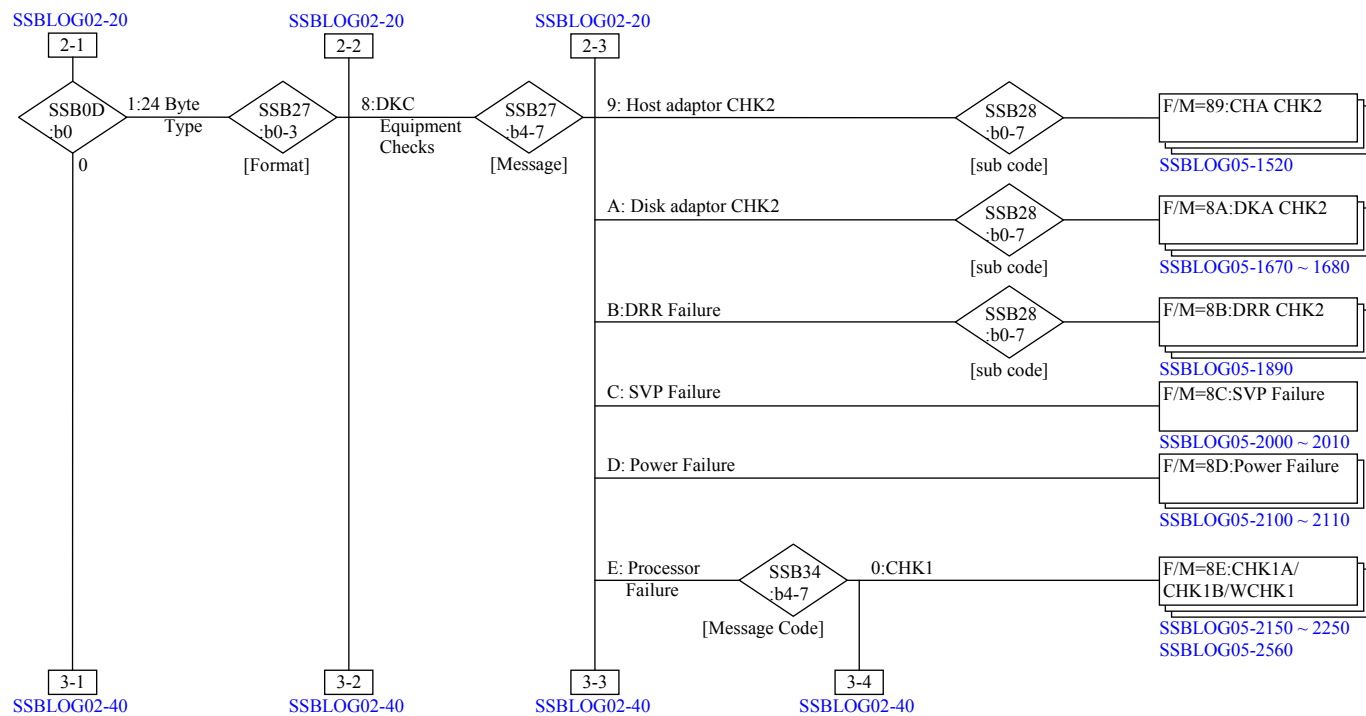
SSBLOG02-30

Fig. 2-1 Search method for SSB tables (2/5)

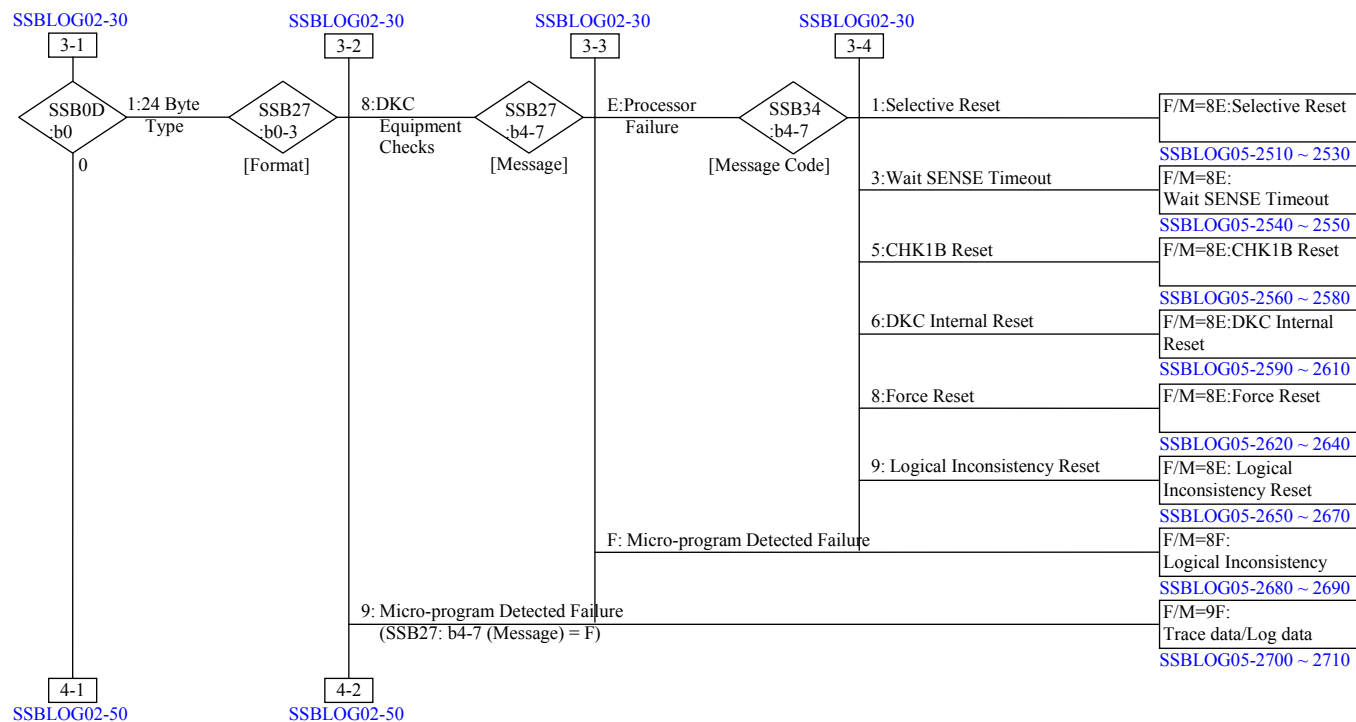
SSBLOG02-40

Fig. 2-1 Search method for SSB tables (3/5)

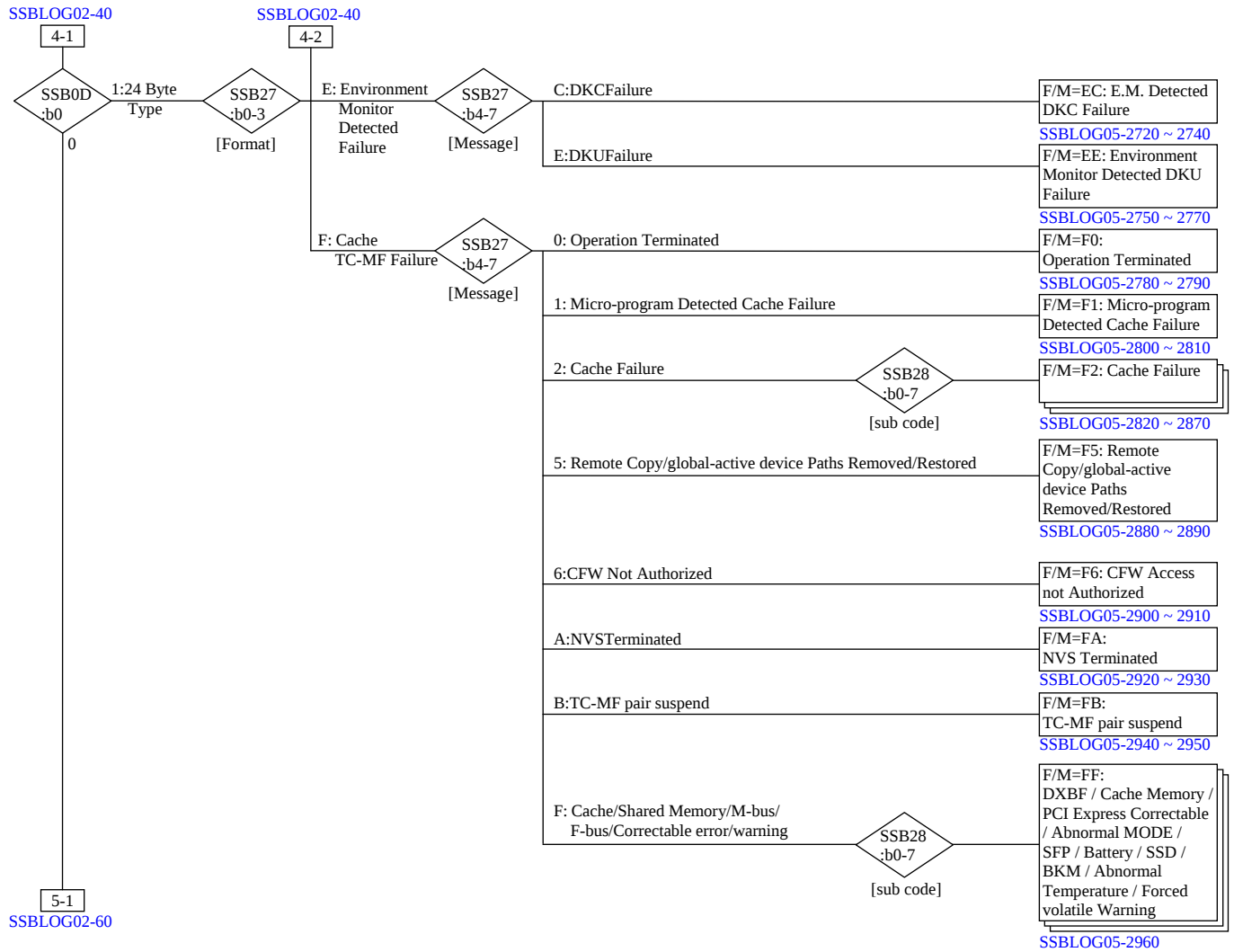
SSBLOG02-50

Fig. 2-1 Search method for SSB tables (4/5)

SSBLOG02-60

SSBLOG02-50

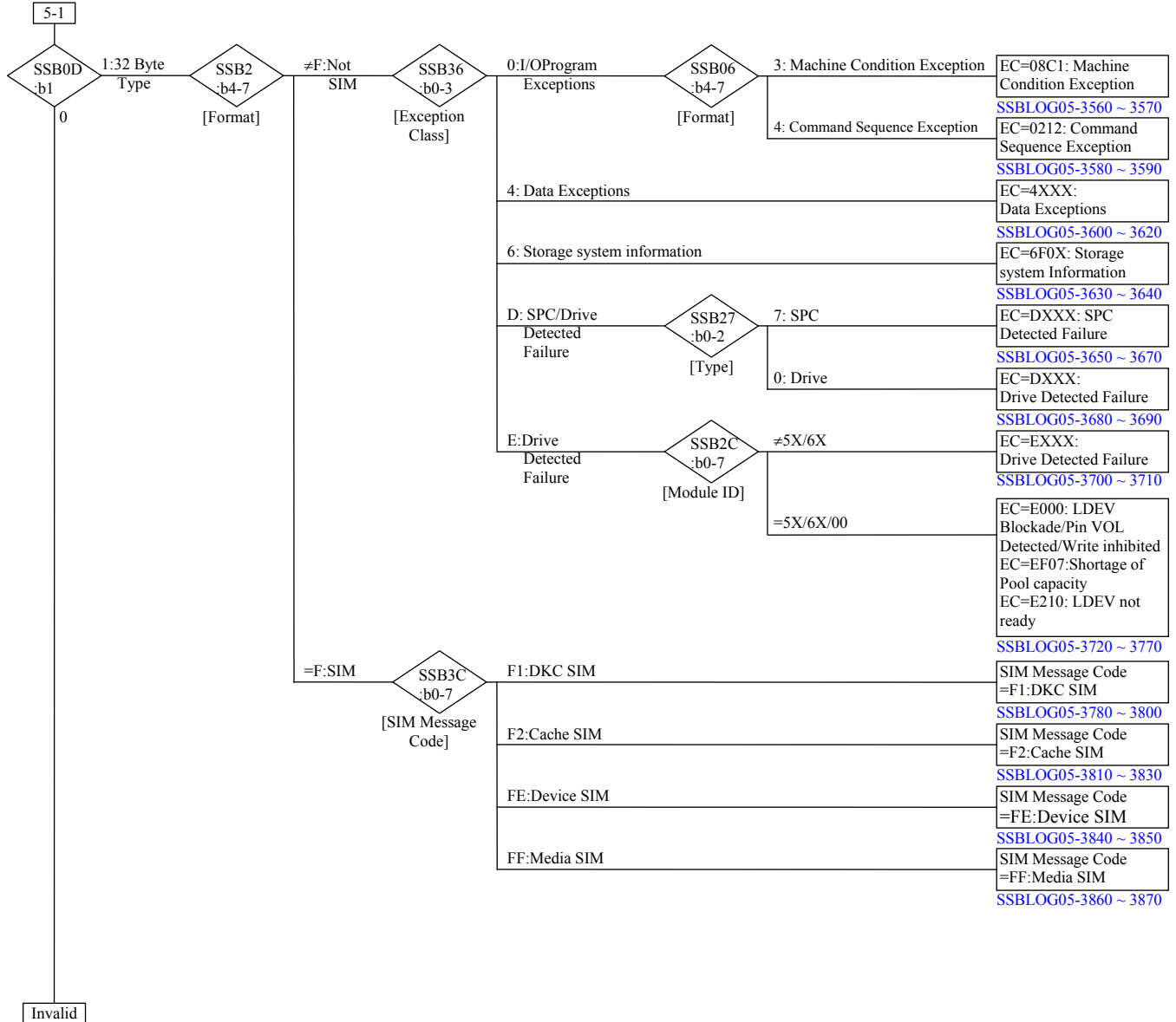


Fig. 2-1 Search method for SSB tables (5/5)

SSBLOG03-10**3. Common information description**

	0	s	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20								
	Host SSB Information							
3F								
40								
	Internal SSB Format-Dependent Information							
7F								

Common Information (1/2)

Byte	Bit	Meaning	Remarks
00 ~ 05		Time stamp The time error occurred	
06		Format Message	
07		Processor number (Internal ID) x'00' ~ x'7F': CHA0x00 ~ 0x7F x'80' ~ x'BF': DKA0x80 ~ 0xBF	
08 ~ 09		System error code	
	0 ~ 7	Module ID	
	8 ~ 15	Routine ID	
0A ~ 0B		JCB#	
0C		REQ ID	
0D		SSB type	
	0	24 byte type flag 0: Invalid 1: 24 byte type SSB	
	1	32 byte type flag 0: Invalid 1: 32 byte type SSB	
	2	Flag for SCSI host processor detected the error 0: SSB detected by CHA and DKA 1: SSB detected by CHS	
	3	Remote copy flag 0: Invalid 1: Remote copy valid	
	4	Separately stored log flag 0: General log 1: Separately stored log	
	5	Host report flag 0: Pending 1: Reported	
	6	SVP report flag 0: Pending 1: Reported	
	7	Forcible log flag 0: Normal 1: Forcibly ejected log	
0E		Valid flag	
	0	LDEV number valid or invalid 0: Invalid 1: Valid	
	1	PDEV number valid or invalid 0: Invalid 1: Valid	
	2	SSB number valid or invalid 0: Invalid 1: Valid	
	3	Related failure log number valid or invalid 0: Invalid 1: Valid	

Common Information (2/2)

Byte	Bit	Meaning	Remarks
0E		Valid flag	
	4	Related SIM log number valid or invalid 0: Invalid 1: Valid	
	5	Related SSB log number valid or invalid 0: Invalid 1: Valid	
	6	PK information valid or invalid 0: Invalid 1: Valid	
	7	Reserve	
0F		SSB type	
	0	TC-MF type SSB flag 0: Sync TC-MF SSB 1: Semi Sync TC-MF SSB	
	1	RCU SIM flag 0: Invalid 1: RCU SIM flag	
	2 ~ 7	Reserved	
10		JCB ID	
11		LDEV# high (Logical DKC#)	
12 ~ 13		LDEV# low (LDEV# in Logical DKC)	
14		CDEV#	
15		RDEV#	
16 ~ 17		Internal SSB log number	
18 ~ 19		Related Failure log number	
1A ~ 1B		Related SIM log number	
1C ~ 1D		Related SSB log number	
1E		Reserved area	
1F		SSB control block Use when assemble BASIC SSB of Host SSB. See "SSB Key Code" table.	

SSB Key Code (1/4)

Code	Basic SSB	Meaning	Remarks
00	000000	Dummy SSB	
01	800000	Command reject (A program error or invalid link between BAD and ALT)	
02	400000	Intervention required (An addressed device is offline or not connect)	
03	200000	Bus out check (Channel bus out parity error)	
04	100000	Equipment check (Equipment check in storage system)	
05	080000	Data check (Uncorrectable: retry failed or retry inhibited)	
06	040000	Overrun (Command retry failed when service overrun occurred)	
07	004000	Invalid track format (Track capacity over or track format error)	
08	002000	End of cylinder (A multi track operation outside LOCATE domain)	
09	000800	No record found	
0A	000400	File protected (A seek or multi track operation violates the file mask or extent)	
0B	100200	Equipment check and write inhibited (A write command on the write inhibited path by a DIAG CTL command)	
0C	080040	Data check and correctable (The retry succeeded for data check in PCI fetch mode)	
0D	000020	First log mode error	
0E	800400	Command rejected and file protected (A multi track operation outside primary track)	
0F	800200	Command reject and write inhibited	

SSB Key Code (2/4)

Code	Basic SSB	Meaning	Remarks
10	108000	Equipment check and permanent error (A retry failed for the equipment check)	
11	088000	Data check and permanent error (A retry failed for the data check)	
12	009010	SIM	
13	000000	Reserved	
14	000104	Imprecise ending (This error is for a previously completed CCW)	
15	800104	Command reject and imprecise ending	
16	400104	Intervention required and imprecise ending	
17	200104	Bus out check and imprecise ending	
18	100104	Equipment check and imprecise ending	
19	080104	Data check and imprecise ending	
1A	040104	Overrun and imprecise ending	
1B	004104	Invalid track format and imprecise ending	
1C	002104	End of cylinder and imprecise ending	
1D	000904	No record found and imprecise ending	
1E	000504	File protected and imprecise ending	
1F	100304	Equipment check and write inhibited and imprecise ending	

SSB Key Code (3/4)

Code	Basic SSB	Meaning	Remarks
20	080144	Equipment check and correctable and imprecise ending	
21	000124	First log mode error and imprecise ending	
22	800504	Command reject and file protected and imprecise ending	
23	800304	Command reject and write inhibited and imprecise ending	
24	108104	Equipment check and permanent error and imprecise ending	
25	088104	Data check and permanent error and imprecise ending	
26	009114	SIM and imprecise ending	
27	000000	Reserved	
28	808000	Command reject and permanent error	
29	00C000	Permanent error and invalid track format	
2A	008800	Permanent error and request inhibit write	
2B	088200	Data check and permanent error and write protected	
2C	000000	Reserved	
2D	000000	Reserved	
2E	000000	Reserved	
2F	000000	Reserved	

SSB Key Code (4/4)

Code	Basic SSB	Meaning	Remarks
30	000000	Reserved	
31	000000	Reserved	
32	010000	Incomplete domain (Received commands < LOCATE command parameter)	
33	804000	Command reject and invalid track format (A TLF mismatch in CKD conversion mode)	
34	000000	Reserved	
35	100010	Equipment check and environmental data present (Status change pending)	
36	800010	Command reject and environmental data present	
37	000000	Reserved	
38	000000	Reserved	
39	000000	Reserved	
3A	000000	Reserved	
3B	000000	Reserved	
3C	000000	Reserved	
3D	000000	Reserved	
3E	000000	Reserved	
3F	000000	Reserved	

4. Host SSB information and internal SSB format-depended information description

4.1 24-byte type SSB

00	0	1	2	3	4	5	6	7	
1F	Internal SSB common Information								
20	Command reject	Intervention REQ	Bus out check	Equipment check	Data check	Overrun	Not used	Incomplete domain	Basic SSB
21	Permanent error	Invalid track FMT	End of cylinder	Operator message	No Record found	File protect	Write inhibit	Imprecise ending	
22	Write INH request	Correctable	1st log mode ERR	Environmental DAT	Not used	Imprecise ending	Not used	Not used	
23	Count								
24	Device address								
25	Cylinder address (low)								
26	Cylinder address (high)				Head address				
27	Format (See next page)				Message (See next page)				
28	Host SSB Format-depended Information								
37									
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC Mode (1)	Duplex pair (0)	S-volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	*
3B	24 Byte SSB (1)	DEV ADR valid	TRK ADR valid	28 Bit Cylinder valid	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.								
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.								
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.								
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address								
40	Internal SSB Format-depended Information								
7F									

*1: Configuration information

*2: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB SECTION.

Format and Message (1/2)

Format	Message	Meaning	
0 (program /system check)	0	No message	
	1	Invalid command	
	2	Invalid sequence	
	3	Data short	
	4	Invalid data	
	5	Violate file mask	
	6	Channel disconnect retry	
	7	Invalid retry command	
	8	System reset	
	9	Not used	
	A	Overrun	
	B	Invalid link between bad track and alternate track	
	C	Not used	
	D	Not used	
	E	Invalid command to secondary volume	
	F	Invalid status	
1	0	Intervention required	
	1	Not used	
	2	Drive not ready	
	3	Not used	
	4	Drive report error	
	5 ~ F	Not used	
3	0 ~ E	Not used	
	F	Reset allegiance	
4	0	Data check in HA field	
	1	Data check in C field	
	2	Data check in K field	
	3	Data check in D field	
	4	Not used	
	5	PA error	
	6 ~ F	Not used	
6	0 ~ 7	Statistics	
	8 ~ F	Not used	
7	0 ~ 3	Not used	
	4	FPC report error	
	5 ~ 8	Not used	
	9	SCSI bus parity error	
	A ~ F	Not used	

Format and Message (2/2)

Format	Message	Meaning
8	0	Not used
	1	Shared memory failure
	2 ~ 8	Not used
	9	Host adaptor CHK2
	A	Disk adaptor CHK2
	B	DRR failure
	C	SVP failure
	D	Power failure
	E	Processor failure
	F	Micro-program detected error (Logical Inconsistency)
9	0 ~ E	Not used
	F	Micro-program detected error (Trace data/Log data)
E	0 ~ B	Not used
	C	Environmental monitor detected DKC failure
	D	Not used
	E	Environmental monitor detected DKU failure
	F	Not used
F	0	Operation Terminated
	1	Microprogram detected cache failure
	2	Cache failure
	3 ~ 4	Not used
	5	Remote Copy/global-active device Paths Removed/Restored
	6	CFW not authorized
	7 ~ 9	Not used
	A	NVS terminated
	B	TrueCopy for Mainframe Pair suspend
	C ~ E	Not used
	F	Cache/Shared memory/M-bus/F-bus/J-bus warning

4.2 32-byte type SSB

	0	1	2	3	4	5	6	7	
00 1F	Internal SSB common Information								Basic SSB
20	Command reject	Intervention REQ	Not used	Equipment check	Data check	Not used	Not used	Incomplete domain	
21	Permanent error	Invalid TRK FMT	End of cylinder	Operator message	Not used	File protect	Write inhibit	precise ending	
22	EC = 0/1/2/3/F: Environmental data present (x'10') EC = 4/6/D/E: Storage control type (x'06': CACHE DKC)								
23	Count / Command overrun threshold is exceeded.								
24	Device address								
25	Device type (x'24':DKU871-3)								
26	SSB2/SSB5 valid	SSB4 valid	SSB29-31 valid	Not used	Format				
27 37	Host SSB Format-depended Information								*
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC mode(1)	Duplex pair (0)	S-Volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	
3B	32 Byte SSB (0)	Not used	Not used	Not used	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Case of Byte3B Bit0=0: Message code Bit0=1: Indicate cylinder address bit 12-19.								
3D	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 20-27.								
3E	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 0-7.								
3F	Case of Byte3B Bit0=0: 0x00 Bit0=0: Head address Bit0=1: Indicate cylinder address bit 8-11. Bit0=1: Head address								
40 7F	Internal SSB Format-depended Information								

*1: Configuration information

*2: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB SECTION.

Exception Class (Byte36:bit0-3) and Format (Byte26:bit4-7)

Exception Class	Format	Meaning
0	0 ~ 2	Not used
	3	Machine condition exception
	4	I/O Program exception (Incomplete Domain)
	5 ~ F	Not used
2	0 ~ E	Not used
	F	DKC SIM
3	0 ~ E	Not used
	F	DKC SIM
4	0	Not used
	1	Data exception (PCI, Permanent) (*1)
	2 ~ E	Not used
	F	MEDIA SIM
5	0 ~ E	Not used
	F	DEVICE SIM/Media SIM
6	0	Not used
	1	Statistics (*1)
	2 ~ F	Not used
C	0 ~ E	Not used
	F	DKC SIM
D	0	Drive detected failure (*1)
	1	SPC detected failure (*1)
	0 ~ E	Not used
	F	DKC SIM
E	0	Drive detected failure/LDEV blockade/PIN VOL detected/ LDEV not ready/Write inhibited (*1)/Shortage of Pool capacity
	1 ~ E	Not used
	F	DEVICE SIM
F	0 ~ E	Not used
	F	DKC SIM/CACHE SIM

*1: For DKU87I (IBM 3390 emulation mode) only.

4.3 SCSI host report format SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common information (*1)							
20	Information byte valid	Error code (0x7)						
21	Segment No (0)							
22	File mark (0)	ECM (0)	ILI (0)	Not used	Sense key			
23 26	Information byte							
27	Additional sense data length (0x58)							
28 2B	Command specific information							
2C	Sense code							
2D								
2E	Field Replaceable Unit (0)							
2F 30 31	Sense key specific information	Sense key specific information						
32 7F	Additional sense data valid	Additional sense data						

*1: 0xCX (X=sense key) is set to byte 7 (log type low-order byte). SSB on SVP is indicated as F/M=CX.

Details of SCSI host report format SSB information

Byte	Bit	Description	Setting	Remarks
20	0	Information byte valid/invalid 0: Invalid 1: Valid	CHS command Control	
	1 ~ 7	Error code (0x7) Indicates an I/O process error.		
21		Segment No. (0)		
22	0	File mark (0)		
	1	ECM (0)		
	2	ILI (0)		
	3	Not used		
	4 ~ 7	Sense key: For more details, see “sense keys list”.		
23 ~ 26		Information bytes: LBA related to sense key if information byte is valid.		
27		Additional sense data length (0x58) Indicates sense data length after byte 8 or later.		
28 ~ 2B		Command specific information		
2C ~ 2D		Sense codes: For more details, see “sense codes list”.		
2E		Field Replaceable Unit (0)		
2F ~ 31	0	Sense key specific information valid/invalid 0: Invalid 1: Valid		
	1 ~ 17	Sense key specific information		
32 ~ 7F	0	Additional sense data valid/invalid 0: Invalid 1: Valid	Error detection source	
	1 ~ END	Additional sense data		

Sense Keys List

Sense key	Description
0	NO SENSE
1	RECOVERED ERROR
2	NOT READY
3	MEDIUM ERROR
4	HARDWARE ERROR
5	ILLEGAL REQUEST
6	UNIT ATTENTION
7	DATA PROTECT
8	BLANK CHECK (No used)
9	VENDER UNIQUE
A	COPY ABORTED
B	ABORTED COMMAND
C	EQUAL (No used)
D	VOLUME OVERFLOW
E	MISCOMPARE
F	(RESERVED)

5. Format-depended information description

SSBLOG05-20

Byte27 = 0X (X≠8/9/A: System/Program Check) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'X' : X≠8/A)			
28	Command code (for F/M ≠ F)/Reason code (for F/M=F) (NOTE1)							
29	Detail failure information 1 (NOTE2)							
2A								
2B								
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID (NOTE4)							
31	Routine ID (NOTE4)							
32	PCB number							
33	Detail failure information 2 (NOTE3)							
34	SSID of self storage system							
35								
36	Symptom code (x'0F0X')				(x'2F00' for dummy SENSE)			
37	X=Message code							

SSBLOG05-30

Byte27 = 0X (X≠8/9/A: System/Program Check) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
55								
56	Module ID (detected the error)							
57	Module ID (detected the error)							
58	Not used							
7F								

(NOTE1) F/M = 0F:Byte08 Reason code

Code	Reason
00	No message
01	Trying to make cache available and cache status is pending.
02	Not used
03	Trying to make cache force unavailable and cache status is not pending with destage complete.
04	Trying to activate CFW and CFW status is pending.
05	Trying to activate caching (device) and cache status is pending.
06	Trying to destage (track) and NVS is failed.
07	Trying to make NVS unavailable and NVS is initializing.
08	Trying to deactivate caching , DFW or DISCARD (device) , and DFW status is failed.
09	Trying to force deactivate DFW (device) and status is not as follows. • EDFW status is pending or failed. • ENVN is failed and the data exists in NVS, but not in cache.
0A	Trying to make NVS available and DFW status (device) is pending or failed.
0B	Trying to make NVS available and NVS status is pending.
0C	When NVS is failed, trying to stop caching or DFW (device) and the data exists in NVS, but not in cache.
0D	Command requires NVS and NVS is unavailable.
0E	Command requires cache and cache is unavailable.
0F	Not used
10~28	Not used
29	PFS command (Set Special Interrupt Condition) is issued on an interface on which path group is not established.
2A	Trying to make path group include parallel channel and serial channel.
2B	Message buffer is full.
2C~34	Not used

(To be continued)

(Continued from the preceding page)

Code	Reason
35	A Set Guaranteed Path subcommand is issued on a fenced path.
36~3E	Not used
3F	A Make NVS Avail for Subsystem subcommand is issued, but NVS is disable. (‘Disable NVS Capability’ is set to disable in VPD.)
40	Not used
41	A normal command is issued to a device reserved by maintenance
42~56	Not used
57	In the device for which the address is specified, the TrueCopy pair is in the state of Suspend. All the Write commands where Normal Authorization is shown are rejected.
5~7F	Not used
80	A special command is issued on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
81	An attention report on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
82~FF	Not used

Byte27 = 0X (X≠8/9/A: System/Program Check) (3/3)

(NOTE2)

(1) Issued command information when F/M=02

B/b	0	1	2	3	4	5	6	7
29	SCH ID	SCH KEY	Locate, LC EXT	SK, SK CYL	RD IPL	RECAL	SUSP MPR	RD C
2A	DIAG CTL	DIAG CTL (LOC DOM)	DEF EXT	SET FM	SET SUB MOD	SP C	WR CKD	WR R0
2B	SET INT FID	DIAG CTL (PRE REM)	0	Write through	RD C chaining	All byte match	LC domain	ERASE execute
2C	Write through	0	0	Allow WR KD	Allow WR D	Allow WR CKD	Allow WR R0	Allow WR HA

(2) Short byte flag when F/M=03

Byte29 : x'00'
 Byte2A : x'40'(short byte)
 Byte2B : x'00'
 Byte2C : x'00'

(3) Mask information when F/M=05

Byte 29 : Mask for write control
 x'00' : Allows all write operation except WR HA, WR R0.
 x'40' : Inhibits all write operation.
 x'80' : Allows update write operation.
 x'C0' : Allows all write operation.
 Byte 2A : Mask for seek control
 x'00' : Allows all seek commands and RECAL.
 x'08' : Allows SK CYL and SK HD only.
 x'10' : Allows SK HD only.
 x'18' : Inhibits all seek command and multi operation.
 Byte 2B : Mask for access
 x'00' : Allows normal access.
 x'02' : Allows device support.
 x'04' : Allows diagnostic access.
 x'06' : Allows device support inhibit data check correction and retry.
 Byte 2C : Not used (x'00')

(4) Record address when other F/M

Byte 29 and 2A : Cylinder address
 Byte 2B : Head address
 Byte 2C : Record number

(NOTE3) Bit 4-7 of byte 32 : Type of byte 33

x'08' : Byte 33 is cylinder address (high).

x'09' : Byte 33 is cylinder address (low).

x'0A' : Byte 33 is head address (high).

x'0B' : Byte 33 is head address (low).

x'0C' : Byte 33 is record number.

Byte 33 : One byte of CCHHR.

(NOTE4) The details information of every Module ID & Routine ID

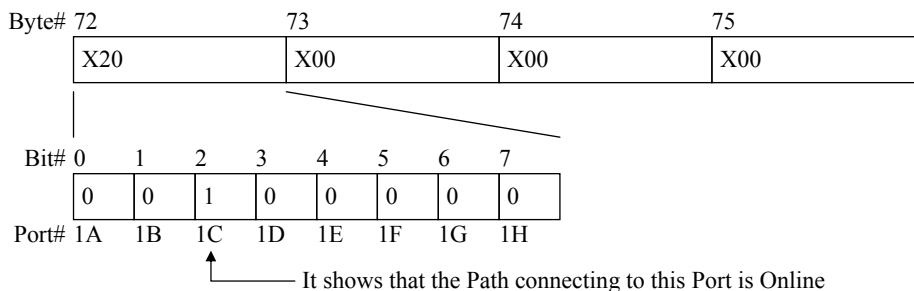
•Module ID & Routine ID=0xC0CA : The Specified R-VOL of TC-MF is still Online from the HOST.

(Add Pair operation from the Web Console was rejected)

Check the SSB.LOG on SVP of the specified RCU. SSB containing EC=C0CA on the specified RVOL number should be logged.

(1) Analyze the SSB.LOG

Both SSB are same format, from SSB byte72(0x48) though byte75(0x4B) show the Bit Map of Port indicating Online from the HOST. Each one bit corresponds to each port. It indicates the Port on which the valid Path Group is established for this device.



(2) Run DSF/ANALYZE-ALLCHPID to trace the Path Group ID of the Online route.

(2.1) In case that DKC Emulation Type is 2107.

Run DSF/ANALYZE with ALLCHPID. The output shows the Path Group ID which is Online to the storage system.

LOGICAL PATH		SYSTEM ADAPTER ID	FULL ESCON LINK ADDRESS	SP FENCE 0 1 2 3	HOST PATH GROUP ID		
NUMBER	type				CPU SERIAL#	CPU TYPE	CPU TIME STAMP
1	E	00	0101		XXXXXXXX	YYYY	ZZZZZZZZ
2-32	N/E	00-01					
33	E	02	0200		XXXXXXXX	YYYY	ZZZZZZZZ

“SYSTEM ADAPTER ID” corresponds to the number of the CHL adapter port.

(Ex; Port#1C is “02”.)

Trace the Path Group ID indicated the SYSTEM ADAPTER ID corresponding to the value on SSB LOG of EC=C0CA or C0A6.

The relationship between SAID and Port ID is shown in [THEORY03-04-30](#), Table 3.4-4.

SSBLOG05-80

Byte27 = 08 (Reset Notification) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	EPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'8')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (NOTE1)							
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'0')			
33	Not used (x'00')							
34	SSID of self storage system							
35								
36	Symptom code (x'2FFF')							
37								

SSBLOG05-90**Byte27 = 08 (Reset Notification) (2/2)**

	0	1	2	3	4	5	6	7
38	Log & message control (x'00')							
39	Program action code (x'16')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
7F								

(NOTE1) Hardware Level

Bit0 : Serial Channel or Parallel Channel	
Bit0=1(Serial Channel)	
Bit 1	: Not used
Bit 2~3	: Storage Path Number
Bit 4~7	: 0xa : Fixed
Bit 8	: Dual Frame
	0 : DUAL FRAME
	1 : MODULAR POWER
Bit 9~11	: Cache size
	000 : none Cache
	001 : 256MB
	010 : 512MB
	011 : 768MB
	100 : 1024MB
	101 : 1280MB
	110 : 1536MB
	111 : Over 1536MB
Bit 12~13	: Cluster hardware level
Bit 14~15	: Cache/NVS hardware level

SSBLOG05-100

Byte27 = 09 (Intervention required by the key lock) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	SEPARATE	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'9')			
28	READY	ENABLE	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('00000')					
2B	HOST TYPE	DKC TYPE	Not used (x'00')		DKU TYPE			
2C	Module ID (x'50')							
2D	Routine ID (x'10')							
2E	Command code							
2F	Not used (x'00')							

SSBLOG05-110

Byte27 = 09 (Intervention required by the key lock) (2/2)

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0000')			
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
	Not used							
7F								

SSBLOG05-120

Byte27 = 0A (Overflow (Byte3A: b4 = 1)) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'A')			
28	Command code							
29	Cylinder address							
2A	Head address							
2B	Record number							
2C								
2D	SSID of mate storage system(x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

SSBLOG05-130

Byte27 = 0A (Overflow (Byte3A: b4 = 1)) (2/2)

	0	1	2	3	4	5	6	7
30	Module ID							
31	Routine ID							
32	PCB number				BBF OVR	PND OVR	STOP	OVER
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'0F0A')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
7F	Not used							

SSBLOG05-140

Byte27 = 10 (Intervention required) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	SEPARATE	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'0')			
28	READY	ENABLE	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('00000')					
2B	HOST TYPE	DKC TYPE	Not used (x'00')		DKU TYPE			
2C	Module ID (x'50')							
2D	Routine ID (x'10')							
2E	Command code							
2F	Not used (x'00')							

SSBLOG05-150

Byte27 = 10 (Intervention required) (2/2)

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0000')			
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						
40								
	Not used							
7F								

SSBLOG05-160

Byte27 = 1X (Drive report error) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'2/4')			
28	Additional Sense Code + Additional Sense Code Qualifier (*2)							
29								
2A	SCSI Command Code (*2)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used (x'0')				DKA#			
2F	CDEV#				RDEV#			
30	Type Code ('000')			0	Sense Key (*1)			
31	Not used (x'00')							
32	Not used (x'00')							
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F1X' X: Message)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								

Byte27 = 1X (Drive report error) (2/2)

	0	1	2	3	4	5	6	7
3C		Case of Byte3B	Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.					
3D		Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E		Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F		Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40		Status byte						
41		Message						
42		CDB						
4B								
4C		SPC status register						
4D		SPC nexus status register						
4E		SPC data block register						
4F								
50		SPC data byte register						
51								
52								
53		SPC SCSI control signal status register						
54		SCSI Extended Sense Data						
73								
74		Not used						
7F								

*1: Sense key

x'0' : No sense
 x'1' : Recovered error
 x'2' : Not ready
 x'3' : Medium error
 x'4' : Hardware error
 x'5' : Illegal request
 x'6' : Unit attention
 x'7' : Data protect
 x'8' : Blank check (Not used)
 x'9' : Vender unique (Not used)
 x'A' : Copy aborted (Not used)
 x'B' : Aborted command
 x'C' : Equal (Not used)
 x'D' : Volume overflow (Not used)
 x'E' : Miscompare
 x'F' : (Reserved)

*2: SCSI command

x'00' : Test unit ready
 x'03' : Request sense
 x'04' : Format unit
 x'07' : Reassign blocks
 x'12' : Inquiry
 x'15' : Mode select
 x'1A' : Mode sense
 x'1B' : Start/stop unit
 x'1C' : Receive diagnostic result
 x'1D' : Send diagnostic
 x'28' : Read (extend)
 x'2A' : Write (extend)
 x'2E' : Write and verify
 x'3B' : Write buffer

SSBLOG05-180

Byte27 = 3F (Reset Allegiance) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (NOTE)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'3')				Message (x'F')			
28	Not used							
29	Logical path number							
2A	DCN status (*1)							
2B								
2C	Command code							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'F')			
33	Not used (x'00')							
34	SSID of self storage system							
35								
36	Symptom code (x'3F3F')							
37								

Byte27 = 3F (Reset Allegiance) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40								
7F	Not used							

*1:

	0	1	2	3	4	5	6	7
2A	DEVICE BUSY	ECI	COMMAND CHAINING	PATH RESERVE	STACK STATUS	WAIT SENSE	LONG BUSY	PROCESSOR CONNECT
2B	SINGLE PATH MODE	GUARANT PATH MODE	MCB CHAINING	0	BLOCK SWITCH	0	0	0

SSBLOG05-200

Byte27 = 4X (Data check, Common information) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'4')				Message (x'X')			
28	Cylinder address (*1)							
29								
2A	Head address (*1)							
2B								
2C	Record number (*1)							
2D	Sector number (*1)							
2E	Controller ID							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'00')			
33	Not used ('x00')							
34	Command code							
35	SSID (low) of self storage system							
36	Symptom code (x'4XY Y') (X: *2) (YY: *3)							
37								

Byte27 = 4X (Data check, Common information) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B		Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B		Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B		Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B		Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					

*1: Not determined for occurrence in HA or R0

*2: Contents of message

x'0' : Data check in HA field
 x'1' : Data check in C field
 x'2' : Data check in K field
 x'3' : Data check in D field
 x'4' : Missing SYNC byte in HA field
 x'5' : Missing SYNC byte in C field (PA error)
 x'6' : Missing SYNC byte in K field
 x'7' : Missing SYNC byte in D field
 x'8' : Not used
 x'9' : Missing AM during retry
 x'A' to x'F' : Not used

*3: Contents of correction flag

Bits 0 and 1 : ECC correction bit
 10 : Correctable (Recovered)
 11 : Uncorrectable
 Bit 2 : Offset active
 Bits 3 to 7 : Not used

Byte27 = 4X (Data check, When Byte27 = 43) (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56								
	Not used (x'00)							
7F								

*1: EXIT FLAG

x'08' : CHL I/F CHK2 time over	x'41' : 'Waiting free segment' requested
x'0D' : EOF	x'42' : 'Time over of waiting for staging' requested
x'0E' : DSB = 0E requested	x'43' : 'Staging for PA unmatched' requested
x'0F' : Logical error	x'45' : 'Destaging' requested
x'1E' : Make sense only	x'46' : 'High priority destaging' requested
x'1F' : Logical error (make sense only)	x'47' : 'Write through process' requested
x'30' : Selective reset requested	x'4B' : 'DSB = 4E retry' requested (data transfer)
x'40' : 'Staging' requested	x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F' : STG requested (Read or STG in progress)

*2: Slot failure

x'80' : Sector write error	x'08' : No decision
x'40' : Sector read error	
x'20' : ECC/LRC error	
x'10' : Incomplete data	

*3: Obtained information flag

bit0-1 : Block type	bit4 : Segment
00 : Data block	0 : No segment
01 : HA/RO block	1 : Segment exist
10 : Bit map block	bit5-7 : Reserved
bit2-3 : Side	
00 : Cache write side	
01 : NVS write side	
10 : Cache read side	

Byte27 = 4X (Data check, When Byte27 = 45) (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	PACC in CBUF							
55								
56	PAHF in CBUF							
57								
58	Current CC in JCB							
59								
5A	Current HH in JCB							
5B								
5C								
	Reserved (x'00')							
7F								

*1: EXIT FLAG

x'08' : CHL I/F CHK2 time over	x'41' : 'Waiting free segment' requested
x'0D' : EOF	x'42' : 'Time over of waiting for staging' requested
x'0E' : DSB = 0E requested	x'43' : 'Staging for PA unmatched' requested
x'0F' : Logical error	x'45' : 'Destaging' requested
x'1E' : Make sense only	x'46' : 'High priority destaging' requested
x'1F' : Logical error (make sense only)	x'47' : 'Write through process' requested
x'30' : Selective reset requested	x'4B' : 'DSB = 4E retry' requested (data transfer)
x'40' : 'Staging' requested	x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F' : STG requested (Read or STG in progress)

***2: Slot failure**

x'80' : Sector write error

x'40' : Sector read error

x'20' : ECC/LRC error

x'10' : Incomplete data

x'08' : No decision

***3: Obtained information flag**

bit0-1 : Block type

00 : Data block

01 : HA/RO block

10 : Bit map block

bit2-3 : Side

00 : Cache write side

01 : NVS write side

10 : Cache read side

bit4 : Segment

0 : No segment

1 : Segment exist

bit5-7 : Reserved

SSBLOG05-250

Byte27 = 6X (Statistics) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (NOTE)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Controller ID							
24	Device address							
25	Command overrun threshold exceeded							
26	Data overrun threshold exceeded							
27	Format (x'6')				Message (x'X')			
28	Read/search byte count							
29								
2A								
2B								
2C	Seek count							
2D								
2E	Not used (x'00')							
2F	Manufacture code ('000000')						Factory code ('00')	
30	Not used (x'00')							
31	DKC serial number							
32								
33								
34	SSID of self storage system							
35								
36	Symptom code (x'6FXY')		X: 0: Statistics/RRBL command, 2: Channel data overrun					
37			Y: CHL# (parallel CHL)/Port# (serial CHL))					

SSBLOG05-260

Byte27 = 6X (Statistics) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
7F								

SSBLOG05-270

Byte27 = 74 & Byte40: Bit7 = 0 (FPC Detected Failure: DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VLD	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06': Cache DKC)							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								

Byte27 = 74 & Byte40: Bit7 = 0 (FPC Detected Failure: DSP) (2/2)

	0	1	2	3	4	5	6	7
3C		Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.					
3D		Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.					
3E		Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.					
3F		Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40		SSB edit format form (x'00')						
41		Not used						
42		CDB information						
4B								
4C		(x'AB')						
4D		(x'CD')						
4E		CMD BLK#						
4F		Data transfer end flag						
50		IO status						
51								
52		PDEV status						
53								
54		LOOP status						
55								
56								
57								
58		Frame Manager status						
59								
5A								
5B								
5C		TL status						
5D								
5E								
5F								
60		IO issued status1						
61								
62		IO issued status2						
63								
64		Not used						
65								
66								
67								
68		JOB require info						
69								
6A								
6B								
6C		Data transfer status						
6D								
6E								
6F								
70		Not used						
7F								

Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format ID							
04								
05								
06	PCB type		MP number					
07								
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								

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Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	SSB edit format (x'01')							
41	Not used							
42								
43								
44	ERQ index							
45								
46								
47	IMQ index							
48								
49								
4A	IO request information							
4B								
4C								
4D								
4E								
4F								

Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP) (3/3)

	0	1	2	3	4	5	6	7
50	FPC Config/Control register							
51								
52								
53								
54	FPC Status register							
55								
56								
57								
58	FM Configuration register							
59								
5A								
5B								
5C	FM Status register							
5D								
5E								
5F								
60	FM Link Status1 register							
61								
62								
63								
64	FM Link Status2 register							
65								
66								
67								
68	FCA register 1							
69								
6A								
6B								
6C	FCA register 2							
6D								
6E								
6F								
70	FPC PCI register							
71	AL_PA							
72								
73								
74	FPC Config Command							
75								
76								
77								
78	Fibre status							
79								
7A								
7B								
7C	Fibre error outbreak count							
7D								
7E								
7F								

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Byte27 = 81 (CHK3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	Separation	Host report	SVP report	Compulsion
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV							
15	RDEV							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'1')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF81' : LDEV type = DKU87I/x'EF81' : LDEV type = DKU86I)*							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40	Hardware information (See following pages)							
41								
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 81 (CHK3) Byte28-31 Detail

(1) Byte28 = [00 to 07] or [10 to 17] or [20 to 2B] or [30 to 35] or [40] or [50] or [F0]

	0	1	2	3	4	5	6	7
28	Subcode ([x'00' to x'07'] or [x'10' to x'17'] or [x'20' to x'2B'] or [x'30' to '35'] or [x'40'] or [x'50'] or [x'F0'])							
29	Location							
	Not used		Module ID		MSW LSI number			
2A	Reserve							
	Not used							
2B	Reserve							
	Not used							
2C	MSW_ERRSTS: B3							
	X3	X2	X1	X0	T3P	T2P	T1P	T0P
2D	MSW_ERRSTS: B2							
	PP7	PP6	PP5	PP4	PP3	PP2	PP1	PP9
2E	MSW_ERRSTS: B1							
	T3E	T2E	T1E	T0E	E3	E2	E1	E0
2F	MSW_ERRSTS: B0							
	MCTL3	MCTL2	MCTL1	MCTL0	I1	I0	Micro or Logical	Hard
30	Reserve							
	Not used							
31	Reserve							
	Not used							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = [00 to 07] (1/4)

	0	1	2	3	4	5	6	7
40	MPA_CRG_ERRLOG_ERRSTS: B0							
	Not used			CRG_CHK3_H_OR	Not used	S_CHK3_H_OR	Not used	E_CHK3_H_OR
41	MPA_CRG_ERRLOG_ERRSTS: B2							
	Not used					S_SLAVE_OR	Not used	E_SLAVE_OR
42	MPA_CRG_ERRLOG_H_STS_E: B2							
	Not used						ECTLLOG_ERRRPT_OR	ETBL_ERRRPT_OR
43	MPA_CRG_ERRLOG_H_STS_E: B1							
	MINT_ENP_C_ERRRPT_OR	ETX_NP_ER_RRPT_OR	ETX_PC_ERRRPT_OR	ETSEQ_ERRRPT_OR	ETXARB_ERRRPT_OR	MINT_ERRRPT_OR	MINTSEQ_ERRRPT_OR	ENP_ERRRPT_OR
44	MPA_CRG_ERRLOG_H_STS_E: B0							
	ENPSEQ_ERRRPT_OR	Not used			ECTL_ERRRPT_OR	ECSEQ_ERRRPT_OR	ERX_ERRRPT_OR	Not used
45	MPA_CRG_ERRLOG_H_STS_S: B2							
	Not used							STBL_ERRRPT_OR
46	MPA_CRG_ERRLOG_H_STS_S: B1							
	Not used	STX_NP_ERRRPT_OR	STX_PC_ERRRPT_OR	STSEQ_ERRRPT_OR	STXARB_ERRRPT_OR	Not used		SNP_ERRRPT_OR
47	MPA_CRG_ERRLOG_H_STS_S: B0							
	SNPSEQ_ERRRPT_OR	Not used			SCTL_ERRRPT_OR	SCSEQ_ERRRPT_OR	SRX_ERRRPT_OR	Not used
48	MSW_EPG_ERRSTS: B0							
	EGRT1_ERRRPT0_OR	EGRC1_ERRRPT0_OR	EGRS1_ERRRPT0_P_OR	EGRS1_ERRRPT0_D_OR	EGRT0_ERRRPT0_OR	EGRC0_ERRRPT0_OR	EGRS0_ERRRPT0_P_OR	EGRS0_ERRRPT0_D_OR
49	MSW_EPG_ERRSTS: B1							
	EGTT0_ERRRPT0_OR	EGTC0_ERRRPT0_OR	EGTS0_ERRRPT0_P_OR	EGTS0_ERRRPT0_D_OR	EGABC_ERRRPT0_OR	EGAB_ERRRPT0_P_OR	EGAB_ERRRPT0_D_OR	EGRBF_ERRRPT0_OR
4A	MSW_EPG_ERRSTS: B2							
	Not used		EGTBF_ERRRPT	EGTBF_RAM_ERR	EGTT1_ERRRPT0_OR	EGTC1_ERRRPT0_OR	EGTS1_ERRRPT0_P_OR	EGTS1_ERRRPT0_D_OR
4B	MSW_SPG_ERRSTS: B0							
			SGRBF_RAM_ERR	SGRBF_ERRRPT0_OR	SGRT_ERRRPT0_OR	SGRC_ERRRPT0_OR	SGRS_ERRRPT0_P_OR	SGRS_ERRRPT0_D_OR
4C	MSW_RCTL_ERRLOG: B2							
	Not used			PLD HWERR	Not used			RGARB HWERR
4D	MSW_XREG_ERRSTS: B1							
	Not used						XREG MERR_OR	XREG HWERR_OR
4E	MSW_CRM_RAM_BODERR: B0							
	RAMREG_BODERR	RWB_WBODERR	RWB_RBODERR	RWDCTL_BODERR	RRDCTL_BODERR	RRB_WBODERR	RRB_RBODERR	RTNCTL_BODERR
4F	MSW_SPG_RT_ERRLOG0: B0							
	Not used						SGRTMR_TMRPER_LOG	SGRTMR_TOV_LOG

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Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = [00 to 07] (2/4)

	0	1	2	3	4	5	6	7
50	MSW_EPG_RT0_ERRLOG0: B0							
	Not used						EGRTMR0_TMRPER_LOG	EGRTMR0_TIMEOUT_LOG
51	MSW_EPG_RT1_ERRLOG0: B0							
	Not used	EGRTMR1_ACKOFF_TMRPER_LOG	EGRTMR1_ACKON_TMRPER_LOG	EGRTMR1_TMRPER_LOG	Not used	EGRTMR1_ACKOFF_TIMEOUT_LOG	EGRTMR1_ACKON_TIMEOUT_LOG	EGRTMR1_TIMEOUT_LOG
52	MSW_EPG_TT0_ERRLOG0: B0							
	Not used						EGTTMR0_TMRPER_LOG	EGTTMR0_TIMEOUT_LOG
53	MSW_EPG_TT1_ERRLOG0: B0							
	Not used				EGTTMR1_ACKOFF_TMRPER_LOG	EGTTMR1_ACKON_TMRPER_LOG	EGTTMR1_ACKOFF_TOV_LOG	EGTTMR1_ACKON_TOV_LOG
54	MSW_DS_ERR_ALL: B3 (DCTL0)							
	MAIN_SEQERR	TX_SEQERR	RX_SEQERR	ARCCNT_PTYERR	ACKOFF_TIMEOUT_ERR	ACKOFF_TIM_PTYERR	TXREQ_TIMEOUT_ERR	TXREQ_TIM_PTYERR
55	MSW_DS_ERR_ALL: B2 (DCTL0)							
	Not used			TXCNT_PTYERR	TXLEN_CNT_PTYERR	TX_WRDAT_PTYERR	BCRXC_PTYERR	PKTDAT_PTYERR
56	MSW_DS_ERR_ALL: B1 (DCTL0)							
	DAT_CMPERR	CORST_SERR	UNCST_SERR	HSNST_SERR	RXHDR_PTYERR	RXHDR_INVERR	HDCRC_CMPERR	HDCRC_PTYERR
57	MSW_DS_ERR_ALL: B0 (DCTL0)							
	Not used			RXCNT_PTYERR	RXPKT_PTYERR	PKTCRC_CMPERR	PKTCRC_PTYERR	Not used
58	MSW_DS_ERR_ALL: B3 (DCTL1)							
	MAIN_SEQERR	TX_SEQERR	RX_SEQERR	ARCCNT_PTYERR	ACKOFF_TIMEOUT_ERR	ACKOFF_TIM_PTYERR	TXREQ_TIMEOUT_ERR	TXREQ_TIM_PTYERR
59	MSW_DS_ERR_ALL: B2 (DCTL1)							
	Not used			TXCNT_PTYERR	TXLEN_CNT_PTYERR	TX_WRDAT_PTYERR	BCRXC_PTYERR	PKTDAT_PTYERR
5A	MSW_DS_ERR_ALL: B1 (DCTL1)							
	DAT_CMPERR	CORST_SERR	UNCST_SERR	HSNST_SERR	RXHDR_PTYERR	RXHDR_INVERR	HDCRC_CMPERR	HDCRC_PTYERR
5B	MSW_DS_ERR_ALL: B0 (DCTL1)							
	Not used			RXCNT_PTYERR	RXPKT_PTYERR	PKTCRC_CMPERR	PKTCRC_PTYERR	Not used
5C	MSW_DS_ERR_ALL: B3 (DCTL2)							
	MAIN_SEQERR	TX_SEQERR	RX_SEQERR	ARCCNT_PTYERR	ACKOFF_TIMEOUT_ERR	ACKOFF_TIM_PTYERR	TXREQ_TIMEOUT_ERR	TXREQ_TIM_PTYERR
5D	MSW_DS_ERR_ALL: B2 (DCTL2)							
	Not used			TXCNT_PTYERR	TXLEN_CNT_PTYERR	TX_WRDAT_PTYERR	BCRXC_PTYERR	PKTDAT_PTYERR
5E	MSW_DS_ERR_ALL: B1 (DCTL2)							
	DAT_CMPERR	CORST_SERR	UNCST_SERR	HSNST_SERR	RXHDR_PTYERR	RXHDR_INVERR	HDCRC_CMPERR	HDCRC_PTYERR
5F	MSW_DS_ERR_ALL: B0 (DCTL2)							
	Not used			RXCNT_PTYERR	RXPKT_PTYERR	PKTCRC_CMPERR	PKTCRC_PTYERR	Not used

Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = [00 to 07] (3/4)

	0	1	2	3	4	5	6	7
60	MSW_DS_ERR_ALL: B3 (DCTL3)							
	MAIN SEQERR	TX SEQERR	RX SEQERR	ARCCNT PTYERR	ACKOFF TIMOUT ERR	ACKOFF TIM PTYERR	TXREQ TIMOUT ERR	TXREQ TIM PTYERR
61	MSW_DS_ERR_ALL: B2 (DCTL3)							
	Not used			TXCNT PTYERR	TXLEN CNT PTYERR	TX WRDAT PTYERR	BCRXC PTYERR	PKTDAT PTYERR
62	MSW_DS_ERR_ALL: B1 (DCTL3)							
	DAT CMPERR	CORST SERR	UNCST SERR	HSNST SERR	RXHDR PTYERR	RXHDR INVERR	HDRCRC CMPERR	HDRCRC PTYERR
63	MSW_DS_ERR_ALL: B0 (DCTL3)							
	Not used			RXCNT PTYERR	RXPKT PTYERR	PKTCRC CMPERR	PKTCRC PTYERR	Not used
64	MSW_DS_ERR_ALL: B3 (DCTL4)							
	MAIN SEQERR	TX SEQERR	RX SEQERR	ARCCNT PTYERR	ACKOFF TIMOUT ERR	ACKOFF TIM PTYERR	TXREQ TIMOUT ERR	TXREQ TIM PTYERR
65	MSW_DS_ERR_ALL: B2 (DCTL4)							
	Not used			TXCNT PTYERR	TXLEN CNT PTYERR	TX WRDAT PTYERR	BCRXC PTYERR	PKTDAT PTYERR
66	MSW_DS_ERR_ALL: B1 (DCTL4)							
	DAT CMPERR	CORST SERR	UNCST SERR	HSNST SERR	RXHDR PTYERR	RXHDR INVERR	HDRCRC CMPERR	HDRCRC PTYERR
67	MSW_DS_ERR_ALL: B0 (DCTL4)							
	Not used			RXCNT PTYERR	RXPKT PTYERR	PKTCRC CMPERR	PKTCRC PTYERR	Not used
68	Reserve							
	Not used							
69	Reserve							
	Not used							
6A	Reserve							
	Not used							
6B	Reserve							
	Not used							
6C	Reserve							
	Not used							
6D	Reserve							
	Not used							
6E	Reserve							
	Not used							
6F	Reserve							
	Not used							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = [00 to 07] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = [10 to 17] (1/4)

	0	1	2	3	4	5	6	7
40	MPCI_ERRSTS_ALL: B3							
	STS	Not used			ERRSTS	WRNSTS	Not used	LOGSTS
41	MPCI_ERRSTS_ALL: B2							
	Not used	PHY/ SERDES_ ERRSTS	PHY/ SERDES_ WRNSTS	Not used	CMN_ LOGSTS (ZCORE)	CMN_ ERRSTS (ZCORE)	CMN_ WRNSTS (ZCORE)	
42	MPCI_ERRSTS_ALL: B1							
	Not used	WRNSTS (VC1-CMN)	WRNSTS (VC1-RX)	WRNSTS (VC1-TX)	Not used	ERRSTS (VC1-CMN)	ERRSTS (VC1-RX)	ERRSTS (VC1-TX)
43	MPCI_ERRSTS_ALL: B0							
	Not used	WRNSTS (VC0-CMN)	WRNSTS (VC0-RX)	WRNSTS (VC0-TX)	Not used	ERRSTS (VC0-CMN)	ERRSTS (VC0-RX)	ERRSTS (VC0-TX)
44	MPCI_CMN500_ERRSTS: B3							
	Not used	LINK_ DOWN_ERR	LINK_ WIDTH_ERR	LINK_ SPEED_ERR	LINK_ TRAINING_ ERR	Not used	DLM_RAM_ ADR_PARIT_ Y_ERROR	DLM_ SURPRISE_ DW_ERR
45	MPCI_CMN500_ERRSTS: B2							
	DLM_ REPLAY_ RO_ERR	DLM_ PTC_ERR	TLM_ FCPTC_ERR	TLM_ ROVF_ERR	TLM_ PTLP_ERR	TLM_ MFTLP_ERR	TLM_ ECRCE_ERR	TLM_ UR_ERR
46	MPCI_CMN500_ERRSTS: B1							
	Not used			ZPEX_TLM_ PARITY_ ERROR_STOP	PCMN_ TTMRSEQ_ TMRPERR	PCMN_ TTMRSEQ_ TMRTOV	PCMN_ TTMRSEQ_ ENCERR	PCMN_ TTMRSEQ_ CSTPERR
47	MPCI_CMN500_ERRSTS: B0							
	Not used	MONI_ ADR_PERR	MONI_ ERRADR_ PERR	MONI_ ERRCNT_ PERR	Not used	ZPEX_DLM_ ECC_ERROR_ NOR	ZPEX_DLM_ ECC_ERROR_ REP	ZPEX_ ERRINJ_ ECC_ERROR
48	MPCI_CORE_LINK_STATUS: B3							
	DLM_ VC1_ EN	DLM_ VC0_ EN	Not used	DLM_ DL_ UP	Not used		PLM_LINK_ WIDTH [5:4]	
49	MPCI_CORE_LINK_STATUS: B2							
	PLM_LINK_WIDTH [3:0]				PLM_LTSSM_STATE			
4A	MPCI_CORE_LINK_STATUS: B1							
	LTSSM_SST			PLM_LINK_ TRAINING	PLM_LINK_SPEED			
4B	MPCI_CORE_LINK_STATUS: B0							
	PLM_ RXSPEED				Not used			PLM_SPEE DCHG_MSK
4C	MPCI_PIF500_ERRSTS_RX0: B3							
	PCIRIF_ URERR	PCIRIF_ POISONED ERR	PCIRIF_ ECRC_ ERR	PCIRIF_ TLM_ RX_PERR	PCIRIF_TLM_ RAM_RXx_AAD R_PARITY_ERR OR	PCIRIF_TLM_ RAM_RXx_BAD R_PARITY_ERR OR	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_RX	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_ ALLOCATE
4D	MPCI_PIF500_ERRSTS_RX0: B1							
	PCIRSEQ_ TMR_PERR	PCIRSEQ_ TMR_TOV0	PCIRSEQ_ TMR_TOV1	PCIRSEQ_ TMR_TOV2	PIFRX_ ASYNC_ AMSETERR3	PIFRX_ ASYNC_ AMSETERR2	PIFRX_ ASYNC_ AMSETERR1	PIFRX_ ASYNC_ AMSETERR0
4E	MPCI_PIF500_ERRSTS_TX1: B3							
	PCITSEQ_ TMR_PERR	PCITSEQ_ TMR_TOV0	PCITSEQ_ TMR_TOV1	PCITSEQ_ TMR_TOV2	PIFTX_ ASYNC_ BMCLRERR3	PIFTX_ ASYNC_ BMCLRERR2	PIFTX_ ASYNC_ BMCLRERR1	PIFTX_ ASYNC_ BMCLRERR0
4F	MPCI_PIF333_ERRSTS_RX0: B1							
	Not used				ADPTSEQ1_ TMR_PERR	ADPTSEQ1_ TMR_TOV0	ADPTSEQ1_ TMR_TOV1	Not used

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Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = [10 to 17] (2/4)

	0	1	2	3	4	5	6	7
50	MPCI_PIF333_ERRSTS_TX0: B1							
	ADPRSEQ_TMR_PERR	ADPRSEQ_TMR_TOV0	ADPRSEQ_TMR_TOV1	Not used				
51	MPCI_CMN500_WRNSTS: B0							
	PLM_RECEIVER_ERR	DLM_DLLP_ERR	DLM_TLP_ERR	DLM_REPLAY_TO_ERR	Not used	ZPEX_DLM_ECC_RECOVERY_NOR	ZPEX_DLM_ECC_RECOVERY_REP	ZPEX_ERRI_NJ_ECC_RECOVERY
52	MPCI_CMN500_STS: B0							
	Not used						LOGFLG	MFTLPFLG
53	MPCI_TLM_MFTLP_CODE: B0							
	TLM_MFTLP_CODE							
54	MPCI_PIPE_AHB_IF_3: B3							
	pipe_AHBEND	Not used	pipe_HRESP		Not used			
55	MPCI_SERDES_AHB_IF_3: B3							
	ser_AHBEND	Not used	ser_HRESP		Not used			
56	MPCI_ZCORE_APPREG_18: B2							
	Detect/Deskew Fail Lane							
57	MPCI_ZCORE_APPREG_18: B0							
	Detect/Deskew Fail Lane							
58	MPCI_CORE_SYS_LRESETN: B0							
	Not used							sys_lresetn
59	MPCI_PCLK_STOP: B0							
	Not used							CLK_STOP
5A	MPCI_HSNMODE: B0							
	Not used							HSNMODE
5B	MPCI_ZCORE_SEC_PEEXCAP_2: B0							
	Lane Error Status Register							
5C	MPA_CRG_ERRLOG_ERRSTS : B0 (b7: 0)							
	Not used	Not used	Not used	CRG_CHK3_H_OR	Not used	S_CHK3_H_OR	Not used	E_CHK3_H_OR
5D	MPA_CRG_ERRLOG_ERRSTS : B2 (b23: 16)							
	Not used	Not used	Not used	Not used	Not used	S_SLAVE_OR	Not used	E_SLAVE_OR
5E	MPA_CRG_ERRLOG_H_STS_E : B2 (b23: 16)							
	Not used	Not used	Not used	Not used	Not used	Not used	ECTLLOG_ERRRPT_OR	ETBL_ERRRPT_OR
5F	MPA_CRG_ERRLOG_H_STS_E : B1 (b15: 8)							
	MINT_ENP_C_ERRRPT_OR	ETX_NP_ERRRPT_OR	ETX_PC_ERRRPT_OR	ETSEQ_ERRRPT_OR	ETXARB_ERRRPT_OR	MINT_ERRRPT_OR	MINTSEQ_ERRRPT_OR	ENP_ERRRPT_OR

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Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = [10 to 17] (3/4)

	0	1	2	3	4	5	6	7
60	MPA_CRG_ERRLOG_H_STS_E : B0 (b7:0)							
	ENPSEQ_ERRRPT_OR	Not used	Not used	Not used	ECTL_ERRRPT_OR	ECSEQ_ERRRPT_OR	ERX_ERRRPT_OR	Not used
61	MPA_CRG_ERRLOG_H_STS_S : B2 (b23:16)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	STBL_ERRRPT_OR
62	MPA_CRG_ERRLOG_H_STS_S : B1 (b15:8)							
	Not used	STX_NP_ERRRPT_OR	STX_PC_ERRRPT_OR	STSEQ_ERRRPT_OR	STXARB_ERRRPT_OR	Not used	Not used	SNP_ERRRPT_OR
63	MPA_CRG_ERRLOG_H_STS_S : B0 (b7:0)							
	SNPSEQ_ERRRPT_OR	Not used	Not used	Not used	SCTL_ERRRPT_OR	SCSEQ_ERRRPT_OR	SRX_ERRRPT_OR	Not used
64	MSW_EPG_ERRSTS : B0 (b7:0)							
	EGRT1_ERRRPT0_OR	EGRC1_ERRRPT0_OR	EGRS1_ERRRPT0_P_OR	EGRS1_ERRRPT0_D_OR	EGRT0_ERRRPT0_OR	EGRC0_ERRRPT0_OR	EGRS0_ERRRPT0_P_OR	EGRS0_ERRRPT0_D_OR
65	MSW_EPG_ERRSTS : B1 (b15:8)							
	EGTT0_ERRRPT0_OR	EGTC0_ERRRPT0_OR	EGTS0_ERRRPT0_P_OR	EGTS0_ERRRPT0_D_OR	EGABC_ERRRPT0_OR	EGAB_ERRRPT0_P_OR	EGAB_ERRRPT0_D_OR	EGRBF_ERRRPT0_OR
66	MSW_EPG_ERRSTS : B2 (b23:16)							
	Not used	Not used	EGTBF_ERRRPT	EGTBF_RAM_ERR	EGTT1_ERRRPT0_O_R	EGTC1_ERRRPT0_O_R	EGTS1_ERRRPT0_P_OR	EGTS1_ERRRPT0_D_OR
67	MSW_SPG_ERRSTS : B0 (b7:0)							
	Not used	Not used	SGRBF_RAM_ERR	SGRBF_ERRRPT0_OR	SGRT_ERRRPT0_OR	SGRC_ERRRPT0_OR	SGRS_ERRRPT0_P_OR	SGRS_ERRRPT0_D_OR
68	MSW_SPG_RT_ERRLOG0 : B0 (b7:0)							
	Not used	Not used	Not used	Not used	Not used	Not used	SGRTMR_TMRPER_LOG	SGRTMR_TOV_LOG
69	MSW_EPG_RT0_ERRLOG0 : B0 (b7:0)							
	Not used	Not used	Not used	Not used	Not used	Not used	EGRTMR0_TMRPER_LOG	EGRTMR0_TIMEOUT_LOG
6A	MSW_EPG_RT1_ERRLOG0 : B0 (b7:0)							
	Not used	EGRTMR1_ACKOFF_TMRPER_LOG	EGRTMR1_ACKON_TMRPER_LOG	EGRTMR1_TMRPER_LOG	Not used	EGRTMR1_ACKOFF_TIMEOUT_LOG	EGRTMR1_ACKON_TIMEOUT_LOG	EGRTMR1_TIMEOUT_LOG
6B	MSW_EPG_TT0_ERRLOG0 : B0 (b7:0)							
	Not used	Not used	Not used	Not used	Not used	Not used	EGTTMR0_TMRPER_LOG	EGTTMR0_TIMEOUT_LOG
6C	MSW_EPG_TT1_ERRLOG0 : B0 (b7:0)							
	Not used	Not used	Not used	Not used	EGTTMR1_ACKOFF_TMRPER_LOG	EGTTMR1_ACKON_TMRPER_LOG	EGTTMR1_ACKOFF_TOV_LOG	EGTTMR1_ACKON_TOV_LOG
6D	Reserve							
	Not used							
6E	Reserve							
	Not used							
6F	Reserve							
	Not used							

Byte27 = 81 (CHK3) Byte40-7F Detail

(2) Byte28 = [10 to 17] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = [20 to 2B] (1/4)

	0	1	2	3	4	5	6	7
40	MPCI_ERRSTS_ALL: B3							
	STS	Not used			ERRSTS	WRNSTS	Not used	LOGSTS
41	MPCI_ERRSTS_ALL: B2							
	Not used		PHY/ SERDES ERRSTS	PHY/ SERDES WRNSTS	Not used	CMN LOGSTS (ZCORE)	CMN ERRSTS (ZCORE)	CMN WRNSTS (ZCORE)
42	MPCI_ERRSTS_ALL: B1							
	Not used	WRNSTS (VC1-CMN)	WRNSTS (VC1-RX)	WRNSTS (VC1-TX)	Not used	ERRSTS (VC1-CMN)	ERRSTS (VC1-RX)	ERRSTS (VC1-TX)
43	MPCI_ERRSTS_ALL: B0							
	Not used	WRNSTS (VC0-CMN)	WRNSTS (VC0-RX)	WRNSTS (VC0-TX)	Not used	ERRSTS (VC0-CMN)	ERRSTS (VC0-RX)	ERRSTS (VC0-TX)
44	MPCI_CMN500_ERRSTS: B3							
	Not used	LINK DOWN_ERR	LINK WIDTH_ERR	LINK SPEED_ERR	LINK TRAINING_ ERR	Not used	DLM_RAM_ ADR_PARITY_ ERROR	DLM_ SURPRISE_ DW_ERR
45	MPCI_CMN500_ERRSTS: B2							
	DLM_ REPLAY_ RO_ERR	DLM_ PTC_ERR	TLM_ FCPTC_ERR	TLM_ ROVF_ERR	TLM_ PTLP_ERR	TLM_ MFTLP_ERR	TLM_ ECRCE_ERR	TLM_ UR_ERR
46	MPCI_CMN500_ERRSTS: B1							
	Not used			ZPEX_TLM_ PARITY_ ERROR_STOP	PCMN_ TTMRSEQ_ TMRPERR	PCMN_ TTMRSEQ_ TMRTOV	PCMN_ TTMRSEQ_ ENCERR	PCMN_ TTMRSEQ_ CSTPERR
47	MPCI_CMN500_ERRSTS: B0							
	Not used	MONI_ ADR_PERR	MONI_ ERRADR_ PERR	MONI_ ERRCNT_ PERR	Not used	ZPEX_DLM_ ECC_ERROR_ NOR	ZPEX_DLM_ ECC_ERROR_ REP	ZPEX_ ERRINJ_ECC_ ERROR
48	MPCI_CORE_LINK_STATUS: B3							
	DLM_ VC1_EN	DLM_ VC0_EN	Not used	DLM_ DL_UP	Not used		PLM_LINK_WIDTH [5:4]	
49	MPCI_CORE_LINK_STATUS: B2							
	PLM_LINK_WIDTH [3:0]				PLM_LTSSM_STATE			
4A	MPCI_CORE_LINK_STATUS: B1							
	LTSSM_SST			PLM_LINK_ TRAINING	PLM_LINK_SPEED			
4B	MPCI_CORE_LINK_STATUS: B0							
	PLM_RXSPEED				Not used			PLM_ SPEEDCHG_ MSK
4C	MPCI_PIF500_ERRSTS_RX0: B3							
	PCIRIF_ URERR	PCIRIF_ POISONED ERR	PCIRIF_ECR_ C ERR	PCIRIF_TLM_ RX_PERR	PCIRIF_TLM_ RAM_RXx_ AADR_PARITY_ ERROR	PCIRIF_TLM_ RAM_RXx_ BADR_PARITY_ ERROR	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_RX	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_ ALLOCATE
4D	MPCI_PIF500_ERRSTS_RX0: B1							
	PCIRSEQ_ TMR_PERR	PCIRSEQ_ TMR_TOV0	PCIRSEQ_ TMR_TOV1	PCIRSEQ_ TMR_TOV2	PIFRX_ ASYNC_ AMSETERR3	PIFRX_ ASYNC_ AMSETERR2	PIFRX_ ASYNC_ AMSETERR1	PIFRX_ ASYNC_ AMSETERR0
4E	MPCI_PIF500_ERRSTS_TX1: B3							
	PCITSEQ_ TMR_PERR	PCITSEQ_ TMR_TOV0	PCITSEQ_ TMR_TOV1	PCITSEQ_ TMR_TOV2	PIFTX_ ASYNC_ BMCLRERR3	PIFTX_ ASYNC_ BMCLRERR2	PIFTX_ ASYNC_ BMCLRERR1	PIFTX_ ASYNC_ BMCLRERR0
4F	MPCI_PIF333_ERRSTS_RX0: B1							
	Not used				ADPTSEQ1_ TMR_PERR	ADPTSEQ1_ TMR_TOV0	ADPTSEQ1_ TMR_TOV1	Not used

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Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = [20 to 2B] (2/4)

	0	1	2	3	4	5	6	7
50	MPCI_PIF333_ERRSTS_TX0: B1							
	ADPRSEQ_TMR_PERR	ADPRSEQ_TMR_TOV0	ADPRSEQ_TMR_TOV1	Not used				
51	MPCI_CMN500_WRNSTS: B0							
	PLM_RECEIVER_ERR	DLM_DLLP_ERR	DLM_TLP_ERR	DLM_REPLAY_TO_ERR	Not used	ZPEX_DLM_EC_C_RECOVERY_NOR	ZPEX_DLM_EC_C_RECOVERY_REP	ZPEX_ERRI_NJ_ECC_RECOVERY
52	MPCI_CMN500_STS: B0							
	Not used						LOGFLG	MFTLPFLG
53	MPCI_TLM_MFTLP_CODE: B0							
	TLM_MFTLP_CODE							
54	MPCI_PIPE_AHB_IF_3: B3							
	pipe_AHBEND	Not used	pipe_HRESP		Not used			
55	MPCI_SERDES_AHB_IF_3: B3							
	ser_AHBEND	Not used	ser_HRESP		Not used			
56	MPCI_ZCORE_APPREG_18: B2							
	Detect/Deskew Fail Lane							
57	MPCI_ZCORE_APPREG_18: B0							
	Detect/Deskew Fail Lane							
58	MPCI_CORE_SYS_LRESETN: B0							
	Not used							sys_lresetn
59	MPCI_PCLK_STOP: B0							
	Not used							CLK_STOP
5A	MPCI_HSNMODE: B0							
	Not used							HSNMODE
5B	MPCI_ZCORE_SEC_PEEXCAP_2: B0							
	Lane Error Status Register							
5C	MSW_XPG_ERRSTS: B0							
	XGTT1_ERRRPT0_OR	XGTC1_ERRRPT0_OR	XGTS1_ERRRPT0_P_OR	XGTS1_ERRRPT0_D_OR	XGTT0_ERRRPT0_OR	XGTC0_ERRRPT0_OR	XGTS0_ERRRPT0_P_OR	XGTS0_ERRRPT0_D_OR
5D	MSW_XPG_ERRSTS: B1							
	XGRBF_RAM_ERR	XGRBF_ERRRPT0_OR	XGTBF_RAM_ERR	XGTBF_ERRRPT0_OR	XGRT_ERRRPT0_OR	XGRC_ERRRPT0_OR	XGRS_ERRRPT0_P_OR	XGRS_ERRRPT0_D_OR
5E	MSW_XPG_ERRSTS: B2							
	Not used							XGTARB_ERRRPT0_OR
5F	MSW_XPG_TBF_ERRLOG0: B0							
	Not used		CRCERR_LOG	RAM_HWERR_LOG	Not used		XGTBF_DTMC_CNT_PER_LOG	XGTBF_DT_PER_LOG

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Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = [20 to 2B] (3/4)

	0	1	2	3	4	5	6	7
60	MSW_XPG_RBF_ADP_CND: B0							
	Not used			PIF _x ERR_TX	PIF _x ERR_RX	PIF _x ERR_CMN	PCI ERR_CMN	Linkdown
61	MSW_XPG_RBF_ADP_CND: B1							
	Not used				TOV_LOG	BLOCKADE	PATH DISABLE	ADP_NOT_ SELECT
62	MSW_XPG_TT0_ERRLOG0: B0							
	Not used						XGTTMR0 LST_TMRPE R_LOG	XGTTMR0 LST_TOV_ LOG
63	MSW_XPG_TT1_ERRLOG0: B0							
	Not used		XGTTMR1_GNT _TMRPER_ LOG	XGTTMR1_ACK OFF_TMRPER_ LOG	XGTTMR1_ACK ON_TMRPER_ LOG	XGTTMR1_ GNT TOV_LOG	XGTTMR1_ ACKOFF_ TOV_LOG	XGTTMR1_ ACKON_ TOV_LOG
64	MSW_XPG_RT_ERRLOG0:B0							
	XGRTMR_GNT _TMRPER_ LOG	XGRTMR_ACK OFF_TMRPER_ LOG	XGRTMR_ACK ON_TMRPER_ LOG	XGRTMR_GNT TOV_LOG	XGRTMR_SW ACKOFF_TOV_ LOG	XGRTMR_SW ACKON_TOV_ LOG	XGRTMR_ ACKOFF_ TOV_LOG	XGRTMR_ ACKON_ TOV_LOG
65	Reserve							
	Not used							
66	Reserve							
	Not used							
67	Reserve							
	Not used							
68	Reserve							
	Not used							
69	Reserve							
	Not used							
6A	Reserve							
	Not used							
6B	Reserve							
	Not used							
6C	Reserve							
	Not used							
6D	Reserve							
	Not used							
6E	Reserve							
	Not used							
6F	Reserve							
	Not used							

Byte27 = 81 (CHK3) Byte40-7F Detail

(3) Byte28 = [20 to 2B] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = [30 to 35] (1/4)

	0	1	2	3	4	5	6	7
40	MPCI_ERRSTS_ALL: B3							
	STS	Not used			ERRSTS	WRNSTS	Not used	LOGSTS
41	MPCI_ERRSTS_ALL: B2							
	Not used		PHY/ SERDES ERRSTS	PHY/ SERDES WRNSTS	Not used	CMN LOGSTS (ZCORE)	CMN ERRSTS (ZCORE)	CMN WRNSTS (ZCORE)
42	MPCI_ERRSTS_ALL: B1							
	Not used	WRNSTS (VC1-CMN)	WRNSTS (VC1-RX)	WRNSTS (VC1-TX)	Not used	ERRSTS (VC1-CMN)	ERRSTS (VC1-RX)	ERRSTS (VC1-TX)
43	MPCI_ERRSTS_ALL: B0							
	Not used	WRNSTS (VC0-CMN)	WRNSTS (VC0-RX)	WRNSTS (VC0-TX)	Not used	ERRSTS (VC0-CMN)	ERRSTS (VC0-RX)	ERRSTS (VC0-TX)
44	MPCI_CMN500_ERRSTS: B3							
	Not used	LINK DOWN_ERR	LINK WIDTH_ERR	LINK SPEED_ERR	LINK TRAINING_ ERR	Not used	DLM_RAM_ ADR_PARITY_ ERROR	DLM SURPRISE_ DW_ERR
45	MPCI_CMN500_ERRSTS: B2							
	DLM REPLAY_ RO_ERR	DLM PTC_ERR	TLM FCPTC_ERR	TLM ROVF_ERR	TLM PTLP_ERR	TLM MFTLP_ERR	TLM ECRCE_ERR	TLM UR_ERR
46	MPCI_CMN500_ERRSTS: B1							
	Not used			ZPEX_TLM_ PARITY_ ERROR_STOP	PCMN TTMRSEQ_ TMRPERR	PCMN TTMRSEQ_ TMRTOV	PCMN TTMRSEQ_ ENCERR	PCMN TTMRSEQ_ CSTPERR
47	MPCI_CMN500_ERRSTS: B0							
	Not used	MONI ADR_PERR	MONI ERRADR_ PERR	MONI ERRCNT_ PERR	Not used	ZPEX_DLM_ ECC_ERROR_ NOR	ZPEX_DLM_ ECC_ERROR_ REP	ZPEX_ ERRINJ_ECC_ ERROR
48	MPCI_CORE_LINK_STATUS: B3							
	DLM VC1_EN	DLM VC0_EN	Not used	DLM DL_UP	Not used		PLM_LINK WIDTH [5:4]	
49	MPCI_CORE_LINK_STATUS: B2							
	PLM_LINK_WIDTH [3:0]				PLM_LTSSM_STATE			
4A	MPCI_CORE_LINK_STATUS: B1							
	LTSSM_SST			PLM_LINK_ TRAINING	PLM_LINK_SPEED			
4B	MPCI_CORE_LINK_STATUS: B0							
	PLM_RXSPEED				Not used			PLM SPEEDCHG _MSK
4C	MPCI_PIF500_ERRSTS_RX0 (VC0): B3							
	PCIRIF_ URERR	PCIRIF POISONED ERR	PCIRIF_ECR C ERR	PCIRIF TLM_RX_ PERR	PCIRIF_TLM_ RAM_RXx_ AADR_PARITY_ ERROR	PCIRIF_TLM_ RAM_RXx_ BADR_PARITY_ ERROR	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_RX	PCIRIF_ZPEX_ TLM_PARITY_ ERROR_ ALLOCATE
4D	MPCI_PIF500_ERRSTS_RX0 (VC0): B1							
	PCIRSEQ_ TMR_PERR	PCIRSEQ_ TMR_TOV0	PCIRSEQ_ TMR_TOV1	PCIRSEQ_ TMR_TOV2	PIFRX_ ASYNC_ AMSETERR3	PIFRX_ ASYNC_ AMSETERR2	PIFRX_ ASYNC_ AMSETERR1	PIFRX_ ASYNC_ AMSETERR0
4E	MPCI_PIF500_ERRSTS_TX1 (VC0): B3							
	PCITSEQ_ TMR_PERR	PCITSEQ_ TMR_TOV0	PCITSEQ_ TMR_TOV1	PCITSEQ_ TMR_TOV2	PIFTX_ ASYNC_ BMCLRERR3	PIFTX_ ASYNC_ BMCLRERR2	PIFTX_ ASYNC_ BMCLRERR1	PIFTX_ ASYNC_ BMCLRERR0
4F	MPCI_PIF333_ERRSTS_RX0 (VC0): B1							
	Not used				ADPTSEQ1_ TMR_PERR	ADPTSEQ1_ TMR_TOV0	ADPTSEQ1_ TMR_TOV1	Not used

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Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = [30 to 35] (2/4)

	0	1	2	3	4	5	6	7
50	MPCI_PIF333_ERRSTS_TX0 (VC0): B1							
	ADPRSEQ_TMR_PERR	ADPRSEQ_TMR_TOV0	ADPRSEQ_TMR_TOV1	Not used				
51	MPCI_CMN500_WRNSTS: B0							
	PLM_RECEIVER_ERR	DLM_DLLP_ERR	DLM_TLP_ERR	DLM_REPLAY_TO_ERR	Not used	ZPEX_DLM_ECC_RECOVERY_NOR	ZPEX_DLM_ECC_RECOVERY_REP	ZPEX_ERRINJ_ECC_RECOVERY
52	MPCI_CMN500_STS: B0							
	Not used						LOGFLG	MFTLPFLG
53	MPCI_TLM_MFTLP_CODE: B0							
	TLM_MFTLP_CODE							
54	MPCI_PIPE_AHB_IF_3: B3							
	pipe_AHBEND	Not used	pipe_HRESP		Not used			
55	MPCI_SERDES_AHB_IF_3: B3							
	ser_AHBEND	Not used	ser_HRESP		Not used			
56	MPCI_ZCORE_APPREG_18: B2							
	Detect/Deskew Fail Lane							
57	MPCI_ZCORE_APPREG_18: B0							
	Detect/Deskew Fail Lane							
58	MPCI_CORE_SYS_LRESETN: B0							
	Not used							sys_lresetn
59	MPCI_PCLK_STOP: B0							
	Not used							CLK_STOP
5A	MPCI_HSNMODE: B0							
	Not used							HSNMODE
5B	MPCI_ZCORE_SEC_PEEEXCAP_2: B0							
	Lane Error Status Register							
5C	MPCI_PIF500_ERRSTS_RX0 (VC1): B3							
	PCIRIF_URERR	PCIRIF_POISONED_ERR	PCIRIF_ECRC_ERR	PCIRIF_TLM_RX_PERR	PCIRIF_TLM_RAM_RXx_AADR_PARITY_ERROR	PCIRIF_TLM_RAM_RXx_BADR_PARITY_ERROR	PCIRIF_ZPEX_TLM_PARITY_ERROR_RX	PCIRIF_ZPEX_TLM_PARITY_ERROR_ALLOCATE
5D	MPCI_PIF500_ERRSTS_RX0 (VC1): B1							
	PCIRSEQ_TMR_PERR	PCIRSEQ_TMR_TOV0	PCIRSEQ_TMR_TOV1	PCIRSEQ_TMR_TOV2	PIFRX_ASYNC_AMSETERR3	PIFRX_ASYNC_AMSETERR2	PIFRX_ASYNC_AMSETERR1	PIFRX_ASYNC_AMSETERR0
5E	MPCI_PIF500_ERRSTS_TX1 (VC1): B3							
	PCITSEQ_TMR_PERR	PCITSEQ_TMR_TOV0	PCITSEQ_TMR_TOV1	PCITSEQ_TMR_TOV2	PIFTX_ASYNC_BMCLRERR3	PIFTX_ASYNC_BMCLRERR2	PIFTX_ASYNC_BMCLRERR1	PIFTX_ASYNC_BMCLRERR0
5F	MPCI_PIF333_ERRSTS_RX0 (VC1): B1							
	Not used				ADPTSEQ1_TMR_PERR	ADPTSEQ1_TMR_TOV0	ADPTSEQ1_TMR_TOV1	Not used

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = [30 to 35] (3/4)

	0	1	2	3	4	5	6	7
60	MPCI_PIF333_ERRSTS_TX0 (VC1): B1							
	ADPRSEQ_TMR_PERR	ADPRSEQ_TMR_TOV0	ADPRSEQ_TMR_TOV1	Not used				
61	MSW_XPG_ERRSTS: B0							
	XGTT1_ERRRPT0_OR	XGTC1_ERRRPT0_OR	XGTS1_ERRRPT0_P_OR	XGTS1_ERRRPT0_D_OR	XGTT0_ERRRPT0_OR	XGTC0_ERRRPT0_OR	XGTS0_ERRRPT0_P_OR	XGTS0_ERRRPT0_D_OR
62	MSW_XPG_ERRSTS: B1							
	XGRBF_RAM_ERR	XGRBF_ERRRPT0_OR	XGTBF_RAM_ERR	XGTBF_ERRRPT0_OR	XGRT_ERRRPT0_OR	XGRC_ERRRPT0_OR	XGRS_ERRRPT0_P_OR	XGRS_ERRRPT0_D_OR
63	MSW_XPG_ERRSTS: B2							
	Not used							XGTARB_ERRRPT0_OR
64	MSW_XPG_TBF_ERRLOG0: B0							
	Not used		CRCERR_LOG	RAM_HWERR_LOG	Not used		XGTBF_DTMC_CNT_PER_LOG	XGTBF_DT_PER_LOG
65	MSW_XPG_RBF_ADP_CND: B0							
	Not used			PIF _x ERR_TX	PIF _x ERR_RX	PIF _x ERR_CMN	PCI_ERR_CMN	Link down
66	MSW_XPG_RBF_ADP_CND: B1							
	Not used				TOV_LOG	BLOCKADE	PATH_DISABLE	ADP_NOT_SELECT
67	MSW_SXPG_ERRSTS: B0							
	SXGTT1_ERRRPT0_OR	SXGTC1_ERRRPT0_OR	SXGTS1_ERRRPT0_P_OR	SXGTS1_ERRRPT0_D_OR	SXGTT0_ERRRPT0_OR	SXGTC0_ERRRPT0_OR	SXGTS0_ERRRPT0_P_OR	SXGTS0_ERRRPT0_D_OR
68	MSW_SXPG_ERRSTS: B1							
	SXGRBF_RAM_ERR	SXGRBF_ERRRPT0_OR	SXGTBF_RAM_ERR	SXGTBF_ERRRPT0_OR	SXGRT_ERRRPT0_OR	SXGRC_ERRRPT0_OR	SXGRS_ERRRPT0_P_OR	SXGRS_ERRRPT0_D_OR
69	MSW_SXPG_ERRSTS: B2							
	Not used							SXGTARB_ERRRPT0_OR
6A	MSW_SXPG_TBF_ERRLOG0: B0							
	Not used		CRCERR_LOG	RAM_HWERR_LOG	Not used	SXGTBF_HARD_ERR_LOG	SXGTBF_DTMC_CNT_PER_LOG	SXGTBF_DT_PER_LOG
6B	MSW_SXPG_RBF_ADP_CND: B0							
	Not used			PIF _x ERR_TX	PIF _x ERR_RX	PIF _x ERR_CMN	PCI_ERR_CMN	Link down
6C	MSW_SXPG_RBF_ADP_CND: B1							
	Not used				TOV_LOG	BLOCKADE	PATH_DISABLE	ADP_NOT_SELECT
6D	HSNSTS B0							
	Error	Warning	Module 0 or 1		LSI LR = 0x0 to 0xB MSW = 0xC to 0xF			
6E	HSNSTS B1							
	Function							
6F	HSNSTS B2							
	Micro error	Hard error	Not used	Not used	Format			

Byte27 = 81 (CHK3) Byte40-7F Detail

(4) Byte28 = [30 to 35] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = [40] (1/4)

	0	1	2	3	4	5	6	7
40	MPA_CRG_ERRLOG_MERR_MP00: B3							
	LM17 L-CHK3	LM16 L-CHK3	LM15 L-CHK3	LM14 L-CHK3	LM13 L-CHK3	LM12 L-CHK3	LM11 L-CHK3	LM10 L-CHK3
41	MPA_CRG_ERRLOG_MERR_MP00: B2							
	LM07 L-CHK3	LM06 L-CHK3	LM05 L-CHK3	LM04 L-CHK3	LM03 L-CHK3	LM02 L-CHK3	LM01 L-CHK3	LM00 L-CHK3
42	MPA_CRG_ERRLOG_MERR_MP00: B1							
	SM13 L-CHK3	SM12 L-CHK3	SM11 L-CHK3	SM10 L-CHK3	SM03 L-CHK3	SM02 L-CHK3	SM01 L-CHK3	SM00 L-CHK3
43	MPA_CRG_ERRLOG_MERR_MP00: B0							
	Not used			ILLCPL- CHK3	Not used		M-MPA- CHK3	M-HSN- CHK3
44	MPA_CRG_ERRLOG_MERR_MP01: B3							
	Not used				LR1B L-CHK3	LR1A L-CHK3	LR19 L-CHK3	LR18 L-CHK3
45	MPA_CRG_ERRLOG_MERR_MP01: B2							
	LR17 L-CHK3	LR16 L-CHK3	LR15 L-CHK3	LR14 L-CHK3	LR13 L-CHK3	LR12 L-CHK3	LR11 L-CHK3	LR10 L-CHK3
46	MPA_CRG_ERRLOG_MERR_MP01: B1							
	Not used				LR0B L-CHK3	LR0A L-CHK3	LR09 L-CHK3	LR08 L-CHK3
47	MPA_CRG_ERRLOG_MERR_MP01: B0							
	LR07 L-CHK3	LR06 L-CHK3	LR05 L-CHK3	LR04 L-CHK3	LR03 L-CHK3	LR02 L-CHK3	LR01 L-CHK3	LR00 L-CHK3
48	MPA_CRG_ERRLOG_TOV_MP00: B3							
	LM17 TOV-CHK3	LM16 TOV-CHK3	LM15 TOV-CHK3	LM14 TOV-CHK3	LM13 TOV-CHK3	LM12 TOV-CHK3	LM11 TOV-CHK3	LM10 TOV-CHK3
49	MPA_CRG_ERRLOG_TOV_MP00: B2							
	LM07 TOV-CHK3	LM06 TOV-CHK3	LM05 TOV-CHK3	LM04 TOV-CHK3	LM03 TOV-CHK3	LM02 TOV-CHK3	LM01 TOV-CHK3	LM00 TOV-CHK3
4A	MPA_CRG_ERRLOG_TOV_MP00: B1							
	SM13 TOV-CHK3	SM12 TOV-CHK3	SM11 TOV-CHK3	SM10 TOV-CHK3	SM03 TOV-CHK3	SM02 TOV-CHK3	SM01 TOV-CHK3	SM00 TOV-CHK3
4B	MPA_CRG_ERRLOG_TOV_MP00: B0							
	Not used							
4C	MPA_CRG_ERRLOG_TOV_MP01: B3							
	Not used				LR1B TOV-CHK3	LR1A TOV-CHK3	LR19 TOV-CHK3	LR18 TOV-CHK3
4D	MPA_CRG_ERRLOG_TOV_MP01: B2							
	LR17 TOV-CHK3	LR16 TOV-CHK3	LR15 TOV-CHK3	LR14 TOV-CHK3	LR13 TOV-CHK3	LR12 TOV-CHK3	LR11 TOV-CHK3	LR10 TOV-CHK3
4E	MPA_CRG_ERRLOG_TOV_MP01: B1							
	Not used				LR0B TOV-CHK3	LR0A TOV-CHK3	LR09 TOV-CHK3	LR08 TOV-CHK3
4F	MPA_CRG_ERRLOG_TOV_MP01: B0							
	LR07 TOV-CHK3	LR06 TOV-CHK3	LR05 TOV-CHK3	LR04 TOV-CHK3	LR03 TOV-CHK3	LR02 TOV-CHK3	LR01 TOV-CHK3	LR00 TOV-CHK3

Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = [40] (2/4)

	0	1	2	3	4	5	6	7
50	MPA_SCTL_LOG_HSNSTS_MP00: B3							
	Error	Warning	Module number		LSI number			
51	MPA_SCTL_LOG_HSNSTS_MP00: B2							
	Function Number							
52	MPA_SCTL_LOG_HSNSTS_MP00: B1							
	Error class				Format number			
53	MPA_SCTL_LOG_HSNSTS_MP00: B0							
	FB format							
54	MPA_SCTL_LOG_HSNSTS_MP01: B3							
	FB format							
55	MPA_SCTL_LOG_HSNSTS_MP01: B2							
	FB format							
56	MPA_SCTL_LOG_HSNSTS_MP01: B1							
	FB format							
57	MPA_SCTL_LOG_HSNSTS_MP01: B0							
	FB format							
58	MPA_SCTL_LOG_HSNSTS_MP02: B3							
	DataLength_4B_Over	ACC			OPT_MODE			
59	MPA_SCTL_LOG_HSNSTS_MP02: B2							
	Fmt		Type		X_NUM		MDL_NUM	
5A	MPA_SCTL_LOG_HSNSTS_MP02: B1							
	LSI_NUM				FNC_NUM [7:4]			
5B	MPA_SCTL_LOG_HSNSTS_MP02: B0							
	FNC_NUM [3:0]				DST_ADDRESS [35:32]			
5C	MPA_SCTL_LOG_HSNSTS_MP03: B3							
	DST_ADDRESS [31:24]							
5D	MPA_SCTL_LOG_HSNSTS_MP03: B2							
	DST_ADDRESS [23:16]							
5E	MPA_SCTL_LOG_HSNSTS_MP03: B1							
	DST_ADDRESS [15:8]							
5F	MPA_SCTL_LOG_HSNSTS_MP03: B0							
	DST_ADDRESS [7:0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = [40] (3/4)

	0	1	2	3	4	5	6	7
60	MPA_SCTL_LOG_TOVTBL_MP00: B3							
	DataLength_4B_Over	ACC			OPT_MODE			
61	MPA_SCTL_LOG_TOVTBL_MP00: B2							
	Fmt		Type		X_NUM		MDL_NUM	
62	MPA_SCTL_LOG_TOVTBL_MP00: B1							
	LSI_NUM				FNC_NUM [7:4]			
63	MPA_SCTL_LOG_TOVTBL_MP00: B0							
	FNC_NUM [3:0]				DST_ADDRESS [35:32]			
64	MPA_SCTL_LOG_TOVTBL_MP01: B3							
	DST_ADDRESS [31:24]							
65	MPA_SCTL_LOG_TOVTBL_MP01: B2							
	DST_ADDRESS [23:16]							
66	MPA_SCTL_LOG_TOVTBL_MP01: B1							
	DST_ADDRESS [15:8]							
67	MPA_SCTL_LOG_TOVTBL_MP01: B0							
	DST_ADDRESS [7:0]							
68	MPA_SCTL_LOG_ILLCPL_MP00: B3							
	LM_TGTADR [15:8]							
69	MPA_SCTL_LOG_ILLCPL_MP00: B2							
	LM_TGTADR [7:0]							
6A	MPA_SCTL_LOG_ILLCPL_MP00: B1							
	HDR_LEN							
6B	MPA_SCTL_LOG_ILLCPL_MP00: B0							
	TBL_LEN							
6C	MPA_SCTL_LOG_ILLCPL_MP01: B3							
	HDR_REQID [15:8]							
6D	MPA_SCTL_LOG_ILLCPL_MP01: B2							
	HDR_REQID [7:0]							
6E	MPA_SCTL_LOG_ILLCPL_MP01: B1							
	HDR_DESID [11:4]							
6F	MPA_SCTL_LOG_ILLCPL_MP01: B0							
	HDR_TAG							

Byte27 = 81 (CHK3) Byte40-7F Detail

(5) Byte28 = [40] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = [50] (1/4)

	0	1	2	3	4	5	6	7
40	MPA_ECTL_LOG_MER_MP03: B3							
	Not used		FMT		TYPE			
41	MPA_ECTL_LOG_MER_MP03: B2							
	LENGTH							
42	MPA_ECTL_LOG_MER_MP03: B1							
	NTV_BE							
43	MPA_ECTL_LOG_MER_MP03: B0							
	MP_ADR [39:32]							
44	MPA_ECTL_LOG_MER_MP02: B3							
	MP_ADR [31:24]							
45	MPA_ECTL_LOG_MER_MP02: B2							
	MP_ADR [23:16]							
46	MPA_ECTL_LOG_MER_MP02: B1							
	MP_ADR [15:8]							
47	MPA_ECTL_LOG_MER_MP02: B0							
	MP_ADR [7:0]							
48	MPA_ECTL_LOG_MER_MP01: B3							
	LM OFFSET ADR [19:12]							
49	MPA_ECTL_LOG_MER_MP01: B2							
	LM OFFSET ADR [11:8]				Not used		MDL	
4A	MPA_ECTL_LOG_MER_MP01: B1							
	LSI				FNC [7:4]			
4B	MPA_ECTL_LOG_MER_MP01: B0							
	FNC [3:0]				HSN_ADR [35:32]			
4C	MPA_ECTL_LOG_MER_MP00: B3							
	HSN_ADR [31:24]							
4D	MPA_ECTL_LOG_MER_MP00: B2							
	HSN_ADR [23:16]							
4E	MPA_ECTL_LOG_MER_MP00: B1							
	HSN_ADR [15:8]							
4F	MPA_ECTL_LOG_MER_MP00: B0							
	HSN_ADR [7:0]							

Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = [50] (2/4)

	0	1	2	3	4	5	6	7
50	MPA_SCTL_LOG_HSNSTS_MP00: B3							
	Error	Warning	Module number		LSI number			
51	MPA_SCTL_LOG_HSNSTS_MP00: B2							
	Function Number							
52	MPA_SCTL_LOG_HSNSTS_MP00: B1							
	Error class				Format number			
53	MPA_SCTL_LOG_HSNSTS_MP00: B0							
	FB format							
54	MPA_SCTL_LOG_HSNSTS_MP01: B3							
	FB format							
55	MPA_SCTL_LOG_HSNSTS_MP01: B2							
	FB format							
56	MPA_SCTL_LOG_HSNSTS_MP01: B1							
	FB format							
57	MPA_SCTL_LOG_HSNSTS_MP01: B0							
	FB format							
58	MPA_SCTL_LOG_HSNSTS_MP02: B3							
	DataLength_4B_Over	ACC			OPT_MODE			
59	MPA_SCTL_LOG_HSNSTS_MP02: B2							
	Fmt		Type		X_NUM		MDL_NUM	
5A	MPA_SCTL_LOG_HSNSTS_MP02: B1							
	LSI_NUM				FNC_NUM [7:4]			
5B	MPA_SCTL_LOG_HSNSTS_MP02: B0							
	FNC_NUM [3:0]				DST_ADDRESS [35:32]			
5C	MPA_SCTL_LOG_HSNSTS_MP03: B3							
	DST_ADDRESS [31:24]							
5D	MPA_SCTL_LOG_HSNSTS_MP03: B2							
	DST_ADDRESS [23:16]							
5E	MPA_SCTL_LOG_HSNSTS_MP03: B1							
	DST_ADDRESS [15:8]							
5F	MPA_SCTL_LOG_HSNSTS_MP03: B0							
	DST_ADDRESS [7:0]							

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Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = [50] (3/4)

	0	1	2	3	4	5	6	7
60	MSW_XREG HARX_ERRSTS_FST: B0							
	Not used		HARX REG8B_DTL EN_MERR	HARX REG8B BE_MERR	HARX REG8B_ADR _MERR	HARX REG4B_DTL EN_MERR	HARX REG4B BE_MERR	HARX REG4B ADR_MERR
61	MSW_XREG RAIF_ERRSTS_FST: B3							
	RAIF_RGA RB_CMP XRMERR	RAIF TIMEOUT_ WT	RAIF TIMEOUT_RD	Not used	Not used			
62	MSW_XREG HARX_ERRSTS_ALL: B0							
	Not used		HARX REG8B DTLEN_MERR	HARX REG8B BE_MERR	HARX REG8B ADR_MERR	HARX REG4B DTLEN_MERR	HARX REG4B BE_MERR	HARX REG4B _ADR_MERR
63	MSW_XREG RAIF_ERRSTS_ALL: B3							
	RAIF_RGA RB_CMP XRMERR	RAIF TIMEOUT_ WT	RAIF TIMEOUT_RD	Not used				
64	MPA_CRG_ERRLOG_ERRSTS: B1							
	Not used	TOV_OR	Not used	HSNSTS ERR_OR	Not used	S_CHK3 _M_OR	Not used	E_CHK3 _M_OR
65	MPA_CRG_ERRLOG_MERR_E_MP0: B1							
	Not used						ECTL BNDRY ERR_LOG	ECTL INVDST ERR_LOG
66	MPA_CRG_ERRLOG_MERR_E_MP0: B0							
	ECTL_P REGWT MERR_LOG	ECTL INDWT MERR_LOG	ECTL INDRD MERR_LOG	ECTL ATM MERR_LOG	ECTL INV_CMD ERR_LOG	ECTL PCODE ERR_LOG	Not used	
67	MPA_CRG_ERRLOG_MERR_S_MP0: B2							
	Not used					SCTL_LM CPLADR_INV ERR_LOG	SCTL_CPL LENGTH ERR_LOG	SCTL_CPL NORDACK ERR_LOG
68	MSW_CRM_RWDCTL_ERROR_ALL: B1							
	Not used	ANYINV CMDERR	HITCHK HERR	HITCHK MERR	ACSERR	ADROVER ERR	DSCBSY ERR	RAMNOT RDYERR
69	Reserve							
	Not used							
6A	Reserve							
	Not used							
6B	Reserve							
	Not used							
6C	Reserve							
	Not used							
6D	Reserve							
	Not used							
6E	Reserve							
	Not used							
6F	Reserve							
	Not used							

Byte27 = 81 (CHK3) Byte40-7F Detail

(6) Byte28 = [50] (4/4)

	0	1	2	3	4	5	6	7
70	Access type: B7				Access type : B7			
71	Access type: B6				Access type : B6			
72	Access type: B5				Access type : B5			
73	Access type: B4				Access type : B4			
74	Access type: B3				Access type : B3			
75	Access type: B2				Access type : B2			
76	Access type: B1				Access type : B1			
77	Access type: B0				Access type : B0			
78	Access address: B7				Access address : B7			
79	Access address: B6				Access address : B6			
7A	Access address: B5				Access address : B5			
7B	Access address: B4				Access address : B4			
7C	Access address: B3				Access address : B3			
7D	Access address: B2				Access address : B2			
7E	Access address: B1				Access address : B1			
7F	Access address: B0				Access address : B0			

Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = [F0] (1/4)

	0	1	2	3	4	5	6	7
40	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
41	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
42	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
43	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
44	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
45	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
46	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
47	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
48	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
49	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4A	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4B	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4C	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4D	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4E	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
4F	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro

Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = [F0] (2/4)

	0	1	2	3	4	5	6	7
50	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
51	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
52	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
53	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
54	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
55	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
56	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
57	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
58	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
59	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5A	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5B	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5C	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5D	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5E	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
5F	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro

Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = [F0] (3/4)

	0	1	2	3	4	5	6	7
60	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
61	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
62	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
63	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
64	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
65	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
66	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
67	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
68	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
69	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6A	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6B	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6C	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6D	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6E	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro
6F	Micro							
	Micro	Micro	Micro	Micro	Micro	Micro	Micro	Micro

Byte27 = 81 (CHK3) Byte40-7F Detail

(7) Byte28 = [F0] (4/4)

	0	1	2	3	4	5	6	7
70	Access type:B7							
	Access type : B7							
71	Access type:B6							
	Access type : B6							
72	Access type:B5							
	Access type : B5							
73	Access type:B4							
	Access type : B4							
74	Access type:B3							
	Access type : B3							
75	Access type:B2							
	Access type : B2							
76	Access type:B1							
	Access type : B1							
77	Access type:B0							
	Access type : B0							
78	Access address:B7							
	Access address : B7							
79	Access address:B6							
	Access address : B6							
7A	Access address:B5							
	Access address : B5							
7B	Access address:B4							
	Access address : B4							
7C	Access address:B3							
	Access address : B3							
7D	Access address:B2							
	Access address : B2							
7E	Access address:B1							
	Access address : B1							
7F	Access address:B0							
	Access address : B0							

SSBLOG05-620

Byte27 = 84 (CHK4)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'4')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number(*1)				Message code(x'0')			
35	SSID of self storage system (low)							
36	Symptom code (x'FF84')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Hardware information (See following pages)							
41								
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 84 (CHK4) Byte28-31 Detail

(1) FC IF CHK4

	0	1	2	3	4	5	6	7
28	PK_TYP			SubCode(x'01' : INT/FMC Error) SubCode(x'02' : LRP4 Error) SubCode(x'03' : SWIF Error) SubCode(x'04' : XD/FMT Error) SubCode(x'05' : DXC Error) SubCode(x'06' : DXA Error) SubCode(x'07' : MB Error) SubCode(x'08' : RGARB/SMBUS Error) SubCode(x'09' : XREG Error) SubCode(x'0A' : DXBF Error) SubCode(x'0B' : DMA Error) SubCode(x'0C' : DRR Error) SubCode(x'0D' : MFPS Error) SubCode(x'0E' : MFDMA Error) SubCode(x'0F' : HNS P-Path Error) SubCode(x'10' : Micro REG Access Error) SubCode(x'11' : Micro XD Illegal Error) SubCode(x'12' : LRP Error) SubCode(x'13' : Protocol Error) SubCode(x'14' : Protocol / Port Error) SubCode(x'15' : Protocol Temperature Error) SubCode(x'1F' : No Factor)				
29	LR_INT_LR_STATUS (b31-24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID
2A	LR_INT_LR_STATUS (b15-8)							
	LSI REV(7)	LSI REV(6)	LSI REV(5)	LSI REV(4)	LSI REV(3)	LSI REV(2)	LSI REV(1)	LSI REV(0)
2B	LRP_INFO_TABLE** (b23 – 16)							
	DXBF1 FORMAT	DXBF0 FORMAT	Reserved	Reserved	Reserved	Reserved	PPCI1	PPCI0
2C	LRP_INFO_TABLE** (b15 – 8)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	DXBF1	DXBF0
2D	LRP_INFO_TABLE** (b7 – 0)							
	P3	P2	P1	P0	Reserved	Reserved	Reserved	LR8 CHIP
2E	LR_INT_INTSTS0 (b31 – 24)							
	INT CHK1B	LRP4 CHK1B	LRP3-0 CHK1B	SWIF CHK1B	PARB1 CHK1B	PARB0 CHK1B	HARB3 CHK1B	HARB2 CHK1B
2F	LR_INT_INTSTS0 (b23 – 16)							
	HARB1 CHK1B	HARB0 CHK1B	XD CHK1B	FMT CHK1B	DX CHK1B	DRR CHK1B	DMAMB CHK1B	DMA CHK1B
30	LR_INT_INTSTS0 (b15 – 08)							
	REG CHK1B	SMBUS CHK1B	FIVE1	FIVE0	CHK4	CHK3	CHK2	WARN
31	LR_INT_INTSTS0 (b07 – 00)							
	Reserved	Reserved	MBOX INT	LRP4 INT	LRP3 INT	LRP2 INT	LLRP1 INT	LRP0 INT

Byte27 = 84 (CHK4) Byte40-7F Detail

(1) Byte28 = 01 (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_INT_ERR_STS0 (bit31 -24)							
	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS
41	LR_INT_INT_ERR_STS0 (bit23 -16)							
	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS
42	LR_INT_INT_ERR_STS0 (bit15 -08)							
	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS
43	LR_INT_INT_ERR_STS0 (bit07 -00)							
	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS	EATR ERR STS
44	LR_INT_INT_ERR_STS1 (bit31 -24)							
	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS	MPLOG1 ERR STS
45	LR_INT_INT_ERR_STS1 (bit23 -16)							
	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS	MPLOG0 ERR STS
46	LR_INT_INT_ERR_STS1 (bit15 -08)							
	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS
47	LR_INT_INT_ERR_STS1 (bit07 -00)							
	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS	EAC ERR STS
48	LR_INT_INT_ERR_STS2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_INT_INT_ERR_STS2 (bit23 -16)							
	MSI ERR STS	MSI ERR STS	MSI ERR STS	MSI ERR STS	MSI ERR STS	MSI ERR STS	MSI ERR STS	MSI ERR STS
4A	LR_INT_INT_ERR_STS2 (bit15 -08)							
	DMA ERB STS	DMA ERB STS	DMA ERB STS	DMA ERB STS	DMA ERB STS	DMA ERB STS	DMA ERB STS	DMA ERB STS
4B	LR_INT_INT_ERR_STS2 (bit07 -00)							
	DMA ERA STS	DMA ERA STS	DMA ERA STS	DMA ERA STS	DMA ERA STS	DMA ERA STS	DMA ERA STS	DMA ERA STS
4C	LR_INT_INT_ERR_STS3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_INT_INT_ERR_STS3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_INT_INT_ERR_STS3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	MBOX ASEQ ERR STS
4F	LR_INT_INT_ERR_STS3 (bit07 -00)							
	LOCKR3 ASEQ ERR STS	LOCKR2 ASEQ ERR STS	LOCKR1 ASEQ ERR STS	LOCKR0 ASEQ ERR STS	Reserved	GPTIM2 PERR STS	GPTIM1 PERR STS	GPTIM0 PERR STS

Byte27 = 84 (CHK4) Byte40-7F Detail

(1) Byte28 = 01 (2/4)

	0	1	2	3	4	5	6	7
50	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR_FMC_ERR0 (bit31 -24)							
	SWRX_E01	SWRX_E02	SWRX_E03	SWRX_E04	SWRX_E05	SWRX_E06	SWRX_E07	SWRX_E08
59	LR_FMC_ERR0 (bit23 -16)							
	SWRX_E09	Reserved	Reserved	SWRX_E12	SWRX_E13	SWRX_E14	Reserved	SW_EP_ER R
5A	LR_FMC_ERR0 (bit15 -08)							
	SWTX_E01	SWTX_E02	SWTX_E03	SWTX_E04	SWTX_E05	SWTX_E06	SWTX_E07	SWTX_E08
5B	LR_FMC_ERR0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_FMC_ERR1 (bit31 -24)							
	SQS ENCERR	SQS DECERR	RACNT_ PERR	BLKS UNCRERR_ LT	BLKS ADRERR_ LT	COMP_DT_ PERR	Reserved	Reserved
5D	LR_FMC_ERR1 (bit23 -16)							
	SQF ENCERR	SQF_DECER R	BCNT_PERR	SCNT_PERR	DIAG WACNT_ PERR	PPTIM_PER R	CMD_PERR	ADR_PERR
5E	LR_FMC_ERR1 (bit15 -08)							
	WDT_PERR	TCVAL_ PERR	Reserved	Reserved	PPTO_ERR	RG_ENCMD _ACCERR	SW_ENCMD _ACCERR	RG_LEN0_ ERR
5F	LR_FMC_ERR1 (bit07 -00)							
	SW_LEN0_ ERR	SW_CMD_ ERR	SW_WTLN _ERR	SW_RDLEN _ERR	SW_BOUND _ERR	RG_CMD_ ERR	RG_RDLEN _ERR	RG_BOUND _ERR

Byte27 = 84 (CHK4) Byte40-7F Detail

(1) Byte28 = 01 (3/4)

	0	1	2	3	4	5	6	7
60	LR_FMC_SEQ (bit31 -24)							
	SWRX_CST (7)	SWRX_CST (6)	SWRX_CST (5)	SWRX_CST (4)	SWRX_CST (3)	SWRX_CST (2)	SWRX_CST (1)	SWRX_CST (0)
61	LR_FMC_SEQ (bit23 -16)							
	SWTX_CST (7)	SWTX_CST (6)	SWTX_CST (5)	SWTX_CST (4)	SWTX_CST (3)	SWTX_CST (2)	SWTX_CST (1)	SWTX_CST (0)
62	LR_FMC_SEQ (bit15 -08)							
	SQS_CST (7)	SQS_CST (6)	SQS_CST (5)	SQS_CST (4)	SQS_CST (3)	SQS_CST (2)	SQS_CST (1)	SQS_CST (0)
63	LR_FMC_SEQ (bit07 -00)							
	SQF_CST (7)	SQF_CST (6)	SQF_CST (5)	SQF_CST (4)	SQF_CST (3)	SQF_CST (2)	SQF_CST (1)	SQF_CST (0)
64	LR_FMC_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_FMC_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR_FMC_STS (bit15 -08)							
	SWRX_ERR	SWTX_ERR	SQS_ERR	INTL_ERR	PPTO	SOFT_ERR	RGIF_ERR	Reserved
67	LR_FMC_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	CIPSTS
68	LR_FMC_CTL00 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR_FMC_CTL00 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR_FMC_CTL00 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR_FMC_CTL00 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OUT_ENBL
6C	LR_FMC_CTL01 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR_FMC_CTL01 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR_FMC_CTL01 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR_FMC_CTL01 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PROTECT

Byte27 = 84 (CHK4) Byte40-7F Detail

(1) Byte28 = 01 (4/4)

	0	1	2	3	4	5	6	7
70	LR_FMC_CTL02 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR_FMC_CTL02 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	LR_FMC_CTL02 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR_FMC_CTL02 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	HOLD
74	LR_FMC_CTL05 (bit31 -24)							
	RG_CMD (7)	RG_CMD (6)	RG_CMD (5)	RG_CMD (4)	RG_CMD (3)	RG_CMD (2)	RG_CMD (1)	RG_CMD (0)
75	LR_FMC_CTL05 (bit23 -16)							
	RG_ADR (23)	RG_ADR (22)	RG_ADR (21)	RG_ADR (20)	RG_ADR (19)	RG_ADR (18)	RG_ADR (17)	RG_ADR (16)
76	LR_FMC_CTL05 (bit15 -08)							
	RG_ADR (15)	RG_ADR (14)	RG_ADR (13)	RG_ADR (12)	RG_ADR (11)	RG_ADR (10)	RG_ADR (9)	RG_ADR (8)
77	LR_FMC_CTL05 (bit07 -00)							
	RG_ADR (7)	RG_ADR (6)	RG_ADR (5)	RG_ADR (4)	RG_ADR (3)	RG_ADR (2)	RG_ADR (1)	RG_ADR (0)
78	LR_FMC_CMD_LOG0 (bit31 -24)							
	ACC_LOG7 (7)	ACC_LOG7 (6)	ACC_LOG7 (5)	ACC_LOG7 (4)	ACC_LOG7 (3)	ACC_LOG7 (2)	ACC_LOG7 (1)	ACC_LOG7 (0)
79	LR_FMC_CMD_LOG0 (bit23 -16)							
	ACC_LOG6 (7)	ACC_LOG6 (6)	ACC_LOG6 (5)	ACC_LOG6 (4)	ACC_LOG6 (3)	ACC_LOG6 (2)	ACC_LOG6 (1)	ACC_LOG6 (0)
7A	LR_FMC_CMD_LOG0 (bit15 -08)							
	ACC_LOG5 (7)	ACC_LOG5 (6)	ACC_LOG5 (5)	ACC_LOG5 (4)	ACC_LOG5 (3)	ACC_LOG5 (2)	ACC_LOG5 (1)	ACC_LOG5 (0)
7B	LR_FMC_CMD_LOG0 (bit07 -00)							
	ACC_LOG4 (7)	ACC_LOG4 (6)	ACC_LOG4 (5)	ACC_LOG4 (4)	ACC_LOG4 (3)	ACC_LOG4 (2)	ACC_LOG4 (1)	ACC_LOG4 (0)
7C	LR_FMC_CMD_LOG1 (bit31 -24)							
	ACC_LOG3 (7)	ACC_LOG3 (6)	ACC_LOG3 (5)	ACC_LOG3 (4)	ACC_LOG3 (3)	ACC_LOG3 (2)	ACC_LOG3 (1)	ACC_LOG3 (0)
7D	LR_FMC_CMD_LOG1 (bit23 -16)							
	ACC_LOG2 (7)	ACC_LOG2 (6)	ACC_LOG2 (5)	ACC_LOG2 (4)	ACC_LOG2 (3)	ACC_LOG2 (2)	ACC_LOG2 (1)	ACC_LOG2 (0)
7E	LR_FMC_CMD_LOG1 (bit15 -08)							
	ACC_LOG1 (7)	ACC_LOG1 (6)	ACC_LOG1 (5)	ACC_LOG1 (4)	ACC_LOG1 (3)	ACC_LOG1 (2)	ACC_LOG1 (1)	ACC_LOG1 (0)
7F	LR_FMC_CMD_LOG1 (bit07 -00)							
	ACC_LOG0 (7)	ACC_LOG0 (6)	ACC_LOG0 (5)	ACC_LOG0 (4)	ACC_LOG0 (3)	ACC_LOG0 (2)	ACC_LOG0 (1)	ACC_LOG0 (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(2) Byte28 = 02 (1/4)

	0	1	2	3	4	5	6	7
40	LR_LRP_CTL (bit31 -24)							
	START	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_LRP_CTL (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_LRP_CTL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_LRP_CTL (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR_LRP_STS (bit31 -24)							
	ACT	ERRSTP	Reserved	ERR	WRN	Reserved	DGWAIT	DGPASS
45	LR_LRP_STS (bit23 -16)							
	SEQ	SEQ	SEQ	SEQ	SEQ	SEQ	SEQ	SEQ
46	LR_LRP_STS (bit15 -08)							
	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR
47	LR_LRP_STS (bit07 -00)							
	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR	Now ADR
48	LR_LRP_ERR0 (bit31 -24)							
	ALUCMPERR	SEQEERR	TIMOUT	OJUMP	TIMERPERR	X2PERR	X1PERR	X0PERR
49	LR_LRP_ERR0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_LRP_ERR0 (bit15 -08)							
	Reserved	Reserved	CSADECC	CSUCECC	CSODTP	CSOADP	CSIDTP	CSIADP
4B	LR_LRP_ERR0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR_LRP_ERR1 (bit31 -24)							
	DSO_LRP	DSO_DMA	DSO_XR	DSO_PPCI	Reserved	DSI_LRP	DSI_XR	DSI_PPCI
4D	LR_LRP_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_LRP_ERR1 (bit15 -08)							
	DSOAW3	DSOAW2	DSOAW1	DSOAW0	DSOUNCW3	DSOUNCW2	DSOUNCW1	DSOUNCW0
4F	LR_LRP_ERR1 (bit07 -00)							
	DSOADP	DSODTP	Reserved	DSIADP	DSIDTPW3	DSIDTPW2	DSIDTPW1	DSIDTPW0

Byte27 = 84 (CHK4) Byte40-7F Detail

(2) Byte28 = 02 (2/4)

	0	1	2	3	4	5	6	7
50	LR_LRP_PCIRX_ERR (bit31 -24)							
	Reserved	Reserved	FPCI_RX_WOV_ERR	FPCI_RX_DLRC_ERR	FPCI_RX_BE_ERR	FPCI_RX_MEMDPERR	FPCI_RX_ECCERR	FPCI_RX_ADRECCERR
51	LR_LRP_PCIRX_ERR (bit23 -16)							
	Reserved	Reserved	FPCI_RX_RAMOPERR	FPCI_RX_RAMINPERR	FPCI_RX_TXADRPERR	Reserved	FPCI_RX_SEQDECERR	FPCI_RX_RDTOERR
52	LR_LRP_PCIRX_ERR (bit15 -08)							
	Reserved	FPCI_RX14_REQ	FPCI_RX13_VLD	FPCI_RX12_DLRCTIM	FPCI_RX11_PCCNT	FPCI_RX10_NPCNT	FPCI_RX9_HED	FPCI_RX8_DLRC_NEQ
53	LR_LRP_PCIRX_ERR (bit07 -00)							
	FPCI_RX7_HLRC_NEQ	FPCI_RX6_DT	FPCI_RX5_TOCNT	FPCI_RX4_TOCNT_TO	FPCI_RX3_WDCNT	FPCI_RX2_ENC_ERR	FPCI_RX1_DEC_ERR	Reserved
54	LR_LRP_PCITX_ERR (bit31 -24)							
	EN_ECCWRN	Reserved	EN_WOVERR	EN_DLRCERR	EN_BEERR	EN_MEMDPERR	EN_ECCERR	EN_ADRECCERR
55	LR_LRP_PCITX_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_LRP_PCITX_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_LRP_PCITX_ERR (bit07 -00)							
	Reserved	FPCI_TX_LENERR	Reserved	FPCI_TX_H423PERR	Reserved	Reserved	FPCI_TX_RDDTPERR	FPCI_TX_RDADRPERR
58	LR_LRP_FCTL_ERR (bit31 -24)							
	CSLD_LRCERR	CSLD_XFRC_NTERR2	CSLD_SEQERROR	CSLD_TOCNTPERR	CSLD_TIME_OUTERR	CSLD_XFRC_NTPERR	CSLD_X2KC_NTPERR	CSLD_CSA_DRPERR
59	LR_LRP_FCTL_ERR (bit23 -16)							
	CSLD_CSDTPERR	CSLD_CSREQ_BADRPERR	CSLD_CSREQ_QADRPERR	CSLD_DXCS_DXBFPERR	CSLD_DXCS_DXBFPERR	Reserved	Reserved	Reserved
5A	LR_LRP_FCTL_ERR (bit15 -08)							
	XRDS_RDDTERR	XRDS_ADRERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_LRP_FCTL_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_LRP_FIF_DXLPR_ERR (bit31 -24)							
	FIF_DXBF_RDCPLERR	FIF_DXBF_DTOUTPERR	FIF_DXBF_DTINPERR	FIF_DXBF_SEQOUT	FIF_DXBF_BEERR	FIF_DXBF_ADRERR	FIF_DXBF_SEQCLR2	FIF_DXBF_SEQCLR3
5D	LR_LRP_FIF_DXLPR_ERR (bit23 -16)							
	FIF_DXBF_TX7_HLRC_NEQ	FIF_DXBF_TX6_DT	FIF_DXBF_TX5_TOCNT	FIF_DXBF_TX4_TOCNT_TO	FIF_DXBF_TX3_WDCNT	FIF_DXBF_TX2_ENC_ERR	FIF_DXBF_TX1_DEC_ERR	FIF_DXBF_TX8_DLRC_NEQ
5E	LR_LRP_FIF_DXLPR_ERR (bit15 -08)							
	Reserved	FIF_DXBF_RX14_REQ	FIF_DXBF_RX13_VLD	FIF_DXBF_RX12_DLRCTIM	FIF_DXBF_RX11_PCCNT	FIF_DXBF_RX10_NPCNT	FIF_DXBF_RX9_HED	FIF_DXBF_RX8_DLRC_NEQ
5F	LR_LRP_FIF_DXLPR_ERR (bit07 -00)							
	FIF_DXBF_RX7_HLRC_NEQ	FIF_DXBF_RX6_DT	FIF_DXBF_RX5_TOCNT	FIF_DXBF_RX4_TOCNT_TO	FIF_DXBF_RX3_WDCNT	FIF_DXBF_RX2_ENC_ERR	FIF_DXBF_RX1_DEC_ERR	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(2) Byte28 = 02 (3/4)

	0	1	2	3	4	5	6	7
60	LR_LRP_FIF_XRPCI_ERR (bit31 -24)							
	FIF_XR_AD ROUTPERR	FIF_XR_ DTOUTPERR	FIF_XR_ DTINPERR	FIF_XR_ SEQOUT	FIF_XR_ BEPERR	FIF_XR_ SEQCLR2	FIF_XR_ STSPERR	Reserved
61	LR_LRP_FIF_XRPCI_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_LRP_FIF_XRPCI_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_LRP_FIF_XRPCI_ERR (bit07 -00)							
	FIF_FOP_ ADRPERR	FIF_FOP_ WDTP	FIF_FOP_ WBEP	FIF_FOP_ RDTP	FIF_FOP_ WRSELERR	FIF_FOP_ RDSELERR	FIF_FOP_ WRCMPERR	FIF_FOP_ SEQSTERR
64	LR_LRPDMA_FDMA_ERR_STS0 (bit31 -24)							
	FDR ERR (7)	FDR ERR (6)	FDR ERR (5)	FDR ERR (4)	FDR ERR (3)	FDR ERR (2)	FDR ERR (1)	FDR ERR (0)
65	LR_LRPDMA_FDMA_ERR_STS0 (bit23 -16)							
	FCR ERR (7)	FCR ERR (6)	FCR ERR (5)	FCR ERR (4)	FCR ERR (3)	FCR ERR (2)	FCR ERR (1)	FCR ERR (0)
66	LR_LRPDMA_FDMA_ERR_STS0 (bit15 -08)							
	FBR ERR (7)	FBR ERR (6)	FBR ERR (5)	FBR ERR (4)	FBR ERR (3)	FBR ERR (2)	FBR ERR (1)	FBR ERR (0)
67	LR_LRPDMA_FDMA_ERR_STS0 (bit07 -00)							
	FAR ERR (7)	FAR ERR (6)	FAR ERR (5)	FAR ERR (4)	FAR ERR (3)	FAR ERR (2)	FAR ERR (1)	FAR ERR (0)
68	LR_LRPDMA_FDMA_ERR_STS1 (bit31 -24)							
	FAT ERR (7)	FAT ERR (6)	FAT ERR (5)	FAT ERR (4)	FAT ERR (3)	FAT ERR (2)	FAT ERR (1)	FAT ERR (0)
69	LR_LRPDMA_FDMA_ERR_STS1 (bit23 -16)							
	FAD ERR2 (7)	FAD ERR2 (6)	FAD ERR2 (5)	FAD ERR2 (4)	FAD ERR2 (3)	FAD ERR2 (2)	FAD ERR2 (1)	FAD ERR2 (0)
6A	LR_LRPDMA_FDMA_ERR_STS1 (bit15 -08)							
	FAD ERR (7)	FAD ERR (6)	FAD ERR (5)	FAD ERR (4)	FAD ERR (3)	FAD ERR (2)	FAD ERR (1)	FAD ERR (0)
6B	LR_LRPDMA_FDMA_ERR_STS1 (bit07 -00)							
	FAC ERR (7)	FAC ERR (6)	FAC ERR (5)	FAC ERR (4)	FAC ERR (3)	FAC ERR (2)	FAC ERR (1)	FAC ERR (0)
6C	LR_LRPDMA_FDMA_ERR_STS2 (bit31 -24)							
	FBT ERR (7)	FBT ERR (6)	FBT ERR (5)	FBT ERR (4)	FBT ERR (3)	FBT ERR (2)	FBT ERR (1)	FBT ERR (0)
6D	LR_LRPDMA_FDMA_ERR_STS2 (bit23 -16)							
	FBD ERR2 (7)	FBD ERR2 (6)	FBD ERR2 (5)	FBD ERR2 (4)	FBD ERR2 (3)	FBD ERR2 (2)	FBD ERR2 (1)	FBD ERR2 (0)
6E	LR_LRPDMA_FDMA_ERR_STS2 (bit15 -08)							
	FBD ERR (7)	FBD ERR (6)	FBD ERR (5)	FBD ERR (4)	FBD ERR (3)	FBD ERR (2)	FBD ERR (1)	FBD ERR (0)
6F	LR_LRPDMA_FDMA_ERR_STS2 (bit07 -00)							
	FBC ERR (7)	FBC ERR (6)	FBC ERR (5)	FBC ERR (4)	FBC ERR (3)	FBC ERR (2)	FBC ERR (1)	FBC ERR (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(2) Byte28 = 02 (4/4)

	0	1	2	3	4	5	6	7
70	LR_LRPDMA_FDMA_ERR_STS3 (bit31 -24)							
	FCT ERR (7)	FCT ERR (6)	FCT ERR (5)	FCT ERR (4)	FCT ERR (3)	FCT ERR (2)	FCT ERR (1)	FCT ERR (0)
71	LR_LRPDMA_FDMA_ERR_STS3 (bit23 -16)							
	FCD ERR2 (7)	FCD ERR2 (6)	FCD ERR2 (5)	FCD ERR2 (4)	FCD ERR2 (3)	FCD ERR2 (2)	FCD ERR2 (1)	FCD ERR2 (0)
72	LR_LRPDMA_FDMA_ERR_STS3 (bit15 -08)							
	FCD ERR (7)	FCD ERR (6)	FCD ERR (5)	FCD ERR (4)	FCD ERR (3)	FCD ERR (2)	FCD ERR (1)	FCD ERR (0)
73	LR_LRPDMA_FDMA_ERR_STS3 (bit07 -00)							
	FCC ERR (7)	FCC ERR (6)	FCC ERR (5)	FCC ERR (4)	FCC ERR (3)	FCC ERR (2)	FCC ERR (1)	FCC ERR (0)
74	LR_LRPDMA_FDMA_ERR_STS4 (bit31 -24)							
	FDT ERR (7)	FDT ERR (6)	FDT ERR (5)	FDT ERR (4)	FDT ERR (3)	FDT ERR (2)	FDT ERR (1)	FDT ERR (0)
75	LR_LRPDMA_FDMA_ERR_STS4 (bit23 -16)							
	FDD ERR2 (7)	FDD ERR2 (6)	FDD ERR2 (5)	FDD ERR2 (4)	FDD ERR2 (3)	FDD ERR2 (2)	FDD ERR2 (1)	FDD ERR2 (0)
76	LR_LRPDMA_FDMA_ERR_STS4 (bit15 -08)							
	FDD ERR (7)	FDD ERR (6)	FDD ERR (5)	FDD ERR (4)	FDD ERR (3)	FDD ERR (2)	FDD ERR (1)	FDD ERR (0)
77	LR_LRPDMA_FDMA_ERR_STS4 (bit07 -00)							
	FDC ERR (7)	FDC ERR (6)	FDC ERR (5)	FDC ERR (4)	FDC ERR (3)	FDC ERR (2)	FDC ERR (1)	FDC ERR (0)
78	LR_LRPDMA_FDMA_ERR_STS5 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_LRPDMA_FDMA_ERR_STS5 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	LR_LRPDMA_FDMA_ERR_STS5 (bit15 -08)							
	PAR ERR (7)	PAR ERR (6)	PAR ERR (5)	PAR ERR (4)	PAR ERR (3)	PAR ERR (2)	PAR ERR (1)	PAR ERR (0)
7B	LR_LRPDMA_FDMA_ERR_STS5 (bit07 -00)							
	CSD ERR (7)	CSD ERR (6)	CSD ERR (5)	CSD ERR (4)	CSD ERR (3)	CSD ERR (2)	CSD ERR (1)	CSD ERR (0)
7C	LR_LRPDMA_FDMA_ERR_STS6 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR_LRPDMA_FDMA_ERR_STS6 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	LR_LRPDMA_FDMA_ERR_STS6 (bit15 -08)							
	EAC ERR (15)	EAC ERR (14)	EAC ERR (13)	EAC ERR (12)	EAC ERR (11)	EAC ERR (10)	EAC ERR (9)	EAC ERR (8)
7F	LR_LRPDMA_FDMA_ERR_STS6 (bit07 -00)							
	EAC ERR (7)	EAC ERR (6)	EAC ERR (5)	EAC ERR (4)	EAC ERR (3)	EAC ERR (2)	EAC ERR (1)	EAC ERR (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(3) Byte28 = 03 (1/4)

	0	1	2	3	4	5	6	7
40	LR_SWIF_ERR_STS_00 (bit31 -24)							
	ERR STATUS(31)	ERR STATUS(30)	ERR STATUS(29)	ERR STATUS(28)	ERR STATUS(27)	ERR STATUS(26)	ERR STATUS(25)	ERR STATUS(24)
41	LR_SWIF_ERR_STS_00 (bit23 -16)							
	ERR STATUS(23)	ERR STATUS(22)	ERR STATUS(21)	ERR STATUS(20)	ERR STATUS(19)	ERR STATUS(18)	ERR STATUS(17)	ERR STATUS(16)
42	LR_SWIF_ERR_STS_00 (bit15 -08)							
	ERR STATUS(15)	ERR STATUS(14)	ERR STATUS(13)	ERR STATUS(12)	ERR STATUS(11)	ERR STATUS(10)	ERR STATUS(9)	ERR STATUS(8)
43	LR_SWIF_ERR_STS_00 (bit07 -00)							
	ERR STATUS(7)	ERR STATUS(6)	ERR STATUS(5)	ERR STATUS(4)	ERR STATUS(3)	ERR STATUS(2)	ERR STATUS(1)	ERR STATUS(0)
44	LR_SWIF_ERR_STS_01 (bit31 -24)							
	ERR STATUS(63)	ERR STATUS(62)	ERR STATUS(61)	ERR STATUS(60)	ERR STATUS(59)	ERR STATUS(58)	ERR STATUS(57)	ERR STATUS(56)
45	LR_SWIF_ERR_STS_01 (bit23 -16)							
	ERR STATUS(55)	ERR STATUS(54)	ERR STATUS(53)	ERR STATUS(52)	ERR STATUS(51)	ERR STATUS(50)	ERR STATUS(49)	ERR STATUS(48)
46	LR_SWIF_ERR_STS_01 (bit15 -08)							
	ERR STATUS(47)	ERR STATUS(46)	ERR STATUS(45)	ERR STATUS(44)	ERR STATUS(43)	ERR STATUS(42)	ERR STATUS(41)	ERR STATUS(40)
47	LR_SWIF_ERR_STS_01 (bit07 -00)							
	ERR STATUS(39)	ERR STATUS(38)	ERR STATUS(37)	ERR STATUS(36)	ERR STATUS(35)	ERR STATUS(34)	ERR STATUS(33)	ERR STATUS(32)
48	LR_SWIF_ERR_STS_02 (bit31 -24)							
	ERR STATUS(95)	ERR STATUS(94)	ERR STATUS(93)	ERR STATUS(92)	ERR STATUS(91)	ERR STATUS(90)	ERR STATUS(89)	ERR STATUS(88)
49	LR_SWIF_ERR_STS_02 (bit23 -16)							
	ERR STATUS(87)	ERR STATUS(86)	ERR STATUS(85)	ERR STATUS(84)	ERR STATUS(83)	ERR STATUS(82)	ERR STATUS(81)	ERR STATUS(80)
4A	LR_SWIF_ERR_STS_02 (bit15 -08)							
	ERR STATUS(79)	ERR STATUS(78)	ERR STATUS(77)	ERR STATUS(76)	ERR STATUS(75)	ERR STATUS(74)	ERR STATUS(73)	ERR STATUS(72)
4B	LR_SWIF_ERR_STS_02 (bit07 -00)							
	ERR STATUS(71)	ERR STATUS(70)	ERR STATUS(69)	ERR STATUS(68)	ERR STATUS(67)	ERR STATUS(66)	ERR STATUS(65)	ERR STATUS(64)
4C	SWIF ERR 00 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
4D	SWIF ERR 00 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
4E	SWIF ERR 00 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
4F	SWIF ERR 00 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(3) Byte28 = 03 (2/4)

	0	1	2	3	4	5	6	7
50	SWIF ERR 01 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
51	SWIF ERR 01 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
52	SWIF ERR 01 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
53	SWIF ERR 01 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
54	SWIF ERR 02 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
55	SWIF ERR 02 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
56	SWIF ERR 02 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
57	SWIF ERR 02 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
58	SWIF ERR 03 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
59	SWIF ERR 03 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
5A	SWIF ERR 03 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
5B	SWIF ERR 03 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
5C	SWIF ERR 04 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
5D	SWIF ERR 04 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
5E	SWIF ERR 04 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	SWIF ERR 04 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(3) Byte28 = 03 (3/4)

	0	1	2	3	4	5	6	7
60	SWIF ERR 05 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	SWIF ERR 05 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	SWIF ERR 05 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	SWIF ERR 05 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	SWIF ERR 06 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
65	SWIF ERR 06 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
66	SWIF ERR 06 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
67	SWIF ERR 06 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
68	SWIF ERR 07 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
69	SWIF ERR 07 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6A	SWIF ERR 07 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6B	SWIF ERR 07 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6C	SWIF ERR 08 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6D	SWIF ERR 08 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6E	SWIF ERR 08 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
6F	SWIF ERR 08 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(3) Byte28 = 03 (4/4)

	0	1	2	3	4	5	6	7
70	SWIF ERR 09 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
71	SWIF ERR 09 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
72	SWIF ERR 09 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
73	SWIF ERR 09 (bit07 -00)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
74	SWIF ERR 10 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
75	SWIF ERR 10 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
76	SWIF ERR 10 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
77	SWIF ERR 10 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	SWIF ERR 11 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
79	SWIF ERR 11 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
7A	SWIF ERR 11 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
7B	SWIF ERR 11 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	SWIF ERR 12 (bit31 -24)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
7D	SWIF ERR 12 (bit23 -16)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
7E	SWIF ERR 12 (bit15 -08)							
	PC_LSTGNT_ERR	PC_GNT_ERR	NP_LSTGNT_ERR	NP_GNT_ERR	Reserved	Reserved	Reserved	Reserved
7F	SWIF ERR 12 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(4) Byte28 = 04 (1/4)

	0	1	2	3	4	5	6	7
40	LR_XD_CTL_CHK1B_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_XD_CTL_CHK1B_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_XD_CTL_CHK1B_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_XD_CTL_CHK1B_MONI (bit07 -00)							
	Reserved	Reserved	RREG CHK1B	Reserved	DTX_CHK1B	ETX_CHK1B	DRX_CHK1B	ERX_CHK1B
44	LR_XD_ERX_ERR_STS (bit31 -24)							
	Reserved	Reserved	REQ	VLD	DLRCTIM	PCCNT	NPCNT	HED
45	LR_XD_ERX_ERR_STS (bit23 -16)							
	DLRCNEQ	HLRCNEG	DT	TCNT	TOCNTTO	WDCNT	ENC_RXER	DEC_RXER
46	LR_XD_ERX_ERR_STS (bit15 -08)							
	Reserved	Reserved	ENCRXERR	DECRXERR	MP_PER	PCTIMOUT	NPTIMOUT	PER6
47	LR_XD_ERX_ERR_STS (bit07 -00)							
	PER5	TRCNTPE	PER3	PER2	PER1	PDXWRAD	PDWBANK	FINCNT
48	LR_XD_DRX_ERR_STS (bit31 -24)							
	Reserved	Reserved	REQ	VLD	DLRCTIM	PCCNT	NPCNT	HED
49	LR_XD_DRX_ERR_STS (bit23 -16)							
	DLRCNEQ	HLRCNEG	DT	TCNT	TOCNTTO	WDCNT	ENC_RXER	DEC_RXER
4A	LR_XD_DRX_ERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TRCNTPE	ENCERR	DECERR
4B	LR_XD_DRX_ERR_STS (bit07 -00)							
	PCTIMEOUT	NPTIMOUT	PER6	PER5	PER4	PER3	PER2	PER1
4C	LR_XD_ETX_ERR_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_XD_ETX_ERR_STS (bit23 -16)							
	DLRCNEQ	HLRCNEG	DT	TCNT	TOCNTTO	WDCNT	ENC_RXER	DEC_RXER
4E	LR_XD_ETX_ERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR_XD_ETX_ERR_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	DRXLRC	PERR	POER1ER

Byte27 = 84 (CHK4) Byte40-7F Detail

(4) Byte28 = 04 (2/4)

	0	1	2	3	4	5	6	7
50	LR_XD_DTX_ERR_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_XD_DTX_ERR_STS (bit23 -16)							
	DLRCNEQ	HLRCNEG	DT	TCNT	TOCNTTO	WDCNT	ENC_RXER	DEC_RXER
52	LR_XD_DTX_ERR_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR_XD_DTX_ERR_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TRASH	PORT1ERR	ENCERR
54	LR_XD_RREG4_CHK1B (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_XD_RREG4_CHK1B (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_XD_RREG4_CHK1B (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_XD_RREG4_CHK1B (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	CPLTO	REQ
58	LR_XD_RREGCOM_CHK1B_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	DNP	ENP	DP	EP
59	LR_XD_RREGCOM_CHK1B_STS (bit23 -16)							
	Reserved	Reserved	R15	R14	R13	R12	R11	R10
5A	LR_XD_RREGCOM_CHK1B_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_XD_RREGCOM_CHK1B_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(4) Byte28 = 04 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(4) Byte28 = 04 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR_FMT_ERROR0 (bit31 -24)							
	FMRX_SEQERR	HWP_V_PERR	HRP_V_PERR	HDIF_V_PERR	HEAD_PERR	INITTAG_PERR	AMARI_PERR	F1_LEN_PERR
75	LR_FMT_ERROR0 (bit23 -16)							
	SWRXIF_SEQERR	E03_WDCNT_PERR	E04_TOCNT_TO_ERR	E05_TOCNT_PERR	E06_DT_PERR	E07_HLRC_NEQ_ERR	E08_DLRC_NEQ_ERR	E09_HED_PERR
76	LR_FMT_ERROR0 (bit15 -08)							
	E10_NPCNT_PERR	E11_PCCNT_PERR	E12_DLRCTIM_ERR	E13_VLD_ERR	E14_REQ_ERR	TX_ERRSTS	RTAG3_D_PERR	ADR_Q2_ZB23_PERR
77	LR_FMT_ERROR0 (bit07 -00)							
	E_ADR_IN_76_PERR	E_SUBOU_D_76_PERR	E_ADR_IN_54_PERR	E_SUBOU_D_54_PERR	E_ADR_IN_32_PERR	E_SUBOU_D_32_PERR	E_ADR_IN_10_PERR	E_SUBOU_D_10_PERR
78	LR_FMT_ERROR1 (bit31 -24)							
	FMTX_SEQERR	HEAD_D_PERR	LEN_D_PERR	INITRTAG_D_PERR	AMARI_D_PERR	RTAG_PERR	AMARI_8BCNT_PERR	FTFMTDT_Q3_PERR
79	LR_FMT_ERROR1 (bit23 -16)							
	FTCRCDT_Q3_PERR	F0_FTFMTMODE_PERR	WDCNT_PERR	DLRC_CAL_DT_L_PERR	RTAG3_D_PERR	RTAG3CNT_PERR	TINF_PERR	TINF_CNT_PERR
7A	LR_FMT_ERROR1 (bit15 -08)							
	SWTXIF_SEQERR	E03_WDCNT_PERR	E04_TOCNT_TO_ERR	E05_TOCNT_PERR	E06_DT_PERR	E07_HLRC_NEQ_ERR	E08_DLRC_NEQ_ERR	RX_ERRSTS
7B	LR_FMT_ERROR1 (bit07 -00)							
	FMREG_SEQERR	PORT_1_ERR	CRC16DT_PERR	CRC3DTCNT_PERR	Reserved	Reserved	Reserved	Reserved
7C	LR_FMT_ERROR2 (bit31 -24)							
	RAM_UNC_W0	RAM_UNC_W1	RAM_UNC_W2	RAM_UNC_W3	RAM_ADR_W0	RAM_ADR_W1	RAM_ADR_W2	RAM_ADR_W3
7D	LR_FMT_ERROR2 (bit23 -16)							
	TINF_PERR	TINF_MPRD_ACC_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	LR_FMT_ERROR2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	LR_FMT_ERROR2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(5) Byte28 = 05 (1/4)

	0	1	2	3	4	5	6	7
40	LR_DXC_REP_ERROR (bit31 -24)							
	MICRO_ERR	MEMORY_ERR	HARD_ERR	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_DXC_REP_ERROR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_DXC_REP_ERROR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_DXC_REP_ERROR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR_DXC_ERROR_DETAIL1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_DXC_ERROR_DETAIL1 (bit23 -16)							
	Reserved	Reserved	Reserved	SEQ_TIME_OVER	HEADER_PTY_ERR	PARITY_ERR	SEQ_ENC_ERR	DATA_PTY_ERR
46	LR_DXC_ERROR_DETAIL1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_DXC_ERROR_DETAIL1 (bit07 -00)							
	Reserved	Reserved	Reserved	ACC_CODE_ERR	CLOSE_ACC_ERR	LEN_ERR	CMD_ERR	ADR_ERR
48	LR_DXC_ERROR_DETAIL2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_DXC_ERROR_DETAIL2 (bit23 -16)							
	STR_ADR_PERR	LST_ADR_PERR	RMW_WDAT_PERR	WDAT_PERR	RMWF_DT1_PERR	RMWF_DT2_PERR	RMWL_DT1_PERR	RMWL_DT2_PERR
4A	LR_DXC_ERROR_DETAIL2 (bit15 -08)							
	WDAT_ECC0_PERR	WDAT_ECC1_PERR	WDAT_ECC2_PERR	WDAT_ECC3_PERR	WDAT_ECC4_PERR	WDAT_ECC5_PERR	WDAT_ECC6_PERR	WDAT_ECC7_PERR
4B	LR_DXC_ERROR_DETAIL2 (bit07 -00)							
	DI_WDAT2_PERR	STR_ADR_PERR	ADR_CNT_PERR	DM_PERR	DT_C0_W_PDQ_PERR	DT_C0_W_NDQ_PERR	DT_C1_W_PDQ_PERR	DT_C1_W_NDQ_PERR
4C	LR_DXC_ERROR_DETAIL3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_DXC_ERROR_DETAIL3 (bit23 -16)							
	RDAT1_PERR	RDAT2_PERR	FI_ADR_PERR	ADR_CNT_PERR	AUTO_REWR_ADR_PERR	ENCERR	TIME_OVRE	TIME_PERR
4E	LR_DXC_ERROR_DETAIL3 (bit15 -08)							
	UNC_EC0	UNC_EC1	UNC_EC2	UNC_EC3	UNC_EC4	UNC_EC5	UNC_EC6	UNC_EC7
4F	LR_DXC_ERROR_DETAIL3 (bit07 -00)							
	ADRE_EC0	ADRE_EC1	ADRE_EC2	ADRE_EC3	ADRE_EC4	ADRE_EC5	ADRE_EC6	ADRE_EC7

Byte27 = 84 (CHK4) Byte40-7F Detail

(5) Byte28 = 05 (2/4)

	0	1	2	3	4	5	6	7
50	LR_DXC_ERROR_DETAIL4 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_DXC_ERROR_DETAIL4 (bit23 -16)							
	RC_RDAT_PERR	HEAD_PERR	RDAT_PERR	CMPHEAD_PERR	CA_CNT_PERR	ENCERR	TIME_OVER	TIME_PERR
52	LR_DXC_ERROR_DETAIL4 (bit15 -08)							
	Reserved	TWR_CALC_PTY_ERR	VLD_CALC_PTY_ERR	SEQ3_ENCERR	SEQ1_ENCERR	SEQ2_ENCERR	TIME_OVER	TIME_PERR
53	LR_DXC_ERROR_DETAIL4 (bit07 -00)							
	WLTY_CNT_PERR	TRAS_CNT_PERR	CMDTMG_CNT_PERR	NEXTACT_CNT_PERR	WDTLEN_CNT_PERR	ADRCNT_PERR	STR_ADR_PERR	LST_ADR_PERR
54	LR_DXC_ERROR_DETAIL5 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_DXC_ERROR_DETAIL5 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	REFSEQ_ENCERR	TREFI_CNT_PERR
56	LR_DXC_ERROR_DETAIL5 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_DXC_ERROR_DETAIL5 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SEQ_ENC_ERR	Reserved	ZQ_CNT_PTYERR
58	LR_DXC_ERROR_DETAIL6 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	ENC_ERR	FLG_ERR	TIME_OVER	TIME_PERR
59	LR_DXC_ERROR_DETAIL6 (bit23 -16)							
	INCDAT_PERR	EXE_ADR_PERR	LST_ADR_PERR	STR_ADR_PERR	EXP_ECC_PERR	EXP_DAT_PERR	READ_ECC_PERR	READ_DAT_PERR
5A	LR_DXC_ERROR_DETAIL6 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_DXC_ERROR_DETAIL6 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	ECC_H_CMPERR	ECC_L_CMPERR	DAT_H_CMPERR	DAT_L_CMPERR
5C	LR_DXC_ERROR_DETAIL7 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR_DXC_ERROR_DETAIL7 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR_DXC_ERROR_DETAIL7 (bit15 -08)							
	TJCNT_PTY (15)	TJCNT_PTY (14)	TJCNT_PTY (13)	TJCNT_PTY (12)	TJCNT_PTY (11)	TJCNT_PTY (10)	TJCNT_PTY (9)	TJCNT_PTY (8)
5F	LR_DXC_ERROR_DETAIL7 (bit07 -00)							
	TJCNT_PTY (7)	TJCNT_PTY (6)	TJCNT_PTY (5)	TJCNT_PTY (4)	TJCNT_PTY (3)	TJCNT_PTY (2)	TJCNT_PTY (1)	TJCNT_PTY (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(5) Byte28 = 05 (3/4)

	0	1	2	3	4	5	6	7
60	LR_DXC_ERROR_DETAIL8 (bit31 -24)							
	Reserved	Reserved	RCFI_UNDER_RUN	RCFI_OVER_RUN	RCFI_ETYDCNT_PERR	RCFI_RDCNT_PERR	RCFI_WRCNT_PERR	RCFI_HEAD_PERR
61	LR_DXC_ERROR_DETAIL8 (bit23 -16)							
	Reserved	Reserved	CPFI_UNDER_RUN	CPFI_OVER_RUN	CPFI_ETYDCNT_PERR	CPFI_RDCNT_PERR	CPFI_WRCNT_PERR	CPFI_HEAD_PERR
62	LR_DXC_ERROR_DETAIL8 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_DXC_ERROR_DETAIL8 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR_DXC_RCVIF_SEQ1_MON (bit31 -24)							
	RCVIF_SEQ1ST3(7)	RCVIF_SEQ1ST3(6)	RCVIF_SEQ1ST3(5)	RCVIF_SEQ1ST3(4)	RCVIF_SEQ1ST3(3)	RCVIF_SEQ1ST3(2)	RCVIF_SEQ1ST3(1)	RCVIF_SEQ1ST3(0)
65	LR_DXC_RCVIF_SEQ1_MON (bit23 -16)							
	RCVIF_SEQ1ST2(7)	RCVIF_SEQ1ST2(6)	RCVIF_SEQ1ST2(5)	RCVIF_SEQ1ST2(4)	RCVIF_SEQ1ST2(3)	RCVIF_SEQ1ST2(2)	RCVIF_SEQ1ST2(1)	RCVIF_SEQ1ST2(0)
66	LR_DXC_RCVIF_SEQ1_MON (bit15 -08)							
	RCVIF_SEQ1ST1(7)	RCVIF_SEQ1ST1(6)	RCVIF_SEQ1ST1(5)	RCVIF_SEQ1ST1(4)	RCVIF_SEQ1ST1(3)	RCVIF_SEQ1ST1(2)	RCVIF_SEQ1ST1(1)	RCVIF_SEQ1ST1(0)
67	LR_DXC_RCVIF_SEQ1_MON (bit07 -00)							
	RCVIF_SEQ1ST0(7)	RCVIF_SEQ1ST0(6)	RCVIF_SEQ1ST0(5)	RCVIF_SEQ1ST0(4)	RCVIF_SEQ1ST0(3)	RCVIF_SEQ1ST0(2)	RCVIF_SEQ1ST0(1)	RCVIF_SEQ1ST0(0)
68	LR_DXC_RCVIF_SEQ2_MON (bit31 -24)							
	RCVIF_SEQ2ST3(7)	RCVIF_SEQ2ST3(6)	RCVIF_SEQ2ST3(5)	RCVIF_SEQ2ST3(4)	RCVIF_SEQ2ST3(3)	RCVIF_SEQ2ST3(2)	RCVIF_SEQ2ST3(1)	RCVIF_SEQ2ST3(0)
69	LR_DXC_RCVIF_SEQ2_MON (bit23 -16)							
	RCVIF_SEQ2ST2(7)	RCVIF_SEQ2ST2(6)	RCVIF_SEQ2ST2(5)	RCVIF_SEQ2ST2(4)	RCVIF_SEQ2ST2(3)	RCVIF_SEQ2ST2(2)	RCVIF_SEQ2ST2(1)	RCVIF_SEQ2ST2(0)
6A	LR_DXC_RCVIF_SEQ2_MON (bit15 -08)							
	RCVIF_SEQ2ST1(7)	RCVIF_SEQ2ST1(6)	RCVIF_SEQ2ST1(5)	RCVIF_SEQ2ST1(4)	RCVIF_SEQ2ST1(3)	RCVIF_SEQ2ST1(2)	RCVIF_SEQ2ST1(1)	RCVIF_SEQ2ST1(0)
6B	LR_DXC_RCVIF_SEQ2_MON (bit07 -00)							
	RCVIF_SEQ2ST0(7)	RCVIF_SEQ2ST0(6)	RCVIF_SEQ2ST0(5)	RCVIF_SEQ2ST0(4)	RCVIF_SEQ2ST0(3)	RCVIF_SEQ2ST0(2)	RCVIF_SEQ2ST0(1)	RCVIF_SEQ2ST0(0)
6C	LR_DXC_CMDCTL_SEQ1_MON (bit31 -24)							
	CMDCTL_SEQ1ST3(7)	CMDCTL_SEQ1ST3(6)	CMDCTL_SEQ1ST3(5)	CMDCTL_SEQ1ST3(4)	CMDCTL_SEQ1ST3(3)	CMDCTL_SEQ1ST3(2)	CMDCTL_SEQ1ST3(1)	CMDCTL_SEQ1ST3(0)
6D	LR_DXC_CMDCTL_SEQ1_MON (bit23 -16)							
	CMDCTL_SEQ1ST2(7)	CMDCTL_SEQ1ST2(6)	CMDCTL_SEQ1ST2(5)	CMDCTL_SEQ1ST2(4)	CMDCTL_SEQ1ST2(3)	CMDCTL_SEQ1ST2(2)	CMDCTL_SEQ1ST2(1)	CMDCTL_SEQ1ST2(0)
6E	LR_DXC_CMDCTL_SEQ1_MON (bit15 -08)							
	CMDCTL_SEQ1ST1(7)	CMDCTL_SEQ1ST1(6)	CMDCTL_SEQ1ST1(5)	CMDCTL_SEQ1ST1(4)	CMDCTL_SEQ1ST1(3)	CMDCTL_SEQ1ST1(2)	CMDCTL_SEQ1ST1(1)	CMDCTL_SEQ1ST1(0)
6F	LR_DXC_CMDCTL_SEQ1_MON (bit07 -00)							
	CMDCTL_SEQ1ST0(7)	CMDCTL_SEQ1ST0(6)	CMDCTL_SEQ1ST0(5)	CMDCTL_SEQ1ST0(4)	CMDCTL_SEQ1ST0(3)	CMDCTL_SEQ1ST0(2)	CMDCTL_SEQ1ST0(1)	CMDCTL_SEQ1ST0(0)

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(5) Byte28 = 05 (4/4)

	0	1	2	3	4	5	6	7
70	LR_DXC_CMDCTL_SEQ1_MON (bit31 -24)							
	CMDCTL SEQ2ST3(7)	CMDCTL SEQ2ST3(6)	CMDCTL SEQ2ST3(5)	CMDCTL SEQ2ST3(4)	CMDCTL SEQ2ST3(3)	CMDCTL SEQ2ST3(2)	CMDCTL SEQ2ST3(1)	CMDCTL SEQ2ST3(0)
71	LR_DXC_CMDCTL_SEQ1_MON (bit23 -16)							
	CMDCTL SEQ2ST2(7)	CMDCTL SEQ2ST2(6)	CMDCTL SEQ2ST2(5)	CMDCTL SEQ2ST2(4)	CMDCTL SEQ2ST2(3)	CMDCTL SEQ2ST2(2)	CMDCTL SEQ2ST2(1)	CMDCTL SEQ2ST2(0)
72	LR_DXC_CMDCTL_SEQ1_MON (bit15 -08)							
	CMDCTL SEQ2ST1(7)	CMDCTL SEQ2ST1(6)	CMDCTL SEQ2ST1(5)	CMDCTL SEQ2ST1(4)	CMDCTL SEQ2ST1(3)	CMDCTL SEQ2ST1(2)	CMDCTL SEQ2ST1(1)	CMDCTL SEQ2ST1(0)
73	LR_DXC_CMDCTL_SEQ1_MON (bit07 -00)							
	CMDCTL SEQ2ST0(7)	CMDCTL SEQ2ST0(6)	CMDCTL SEQ2ST0(5)	CMDCTL SEQ2ST0(4)	CMDCTL SEQ2ST0(3)	CMDCTL SEQ2ST0(2)	CMDCTL SEQ2ST0(1)	CMDCTL SEQ2ST0(0)
74	HMA: XARBREG_ERRREG_ERR1 (bit31 -24)							
	CMDCTL SEQ3ST3(7)	CMDCTL SEQ3ST3(6)	CMDCTL SEQ3ST3(5)	CMDCTL SEQ3ST3(4)	CMDCTL SEQ3ST3(3)	CMDCTL SEQ3ST3(2)	CMDCTL SEQ3ST3(1)	CMDCTL SEQ3ST3(0)
75	HMA: XARBREG_ERRREG_ERR1 (bit23 -16)							
	CMDCTL SEQ3ST2(7)	CMDCTL SEQ3ST2(6)	CMDCTL SEQ3ST2(5)	CMDCTL SEQ3ST2(4)	CMDCTL SEQ3ST2(3)	CMDCTL SEQ3ST2(2)	CMDCTL SEQ3ST2(1)	CMDCTL SEQ3ST2(0)
76	HMA: XARBREG_ERRREG_ERR1 (bit15 -08)							
	CMDCTL SEQ3ST1(7)	CMDCTL SEQ3ST1(6)	CMDCTL SEQ3ST1(5)	CMDCTL SEQ3ST1(4)	CMDCTL SEQ3ST1(3)	CMDCTL SEQ3ST1(2)	CMDCTL SEQ3ST1(1)	CMDCTL SEQ3ST1(0)
77	HMA: XARBREG_ERRREG_ERR1 (bit07 -00)							
	CMDCTL SEQ3ST0(7)	CMDCTL SEQ3ST0(6)	CMDCTL SEQ3ST0(5)	CMDCTL SEQ3ST0(4)	CMDCTL SEQ3ST0(3)	CMDCTL SEQ3ST0(2)	CMDCTL SEQ3ST0(1)	CMDCTL SEQ3ST0(0)
78	LR_DXC_RCHK_SEQ_MON (bit31 -24)							
	RCHK SEQST3(7)	RCHK SEQST3(6)	RCHK SEQST3(5)	RCHK SEQST3(4)	RCHK SEQST3(3)	RCHK SEQST3(2)	RCHK SEQST3(1)	RCHK SEQST3(0)
79	LR_DXC_RCHK_SEQ_MON (bit23 -16)							
	RCHK SEQST2(7)	RCHK SEQST2(6)	RCHK SEQST2(5)	RCHK SEQST2(4)	RCHK SEQST2(3)	RCHK SEQST2(2)	RCHK SEQST2(1)	RCHK SEQST2(0)
7A	LR_DXC_RCHK_SEQ_MON (bit15 -08)							
	RCHK SEQST1(7)	RCHK SEQST1(6)	RCHK SEQST1(5)	RCHK SEQST1(4)	RCHK SEQST1(3)	RCHK SEQST1(2)	RCHK SEQST1(1)	RCHK SEQST1(0)
7B	LR_DXC_RCHK_SEQ_MON (bit07 -00)							
	RCHK SEQST0(7)	RCHK SEQST0(6)	RCHK SEQST0(5)	RCHK SEQST0(4)	RCHK SEQST0(3)	RCHK SEQST0(2)	RCHK SEQST0(1)	RCHK SEQST0(0)
7C	LR_DXC_CPLGEN_SEQ_MON (bit31 -24)							
	CPLGEN SEQST3(7)	CPLGEN SEQST3(6)	CPLGEN SEQST3(5)	CPLGEN SEQST3(4)	CPLGEN SEQST3(3)	CPLGEN SEQST3(2)	CPLGEN SEQST3(1)	CPLGEN SEQST3(0)
7D	LR_DXC_CPLGEN_SEQ_MON (bit23 -16)							
	CPLGEN SEQST2(7)	CPLGEN SEQST2(6)	CPLGEN SEQST2(5)	CPLGEN SEQST2(4)	CPLGEN SEQST2(3)	CPLGEN SEQST2(2)	CPLGEN SEQST2(1)	CPLGEN SEQST2(0)
7E	LR_DXC_CPLGEN_SEQ_MON (bit15 -08)							
	CPLGEN SEQST1(7)	CPLGEN SEQST1(6)	CPLGEN SEQST1(5)	CPLGEN SEQST1(4)	CPLGEN SEQST1(3)	CPLGEN SEQST1(2)	CPLGEN SEQST1(1)	CPLGEN SEQST1(0)
7F	LR_DXC_CPLGEN_SEQ_MON (bit07 -00)							
	CPLGEN SEQST0(7)	CPLGEN SEQST0(6)	CPLGEN SEQST0(5)	CPLGEN SEQST0(4)	CPLGEN SEQST0(3)	CPLGEN SEQST0(2)	CPLGEN SEQST0(1)	CPLGEN SEQST0(0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(6) Byte28 = 06 (1/4)

	0	1	2	3	4	5	6	7
40	LR_DXA_F_ERR_A_1 (bit31 -24)							
	RACNT PERR	LENCHK_ PERR	RBCNT PERR	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_DXA_F_ERR_A_1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_DXA_F_ERR_A_1 (bit15 -08)							
	DLRC ERR	DT PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_DXA_F_ERR_A_1 (bit07 -00)							
	WBUFF0 2BERR	WBUFF0 ADR	WBUFF1 2BERR	WBUFF1 ADR	Reserved	Reserved	Reserved	Reserved
44	LR_DXA_F_ERR_A_2 (bit31 -24)							
	RACNT PERR	RBCNT PERR	ENCERR	TO ERR	TOCNT PERR	DLRC ERR	WDCNT PERR	CUTV PERR
45	LR_DXA_F_ERR_A_2 (bit23 -16)							
	DT PERR	LST_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_DXA_F_ERR_A_2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_DXA_F_ERR_A_2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR_DXA_L_ERR_A_1 (bit31 -24)							
	RACNT PERR	LENCHK_ PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_DXA_L_ERR_A_1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_DXA_L_ERR_A_1 (bit15 -08)							
	DLRC ERR	DT PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR_DXA_L_ERR_A_1 (bit07 -00)							
	WBUFF0 2BERR	WBUFF0 ADR	WBUFF1 2BERR	WBUFF1 ADR	Reserved	Reserved	Reserved	Reserved
4C	LR_DXA_L_ERR_A_2 (bit31 -24)							
	RACNT PERR	RBCNT PERR	ENCERR	TO ERR	TOCNT PERR	DLRC ERR	WDCNT PERR	CUTV PERR
4D	LR_DXA_L_ERR_A_2 (bit23 -16)							
	DT PERR	LST_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_DXA_L_ERR_A_2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR_DXA_L_ERR_A_2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(6) Byte28 = 06 (2/4)

	0	1	2	3	4	5	6	7
50	LR_DXA_F_ERR_B_1 (bit31 -24)							
	VLDTIM ERR	DLRCTIM ERR	DT PERR	DLRC ERR	HLRC_PERR	HLRC ERR	ENCERR	DECERR
51	LR_DXA_F_ERR_B_1 (bit23 -16)							
	TO ERR	TOCNT PERR	WDCNT PERR	CUTV PERR	WDB_NUM_ ERR	WDB_NUM_ PERR	RDB_NUM_ ERR	RDB_NUM_ PERR
52	LR_DXA_F_ERR_B_1 (bit15 -08)							
	PCBSYS PERR	NPBSYS PERR	REQOFF ERR	WBCNT_ PERR	LENCHK_ PERR	TXERR RECEV ERR	WDNUM_ PERR	PCBSYNUM PERR
53	LR_DXA_F_ERR_B_1 (bit07 -00)							
	NPBSYNUM PERR	Reserved	Reserved	Reserved	WACNT PERR	CUTADR_ PERR	DT PERR	Reserved
54	LR_DXA_F_ERR_B_2 (bit31 -24)							
	DECERR 0	ENCERR 0	RACNT PERR	EFLG ERR	TO ERR	TOCNT PERR	WDCNT PERR	RBCNT PERR
55	LR_DXA_F_ERR_B_2 (bit23 -16)							
	DT PERR	DLRC ERR	HLRC ERR	DECERR 1	ENCERR 1	Reserved	Reserved	Reserved
56	LR_DXA_F_ERR_B_2 (bit15 -08)							
	RBUFF0 2BERR	RBUFF0 ADR	RBUFF1 2BERR	RBUFF1 ADR	Reserved	Reserved	Reserved	Reserved
57	LR_DXA_F_ERR_B_2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR_DXA_L_ERR_B_1 (bit31 -24)							
	VLDTIM ERR	DLRCTIM ERR	DT PERR	DLRC ERR	HLRC_PERR	HLRC ERR	ENCERR	DECERR
59	LR_DXA_L_ERR_B_1 (bit23 -16)							
	TO ERR	TOCNT PERR	WDCNT PERR	CUTV PERR	WDB_NUM_ ERR	WDB_NUM_ PERR	RDB_NUM_ ERR	RDB_NUM_ PERR
5A	LR_DXA_L_ERR_B_1 (bit15 -08)							
	PCBSYS PERR	NPBSYS PERR	REQOFF ERR	WBCNT_ PERR	LENCHK_ PERR	TXERR RECEV ERR	WDNUM_ PERR	PCBSYNUM PERR
5B	LR_DXA_L_ERR_B_1 (bit07 -00)							
	NPBSYNUM PERR	Reserved	Reserved	Reserved	WACNT PERR	CUTADR_ PERR	DT PERR	Reserved
5C	LR_DXA_L_ERR_B_2 (bit31 -24)							
	DECERR 0	ENCERR 0	RACNT PERR	EFLG ERR	TO ERR	TOCNT PERR	WDCNT PERR	RBCNT PERR
5D	LR_DXA_L_ERR_B_2 (bit23 -16)							
	DT PERR	DLRC ERR	HLRC ERR	DECERR 1	ENCERR 1	Reserved	Reserved	Reserved
5E	LR_DXA_L_ERR_B_2 (bit15 -08)							
	RBUFF0 2BERR	RBUFF0 ADR	RBUFF1 2BERR	RBUFF1 ADR	Reserved	Reserved	Reserved	Reserved
5F	LR_DXA_L_ERR_B_2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(6) Byte28 = 06 (3/4)

	0	1	2	3	4	5	6	7
60	LR_DXA_ERR_C_1 (bit31 -24)							
	ACP OFF ERR	ACT ERR	FLGSEL PERR	ENCERR1	WDCNT PERR	TO0 ERR	TOCNT0 PERR	TO1 ERR
61	LR_DXA_ERR_C_1 (bit23 -16)							
	TOCNT1 PERR	TO2 ERR	TOCNT2 PERR	ENCERR2	TO3 ERR	TOCNT3 PERR	TO4 ERR	TOCNT4 PERR
62	LR_DXA_ERR_C_1 (bit15 -08)							
	EFLG ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_DXA_ERR_C_1 (bit07 -00)							
	HED PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR_DXA_F_SEQMON_A (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_DXA_F_SEQMON_A (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR_DXA_F_SEQMON_A (bit15 -08)							
	RCONT SEQST (7)	RCONT SEQST (6)	RCONT SEQST (5)	RCONT SEQST (4)	RCONT SEQST (3)	RCONT SEQST (2)	RCONT SEQST (1)	RCONT SEQST (0)
67	LR_DXA_F_SEQMON_A (bit07 -00)							
	RCONT SEQST (7)	RCONT SEQST (6)	RCONT SEQST (5)	RCONT SEQST (4)	RCONT SEQST (3)	RCONT SEQST (2)	RCONT SEQST (1)	RCONT SEQST (0)
68	LR_DXA_L_SEQMON_A (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR_DXA_L_SEQMON_A (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR_DXA_L_SEQMON_A (bit15 -08)							
	RCONT SEQST (7)	RCONT SEQST (6)	RCONT SEQST (5)	RCONT SEQST (4)	RCONT SEQST (3)	RCONT SEQST (2)	RCONT SEQST (1)	RCONT SEQST (0)
6B	LR_DXA_L_SEQMON_A (bit07 -00)							
	RCONT SEQST (7)	RCONT SEQST (6)	RCONT SEQST (5)	RCONT SEQST (4)	RCONT SEQST (3)	RCONT SEQST (2)	RCONT SEQST (1)	RCONT SEQST (0)
6C	LR_DXA_F_SEQMON_B_1 (bit31 -24)							
	FWDT SEQST (7)	FWDT SEQST (6)	FWDT SEQST (5)	FWDT SEQST (4)	FWDT SEQST (3)	FWDT SEQST (2)	FWDT SEQST (1)	FWDT SEQST (0)
6D	LR_DXA_F_SEQMON_B_1 (bit23 -16)							
	FWDT SEQST (7)	FWDT SEQST (6)	FWDT SEQST (5)	FWDT SEQST (4)	FWDT SEQST (3)	FWDT SEQST (2)	FWDT SEQST (1)	FWDT SEQST (0)
6E	LR_DXA_F_SEQMON_B_1 (bit15 -08)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
6F	LR_DXA_F_SEQMON_B_1 (bit07 -00)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(6) Byte28 = 06 (4/4)

	0	1	2	3	4	5	6	7
70	LR_DXA_F_SEQMON_B_2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR_DXA_F_SEQMON_B_2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	LR_DXA_F_SEQMON_B_2 (bit15 -08)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
73	LR_DXA_F_SEQMON_B_2 (bit07 -00)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
74	LR_DXA_L_SEQMON_B_1 (bit31 -24)							
	FWDT SEQST (7)	FWDT SEQST (6)	FWDT SEQST (5)	FWDT SEQST (4)	FWDT SEQST (3)	FWDT SEQST (2)	FWDT SEQST (1)	FWDT SEQST (0)
75	LR_DXA_L_SEQMON_B_1 (bit23 -16)							
	FWDT SEQST (7)	FWDT SEQST (6)	FWDT SEQST (5)	FWDT SEQST (4)	FWDT SEQST (3)	FWDT SEQST (2)	FWDT SEQST (1)	FWDT SEQST (0)
76	LR_DXA_L_SEQMON_B_1 (bit15 -08)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
77	LR_DXA_L_SEQMON_B_1 (bit07 -00)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
78	LR_DXA_L_SEQMON_B_2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_DXA_L_SEQMON_B_2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	LR_DXA_L_SEQMON_B_2 (bit15 -08)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
7B	LR_DXA_L_SEQMON_B_2 (bit07 -00)							
	RUIF SEQST (7)	RUIF SEQST (6)	RUIF SEQST (5)	RUIF SEQST (4)	RUIF SEQST (3)	RUIF SEQST (2)	RUIF SEQST (1)	RUIF SEQST (0)
7C	LR_DXA_SEQMON_C (bit31 -24)							
	WSEL SEQST1 (7)	WSEL SEQST1 (6)	WSEL SEQST1 (5)	WSEL SEQST1 (4)	WSEL SEQST1 (3)	WSEL SEQST1 (2)	WSEL SEQST1 (1)	WSEL SEQST1 (0)
7D	LR_DXA_SEQMON_C (bit23 -16)							
	WSEL SEQST1 (7)	WSEL SEQST1 (6)	WSEL SEQST1 (5)	WSEL SEQST1 (4)	WSEL SEQST1 (3)	WSEL SEQST1 (2)	WSEL SEQST1 (1)	WSEL SEQST1 (0)
7E	LR_DXA_SEQMON_C (bit15 -08)							
	WSEL SEQST2 (7)	WSEL SEQST2 (6)	WSEL SEQST2 (5)	WSEL SEQST2 (4)	WSEL SEQST2 (3)	WSEL SEQST2 (2)	WSEL SEQST2 (1)	WSEL SEQST2 (0)
7F	LR_DXA_SEQMON_C (bit07 -00)							
	WSEL SEQST2 (7)	WSEL SEQST2 (6)	WSEL SEQST2 (5)	WSEL SEQST2 (4)	WSEL SEQST2 (3)	WSEL SEQST2 (2)	WSEL SEQST2 (1)	WSEL SEQST2 (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(7) Byte28 = 07 (1/4)

	0	1	2	3	4	5	6	7
40	LR_MB0_ERR_ST00 (bit31 -24)							
	DMPF SEQERR	NO 8B ACS	Reserved	Reserved	NWP3 PERR	NWP2 PERR	NWP1 PERR	NWP0 PERR
41	LR_MB0_ERR_ST00 (bit23 -16)							
	NWDATA PERR	NWADRA PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_MB0_ERR_ST00 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	YWP3 PERR	YWP2 PERR	YWP1 PERR	YWP0 PERR
43	LR_MB0_ERR_ST00 (bit07 -00)							
	YWDATA PERR	YWADRA PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR_MB0_ERR_ST01 (bit31 -24)							
	DPE PFADR3 PERR	DPE PFADR2 PERR	DPE PFADR1 PERR	DPE PFADR0 PERR	DMPE SEQERR3	DMPE SEQERR2	DMPE SEQERR1	DMPE SEQERR0
45	LR_MB0_ERR_ST01 (bit23 -16)							
	YQ3FULLON _CMP_PERR	YQ2FULLON _CMP_PERR	YQ1FULLON _CMP_PERR	YQ0FULLON _CMP_PERR	NQ3FULLON _CMP_PERR	NQ2FULLON _CMP_PERR	NQ1FULLON _CMP_PERR	NQ0FULLON _CMP_PERR
46	LR_MB0_ERR_ST01 (bit15 -08)							
	YRP3 PERR	YRP2 PERR	YRP1 PERR	YRP0 PERR	NRP3 PERR	NRP2 PERR	NRP1 PERR	NRP0 PERR
47	LR_MB0_ERR_ST01 (bit07 -00)							
	YQ3FULLOF _CMP_PERR	YQ2FULLOF _CMP_PERR	YQ1FULLOF _CMP_PERR	YQ0FULLOF _CMP_PERR	NQ3FULLOF _CMP_PERR	NQ2FULLOF _CMP_PERR	NQ1FULLOF _CMP_PERR	NQ0FULLOF _CMP_PERR
48	LR_MB0_ERR_ST02 (bit31 -24)							
	PIDX0_0_ PERR	CIDX0_0_ PERR	IDX_DIF0_0_ PERR	Reserved	PIDX0_1_ PERR	CIDX0_1_ PERR	IDX_DIF0_1_ PERR	Reserved
49	LR_MB0_ERR_ST02 (bit23 -16)							
	PIDX0_2_ PERR	CIDX0_2_ PERR	IDX_DIF0_2_ PERR	Reserved	PIDX0_3_ PERR	CIDX0_3_ PERR	IDX_DIF0_3_ PERR	Reserved
4A	LR_MB0_ERR_ST02 (bit15 -08)							
	PIDX1_0_ PERR	CIDX1_0_ PERR	IDX_DIF1_0_ PERR	Reserved	PIDX1_1_ PERR	CIDX1_1_ PERR	IDX_DIF1_1_ PERR	Reserved
4B	LR_MB0_ERR_ST02 (bit07 -00)							
	PIDX1_2_ PERR	CIDX1_2_ PERR	IDX_DIF1_2_ PERR	Reserved	PIDX1_3_ PERR	CIDX1_3_ PERR	IDX_DIF1_3_ PERR	Reserved
4C	LR_MB0_ERR_ST03 (bit31 -24)							
	NQ_FULL0	NQ_FULL1	NQ_FULL2	NQ_FULL3	YQ_FULL0	YQ_FULL1	YQ_FULL2	YQ_FULL3
4D	LR_MB0_ERR_ST03 (bit23 -16)							
	NPECC UNCRERR3	NPECC ADRERR4	NPECC UNCRERR2	NPECC ADRERR2	NPECC UNCRERR1	NPECC ADRERR1	NPECC UNCRERR0	NPECC ADRERR0
4E	LR_MB0_ERR_ST03 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR_MB0_ERR_ST03 (bit07 -00)							
	YPECC UNCRERR3	YPECC ADRERR4	YPECC UNCRERR2	YPECC ADRERR2	YPECC UNCRERR1	YPECC ADRERR1	YPECC UNCRERR0	YPECC ADRERR0

Byte27 = 84 (CHK4) Byte40-7F Detail

(7) Byte28 = 07 (2/4)

	0	1	2	3	4	5	6	7
50	LR_MB0_ERR_ST04 (bit31 -24)							
	MB_SEQERR	SW_RIF_SEQERR	SW_TIF_SEQERR	TIF_SEQERR	MBUDCNT_PERR	MBRCNT_PERR	MBWCNT_PERR	MB_RAM_OVR
51	LR_MB0_ERR_ST04 (bit23 -16)							
	MBWECCD_ATA	UNCRERR	ADRERR	DMAMB_PERR	ABTERR	MBRSPWCN_T_PERR	MBRSPRCN_T_PERR	MBRSPDIF_CNT_PERR
52	LR_MB0_ERR_ST04 (bit15 -08)							
	TX_WDCNT_PERR	TX_TOCNT_TOV	TX_TOCNT_PERR	TX_DT_PERR	RX_WDCNT_PERR	RX_TOCNT_TOV	RX_TOCNT_PERR	RX_DT_PERR
53	LR_MB0_ERR_ST04 (bit07 -00)							
	RX_HLRC_ERR	RX_DLRC_ERR	RX_HED_PERR	RX_NPCNT_PERR	RX_PCCNT_PERR	RX_DLRCTI_M_ERR	RX_VLD_ERR	RX_REQ_ERR
54	LR_MB0_ERR_ST05 (bit31 -24)							
	NPWP0_D_PERR	NPWP1_D_PERR	NPWP2_D_PERR	NPWP3_D_PERR	NPRP0_D_PERR	NPRP1_D_PERR	NPRP2_D_PERR	NPRP3_D_PERR
55	LR_MB0_ERR_ST05 (bit23 -16)							
	YPWP0_D_PERR	YPWP1_D_PERR	YPWP2_D_PERR	YPWP3_D_PERR	YPRP0_D_PERR	YPRP1_D_PERR	YPRP2_D_PERR	YPRP3_D_PERR
56	LR_MB0_ERR_ST05 (bit15 -08)							
	KEYPRM0_PERR	KEYPRM1_PERR	KEYPRM2_PERR	KEYPRM3_PERR	Reserved	Reserved	Reserved	Reserved
57	LR_MB0_ERR_ST05 (bit07 -00)							
	RX_ERRSTS	TX_ERRSTS	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR_MB0_ERR_ST06 (bit31 -24)							
	TMR_SEQ_ERR0	TMR_SEQ_ERR1	TMR_SEQ_ERR2	TMR_SEQ_ERR3	TMR_CNT_PERR0	TMR_CNT_PERR1	TMR_CNT_PERR2	TMR_CNT_PERR3
59	LR_MB0_ERR_ST06 (bit23 -16)							
	Reserved	NO_HED_HI_T	ND_PKT	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_MB0_ERR_ST06 (bit15 -08)							
	RHED_WCN_T_PERR	RHED_RCN_T_PERR	TAGCNT_PERR	Reserved	KEYPRMW_CNT_PERR	KEYPRMR_CNT_PERR	KEYPRMDIF_CNT_PERR	Reserved
5B	LR_MB0_ERR_ST06 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_MB0_ERR_ST08 (bit31 -24)							
	DMSR_SEQERR	SRCCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
5D	LR_MB0_ERR_ST08 (bit23 -16)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC_AB_CMP_PERR
5E	LR_MB0_ERR_ST08 (bit15 -08)							
	DMSR_SEQERR	SRCCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
5F	LR_MB0_ERR_ST08 (bit07 -00)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC_AB_CMP_PERR

Byte27 = 84 (CHK4) Byte40-7F Detail

(7) Byte28 = 07 (3/4)

	0	1	2	3	4	5	6	7
60	LR_MB0_ERR_ST09 (bit31 -24)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
61	LR_MB0_ERR_ST09 (bit23 -16)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
62	LR_MB0_ERR_ST09 (bit15 -08)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
63	LR_MB0_ERR_ST09 (bit07 -00)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
64	LR_MB0_ERR_ST10 (bit31 -24)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
65	LR_MB0_ERR_ST10 (bit23 -16)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
66	LR_MB0_ERR_ST10 (bit15 -08)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
67	LR_MB0_ERR_ST10 (bit07 -00)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
68	LR_MB0_ERR_ST11 (bit31 -24)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
69	LR_MB0_ERR_ST11 (bit23 -16)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
6A	LR_MB0_ERR_ST11 (bit15 -08)							
	DMSR_SEQERR	SRCNT_BACK_PERR	SRCHCNT_PERR	SRCHTOP_PERR	SRCHFIN_PERR	DMAMBX_PERR	UNCRERR	ADRERR
6B	LR_MB0_ERR_ST11 (bit07 -00)							
	SRCH_ADR_D2_PERR	PFA_DAT_D2_PERR	PFAWDATA_PERR	WADRA_PERR	PE_RADRB_PERR	ABTHIT_ID_PERR	ABTHITCNT_PERR	SRC AB_CMP_PERR
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(7) Byte28 = 07 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(8) Byte28 = 08 (1/4)

	0	1	2	3	4	5	6	7
40	LR_RGARB_CHK_LOG_ALL (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_RGARB_CHK_LOG_ALL (bit23 -16)							
	ALL_XR_MERR	ALL_XR_RDTP	ALL_XR_RSV	ALL_XR_WRBE	Reserved	ALL_RXDT_CMPCHK	ALL_RXDT_PER_W_RDTP	ALL_RXDT_PER_MPN_O
42	LR_RGARB_CHK_LOG_ALL (bit15 -08)							
	ALL_MST_REQOFF	ALL_RXDT_PER_ERR	ALL_RXDT_PER_FUNC	ALL_RXDT_PER_WRBE	ALL_RXDT_PER_ADR	ALL_RXDT_PER_WDT	ALL_RXDT_PER_RDT	ALL_RXDT_CMPCHK
43	LR_RGARB_CHK_LOG_ALL (bit07 -00)							
	ALL_VALID_CHK	Reserved	Reserved	ALL_PENC_GNTZERO	ALL_PENC_GNTEX	ALL_SEQ_PTY	ALL_SEQ_ENC	ALL_SEQ_DEC
44	LR_RGARB_CHK_LOG_FIRST (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_RGARB_CHK_LOG_FIRST (bit23 -16)							
	FST_XR_MERR	FST_XR_RDTP	FST_XR_RSV	FST_XR_WRBE	Reserved	FST_RXDT_CMPCHK	FST_RXDT_PER_W_RDTP	FST_RXDT_PER_MPNO
46	LR_RGARB_CHK_LOG_FIRST (bit15 -08)							
	FST_MST_REQOFF	FST_RXDT_PER_ERR	FST_RXDT_PER_FUNC	FST_RXDT_PER_WRBE	FST_RXDT_PER_ADR	FST_RXDT_PER_WDT	FST_RXDT_PER_RDT	FST_RXDT_CMPCHK
47	LR_RGARB_CHK_LOG_FIRST (bit07 -00)							
	FST_VALID_CHK	Reserved	Reserved	FST_PENC_GNTZERO	FST_PENC_GNTEX	FST_SEQ_PTY	FST_SEQ_ENC	FST_SEQ_DEC
48	LR_RGARB_ACSADR_MONI_CRT (bit31 -24)							
	ACS REG (31)	ACS REG (30)	ACS REG (29)	ACS REG (28)	ACS REG (27)	ACS REG (26)	ACS REG (25)	ACS REG (24)
49	LR_RGARB_ACSADR_MONI_CRT (bit23 -16)							
	ACS REG (23)	ACS REG (22)	ACS REG (21)	ACS REG (20)	ACS REG (19)	ACS REG (18)	ACS REG (17)	ACS REG (16)
4A	LR_RGARB_ACSADR_MONI_CRT (bit15 -08)							
	ACS REG (15)	ACS REG (14)	ACS REG (13)	ACS REG (12)	ACS REG (11)	ACS REG (10)	ACS REG (9)	ACS REG (8)
4B	LR_RGARB_ACSADR_MONI_CRT (bit07 -00)							
	ACS REG (7)	ACS REG (6)	ACS REG (5)	ACS REG (4)	ACS REG (3)	ACS REG (2)	ACS REG (1)	ACS REG (0)
4C	LR_RGARB_REQGNT_MONI (bit31 -24)							
	Reserved	STS GNT (14)	STS GNT (13)	STS GNT (12)	STS GNT (11)	Reserved	STS GNT (9)	STS GNT (8)
4D	LR_RGARB_REQGNT_MONI (bit23 -16)							
	STS GNT (7)	STS GNT (6)	STS GNT (5)	Reserved	STS GNT (3)	STS GNT (2)	STS GNT (1)	STS GNT (0)
4E	LR_RGARB_REQGNT_MONI (bit15 -08)							
	Reserved	STS REQ (14)	STS REQ (13)	STS REQ (12)	STS REQ (11)	Reserved	STS REQ (9)	STS REQ (8)
4F	LR_RGARB_REQGNT_MONI (bit07 -00)							
	STS REQ (7)	STS REQ (6)	STS REQ (5)	Reserved	STS REQ (3)	STS REQ (2)	STS REQ (1)	STS REQ (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(8) Byte28 = 08 (2/4)

	0	1	2	3	4	5	6	7
50	LR_RGARB_SEQ_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_RGARB_SEQ_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR_RGARB_SEQ_MONI (bit15 -08)							
	STS PSEQ (7)	STS PSEQ (6)	STS PSEQ (5)	STS PSEQ (4)	STS PSEQ (3)	STS PSEQ (2)	STS PSEQ (1)	STS PSEQ (0)
53	LR_RGARB_SEQ_MONI (bit07 -00)							
	STS SEQ (7)	STS SEQ (6)	STS SEQ (5)	STS SEQ (4)	STS SEQ (3)	STS SEQ (2)	STS SEQ (1)	STS SEQ (0)
54	LR_RGARB_CHK_LOG2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_RGARB_CHK_LOG2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	FST_RXDT_ PER_W_RDT	FST_RXDT_ PER_MPNO
56	LR_RGARB_CHK_LOG2 (bit15 -08)							
	FST_MST_ REQOFF	FST_RXDT_ PER_ERR	FST_RXDT_ PER_FUNC	FST_RXDT_ PER_WRBE	FST_RXDT_ PER_ADR	FST_RXDT_ PER_WDT	FST_RXDT_ PER_RDT	FST_RXDT_ CMPCHK
57	LR_RGARB_CHK_LOG2 (bit07 -00)							
	FST_VALID_ _CHK	FST_XR_RS V	FST_XR_ WRBE	FST_PENC_ GNTZERO	FST_PENC_ GNTX	FST_SEQ_ PTY	FST_SEQ_ ENC	FST_SEQ_ DEC
58	LR_RGARB_ACSADR_MONI2 (bit31 -24)							
	ACS REG (31)	ACS REG (30)	ACS REG (29)	ACS REG (28)	ACS REG (27)	ACS REG (26)	ACS REG (25)	ACS REG (24)
59	LR_RGARB_ACSADR_MONI2 (bit23 -16)							
	ACS REG (23)	ACS REG (22)	ACS REG (21)	ACS REG (20)	ACS REG (19)	ACS REG (18)	ACS REG (17)	ACS REG (16)
5A	LR_RGARB_ACSADR_MONI2 (bit15 -08)							
	ACS REG (15)	ACS REG (14)	ACS REG (13)	ACS REG (12)	ACS REG (11)	ACS REG (10)	ACS REG (9)	ACS REG (8)
5B	LR_RGARB_ACSADR_MONI2 (bit07 -00)							
	ACS REG (7)	ACS REG (6)	ACS REG (5)	ACS REG (4)	ACS REG (3)	ACS REG (2)	ACS REG (1)	ACS REG (0)
5C	LR_RGARB_REQGNT_MONI2 (bit31 -24)							
	Reserved	STS GNT (14)	STS GNT (13)	STS GNT (12)	STS GNT (11)	Reserved	STS GNT (9)	STS GNT (8)
5D	LR_RGARB_REQGNT_MONI2 (bit23 -16)							
	STS GNT (7)	STS GNT (6)	STS GNT (5)	Reserved	STS GNT (3)	STS GNT (2)	STS GNT (1)	STS GNT (0)
5E	LR_RGARB_REQGNT_MONI2 (bit15 -08)							
	Reserved	STS REQ (14)	STS REQ (13)	STS REQ (12)	STS REQ (11)	Reserved	STS REQ (9)	STS REQ (8)
5F	LR_RGARB_REQGNT_MONI2 (bit07 -00)							
	STS REQ (7)	STS REQ (6)	STS REQ (5)	Reserved	STS REQ (3)	STS REQ (2)	STS REQ (1)	STS REQ (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(8) Byte28 = 08 (3/4)

	0	1	2	3	4	5	6	7
60	LR_RGARB_SEQ_MONI2 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR_RGARB_SEQ_MONI2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_RGARB_SEQ_MONI2 (bit15 -08)							
	STS PSEQ (7)	STS PSEQ (6)	STS PSEQ (5)	STS PSEQ (4)	STS PSEQ (3)	STS PSEQ (2)	STS PSEQ (1)	STS PSEQ (0)
63	LR_RGARB_SEQ_MONI2 (bit07 -00)							
	STS SEQ (7)	STS SEQ (6)	STS SEQ (5)	STS SEQ (4)	STS SEQ (3)	STS SEQ (2)	STS SEQ (1)	STS SEQ (0)
64	LR_RGARB_MPNO_MONI (bit31 -24)							
	NEXTERR_FUNC (7)	NEXTERR_FUNC (6)	NEXTERR_FUNC (5)	NEXTERR_FUNC (4)	NEXTERR_FUNC (3)	NEXTERR_FUNC (2)	NEXTERR_FUNC (1)	NEXTERR_FUNC (0)
65	LR_RGARB_MPNO_MONI (bit23 -16)							
	NEXTERR_MPNO (7)	NEXTERR_MPNO (6)	NEXTERR_MPNO (5)	NEXTERR_MPNO (4)	NEXTERR_MPNO (3)	NEXTERR_MPNO (2)	NEXTERR_MPNO (1)	NEXTERR_MPNO (0)
66	LR_RGARB_MPNO_MONI (bit15 -08)							
	FSTERR_FUNC (7)	FSTERR_FUNC (6)	FSTERR_FUNC (5)	FSTERR_FUNC (4)	FSTERR_FUNC (3)	FSTERR_FUNC (2)	FSTERR_FUNC (1)	FSTERR_FUNC (0)
67	LR_RGARB_MPNO_MONI (bit07 -00)							
	FSTERR_MPNO (7)	FSTERR_MPNO (6)	FSTERR_MPNO (5)	FSTERR_MPNO (4)	FSTERR_MPNO (3)	FSTERR_MPNO (2)	FSTERR_MPNO (1)	FSTERR_MPNO (0)
68	LR_RGARB_WRACC_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR_RGARB_WRACC_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR_RGARB_WRACC_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR_RGARB_WRACC_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	NEXTERR_WR	Reserved	Reserved	Reserved	FSTERR_WR
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(8) Byte28 = 08 (4/4)

	0	1	2	3	4	5	6	7
70	LR_SMBM_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR_SMBM_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	FUNC (4)	FUNC (3)	FUNC (2)	FUNC (1)	FUNC (0)
72	LR_SMBM_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
73	LR_SMBM_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY
74	LR_SMBM_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR_SMBM_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	FUNC (4)	FUNC (3)	FUNC (2)	FUNC (1)	FUNC (0)
76	LR_SMBM_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
77	LR_SMBM_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY
78	LR_SMBM_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_SMBM_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	FUNC (4)	FUNC (3)	FUNC (2)	FUNC (1)	FUNC (0)
7A	LR_SMBM_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
7B	LR_SMBM_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY
7C	LR_SMBM_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR_SMBM_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	FUNC (4)	FUNC (3)	FUNC (2)	FUNC (1)	FUNC (0)
7E	LR_SMBM_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	TC CNT PCHK	CC CNT PCHK	DC CNT PCHK	IC CNT PCHK
7F	LR_SMBM_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	SLV WAIT	BUS IDLE	BUSY

Byte27 = 84 (CHK4) Byte40-7F Detail

(9) Byte28 = 09 (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_STATUS3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	INT_RGARB_CHK1B	INT_REG2_CHK1B	INT_REG1_CHK1B	INT_REG0_CHK1B
41	LR_INT_STATUS3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	INT_SMBUS_M3_CHK1B	INT_SMBUS_M2_CHK1B	INT_SMBUS_M1_CHK1B	INT_SMBUS_M0_CHK1B
42	LR_INT_STATUS3 (bit15 -08)							
	Reserved	Reserved	Reserved	FIVE1MCK	FIVE1MCK3	FIVE1MCK2	FIVE1MCK1	FIVE1MCK0
43	LR_INT_STATUS3 (bit07 -00)							
	Reserved	Reserved	Reserved	FIVE0MCK	FIVE0MCK3	FIVE0MCK2	FIVE0MCK1	FIVE0MCK0
44	LR_INT_CHK3_STATUS000 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_INT_CHK3_STATUS000 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_INT_CHK3_STATUS000 (bit15 -08)							
	Reserved	Reserved	INT_REG2_CHK3_INT_P	INT_REG2_CHK3_P	INT_REG1_CHK3_INT_P	INT_REG1_CHK3_P	INT_REG0_CHK3_INT_P	INT_REG0_CHK3_P
47	LR_INT_CHK3_STATUS000 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	INT_XD_CHK3_INT_P	INT_XD_CHK3_P	INT_DXC1_CHK3_DRAM_P	INT_DXC0_CHK3_DRAM_P
48	LR_XREG_HARX_ERR ALOG (bit31 -24)							
	DEC ERR	ENC ERR	WDCNT PERR	TOCNT TO	TOCNT PERR	DT PERR	HLRC ERR	DLRC ERR
49	LR_XREG_HARX_ERR ALOG (bit23 -16)							
	HED PERR	NPCNT PERR	PCCNT PERR	DLRCTIM ERR	VLD OFF ERR	REQ OFF ERR	Reserved	SEQ_STS_P ERR
4A	LR_XREG_HARX_ERR ALOG (bit15 -08)							
	TX ERR	Reserved	MPNO 128OVR_ER R	LEN0B_ACS	BUF_OVF	FMT_TYPE_ERR	XBF_CNT_PERR	TXBF_CNT_PERR
4B	LR_XREG_HARX_ERR ALOG (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR_XREG_RAIF_ERR ALOG (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RGARB RDTL PCHK	RGARB RDTH PCHK
4D	LR_XREG_RAIF_ERR ALOG (bit23 -16)							
	RGARB ADRBE PCHK	RGARB WDT PCHK	RGARB RDT PCHK	Reserved	SEQ_END TIMEOUT	RGARB ARB	RGARB XR RSV	RGARB BEWRVALI D
4E	LR_XREG_RAIF_ERR ALOG (bit15 -08)							
	MPX RSV REG ACS	MPNO PCHK	RAERR PCHK	TXBF OVFLOW	FUNC PCHK	WDT PCHK	WEBE PCHK	ADR PCHK
4F	LR_XREG_RAIF_ERR ALOG (bit07 -00)							
	SEQ PCHK	SEQ ENC	SEQ DEC	RGARB SEQ	TXBF PCHK	RXBF PCHK	GNT_TO	TIMER PCHK

Byte27 = 84 (CHK4) Byte40-7F Detail

(9) Byte28 = 09 (2/4)

	0	1	2	3	4	5	6	7
50	LR_XREG_HATX_ERR ALOG (bit31 -24)							
	SWIF_DEC_ERR	SWIF_ENC_ERR	WDCNT_PERR	TOCNT_TO	TOCNT_PERR	DT_PERR	HLRC_ERR	DLRC_ERR
51	LR_XREG_HATX_ERR ALOG (bit23 -16)							
	XBF_TAG_PERR	XBF_ID_PERR	XBF_LADR_PERR	XBF_LEN_PERR	XBF_BE_PERR	XBF_MPNO_PERR	TXBF_RDT_PERR	TXBF_ERR_PERR
52	LR_XREG_HATX_ERR ALOG (bit15 -08)							
	RX_ERR	XBF_CNT_PERR	TXBF_CNT_PERR	SWIF_SEQ_PERR	SEQ_PERR	MPX_ERR	XR_ERR	DTA_RA_ERR
53	LR_XREG_HATX_ERR ALOG (bit07 -00)							
	CTL_RA_ERR	DTA_RAIF_ERR	Reserved	DEC_ERR	ENC_ERR	MSI_OFF_ERR	MSI_TIMEOUT_ERR	MSI_CNT_PERR
54	LR_XREG_HARX_MERR ALOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_XREG_HARX_MERR ALOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_XREG_HARX_MERR ALOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
57	LR_XREG_HARX_MERR ALOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY8B	REG4B_BOUNDARY4B	LENOVR_8B	REG4B_LENOVER_4B
58	LR_XREG_HARX_ERR FLOG (bit31 -24)							
	DEC_ERR	ENC_ERR	WDCNT_PERR	TOCNT_TO	TOCNT_PERR	DT_PERR	HLRC_ERR	DLRC_ERR
59	LR_XREG_HARX_ERR FLOG (bit23 -16)							
	HED_PERR	NPCNT_PERR	PCCNT_PERR	DLRCTIM_ERR	VLD_OFF_ERR	REQ_OFF_ERR	Reserved	SEQ_STS_PERR
5A	LR_XREG_HARX_ERR FLOG (bit15 -08)							
	TX_ERR	Reserved	MPNO_128OVR_ERR	LEN0B_ACS	BUF_OVF	FMT_TYPE_ERR	XBF_CNT_PERR	TXBF_CNT_PERR
5B	LR_XREG_HARX_ERR FLOG (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_XREG_RAIF_ERR FLOG (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RGARB_RDTL_PCHK	RGARB_RDTH_PCHK
5D	LR_XREG_RAIF_ERR FLOG (bit23 -16)							
	RGARB_ADRBE_PCHK	RGARB_WDT_PCHK	RGARB_RDT_PCHK	Reserved	SEQ_END_TIMEOUT	RGARB_ARB	RGARB_XR_RSV	RGARB_BEWRVALID
5E	LR_XREG_RAIF_ERR FLOG (bit15 -08)							
	MPX_RSV_REG_ACS	MPNO_PCHK	RAERR_PCHK	TXBF_OVERFLOW	FUNC_PCHK	WDT_PCHK	WEBE_PCHK	ADR_PCHK
5F	LR_XREG_RAIF_ERR FLOG (bit07 -00)							
	SEQ_PCHK	SEQ_ENC	SEQ_DEC	RGARB_SEQ	TXBF_PCHK	RXBF_PCHK	GNT_TO	TIMER_PCHK

Byte27 = 84 (CHK4) Byte40-7F Detail

(9) Byte28 = 09 (3/4)

	0	1	2	3	4	5	6	7
60	LR_XREG_HATX_ERR_FLOG (bit31 -24)							
	SWIF_DEC_ERR	SWIF_ENC_ERR	WDCNT_PERR	TOCNT_TO	TOCNT_PERR	DT_PERR	HLRC_ERR	DLRC_ERR
61	LR_XREG_HATX_ERR_FLOG (bit23 -16)							
	XBF_TAG_PERR	XBF_ID_PERR	XBF_LADR_PERR	XBF_LEN_PERR	XBF_BE_PERR	XBF_MPNO_PERR	TXBF_RDT_PERR	TXBF_ERR_PERR
62	LR_XREG_HATX_ERR_FLOG (bit15 -08)							
	RX_ERR	XBF_CNT_PERR	TXBF_CNT_PERR	SWIF_SEQ_PERR	SEQ_PERR	MPX_ERR	XR_ERR	DTA_RA_ERR
63	LR_XREG_HATX_ERR_FLOG (bit07 -00)							
	CTL_RA_ERR	DTA_RAIF_ERR	Reserved	DEC_ERR	ENC_ERR	MSI_OFF_ERR	MSI_TIMOUT_ERR	MSI_CNT_PERR
64	LR_XREG_HARX_MERR_FLOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_XREG_HARX_MERR_FLOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR_XREG_HARX_MERR_FLOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
67	LR_XREG_HARX_MERR_FLOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY8B	REG4B_BOUNDARY4B	LENOVR_8B	REG4B_LENOVER_4B
68	LR_XREG_SEQSTS_LOG 1 (bit31 -24)							
	HARX STS PSEQ (7)	HARX STS PSEQ (6)	HARX STS PSEQ (5)	HARX STS PSEQ (4)	HARX STS PSEQ (3)	HARX STS PSEQ (2)	HARX STS PSEQ (1)	HARX STS PSEQ (0)
69	LR_XREG_SEQSTS_LOG 1 (bit23 -16)							
	HARX STS SEQ (7)	HARX STS SEQ (6)	HARX STS SEQ (5)	HARX STS SEQ (4)	HARX STS SEQ (3)	HARX STS SEQ (2)	HARX STS SEQ (1)	HARX STS SEQ (0)
6A	LR_XREG_SEQSTS_LOG 1 (bit15 -08)							
	RAIF STS PSEQ (7)	RAIF STS PSEQ (6)	RAIF STS PSEQ (5)	RAIF STS PSEQ (4)	RAIF STS PSEQ (3)	RAIF STS PSEQ (2)	RAIF STS PSEQ (1)	RAIF STS PSEQ (0)
6B	LR_XREG_SEQSTS_LOG 1 (bit07 -00)							
	RAIF STS SEQ (7)	RAIF STS SEQ (6)	RAIF STS SEQ (5)	RAIF STS SEQ (4)	RAIF STS SEQ (3)	RAIF STS SEQ (2)	RAIF STS SEQ (1)	RAIF STS SEQ (0)
6C	LR_XREG_SEQSTS_LOG 2 (bit31 -24)							
	HATX STS PSEQ2 (7)	HATX STS PSEQ2 (6)	HATX STS PSEQ2 (5)	HATX STS PSEQ2 (4)	HATX STS PSEQ2 (3)	HATX STS PSEQ2 (2)	HATX STS PSEQ2 (1)	HATX STS PSEQ2 (0)
6D	LR_XREG_SEQSTS_LOG 2 (bit23 -16)							
	HATX STS SEQ2 (7)	HATX STS SEQ2 (6)	HATX STS SEQ2 (5)	HATX STS SEQ2 (4)	HATX STS SEQ2 (3)	HATX STS SEQ2 (2)	HATX STS SEQ2 (1)	HATX STS SEQ2 (0)
6E	LR_XREG_SEQSTS_LOG 2 (bit15 -08)							
	HATX STS PSEQ (7)	HATX STS PSEQ (6)	HATX STS PSEQ (5)	HATX STS PSEQ (4)	HATX STS PSEQ (3)	HATX STS PSEQ (2)	HATX STS PSEQ (1)	HATX STS PSEQ (0)
6F	LR_XREG_SEQSTS_LOG 2 (bit07 -00)							
	HATX STS SEQ (7)	HATX STS SEQ (6)	HATX STS SEQ (5)	HATX STS SEQ (4)	HATX STS SEQ (3)	HATX STS SEQ (2)	HATX STS SEQ (1)	HATX STS SEQ (0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(9) Byte28 = 09 (4/4)

	0	1	2	3	4	5	6	7
70	LR_XREG_RAIF_HDLOG0 (bit31 -24)							
	HED LOG(127)	HED LOG(126)	HED LOG(125)	HED LOG(124)	HED LOG(123)	HED LOG(122)	HED LOG(121)	HED LOG(120)
71	LR_XREG_RAIF_HDLOG0 (bit23 -16)							
	HED LOG(119)	HED LOG(118)	HED LOG(117)	HED LOG(116)	HED LOG(115)	HED LOG(114)	HED LOG(113)	HED LOG(112)
72	LR_XREG_RAIF_HDLOG0 (bit15 -08)							
	HED LOG(111)	HED LOG(110)	HED LOG(109)	HED LOG(108)	HED LOG(107)	HED LOG(106)	HED LOG(105)	HED LOG(104)
73	LR_XREG_RAIF_HDLOG0 (bit07 -00)							
	HED LOG(103)	HED LOG(102)	HED LOG(101)	HED LOG(100)	HED LOG (99)	HED LOG (98)	HED LOG (97)	HED LOG (96)
74	LR_XREG_RAIF_HDLOG1 (bit31 -24)							
	HED LOG (95)	HED LOG (94)	HED LOG (93)	HED LOG (92)	HED LOG (91)	HED LOG (90)	HED LOG (89)	HED LOG (88)
75	LR_XREG_RAIF_HDLOG1 (bit23 -16)							
	HED LOG (87)	HED LOG (86)	HED LOG (85)	HED LOG (84)	HED LOG (83)	HED LOG (82)	HED LOG (81)	HED LOG (80)
76	LR_XREG_RAIF_HDLOG1 (bit15 -08)							
	HED LOG (79)	HED LOG (78)	HED LOG (77)	HED LOG (76)	HED LOG (75)	HED LOG (74)	HED LOG (73)	HED LOG (72)
77	LR_XREG_RAIF_HDLOG1 (bit07 -00)							
	HED LOG (71)	HED LOG (70)	HED LOG (69)	HED LOG (68)	HED LOG (67)	HED LOG (66)	HED LOG (65)	HED LOG (64)
78	LR_XREG_RAIF_HDLOG2 (bit31 -24)							
	HED LOG (63)	HED LOG (62)	HED LOG (61)	HED LOG (60)	HED LOG (59)	HED LOG (58)	HED LOG (57)	HED LOG (56)
79	LR_XREG_RAIF_HDLOG2 (bit23 -16)							
	HED LOG (55)	HED LOG (54)	HED LOG (53)	HED LOG (52)	HED LOG (51)	HED LOG (50)	HED LOG (49)	HED LOG (48)
7A	LR_XREG_RAIF_HDLOG2 (bit15 -08)							
	HED LOG (47)	HED LOG (46)	HED LOG (45)	HED LOG (44)	HED LOG (43)	HED LOG (42)	HED LOG (41)	HED LOG (40)
7B	LR_XREG_RAIF_HDLOG2 (bit07 -00)							
	HED LOG (39)	HED LOG (38)	HED LOG (37)	HED LOG (36)	HED LOG (35)	HED LOG (34)	HED LOG (33)	HED LOG (32)
7C	LR_XREG_RAIF_HDLOG3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
7D	LR_XREG_RAIF_HDLOG3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
7E	LR_XREG_RAIF_HDLOG3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
7F	LR_XREG_RAIF_HDLOG3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)

SSBLOG05-1000

Byte27 = 84 (CHK4) Byte40-7F Detail

(10) Byte28 = 0A (1/4)

	0	1	2	3	4	5	6	7
40	LR_DXC_ERROR_DETAIL3 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_DXC_ERROR_DETAIL3 (bit23 -16)							
	RDAT1_PERR	RDAT2_PERR	FI_ADR_PERR	ADR_CNT_PERR	AUTO_REW_R_ADR_PERR	ENCERR	TIME_OVER	TIME_PERR
42	LR_DXC_ERROR_DETAIL3 (bit15 -08)							
	UNC_EC0	UNC_EC1	UNC_EC2	UNC_EC3	UNC_EC4	UNC_EC5	UNC_EC6	UNC_EC7
43	LR_DXC_ERROR_DETAIL3 (bit07 -00)							
	ADRE_EC0	ADRE_EC1	ADRE_EC2	ADRE_EC3	ADRE_EC4	ADRE_EC5	ADRE_EC6	ADRE_EC7
44	LR_DXC_ERROR_DETAIL6 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	ENC_ERR	FLG_ERR	TIME_OVER	TIME_PERR
45	LR_DXC_ERROR_DETAIL6 (bit23 -16)							
	INCDAT_PERR	EXE_ADR_PERR	LST_ADR_PERR	STR_ADR_PERR	EXP_ECC_PERR	EXP_DAT_PERR	READ_ECC_PERR	READ_DAT_PERR
46	LR_DXC_ERROR_DETAIL6 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_DXC_ERROR_DETAIL6 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	ECC_H_CMP_ERR	ECC_L_CMP_ERR	DAT_H_CM_PERR	DAT_L_CM_PERR
48	LR_DXC_ECC_ERRADR_MON1 (bit31 -24)							
	ELOG_ADR1 (31)	ELOG_ADR1 (30)	ELOG_ADR1 (29)	ELOG_ADR1 (28)	ELOG_ADR1 (27)	ELOG_ADR1 (26)	ELOG_ADR1 (25)	ELOG_ADR1 (24)
49	LR_DXC_ECC_ERRADR_MON1 (bit23 -16)							
	ELOG_ADR1 (23)	ELOG_ADR1 (22)	ELOG_ADR1 (21)	ELOG_ADR1 (20)	ELOG_ADR1 (19)	ELOG_ADR1 (18)	ELOG_ADR1 (17)	ELOG_ADR1 (16)
4A	LR_DXC_ECC_ERRADR_MON1 (bit15 -08)							
	ELOG_ADR1 (15)	ELOG_ADR1 (14)	ELOG_ADR1 (13)	ELOG_ADR1 (12)	ELOG_ADR1 (11)	ELOG_ADR1 (10)	ELOG_ADR1 (9)	ELOG_ADR1 (8)
4B	LR_DXC_ECC_ERRADR_MON1 (bit07 -00)							
	ELOG_ADR1 (7)	ELOG_ADR1 (6)	ELOG_ADR1 (5)	ELOG_ADR1 (4)	ELOG_ADR1 (3)	ELOG_ADR1 (2)	ELOG_ADR1 (1)	ELOG_ADR1 (0)
4C	LR_DXC_ECC_ERRADR_MON2 (bit31 -24)							
	ELOG_ADR2 (31)	ELOG_ADR2 (30)	ELOG_ADR2 (29)	ELOG_ADR2 (28)	ELOG_ADR2 (27)	ELOG_ADR2 (26)	ELOG_ADR2 (25)	ELOG_ADR2 (24)
4D	LR_DXC_ECC_ERRADR_MON2 (bit23 -16)							
	ELOG_ADR2 (23)	ELOG_ADR2 (22)	ELOG_ADR2 (21)	ELOG_ADR2 (20)	ELOG_ADR2 (19)	ELOG_ADR2 (18)	ELOG_ADR2 (17)	ELOG_ADR2 (16)
4E	LR_DXC_ECC_ERRADR_MON2 (bit15 -08)							
	ELOG_ADR2 (15)	ELOG_ADR2 (14)	ELOG_ADR2 (13)	ELOG_ADR2 (12)	ELOG_ADR2 (11)	ELOG_ADR2 (10)	ELOG_ADR2 (9)	ELOG_ADR2 (8)
4F	LR_DXC_ECC_ERRADR_MON2 (bit07 -00)							
	ELOG_ADR2 (7)	ELOG_ADR2 (6)	ELOG_ADR2 (5)	ELOG_ADR2 (4)	ELOG_ADR2 (3)	ELOG_ADR2 (2)	ELOG_ADR2 (1)	ELOG_ADR2 (0)

SSBLOG05-1010

Byte27 = 84 (CHK4) Byte40-7F Detail

(10) Byte28 = 0A (2/4)

	0	1	2	3	4	5	6	7
50	LR_DXC_ECC_ERRADR_MON3 (bit31 -24)							
	ELOG_ADR3 (31)	ELOG_ADR3 (30)	ELOG_ADR3 (29)	ELOG_ADR3 (28)	ELOG_ADR3 (27)	ELOG_ADR3 (26)	ELOG_ADR3 (25)	ELOG_ADR3 (24)
51	LR_DXC_ECC_ERRADR_MON3 (bit23 -16)							
	ELOG_ADR3 (23)	ELOG_ADR3 (22)	ELOG_ADR3 (21)	ELOG_ADR3 (20)	ELOG_ADR3 (19)	ELOG_ADR3 (18)	ELOG_ADR3 (17)	ELOG_ADR3 (16)
52	LR_DXC_ECC_ERRADR_MON3 (bit15 -08)							
	ELOG_ADR3 (15)	ELOG_ADR3 (14)	ELOG_ADR3 (13)	ELOG_ADR3 (12)	ELOG_ADR3 (11)	ELOG_ADR3 (10)	ELOG_ADR3 (9)	ELOG_ADR3 (8)
53	LR_DXC_ECC_ERRADR_MON3 (bit07 -00)							
	ELOG_ADR3 (7)	ELOG_ADR3 (6)	ELOG_ADR3 (5)	ELOG_ADR3 (4)	ELOG_ADR3 (3)	ELOG_ADR3 (2)	ELOG_ADR3 (1)	ELOG_ADR3 (0)
54	LR_DXC_ECC_ERRADR_MON4 (bit31 -24)							
	ELOG_ADR4 (31)	ELOG_ADR4 (30)	ELOG_ADR4 (29)	ELOG_ADR4 (28)	ELOG_ADR4 (27)	ELOG_ADR4 (26)	ELOG_ADR4 (25)	ELOG_ADR4 (24)
55	LR_DXC_ECC_ERRADR_MON4 (bit23 -16)							
	ELOG_ADR4 (23)	ELOG_ADR4 (22)	ELOG_ADR4 (21)	ELOG_ADR4 (20)	ELOG_ADR4 (19)	ELOG_ADR4 (18)	ELOG_ADR4 (17)	ELOG_ADR4 (16)
56	LR_DXC_ECC_ERRADR_MON4 (bit15 -08)							
	ELOG_ADR4 (15)	ELOG_ADR4 (14)	ELOG_ADR4 (13)	ELOG_ADR4 (12)	ELOG_ADR4 (11)	ELOG_ADR4 (10)	ELOG_ADR4 (9)	ELOG_ADR4 (8)
57	LR_DXC_ECC_ERRADR_MON4 (bit07 -00)							
	ELOG_ADR4 (7)	ELOG_ADR4 (6)	ELOG_ADR4 (5)	ELOG_ADR4 (4)	ELOG_ADR4 (3)	ELOG_ADR4 (2)	ELOG_ADR4 (1)	ELOG_ADR4 (0)
58	LR_DXC_ECC_LOG_EN_MON (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR_DXC_ECC_LOG_EN_MON (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_DXC_ECC_LOG_EN_MON (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_DXC_ECC_LOG_EN_MON (bit07 -00)							
	WLOG_EN(0)	WLOG_EN(1)	WLOG_EN(2)	WLOG_EN(3)	ELOG_EN(0)	ELOG_EN(1)	ELOG_EN(2)	ELOG_EN(3)
5C	LR_DXC_ECC_UNC_MON (bit31 -24)							
	UNC_LOG1 (7)	UNC_LOG1 (6)	UNC_LOG1 (5)	UNC_LOG1 (4)	UNC_LOG1 (3)	UNC_LOG1 (2)	UNC_LOG1 (1)	UNC_LOG1 (0)
5D	LR_DXC_ECC_UNC_MON (bit23 -16)							
	UNC_LOG2 (7)	UNC_LOG2 (6)	UNC_LOG2 (5)	UNC_LOG2 (4)	UNC_LOG2 (3)	UNC_LOG2 (2)	UNC_LOG2 (1)	UNC_LOG2 (0)
5E	LR_DXC_ECC_UNC_MON (bit15 -08)							
	UNC_LOG3 (7)	UNC_LOG3 (6)	UNC_LOG3 (5)	UNC_LOG3 (4)	UNC_LOG3 (3)	UNC_LOG3 (2)	UNC_LOG3 (1)	UNC_LOG3 (0)
5F	LR_DXC_ECC_UNC_MON (bit07 -00)							
	UNC_LOG4 (7)	UNC_LOG4 (6)	UNC_LOG4 (5)	UNC_LOG4 (4)	UNC_LOG4 (3)	UNC_LOG4 (2)	UNC_LOG4 (1)	UNC_LOG4 (0)

SSBLOG05-1020

Byte27 = 84 (CHK4) Byte40-7F Detail

(10) Byte28 = 0A (3/4)

	0	1	2	3	4	5	6	7
60	LR_DXC_ECC_ADRE_MON (bit31 -24)							
	ADRE_LOG1 (7)	ADRE_LOG1 (6)	ADRE_LOG1 (5)	ADRE_LOG1 (4)	ADRE_LOG1 (3)	ADRE_LOG1 (2)	ADRE_LOG1 (1)	ADRE_LOG1 (0)
61	LR_DXC_ECC_ADRE_MON (bit23 -16)							
	ADRE_LOG2 (7)	ADRE_LOG2 (6)	ADRE_LOG2 (5)	ADRE_LOG2 (4)	ADRE_LOG2 (3)	ADRE_LOG2 (2)	ADRE_LOG2 (1)	ADRE_LOG2 (0)
62	LR_DXC_ECC_ADRE_MON (bit15 -08)							
	ADRE_LOG3 (7)	ADRE_LOG3 (6)	ADRE_LOG3 (5)	ADRE_LOG3 (4)	ADRE_LOG3 (3)	ADRE_LOG3 (2)	ADRE_LOG3 (1)	ADRE_LOG3 (0)
63	LR_DXC_ECC_ADRE_MON (bit07 -00)							
	ADRE_LOG4 (7)	ADRE_LOG4 (6)	ADRE_LOG4 (5)	ADRE_LOG4 (4)	ADRE_LOG4 (3)	ADRE_LOG4 (2)	ADRE_LOG4 (1)	ADRE_LOG4 (0)
64	LR_DXC_RCVIF_SEQ1_MON (bit31 -24)							
	RCVIF SEQ1ST3(7)	RCVIF SEQ1ST3(6)	RCVIF SEQ1ST3(5)	RCVIF SEQ1ST3(4)	RCVIF SEQ1ST3(3)	RCVIF SEQ1ST3(2)	RCVIF SEQ1ST3(1)	RCVIF SEQ1ST3(0)
65	LR_DXC_RCVIF_SEQ1_MON (bit23 -16)							
	RCVIF SEQ1ST2(7)	RCVIF SEQ1ST2(6)	RCVIF SEQ1ST2(5)	RCVIF SEQ1ST2(4)	RCVIF SEQ1ST2(3)	RCVIF SEQ1ST2(2)	RCVIF SEQ1ST2(1)	RCVIF SEQ1ST2(0)
66	LR_DXC_RCVIF_SEQ1_MON (bit15 -08)							
	RCVIF SEQ1ST1(7)	RCVIF SEQ1ST1(6)	RCVIF SEQ1ST1(5)	RCVIF SEQ1ST1(4)	RCVIF SEQ1ST1(3)	RCVIF SEQ1ST1(2)	RCVIF SEQ1ST1(1)	RCVIF SEQ1ST1(0)
67	LR_DXC_RCVIF_SEQ1_MON (bit07 -00)							
	RCVIF SEQ1ST0(7)	RCVIF SEQ1ST0(6)	RCVIF SEQ1ST0(5)	RCVIF SEQ1ST0(4)	RCVIF SEQ1ST0(3)	RCVIF SEQ1ST0(2)	RCVIF SEQ1ST0(1)	RCVIF SEQ1ST0(0)
68	LR_DXC_RCVIF_SEQ2_MON (bit31 -24)							
	RCVIF SEQ2ST3(7)	RCVIF SEQ2ST3(6)	RCVIF SEQ2ST3(5)	RCVIF SEQ2ST3(4)	RCVIF SEQ2ST3(3)	RCVIF SEQ2ST3(2)	RCVIF SEQ2ST3(1)	RCVIF SEQ2ST3(0)
69	LR_DXC_RCVIF_SEQ2_MON (bit23 -16)							
	RCVIF SEQ2ST2(7)	RCVIF SEQ2ST2(6)	RCVIF SEQ2ST2(5)	RCVIF SEQ2ST2(4)	RCVIF SEQ2ST2(3)	RCVIF_SEQ2 ST2(2)	RCVIF_SEQ2 ST2(1)	RCVIF_SEQ 2ST2(0)
6A	LR_DXC_RCVIF_SEQ2_MON (bit15 -08)							
	RCVIF SEQ2ST1(7)	RCVIF_SEQ2 ST1(6)	RCVIF_SEQ2 ST1(5)	RCVIF_SEQ2 ST1(4)	RCVIF_SEQ2 ST1(3)	RCVIF_SEQ2 ST1(2)	RCVIF_SEQ2 ST1(1)	RCVIF_SEQ 2ST1(0)
6B	LR_DXC_RCVIF_SEQ2_MON (bit07 -00)							
	RCVIF SEQ2ST0(7)	RCVIF SEQ2ST0(6)	RCVIF SEQ2ST0(5)	RCVIF_SEQ2 ST0(4)	RCVIF_SEQ2 ST0(3)	RCVIF SEQ2ST0(2)	RCVIF SEQ2ST0(1)	RCVIF SEQ2ST0(0)
6C	LR_DXC_CMDCTL_SEQ1_MON (bit31 -24)							
	CMDCTL SEQ1ST3(7)	CMDCTL SEQ1ST3(6)	CMDCTL SEQ1ST3(5)	CMDCTL SEQ1ST3(4)	CMDCTL SEQ1ST3(3)	CMDCTL SEQ1ST3(2)	CMDCTL SEQ1ST3(1)	CMDCTL SEQ1ST3(0)
6D	LR_DXC_CMDCTL_SEQ1_MON (bit23 -16)							
	CMDCTL SEQ1ST2(7)	CMDCTL SEQ1ST2(6)	CMDCTL SEQ1ST2(5)	CMDCTL SEQ1ST2(4)	CMDCTL SEQ1ST2(3)	CMDCTL SEQ1ST2(2)	CMDCTL SEQ1ST2(1)	CMDCTL SEQ1ST2(0)
6E	LR_DXC_CMDCTL_SEQ1_MON (bit15 -08)							
	CMDCTL SEQ1ST1(7)	CMDCTL SEQ1ST1(6)	CMDCTL SEQ1ST1(5)	CMDCTL SEQ1ST1(4)	CMDCTL SEQ1ST1(3)	CMDCTL SEQ1ST1(2)	CMDCTL SEQ1ST1(1)	CMDCTL SEQ1ST1(0)
6F	LR_DXC_CMDCTL_SEQ1_MON (bit07 -00)							
	CMDCTL SEQ1ST0(7)	CMDCTL SEQ1ST0(6)	CMDCTL SEQ1ST0(5)	CMDCTL SEQ1ST0(4)	CMDCTL SEQ1ST0(3)	CMDCTL SEQ1ST0(2)	CMDCTL SEQ1ST0(1)	CMDCTL SEQ1ST0(0)

Byte27 = 84 (CHK4) Byte40-7F Detail

(10) Byte28 = 0A (4/4)

	0	1	2	3	4	5	6	7
70	LR_DXC_CMDCTL_SEQ2_MON (bit31 -24)							
	CMDCTL SEQ2ST3(7)	CMDCTL SEQ2ST3(6)	CMDCTL SEQ2ST3(5)	CMDCTL SEQ2ST3(4)	CMDCTL SEQ2ST3(3)	CMDCTL SEQ2ST3(2)	CMDCTL SEQ2ST3(1)	CMDCTL SEQ2ST3(0)
71	LR_DXC_CMDCTL_SEQ2_MON (bit23 -16)							
	CMDCTL SEQ2ST2(7)	CMDCTL SEQ2ST2(6)	CMDCTL SEQ2ST2(5)	CMDCTL SEQ2ST2(4)	CMDCTL SEQ2ST2(3)	CMDCTL SEQ2ST2(2)	CMDCTL SEQ2ST2(1)	CMDCTL SEQ2ST2(0)
72	LR_DXC_CMDCTL_SEQ2_MON (bit15 -08)							
	CMDCTL SEQ2ST1(7)	CMDCTL SEQ2ST1(6)	CMDCTL SEQ2ST1(5)	CMDCTL SEQ2ST1(4)	CMDCTL SEQ2ST1(3)	CMDCTL SEQ2ST1(2)	CMDCTL SEQ2ST1(1)	CMDCTL SEQ2ST1(0)
73	LR_DXC_CMDCTL_SEQ2_MON (bit07 -00)							
	CMDCTL SEQ2ST0(7)	CMDCTL SEQ2ST0(6)	CMDCTL SEQ2ST0(5)	CMDCTL SEQ2ST0(4)	CMDCTL SEQ2ST0(3)	CMDCTL SEQ2ST0(2)	CMDCTL SEQ2ST0(1)	CMDCTL SEQ2ST0(0)
74	LR_DXC_CMDCTL_SEQ3_MON (bit31 -24)							
	CMDCTL SEQ3ST3(7)	CMDCTL SEQ3ST3(6)	CMDCTL SEQ3ST3(5)	CMDCTL SEQ3ST3(4)	CMDCTL SEQ3ST3(3)	CMDCTL SEQ3ST3(2)	CMDCTL SEQ3ST3(1)	CMDCTL SEQ3ST3(0)
75	LR_DXC_CMDCTL_SEQ3_MON (bit23 -16)							
	CMDCTL SEQ3ST2(7)	CMDCTL SEQ3ST2(6)	CMDCTL SEQ3ST2(5)	CMDCTL SEQ3ST2(4)	CMDCTL SEQ3ST2(3)	CMDCTL SEQ3ST2(2)	CMDCTL SEQ3ST2(1)	CMDCTL SEQ3ST2(0)
76	LR_DXC_CMDCTL_SEQ3_MON (bit15 -08)							
	CMDCTL SEQ3ST1(7)	CMDCTL SEQ3ST1(6)	CMDCTL SEQ3ST1(5)	CMDCTL SEQ3ST1(4)	CMDCTL SEQ3ST1(3)	CMDCTL SEQ3ST1(2)	CMDCTL SEQ3ST1(1)	CMDCTL SEQ3ST1(0)
77	LR_DXC_CMDCTL_SEQ3_MON (bit07 -00)							
	CMDCTL SEQ3ST0(7)	CMDCTL SEQ3ST0(6)	CMDCTL SEQ3ST0(5)	CMDCTL SEQ3ST0(4)	CMDCTL SEQ3ST0(3)	CMDCTL SEQ3ST0(2)	CMDCTL SEQ3ST0(1)	CMDCTL SEQ3ST0(0)
78	LR_DXC_RCHK_SEQ_MON (bit31 -24)							
	RCHK SEQST3(7)	RCHK SEQST3(6)	RCHK SEQST3(5)	RCHK SEQST3(4)	RCHK SEQST3(3)	RCHK SEQST3(2)	RCHK SEQST3(1)	RCHK SEQST3(0)
79	LR_DXC_RCHK_SEQ_MON (bit23 -16)							
	RCHK SEQST2(7)	RCHK SEQST2(6)	RCHK SEQST2(5)	RCHK SEQST2(4)	RCHK SEQST2(3)	RCHK SEQST2(2)	RCHK SEQST2(1)	RCHK SEQST2(0)
7A	LR_DXC_RCHK_SEQ_MON (bit15 -08)							
	RCHK SEQST1(7)	RCHK SEQST1(6)	RCHK SEQST1(5)	RCHK SEQST1(4)	RCHK SEQST1(3)	RCHK SEQST1(2)	RCHK SEQST1(1)	RCHK SEQST1(0)
7B	LR_DXC_RCHK_SEQ_MON (bit07 -00)							
	RCHK SEQST0(7)	RCHK SEQST0(6)	RCHK SEQST0(5)	RCHK SEQST0(4)	RCHK SEQST0(3)	RCHK SEQST0(2)	RCHK SEQST0(1)	RCHK SEQST0(0)
7C	LR_DXC_CPLGEN_SEQ_MON (bit31 -24)							
	CPLGEN SEQST3(7)	CPLGEN SEQST3(6)	CPLGEN SEQST3(5)	CPLGEN SEQST3(4)	CPLGEN SEQST3(3)	CPLGEN SEQST3(2)	CPLGEN SEQST3(1)	CPLGEN SEQST3(0)
7D	LR_DXC_CPLGEN_SEQ_MON (bit23 -16)							
	CPLGEN SEQST2(7)	CPLGEN SEQST2(6)	CPLGEN SEQST2(5)	CPLGEN SEQST2(4)	CPLGEN SEQST2(3)	CPLGEN SEQST2(2)	CPLGEN SEQST2(1)	CPLGEN SEQST2(0)
7E	LR_DXC_CPLGEN_SEQ_MON (bit15 -08)							
	CPLGEN SEQST1(7)	CPLGEN SEQST1(6)	CPLGEN SEQST1(5)	CPLGEN SEQST1(4)	CPLGEN SEQST1(3)	CPLGEN SEQST1(2)	CPLGEN SEQST1(1)	CPLGEN SEQST1(0)
7F	LR_DXC_CPLGEN_SEQ_MON (bit07 -00)							
	CPLGEN SEQST0(7)	CPLGEN SEQST0(6)	CPLGEN SEQST0(5)	CPLGEN SEQST0(4)	CPLGEN SEQST0(3)	CPLGEN SEQST0(2)	CPLGEN SEQST0(1)	CPLGEN SEQST0(0)

SSBLOG05-1040

Byte27 = 84 (CHK4) Byte40-7F Detail

(11) Byte28 = 0B (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_STATUS2 (bit31 -24)							
	Reserved	Reserved	Reserved	INT_XD_CHK1B	Reserved	Reserved	Reserved	INT_FMT_CHK1B
41	LR_INT_STATUS2 (bit23 -16)							
	Reserved	Reserved	INT_DXC1_CHK1B	INT_DXC0_CHK1B	Reserved	Reserved	INT_DXA1_CHK1B	INT_DXA0_CHK1B
42	LR_INT_STATUS2 (bit15 -08)							
	Reserved	Reserved	INT_DRR1_CHK1B	INT_DRR0_CHK1B	Reserved	Reserved	INT_DMAM_B1_CHK1B	INT_DMAM_B0_CHK1B
43	LR_INT_STATUS2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	INT_DMA3_CHK1B	INT_DMA2_CHK1B	INT_DMA1_CHK1B	INT_DMA0_CHK1B
44	LR_DMA_ERR_ST32 (bit31 -24)							
	DMCTL_SEQERR	DMCP_STS_W0_7_PERR	PRERCNT_PERR	PID_PERR	PRM_W0_7_PERR	PRM_W8_E_PERR	STS_W0_7_PERR	STSADR_PERR
45	LR_DMA_ERR_ST32 (bit23 -16)							
	PFAREQID_PERR	REQADR_PERR	PRMCHK_PERR	STSSXP_PERR	Reserved	Reserved	Reserved	Reserved
46	LR_DMA_ERR_ST32 (bit15 -08)							
	LRP2_ADR_CNT_PERR	LRP3_ADRC_NT_PERR	LRPADR_PERR	LRPSTS_PERR	Reserved	Reserved	Reserved	Reserved
47	LR_DMA_ERR_ST32 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR_DMA_ERR_ST33 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	PRMLRC_ERR	REQID_CMP_ERR	CHEN_ERR	MPN_CMP_ERR
49	LR_DMA_ERR_ST33 (bit23 -16)							
	CPLD_STS_NOT_SC	CPL_STS_NOT_SC	EP	CPL_TIM_OVR	PFVLD_ERR	Reserved	Reserved	Reserved
4A	LR_DMA_ERR_ST33 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR_DMA_ERR_ST33 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR_DMA_ERR_ST34 (bit31 -24)							
	IRREGU_INF0(0)	IRREGU_INF0(1)	IRREGU_INF0(2)	IRREGU_INF0(3)	IRREGU_INF0(4)	IRREGU_INF0(5)	IRREGU_INF0(6)	IRREGU_INF0(7)
4D	LR_DMA_ERR_ST34 (bit23 -16)							
	IRREGU_INF0(8)	IRREGU_INF0(9)	IRREGU_INF0(10)	IRREGU_INF0(11)	IRREGU_INF0(12)	IRREGU_INF0(13)	IRREGU_INF0(14)	IRREGU_INF0(15)
4E	LR_DMA_ERR_ST34 (bit15 -08)							
	IRREGU_INF0(16)	IRREGU_INF0(17)	IRREGU_INF0(18)	IRREGU_INF0(19)	IRREGU_INF0(20)	IRREGU_INF0(21)	IRREGU_INF0(22)	IRREGU_INF0(23)
4F	LR_DMA_ERR_ST34 (bit07 -00)							
	IRREGU_INF0(24)	IRREGU_INF0(25)	IRREGU_INF0(26)	IRREGU_INF0(27)	IRREGU_INF0(28)	IRREGU_INF0(29)	IRREGU_INF0(30)	IRREGU_INF0(31)

SSBLOG05-1050

Byte27 = 84 (CHK4) Byte40-7F Detail

(11) Byte28 = 0B (2/4)

	0	1	2	3	4	5	6	7
50	LR_DMA_ERR_ST35 (bit31 -24)							
	IRREGU_INF1(0)	IRREGU_INF1(1)	IRREGU_INF1(2)	IRREGU_INF1(3)	IRREGU_INF1(4)	IRREGU_INF1(5)	IRREGU_INF1(6)	IRREGU_INF1(7)
51	LR_DMA_ERR_ST35 (bit23 -16)							
	IRREGU_INF1(8)	IRREGU_INF1(9)	IRREGU_INF1(10)	IRREGU_INF1(11)	IRREGU_INF1(12)	IRREGU_INF1(13)	IRREGU_INF1(14)	IRREGU_INF1(15)
52	LR_DMA_ERR_ST35 (bit15 -08)							
	IRREGU_INF1(16)	IRREGU_INF1(17)	IRREGU_INF1(18)	IRREGU_INF1(19)	IRREGU_INF1(20)	IRREGU_INF1(21)	IRREGU_INF1(22)	IRREGU_INF1(23)
53	LR_DMA_ERR_ST35 (bit07 -00)							
	IRREGU_INF1(24)	IRREGU_INF1(25)	IRREGU_INF1(26)	IRREGU_INF1(27)	IRREGU_INF1(28)	IRREGU_INF1(29)	IRREGU_INF1(30)	IRREGU_INF1(31)
54	LR_DMA_ERR_ST36 (bit31 -24)							
	DMPC_SEQERR	PCRIF_SEQERR	SWIF_R_SEQERR	TMR0_SEQERR	TMR1_SEQERR	TMR2_SEQERR	TMR3_SEQERR	PFADR_W0_PERR
55	LR_DMA_ERR_ST36 (bit23 -16)							
	PFADR_W1_PERR	PBDIFCNT_PERR	PREWCNT_PERR	PRMCNT_PERR	HDRW3_PERR	TAG_PERR	PRMT_N_HED_PERR	REQ_HED0_PERR
56	LR_DMA_ERR_ST36 (bit15 -08)							
	REQ_HED1_PERR	REQ_HED2_PERR	REQ_HED3_PERR	PRERCNT_PERR	PRDT_W0_7_PERR	PRDT_W8_F_PERR	PRDTINFO_PERR	ZANBUFNUM_PERR
57	LR_DMA_ERR_ST36 (bit07 -00)							
	TOCNT0_PERR	TOCNT1_PERR	TOCNT2_PERR	TOCNT3_PERR	PRDT0_INF0	PRDT0_INF1	PRDT0_INF2	PRDT0_INF3
58	LR_DMA_ERR_ST37 (bit31 -24)							
	PRMLRC_ERR	PRMLRC_ERR2	REQID_CMP_ERR	CHEN_ERR	Reserved	Reserved	Reserved	Reserved
59	LR_DMA_ERR_ST37 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	TIM_OVR0	TIM_OVR1	TIM_OVR2	TIM_OVR3
5A	LR_DMA_ERR_ST37 (bit15 -08)							
	SW_TX_ERRSTS	Reserved	Reserved	NO_HED_HIT	Reserved	WDCNT_PERR	TOCNT_TO_ERR	TO_CNT_PERR
5B	LR_DMA_ERR_ST37 (bit07 -00)							
	DT_PERR	HLRC_ERR	DLRC_ERR	HED_PERR	Reserved	DLRCTIM_ERR	VLD_ERR	REQ_ERR
5C	LR_DMA_ERR_ST38 (bit31 -24)							
	DMSB_SEQERR	SBTIF_SEQERR	STSDIFCNT_PERR	STSRCNT_PERR	STSWCNT_PERR	PC_DT_PERR	PC_HED_PERR	PC_DLRC_PERR
5D	LR_DMA_ERR_ST38 (bit23 -16)							
	HED_INFO0_PERR	HED_INFO1_PERR	HED_INFO2_PERR	HED_INFO3_PERR	Reserved	Reserved	Reserved	Reserved
5E	LR_DMA_ERR_ST38 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR_DMA_ERR_ST38 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1060

Byte27 = 84 (CHK4) Byte40-7F Detail

(11) Byte28 = 0B (3/4)

	0	1	2	3	4	5	6	7
60	LR_DMA_ERR_ST39 (bit31 -24)							
	ABTERR	TIF_SEQERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR_DMA_ERR_ST39 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	WDCNT_PERR	TOCNT_TIMEOUT_ERR	TO_CNT_PERR	DT_PERR
62	LR_DMA_ERR_ST39 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_DMA_ERR_ST39 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RX_ERRSTS
64	LR_DMA_ERR_ST40 (bit31 -24)							
	KEYSEQ_ERR	TMRSEQ_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_DMA_ERR_ST40 (bit23 -16)							
	DEK_INDEX_PERR	N_REQ_HED_PERR	LST_INDEX_PERR	Reserved	TOCNT_PERR	CPL_ERR	Reserved	Reserved
66	LR_DMA_ERR_ST40 (bit15 -08)							
	SW_HED_PERR	SW_DT_PERR	WDCNT_PERR	Reserved	DEKKEY_PERR	IVKEY_PERR	EXPKEY_PERR	LRC_FLG_PERR
67	LR_DMA_ERR_ST40 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR_DMA_ERR_ST41 (bit31 -24)							
	ERR_CPLD_NOT_SC	ERR_CPL_NOT_SC	ERR_EP	TOVER	Reserved	Reserved	Reserved	Reserved
69	LR_DMA_ERR_ST41 (bit23 -16)							
	KEY_FLG_ERR	EXP_EN_ERR	DEK_LRCERR	IV_LRCERR	EXP_LRCERR	KEY_LEN_ERR	KEY_EM_ERR	Reserved
6A	LR_DMA_ERR_ST41 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR_DMA_ERR_ST41 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(11) Byte28 = 0B (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1080

Byte27 = 84 (CHK4) Byte40-7F Detail

(12) Byte28 = 0C (1/4)

	0	1	2	3	4	5	6	7
40	LR_DRR_DM_ERR_ST32 (bit31 -24)							
	DMCTL_SEQERR	Reserved	PRERCNT_PERR	PID_PERR	PRM_W0_7_PERR	PRM_W8_E_PERR	STS_W0_7_PERR	STSADR_PERR
41	LR_DRR_DM_ERR_ST32 (bit23 -16)							
	PFAREQID_PERR	REQADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_DRR_DM_ERR_ST32 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_DRR_DM_ERR_ST32 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR_DRR_DM_ERR_ST33 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	PRMLRC_ERR	REQID_CMP_ERR	CHEN_ERR	MPN_CMP_ERR
45	LR_DRR_DM_ERR_ST33 (bit23 -16)							
	CPLD_STS_NOT_SC	CPL_STS_NOT_SC	Reserved	CPL_TIM_OVR	Reserved	Reserved	Reserved	Reserved
46	LR_DRR_DM_ERR_ST33 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_DRR_DM_ERR_ST33 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR_DRR_DM_ERR_ST34 (bit31 -24)							
	IRREGU_INF0(0)	IRREGU_INF0(1)	IRREGU_INF0(2)	IRREGU_INF0(3)	IRREGU_INF0(4)	IRREGU_INF0(5)	IRREGU_INF0(6)	IRREGU_INF0(7)
49	LR_DRR_DM_ERR_ST34 (bit23 -16)							
	IRREGU_INF0(8)	IRREGU_INF0(9)	IRREGU_INF0(10)	IRREGU_INF0(11)	IRREGU_INF0(12)	IRREGU_INF0(13)	IRREGU_INF0(14)	IRREGU_INF0(15)
4A	LR_DRR_DM_ERR_ST34 (bit15 -08)							
	IRREGU_INF0(16)	IRREGU_INF0(17)	IRREGU_INF0(18)	IRREGU_INF0(19)	IRREGU_INF0(20)	IRREGU_INF0(21)	IRREGU_INF0(22)	IRREGU_INF0(23)
4B	LR_DRR_DM_ERR_ST34 (bit07 -00)							
	IRREGU_INF0(24)	IRREGU_INF0(25)	IRREGU_INF0(26)	IRREGU_INF0(27)	IRREGU_INF0(28)	IRREGU_INF0(29)	IRREGU_INF0(30)	IRREGU_INF0(31)
4C	LR_DRR_DM_ERR_ST35 (bit31 -24)							
	IRREGU_INF1(0)	IRREGU_INF1(1)	IRREGU_INF1(2)	IRREGU_INF1(3)	IRREGU_INF1(4)	IRREGU_INF1(5)	IRREGU_INF1(6)	IRREGU_INF1(7)
4D	LR_DRR_DM_ERR_ST35 (bit23 -16)							
	IRREGU_INF1(8)	IRREGU_INF1(9)	IRREGU_INF1(10)	IRREGU_INF1(11)	IRREGU_INF1(12)	IRREGU_INF1(13)	IRREGU_INF1(14)	IRREGU_INF1(15)
4E	LR_DRR_DM_ERR_ST35 (bit15 -08)							
	IRREGU_INF1(16)	IRREGU_INF1(17)	IRREGU_INF1(18)	IRREGU_INF1(19)	IRREGU_INF1(20)	IRREGU_INF1(21)	IRREGU_INF1(22)	IRREGU_INF1(23)
4F	LR_DRR_DM_ERR_ST35 (bit07 -00)							
	IRREGU_INF1(24)	IRREGU_INF1(25)	IRREGU_INF1(26)	IRREGU_INF1(27)	IRREGU_INF1(28)	IRREGU_INF1(29)	IRREGU_INF1(30)	IRREGU_INF1(31)

SSBLOG05-1090

Byte27 = 84 (CHK4) Byte40-7F Detail

(12) Byte28 = 0C (2/4)

	0	1	2	3	4	5	6	7
50	LR_DRR_DM_ERR_ST36 (bit31 -24)							
	DMPC_SEQERR	PCRIF_SEQERR	SWIF_R_SEQERR	TMR0_SEQERR	TMR1_SEQERR	TMR2_SEQERR	TMR3_SEQERR	PFADR_W0_PERR
51	LR_DRR_DM_ERR_ST36 (bit23 -16)							
	PFADR_W1_PERR	PBDIFCNT_PERR	PREWCNT_PERR	PRMCNT_PERR	HDRW3_PERR	TAG_PERR	PRMT_N_HED_PERR	REQ_HED0_PERR
52	LR_DRR_DM_ERR_ST36 (bit15 -08)							
	REQ_HED1_PERR	REQ_HED2_PERR	REQ_HED3_PERR	PRERCNT_PERR	PRDT_W0_F_PERR	Reserved	Reserved	Reserved
53	LR_DRR_DM_ERR_ST36 (bit07 -00)							
	TOCNT0_PERR	TOCNT1_PERR	TOCNT2_PERR	TOCNT3_PERR	Reserved	Reserved	Reserved	Reserved
54	LR_DRR_DM_ERR_ST37 (bit31 -24)							
	PRMLRC_ERR	REQID_CMP_ERR	CHEN_ERR	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_DRR_DM_ERR_ST37 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	TIM_OVR0	TIM_OVR1	TIM_OVR2	TIM_OVR3
56	LR_DRR_DM_ERR_ST37 (bit15 -08)							
	SW_TX_ERRSTS	Reserved	Reserved	NO_HED_HIT	Reserved	WDCNT_PERR	TOCNT_TO_ERR	TO_CNT_PERR
57	LR_DRR_DM_ERR_ST37 (bit07 -00)							
	DT_PERR	HLRC_ERR	DLRC_ERR	HED_PERR	Reserved	DLRCTIM_ERR	VLD_ERR	REQ_ERR
58	LR_DRR_DM_ERR_ST38 (bit31 -24)							
	DMSB_SEQERR	SBTIF_SEQERR	STSDIFCNT_PERR	STSRCNT_PERR	STSWCNT_PERR	PC_DT_PERR	PC_HED_PERR	PC_DLRC_PERR
59	LR_DRR_DM_ERR_ST38 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_DRR_DM_ERR_ST38 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_DRR_DM_ERR_ST38 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_DRR_DM_ERR_ST39 (bit31 -24)							
	ABTERR	TIF_SEQERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR_DRR_DM_ERR_ST39 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	WDCNT_PERR	TOCNT_TIMEOUT_ERR	TO_CNT_PERR	DT_PERR
5E	LR_DRR_DM_ERR_ST39 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR_DRR_DM_ERR_ST39 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RX_ERRSTS

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Byte27 = 84 (CHK4) Byte40-7F Detail

(12) Byte28 = 0C (3/4)

	0	1	2	3	4	5	6	7
60	LR_DRR_DM_ERR_ST32 (bit31 -24)							
	DMCTL_SEQERR	Reserved	PRERCNT_PERR	PID_PERR	PRM_W0_7_PERR	PRM_W8_E_PERR	STS_W0_7_PERR	STSADR_PERR
61	LR_DRR_DM_ERR_ST32 (bit23 -16)							
	PFAREQID_PERR	REQADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_DRR_DM_ERR_ST32 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_DRR_DM_ERR_ST32 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR_DRR_DM_ERR_ST33 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	PRMLRC_ERR	REQID_CMP_ERR	CHEN_ERR	MPN_CMP_ERR
65	LR_DRR_DM_ERR_ST33 (bit23 -16)							
	CPLD_STS_NOT_SC	CPL_STS_NOT_SC	Reserved	CPL_TIM_OVR	Reserved	Reserved	Reserved	Reserved
66	LR_DRR_DM_ERR_ST33 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR_DRR_DM_ERR_ST33 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR_DRR_DM_ERR_ST34 (bit31 -24)							
	IRREGU_INF0(0)	IRREGU_INF0(1)	IRREGU_INF0(2)	IRREGU_INF0(3)	IRREGU_INF0(4)	IRREGU_INF0(5)	IRREGU_INF0(6)	IRREGU_INF0(7)
69	LR_DRR_DM_ERR_ST34 (bit23 -16)							
	IRREGU_INF0(8)	IRREGU_INF0(9)	IRREGU_INF0(10)	IRREGU_INF0(11)	IRREGU_INF0(12)	IRREGU_INF0(13)	IRREGU_INF0(14)	IRREGU_INF0(15)
6A	LR_DRR_DM_ERR_ST34 (bit15 -08)							
	IRREGU_INF0(16)	IRREGU_INF0(17)	IRREGU_INF0(18)	IRREGU_INF0(19)	IRREGU_INF0(20)	IRREGU_INF0(21)	IRREGU_INF0(22)	IRREGU_INF0(23)
6B	LR_DRR_DM_ERR_ST34 (bit07 -00)							
	IRREGU_INF0(24)	IRREGU_INF0(25)	IRREGU_INF0(26)	IRREGU_INF0(27)	IRREGU_INF0(28)	IRREGU_INF0(29)	IRREGU_INF0(30)	IRREGU_INF0(31)
6C	LR_DRR_DM_ERR_ST35 (bit31 -24)							
	IRREGU_INF1(0)	IRREGU_INF1(1)	IRREGU_INF1(2)	IRREGU_INF1(3)	IRREGU_INF1(4)	IRREGU_INF1(5)	IRREGU_INF1(6)	IRREGU_INF1(7)
6D	LR_DRR_DM_ERR_ST35 (bit23 -16)							
	IRREGU_INF1(8)	IRREGU_INF1(9)	IRREGU_INF1(10)	IRREGU_INF1(11)	IRREGU_INF1(12)	IRREGU_INF1(13)	IRREGU_INF1(14)	IRREGU_INF1(15)
6E	LR_DRR_DM_ERR_ST35 (bit15 -08)							
	IRREGU_INF1(16)	IRREGU_INF1(17)	IRREGU_INF1(18)	IRREGU_INF1(19)	IRREGU_INF1(20)	IRREGU_INF1(21)	IRREGU_INF1(22)	IRREGU_INF1(23)
6F	LR_DRR_DM_ERR_ST35 (bit07 -00)							
	IRREGU_INF1(24)	IRREGU_INF1(25)	IRREGU_INF1(26)	IRREGU_INF1(27)	IRREGU_INF1(28)	IRREGU_INF1(29)	IRREGU_INF1(30)	IRREGU_INF1(31)

Byte27 = 84 (CHK4) Byte40-7F Detail

(12) Byte28 = 0C (4/4)

	0	1	2	3	4	5	6	7
70	LR_DRR_DM_ERR_ST36 (bit31 -24)							
	DMPC_SEQERR	PCRIF_SEQERR	SWIF_R_SEQERR	TMR0_SEQERR	TMR1_SEQERR	TMR2_SEQERR	TMR3_SEQERR	PFADR_W0_PERR
71	LR_DRR_DM_ERR_ST36 (bit23 -16)							
	PFADR_W1_PERR	PBDIFCNT_PERR	PREWCNT_PERR	PRMCNT_PERR	HDRW3_PERR	TAG_PERR	PRMT_N_HED_PERR	REQ_HED0_PERR
72	LR_DRR_DM_ERR_ST36 (bit15 -08)							
	REQ_HED1_PERR	REQ_HED2_PERR	REQ_HED3_PERR	PRERCNT_PERR	PRDT_W0_F_PERR	Reserved	Reserved	Reserved
73	LR_DRR_DM_ERR_ST36 (bit07 -00)							
	TOCNT0_PERR	TOCNT1_PERR	TOCNT2_PERR	TOCNT3_PERR	Reserved	Reserved	Reserved	Reserved
74	LR_DRR_DM_ERR_ST37 (bit31 -24)							
	PRMLRC_ERR	REQID_CMP_ERR	CHEN_ERR	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR_DRR_DM_ERR_ST37 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	TIM_OVR0	TIM_OVR1	TIM_OVR2	TIM_OVR3
76	LR_DRR_DM_ERR_ST37 (bit15 -08)							
	SW_TX_ERR_STS	Reserved	Reserved	NO_HED_HIT	Reserved	WDCNT_PERR	TOCNT_TO_ERR	TO_CNT_PERR
77	LR_DRR_DM_ERR_ST37 (bit07 -00)							
	DT_PERR	HLRC_ERR	DLRC_ERR	HED_PERR	Reserved	DLRCTIM_ERR	VLD_ERR	REQ_ERR
78	LR_DRR_DM_ERR_ST38 (bit31 -24)							
	DMSB_SEQERR	SBTIF_SEQERR	STSDIFCNT_PERR	STSRCNT_PERR	STSWCNT_PERR	PC_DT_PERR	PC_HED_PERR	PC_DLRC_PERR
79	LR_DRR_DM_ERR_ST38 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	LR_DRR_DM_ERR_ST38 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	LR_DRR_DM_ERR_ST38 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	LR_DRR_DM_ERR_ST39 (bit31 -24)							
	ABTERR	TIF_SEQERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR_DRR_DM_ERR_ST39 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	WDCNT_PERR	TOCNT_TIMEOUT_ERR	TO_CNT_PERR	DT_PERR
7E	LR_DRR_DM_ERR_ST39 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	LR_DRR_DM_ERR_ST39 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RX_ERRSTS

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Byte27 = 84 (CHK4) Byte40-7F Detail

(13) Byte28 = 0D (1/4)

	0	1	2	3	4	5	6	7
40	LR_MF_PSIF_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_MF_PSIF_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_MF_PSIF_STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_MF_PSIF_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	HSN_RX_WARN_ERR	HSN_TX_CHK4_ERR	HSN_RX_CHK4_ERR
44	LR_MF_PSIF_INI_VAL (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_MF_PSIF_INI_VAL (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_MF_PSIF_INI_VAL (bit15 -08)							
	BUS_NO_7	BUS_NO_6	BUS_NO_5	BUS_NO_4	BUS_NO_3	BUS_NO_2	BUS_NO_1	BUS_NO_0
47	LR_MF_PSIF_INI_VAL (bit07 -00)							
	FUNC_NO_7	FUNC_NO_6	FUNC_NO_5	FUNC_NO_4	FUNC_NO_3	FUNC_NO_2	FUNC_NO_1	FUNC_NO_0
48	LR_MF_HSN_RX_CMP_BUF_FULL (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_MF_HSN_RX_CMP_BUF_FULL (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_MF_HSN_RX_CMP_BUF_FULL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR_MF_HSN_RX_CMP_BUF_FULL (bit07 -00)							
	HSN_RX_CMP_BUF_FULL_7	HSN_RX_CMP_BUF_FULL_6	HSN_RX_CMP_BUF_FULL_5	HSN_RX_CMP_BUF_FULL_4	HSN_RX_CMP_BUF_FULL_3	HSN_RX_CMP_BUF_FULL_2	HSN_RX_CMP_BUF_FULL_1	HSN_RX_CMP_BUF_FULL_0
4C	LR_MF_HSN_TX_ACK_WAIT_CNT (bit31 -24)							
	HSN_TX_ACK_WAIT_CNT_31	HSN_TX_ACK_WAIT_CNT_30	HSN_TX_ACK_WAIT_CNT_29	HSN_TX_ACK_WAIT_CNT_28	HSN_TX_ACK_WAIT_CNT_27	HSN_TX_ACK_WAIT_CNT_26	HSN_TX_ACK_WAIT_CNT_25	HSN_TX_ACK_WAIT_CNT_24
4D	LR_MF_HSN_TX_ACK_WAIT_CNT (bit23 -16)							
	HSN_TX_ACK_WAIT_CNT_23	HSN_TX_ACK_WAIT_CNT_22	HSN_TX_ACK_WAIT_CNT_21	HSN_TX_ACK_WAIT_CNT_20	HSN_TX_ACK_WAIT_CNT_19	HSN_TX_ACK_WAIT_CNT_18	HSN_TX_ACK_WAIT_CNT_17	HSN_TX_ACK_WAIT_CNT_16
4E	LR_MF_HSN_TX_ACK_WAIT_CNT (bit15 -08)							
	HSN_TX_ACK_WAIT_CNT_15	HSN_TX_ACK_WAIT_CNT_14	HSN_TX_ACK_WAIT_CNT_13	HSN_TX_ACK_WAIT_CNT_12	HSN_TX_ACK_WAIT_CNT_11	HSN_TX_ACK_WAIT_CNT_10	HSN_TX_ACK_WAIT_CNT_9	HSN_TX_ACK_WAIT_CNT_8
4F	LR_MF_HSN_TX_ACK_WAIT_CNT (bit07 -00)							
	HSN_TX_ACK_WAIT_CNT_7	HSN_TX_ACK_WAIT_CNT_6	HSN_TX_ACK_WAIT_CNT_5	HSN_TX_ACK_WAIT_CNT_4	HSN_TX_ACK_WAIT_CNT_3	HSN_TX_ACK_WAIT_CNT_2	HSN_TX_ACK_WAIT_CNT_1	HSN_TX_ACK_WAIT_CNT_0

Byte27 = 84 (CHK4) Byte40-7F Detail

(13) Byte28 = 0D (2/4)

	0	1	2	3	4	5	6	7
50	LR_MF_HSN_RX_CHK4STS (bit31 -24)							
	SW_TX_ERR	Reserved	Reserved	F_PRM_ECC ADR_ERR	F_PRM_ECC UNC_ERR	F_PRAM RDT_PERR	F_PRAM RADR_PERR	F_PRAM WADR_PERR
51	LR_MF_HSN_RX_CHK4STS (bit23 -16)							
	Reserved	Reserved	Reserved	C_PRM_ECC ADR_ERR	C_PRM_ECC UNC_ERR	C_PRM RDT_PERR	C_PRM RADR_PERR	C_PRM WADR_PERR
52	LR_MF_HSN_RX_CHK4STS (bit15 -08)							
	Reserved	BUF_DIFCNT_PERR	BUF_RPTR_PERR	BUF_WPTR_PERR	VLD_CNT_PERR	MPQUE_DIFCNT_PERR	MPQUE_RPTR_PERR	MPQUE_WPTR_PERR
53	LR_MF_HSN_RX_CHK4STS (bit07 -00)							
	Reserved	REQOFF_ERR	DLRC_ERR	DT_PERR	FMTYP_ERR	Reserved	HLRC_ERR	HD_PERR
54	LR_MF_HSN_RX_SEQ_CHK4STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_MF_HSN_RX_SEQ_CHK4STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_MF_HSN_RX_SEQ_CHK4STS (bit15 -08)							
	RFPARB_GNTCHK_ERR	RFPARB_SEQ_ERR	RCPARB_GNTCHK_ERR	RCPARB_SEQ_ERR	PREQARB_GNTCHK_ERR	PREQARB_SEQ_ERR	RIFARB_GNTCHK_ERR	RIFARB_SEQ_ERR
57	LR_MF_HSN_RX_SEQ_CHK4STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	RXSEQ_ENC_PERR	RXSEQ_ST_PERR
58	LR_MF_HSN_TX_CHK4STS (bit31 -24)							
	SW_RX_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR_MF_HSN_TX_CHK4STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_MF_HSN_TX_CHK4STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_MF_HSN_TX_CHK4STS (bit07 -00)							
	TXDATA_PERR	DIF_CNT_PERR	TXDATA_PERR	DIF_CNT_PERR	RPTR_PERR	WPTR_PERR	ACKCNT_PERR	ACK_TOV
5C	LR_MF_HSN_TX_SEQ_CHK4STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR_MF_HSN_TX_SEQ_CHK4STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR_MF_HSN_TX_SEQ_CHK4STS (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	SREQARB_GNTCHK_ERR	SREQARB_SEQ_ERR
5F	LR_MF_HSN_TX_SEQ_CHK4STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TXSEQ_DEC_ERR	TXSEQ_ENC_ERR	TXSEQ_CST_PERR

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Byte27 = 84 (CHK4) Byte40-7F Detail

(13) Byte28 = 0D (3/4)

	0	1	2	3	4	5	6	7
60	LR MF HSN RX SEQ LOG0 (bit31 -24)							
	HSN_RX SEQ_LOG0_31	HSN_RX SEQ_LOG0_30	HSN_RX SEQ_LOG0_29	HSN_RX SEQ_LOG0_28	HSN_RX SEQ_LOG0_27	HSN_RX SEQ_LOG0_26	HSN_RX SEQ_LOG0_25	HSN_RX SEQ_LOG0_24
61	LR MF HSN RX SEQ LOG0 (bit23 -16)							
	HSN_RX SEQ_LOG0_23	HSN_RX SEQ_LOG0_22	HSN_RX SEQ_LOG0_21	HSN_RX SEQ_LOG0_20	HSN_RX SEQ_LOG0_19	HSN_RX SEQ_LOG0_18	HSN_RX SEQ_LOG0_17	HSN_RX SEQ_LOG0_16
62	LR MF HSN RX SEQ LOG0 (bit15 -08)							
	HSN_RX SEQ_LOG0_15	HSN_RX SEQ_LOG0_14	HSN_RX SEQ_LOG0_13	HSN_RX SEQ_LOG0_12	HSN_RX SEQ_LOG0_11	HSN_RX SEQ_LOG0_10	HSN_RX SEQ_LOG0_9	HSN_RX SEQ_LOG0_8
63	LR MF HSN RX SEQ LOG0 (bit07 -00)							
	HSN_RX SEQ_LOG0_7	HSN_RX SEQ_LOG0_6	HSN_RX SEQ_LOG0_5	HSN_RX SEQ_LOG0_4	HSN_RX SEQ_LOG0_3	HSN_RX SEQ_LOG0_2	HSN_RX SEQ_LOG0_1	HSN_RX SEQ_LOG0_0
64	LR MF HSN RX SEQ LOG1 (bit31 -24)							
	HSN_RX SEQ_LOG1_63	HSN_RX SEQ_LOG1_62	HSN_RX SEQ_LOG1_61	HSN_RX SEQ_LOG1_60	HSN_RX SEQ_LOG1_59	HSN_RX SEQ_LOG1_58	HSN_RX SEQ_LOG1_57	HSN_RX SEQ_LOG1_56
65	LR MF HSN RX SEQ LOG1 (bit23 -16)							
	HSN_RX SEQ_LOG1_55	HSN_RX SEQ_LOG1_54	HSN_RX SEQ_LOG1_53	HSN_RX SEQ_LOG1_52	HSN_RX SEQ_LOG1_51	HSN_RX SEQ_LOG1_50	HSN_RX SEQ_LOG1_49	HSN_RX SEQ_LOG1_48
66	LR MF HSN RX SEQ LOG1 (bit15 -08)							
	HSN_RX SEQ_LOG1_47	HSN_RX SEQ_LOG1_46	HSN_RX SEQ_LOG1_45	HSN_RX SEQ_LOG1_44	HSN_RX SEQ_LOG1_43	HSN_RX SEQ_LOG1_42	HSN_RX SEQ_LOG1_41	HSN_RX SEQ_LOG1_40
67	LR MF HSN RX SEQ LOG1 (bit07 -00)							
	HSN_RX SEQ_LOG1_39	HSN_RX SEQ_LOG1_38	HSN_RX SEQ_LOG1_37	HSN_RX SEQ_LOG1_36	HSN_RX SEQ_LOG1_35	HSN_RX SEQ_LOG1_34	HSN_RX SEQ_LOG1_33	HSN_RX SEQ_LOG1_32
68	LR MF HSN TX SEQ LOG0 (bit31 -24)							
	HSN_TX SEQ_LOG0_31	HSN_TX SEQ_LOG0_30	HSN_TX SEQ_LOG0_29	HSN_TX SEQ_LOG0_28	HSN_TX SEQ_LOG0_27	HSN_TX SEQ_LOG0_26	HSN_TX SEQ_LOG0_25	HSN_TX SEQ_LOG0_24
69	LR MF HSN TX SEQ LOG0 (bit23 -16)							
	HSN_TX SEQ_LOG0_23	HSN_TX SEQ_LOG0_22	HSN_TX SEQ_LOG0_21	HSN_TX SEQ_LOG0_20	HSN_TX SEQ_LOG0_19	HSN_TX SEQ_LOG0_18	HSN_TX SEQ_LOG0_17	HSN_TX SEQ_LOG0_16
6A	LR MF HSN TX SEQ LOG0 (bit15 -08)							
	HSN_TX SEQ_LOG0_15	HSN_TX SEQ_LOG0_14	HSN_TX SEQ_LOG0_13	HSN_TX SEQ_LOG0_12	HSN_TX SEQ_LOG0_11	HSN_TX SEQ_LOG0_10	HSN_TX SEQ_LOG0_9	HSN_TX SEQ_LOG0_8
6B	LR MF HSN TX SEQ LOG0 (bit07 -00)							
	HSN_TX SEQ_LOG0_7	HSN_TX SEQ_LOG0_6	HSN_TX SEQ_LOG0_5	HSN_TX SEQ_LOG0_4	HSN_TX SEQ_LOG0_3	HSN_TX SEQ_LOG0_2	HSN_TX SEQ_LOG0_1	HSN_TX SEQ_LOG0_0
6C	LR MF HSN TX SEQ LOG1 (bit31 -24)							
	HSN_TX SEQ_LOG1_31	HSN_TX SEQ_LOG1_30	HSN_TX SEQ_LOG1_29	HSN_TX SEQ_LOG1_28	HSN_TX SEQ_LOG1_27	HSN_TX SEQ_LOG1_26	HSN_TX SEQ_LOG1_25	HSN_TX SEQ_LOG1_24
6D	LR MF HSN TX SEQ LOG1 (bit23 -16)							
	HSN_TX SEQ_LOG1_23	HSN_TX SEQ_LOG1_22	HSN_TX SEQ_LOG1_21	HSN_TX SEQ_LOG1_20	HSN_TX SEQ_LOG1_19	HSN_TX SEQ_LOG1_18	HSN_TX SEQ_LOG1_17	HSN_TX SEQ_LOG1_16
6E	LR MF HSN TX SEQ LOG1 (bit15 -08)							
	HSN_TX SEQ_LOG1_15	HSN_TX SEQ_LOG1_14	HSN_TX SEQ_LOG1_13	HSN_TX SEQ_LOG1_12	HSN_TX SEQ_LOG1_11	HSN_TX SEQ_LOG1_10	HSN_TX SEQ_LOG1_9	HSN_TX SEQ_LOG1_8
6F	LR MF HSN TX SEQ LOG1 (bit07 -00)							
	HSN_TX SEQ_LOG1_7	HSN_TX SEQ_LOG1_6	HSN_TX SEQ_LOG1_5	HSN_TX SEQ_LOG1_4	HSN_TX SEQ_LOG1_3	HSN_TX SEQ_LOG1_2	HSN_TX SEQ_LOG1_1	HSN_TX SEQ_LOG1_0

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Byte27 = 84 (CHK4) Byte40-7F Detail

(13) Byte28 = 0D (4/4)

	0	1	2	3	4	5	6	7
70	LR MF HSN RX FST FLG LOG0 (bit31 -24)							
	HSN_RX_FST _FLG_LOG0_ MP31	HSN_RX_FST _FLG_LOG0_ MP30	HSN_RX_FST _FLG_LOG0_ MP29	HSN_RX_FST _FLG_LOG0_ MP28	HSN_RX_FST _FLG_LOG0_ MP27	HSN_RX_FST _FLG_LOG0_ MP26	HSN_RX_FST _FLG_LOG0_ MP25	HSN_RX_FST _FLG_LOG0_ MP24
71	LR MF HSN RX FST FLG LOG0 (bit23 -16)							
	HSN_RX_FST _FLG_LOG0_ MP23	HSN_RX_FST _FLG_LOG0_ MP22	HSN_RX_FST _FLG_LOG0_ MP21	HSN_RX_FST _FLG_LOG0_ MP20	HSN_RX_FST _FLG_LOG0_ MP19	HSN_RX_FST _FLG_LOG0_ MP18	HSN_RX_FST _FLG_LOG0_ MP17	HSN_RX_FST _FLG_LOG0_ MP16
72	LR MF HSN RX FST FLG LOG0 (bit15 -08)							
	HSN_RX_FST _FLG_LOG0_ MP15	HSN_RX_FST _FLG_LOG0_ MP14	HSN_RX_FST _FLG_LOG0_ MP13	HSN_RX_FST _FLG_LOG0_ MP12	HSN_RX_FST _FLG_LOG0_ MP11	HSN_RX_FST _FLG_LOG0_ MP10	HSN_RX_FST _FLG_LOG0_ MP9	HSN_RX_FST _FLG_LOG0_ MP8
73	LR MF HSN RX FST FLG LOG0 (bit07 -00)							
	HSN_RX_FST _FLG_LOG0_ MP7	HSN_RX_FST _FLG_LOG0_ MP6	HSN_RX_FST _FLG_LOG0_ MP5	HSN_RX_FST _FLG_LOG0_ MP4	HSN_RX_FST _FLG_LOG0_ MP3	HSN_RX_FST _FLG_LOG0_ MP2	HSN_RX_FST _FLG_LOG0_ MP1	HSN_RX_FST _FLG_LOG0_ MP0
74	LR MF HSN RX FST FLG LOG1 (bit31 -24)							
	HSN_RX_FST _FLG_LOG1_ MP63	HSN_RX_FST _FLG_LOG1_ MP62	HSN_RX_FST _FLG_LOG1_ MP61	HSN_RX_FST _FLG_LOG1_ MP60	HSN_RX_FST _FLG_LOG1_ MP59	HSN_RX_FST _FLG_LOG1_ MP58	HSN_RX_FST _FLG_LOG1_ MP57	HSN_RX_FST _FLG_LOG1_ MP56
75	LR MF HSN RX FST FLG LOG1 (bit23 -16)							
	HSN_RX_FST _FLG_LOG1_ MP55	HSN_RX_FST _FLG_LOG1_ MP54	HSN_RX_FST _FLG_LOG1_ MP53	HSN_RX_FST _FLG_LOG1_ MP52	HSN_RX_FST _FLG_LOG1_ MP51	HSN_RX_FST _FLG_LOG1_ MP50	HSN_RX_FST _FLG_LOG1_ MP49	HSN_RX_FST _FLG_LOG1_ MP48
76	LR MF HSN RX FST FLG LOG1 (bit15 -08)							
	HSN_RX_FST _FLG_LOG1_ MP47	HSN_RX_FST _FLG_LOG1_ MP46	HSN_RX_FST _FLG_LOG1_ MP45	HSN_RX_FST _FLG_LOG1_ MP44	HSN_RX_FST _FLG_LOG1_ MP43	HSN_RX_FST _FLG_LOG1_ MP42	HSN_RX_FST _FLG_LOG1_ MP41	HSN_RX_FST _FLG_LOG1_ MP40
77	LR MF HSN RX FST FLG LOG1 (bit07 -00)							
	HSN_RX_FST _FLG_LOG1_ MP39	HSN_RX_FST _FLG_LOG1_ MP38	HSN_RX_FST _FLG_LOG1_ MP37	HSN_RX_FST _FLG_LOG1_ MP36	HSN_RX_FST _FLG_LOG1_ MP35	HSN_RX_FST _FLG_LOG1_ MP34	HSN_RX_FST _FLG_LOG1_ MP33	HSN_RX_FST _FLG_LOG1_ MP32
78	LR MF HSN RX FST FLG LOG2 (bit31 -24)							
	HSN_RX_FST _FLG_LOG2_ MP95	HSN_RX_FST _FLG_LOG2_ MP94	HSN_RX_FST _FLG_LOG2_ MP93	HSN_RX_FST _FLG_LOG2_ MP92	HSN_RX_FST _FLG_LOG2_ MP91	HSN_RX_FST _FLG_LOG2_ MP90	HSN_RX_FST _FLG_LOG2_ MP89	HSN_RX_FST _FLG_LOG2_ MP88
79	LR MF HSN RX FST FLG LOG2 (bit23 -16)							
	HSN_RX_FST _FLG_LOG2_ MP87	HSN_RX_FST _FLG_LOG2_ MP86	HSN_RX_FST _FLG_LOG2_ MP85	HSN_RX_FST _FLG_LOG2_ MP84	HSN_RX_FST _FLG_LOG2_ MP83	HSN_RX_FST _FLG_LOG2_ MP82	HSN_RX_FST _FLG_LOG2_ MP81	HSN_RX_FST _FLG_LOG2_ MP80
7A	LR MF HSN RX FST FLG LOG2 (bit15 -08)							
	HSN_RX_FST _FLG_LOG2_ MP79	HSN_RX_FST _FLG_LOG2_ MP78	HSN_RX_FST _FLG_LOG2_ MP77	HSN_RX_FST _FLG_LOG2_ MP76	HSN_RX_FST _FLG_LOG2_ MP75	HSN_RX_FST _FLG_LOG2_ MP74	HSN_RX_FST _FLG_LOG2_ MP73	HSN_RX_FST _FLG_LOG2_ MP72
7B	LR MF HSN RX FST FLG LOG2 (bit07 -00)							
	HSN_RX_FST _FLG_LOG2_ MP71	HSN_RX_FST _FLG_LOG2_ MP70	HSN_RX_FST _FLG_LOG2_ MP69	HSN_RX_FST _FLG_LOG2_ MP68	HSN_RX_FST _FLG_LOG2_ MP67	HSN_RX_FST _FLG_LOG2_ MP66	HSN_RX_FST _FLG_LOG2_ MP65	HSN_RX_FST _FLG_LOG2_ MP64
7C	LR MF HSN RX FST FLG LOG3 (bit31 -24)							
	HSN_RX_FST _FLG_LOG3_ MP127	HSN_RX_FST _FLG_LOG3_ MP126	HSN_RX_FST _FLG_LOG3_ MP125	HSN_RX_FST _FLG_LOG3_ MP124	HSN_RX_FST _FLG_LOG3_ MP123	HSN_RX_FST _FLG_LOG3_ MP122	HSN_RX_FST _FLG_LOG3_ MP121	HSN_RX_FST _FLG_LOG3_ MP120
7D	LR MF HSN RX FST FLG LOG3 (bit23 -16)							
	HSN_RX_FST _FLG_LOG3_ MP119	HSN_RX_FST _FLG_LOG3_ MP118	HSN_RX_FST _FLG_LOG3_ MP117	HSN_RX_FST _FLG_LOG3_ MP116	HSN_RX_FST _FLG_LOG3_ MP115	HSN_RX_FST _FLG_LOG3_ MP114	HSN_RX_FST _FLG_LOG3_ MP113	HSN_RX_FST _FLG_LOG3_ MP112
7E	LR MF HSN RX FST FLG LOG3 (bit15 -08)							
	HSN_RX_FST _FLG_LOG3_ MP111	HSN_RX_FST _FLG_LOG3_ MP110	HSN_RX_FST _FLG_LOG3_ MP109	HSN_RX_FST _FLG_LOG3_ MP108	HSN_RX_FST _FLG_LOG3_ MP107	HSN_RX_FST _FLG_LOG3_ MP106	HSN_RX_FST _FLG_LOG3_ MP105	HSN_RX_FST _FLG_LOG3_ MP104
7F	LR MF HSN RX FST FLG LOG3 (bit07 -00)							
	HSN_RX_FST _FLG_LOG3_ MP103	HSN_RX_FST _FLG_LOG3_ MP102	HSN_RX_FST _FLG_LOG3_ MP101	HSN_RX_FST _FLG_LOG3_ MP100	HSN_RX_FST _FLG_LOG3_ MP99	HSN_RX_FST _FLG_LOG3_ MP98	HSN_RX_FST _FLG_LOG3_ MP97	HSN_RX_FST _FLG_LOG3_ MP96

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Byte27 = 84 (CHK4) Byte40-7F Detail

(14) Byte28 = 0E (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_STATUS11 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_INT_STATUS11 (bit23 -16)							
	Reserved	Reserved	Reserved	INT_MFPSIF_CHK4_P	Reserved	Reserved	INT_MFDMA1_CHK4_P	INT_MFDMA0_CHK4_P
42	LR_INT_STATUS11 (bit15 -08)							
	Reserved	Reserved	INT_HARB3REG_CHK4_P	INT_HARB3_CHK4_P	Reserved	Reserved	INT_HARB2REG_CHK4_P	INT_HARB2_CHK4_P
43	LR_INT_STATUS11 (bit07 -00)							
	Reserved	Reserved	INT_HARB1REG_CHK4_P	INT_HARB1_CHK4_P	Reserved	Reserved	INT_HARB0REG_CHK4_P	INT_HARB0_CHK4_P
44	LR_MF0_FCL_ERRSTS1 (bit31 -24)							
	HSNSTS0_ERR	HSNSTS1_ERR	Reserved	Reserved	Reserved	DST1_HSN_TOV	DST2_HSN_TOV	Reserved
45	LR_MF0_FCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_MF0_FCL_ERRSTS1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_MF0_FCL_ERRSTS1 (bit07 -00)							
	Reserved	Reserved	SLRC_ERR	FLRC_ERR	LA_ERR	TLRC_ERR	Reserved	PLRC_ERR
48	LR_MF0_FCL_ERRSTS2 (bit31 -24)							
	ST_PERR	DEC_ERR	ENC_ERR	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR
49	LR_MF0_FCL_ERRSTS2 (bit23 -16)							
	SDT_PERR	IDT_DCNT_PERR	CMR_DCNT_PERR	CMW_DCNT1_PERR	CMW_DCNT0_PERR	HTX_DCNT1_PERR	HTX_DCNT0_PERR	XFRINF_PERR
4A	LR_MF0_FCL_ERRSTS2 (bit15 -08)							
	SBK_CNT2_PERR	LEN_PERR	SBKLEN_PERR	IBFLEN_PERR	CKDLEN_PERR	SBKCNT_PERR	ADR2_PERR	ADR1_PERR
4B	LR_MF0_FCL_ERRSTS2 (bit07 -00)							
	BOP_PERR	SOP_PERR	FBADL_PERR	CKDDL_PERR	PRM3_PERR	PRM2_ERR	PRM1_ERR	PRM0_ERR
4C	LR_MF0_CCL_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_MF0_CCL_ERRSTS0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	DXBF_STS_ERR	Reserved	DXRD_TOV
4E	LR_MF0_CCL_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR_MF0_CCL_ERRSTS0 (bit07 -00)							
	PLRC_ERR	SBCRC_ERR	TLRC_ERR	DXP_ERR4	DXP_ERR3	DXP_ERR2	DXP_ERR1	FC_ERR

Byte27 = 84 (CHK4) Byte40-7F Detail

(14) Byte28 = 0E (2/4)

	0	1	2	3	4	5	6	7
50	LR_MF0_CCL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_MF0_CCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
52	LR_MF0_CCL_ERRSTS1 (bit15 -08)							
	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR	SDT_PERR	DXRD_DCNT_PERR	DTX_DCNT_PERR
53	LR_MF0_CCL_ERRSTS1 (bit07 -00)							
	XFRINF_PERR	IR_LEN_PERR	IW_LEN_PERR	DXADR_PERR	PKTCNT_PERR	CCNT_PERR	PRM1_PERR	PRM0_PERR
54	LR_MF0_ICL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
55	LR_MF0_ICL_ERRSTS1 (bit23 -16)							
	Reserved	HW_CNT_PERR	GNT_CNT_PERR	GNT_TOV	F_CURLEN2_PERR	FIW_TLRC2_PERR	C_CURLEN2_PERR	CIW_TLRC2_PERR
56	LR_MF0_ICL_ERRSTS1 (bit15 -08)							
	C_DXWLEN_PERR	C_RENLEN_PERR	CIR_TLRC_PERR	CIW_TLRC_PERR	C_CURLEN_PERR	C_REQLEN2_PERR	C_REQLEN_PERR	C_SETLEN_PERR
57	LR_MF0_ICL_ERRSTS1 (bit07 -00)							
	Reserved	F_RENLEN_PERR	FIR_TLRC_PERR	FIW_TLRC_PERR	F_CURLEN_PERR	F_REQLEN2_PERR	F_REQLEN_PERR	F_SETLEN_PERR
58	LR_MF1_FCL_ERRSTS1 (bit31 -24)							
	HSNSTS0_ERR	HSNSTS1_ERR	Reserved	Reserved	Reserved	DST1_HSN_TOV	DST2_HSN_TOV	Reserved
59	LR_MF1_FCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_MF1_FCL_ERRSTS1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_MF1_FCL_ERRSTS1 (bit07 -00)							
	Reserved	Reserved	SLRC_ERR	FLRC_ERR	LA_ERR	TLRC_ERR	Reserved	PLRC_ERR
5C	LR_MF1_FCL_ERRSTS2 (bit31 -24)							
	ST_PERR	DEC_ERR	ENC_ERR	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR
5D	LR_MF1_FCL_ERRSTS2 (bit23 -16)							
	SDT_PERR	IDT_DCNT_PERR	CMR_DCNT_PERR	CMW_DCNT1_PERR	CMW_DCNT0_PERR	HTX_DCNT1_PERR	HTX_DCNT0_PERR	XFRINF_PERR
5E	LR_MF1_FCL_ERRSTS2 (bit15 -08)							
	SBK_CNT2_PERR	LEN_PERR	SBKLEN_PERR	IBFLEN_PERR	CKDLEN_PERR	SBKCNT_PERR	ADR2_PERR	ADR1_PERR
5F	LR_MF1_FCL_ERRSTS2 (bit07 -00)							
	BOP_PERR	SOP_PERR	FBADL_PERR	CKDDL_PERR	PRM3_PERR	PRM2_PERR	PRM1_PERR	PRM0_PERR

Byte27 = 84 (CHK4) Byte40-7F Detail

(14) Byte28 = 0E (3/4)

	0	1	2	3	4	5	6	7
60	LR_MF1_CCL_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR_MF1_CCL_ERRSTS0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	DXBF_STS_ERR	Reserved	DXRD_TOV
62	LR_MF1_CCL_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_MF1_CCL_ERRSTS0 (bit07 -00)							
	PLRC_ERR	SBCRC_ERR	TLRC_ERR	DXP_ERR4	DXP_ERR3	DXP_ERR2	DXP_ERR1	FC_ERR
64	LR_MF1_CCL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_MF1_CCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
66	LR_MF1_CCL_ERRSTS1 (bit15 -08)							
	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR	SDT_PERR	DXRD_DCNT_PERR	DTX_DCNT_PERR
67	LR_MF1_CCL_ERRSTS1 (bit07 -00)							
	XFRINF_PERR	IR_LEN_PERR	IW_LEN_PERR	DXADR_PERR	PKTCNT_PERR	CCNT_PERR	PRM1_PERR	PRM0_PERR
68	LR_MF1_ICL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
69	LR_MF1_ICL_ERRSTS1 (bit23 -16)							
	Reserved	HW_CNT_PERR	GNT_CNT_PERR	GNT_TOV	F_CURLEN2_PERR	FIW_TLRC2_PERR	C_CURLEN2_PERR	CIW_TLRC2_PERR
6A	LR_MF1_ICL_ERRSTS1 (bit15 -08)							
	C_DXWLEN_PERR	C_RENLEN_PERR	CIR_TLRC_PERR	CIW_TLRC_PERR	C_CURLEN_PERR	C_REQLEN2_PERR	C_REQLEN_PERR	C_SETLEN_PERR
6B	LR_MF1_ICL_ERRSTS1 (bit07 -00)							
	Reserved	F_RENLEN_PERR	FIR_TLRC_PERR	FIW_TLRC_PERR	F_CURLEN_PERR	F_REQLEN2_PERR	F_REQLEN_PERR	F_SETLEN_PERR
6C	LR_MF2_FCL_ERRSTS1 (bit31 -24)							
	HSNSTS0_ERR	HSNSTS1_ERR	Reserved	Reserved	Reserved	DST1_HSN_TOV	DST2_HSN_TOV	Reserved
6D	LR_MF2_FCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR_MF2_FCL_ERRSTS1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR_MF2_FCL_ERRSTS1 (bit07 -00)							
	Reserved	Reserved	SLRC_ERR	FLRC_ERR	LA_ERR	TLRC_ERR	Reserved	PLRC_ERR

Byte27 = 84 (CHK4) Byte40-7F Detail

(14) Byte28 = 0E (4/4)

	0	1	2	3	4	5	6	7
70	LR_MF2_FCL_ERRSTS2 (bit31 -24)							
	ST_PERR	DEC_ERR	ENC_ERR	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR
71	LR_MF2_FCL_ERRSTS2 (bit23 -16)							
	SDT_PERR	IDT_DCNT_PERR	CMR_DCNT_PERR	CMW_DCNT1_PERR	CMW_DCNT0_PERR	HTX_DCNT1_PERR	HTX_DCNT0_PERR	XFRINF_PERR
72	LR_MF2_FCL_ERRSTS2 (bit15 -08)							
	SBK_CNT2_PERR	LEN_PERR	SBKLEN_PERR	IBFLEN_PERR	CKDLEN_PERR	SBKCNT_PERR	ADR2_PERR	ADR1_PERR
73	LR_MF2_FCL_ERRSTS2 (bit07 -00)							
	BOP_PERR	SOP_PERR	FBADL_PERR	CKDDL_PERR	PRM3_PERR	PRM2_PERR	PRM1_PERR	PRM0_PERR
74	LR_MF2_CCL_ERRSTS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR_MF2_CCL_ERRSTS0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	DXBF_STS_ERR	Reserved	DXRD_TOV
76	LR_MF2_CCL_ERRSTS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR_MF2_CCL_ERRSTS0 (bit07 -00)							
	PLRC_ERR	SBCRC_ERR	TLRC_ERR	DXP_ERR4	DXP_ERR3	DXP_ERR2	DXP_ERR1	FC_ERR
78	LR_MF2_CCL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_MF2_CCL_ERRSTS1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
7A	LR_MF2_CCL_ERRSTS1 (bit15 -08)							
	Reserved	XFR_TOV	GNT_TOV	XFR_CNT_PERR	GNT_CNT_PERR	SDT_PERR	DXRD_DCNT_PERR	DTX_DCNT_PERR
7B	LR_MF2_CCL_ERRSTS1 (bit07 -00)							
	XFRINF_PERR	IR_LEN_PERR	IW_LEN_PERR	DXADR_PERR	PKTCNT_PERR	CCNT_PERR	PRM1_PERR	PRM0_PERR
7C	LR_MF2_ICL_ERRSTS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ST_PERR	DEC_ERR	ENC_ERR
7D	LR_MF2_ICL_ERRSTS1 (bit23 -16)							
	Reserved	HW_CNT_PERR	GNT_CNT_PERR	GNT_TOV	F_CURLEN2_PERR	FIW_TLRC2_PERR	C_CURLEN2_PERR	CIW_TLRC2_PERR
7E	LR_MF2_ICL_ERRSTS1 (bit15 -08)							
	C_DXWLEN_PERR	C_RENLEN_PERR	CIR_TLRC_PERR	CIW_TLRC_PERR	C_CURLEN_PERR	C_REQLEN2_PERR	C_REQLEN_PERR	C_SETLEN_PERR
7F	LR_MF2_ICL_ERRSTS1 (bit07 -00)							
	Reserved	F_RENLEN_PERR	FIR_TLRC_PERR	FIW_TLRC_PERR	F_CURLEN_PERR	F_REQLEN2_PERR	F_REQLEN_PERR	F_SETLEN_PERR

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Byte27 = 84 (CHK4) Byte40-7F Detail

(15) Byte28 = 0F (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_STATUS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	INT_SWIF_CHK1B
41	LR_INT_STATUS1 (bit23 -16)							
	Reserved	Reserved	INT_PARB1REG_CHK1B	INT_PARB1_CHK1B	Reserved	Reserved	INT_PARB0REG_CHK1B	INT_PARB0_CHK1B
42	LR_INT_STATUS1 (bit15 -08)							
	Reserved	Reserved	INT_HARB3REG_CHK1B_P	INT_HARB3_CHK1B_P	Reserved	Reserved	INT_HARB2REG_CHK1B_P	INT_HARB2_CHK1B_P
43	LR_INT_STATUS1 (bit07 -00)							
	Reserved	Reserved	INT_HARB1REG_CHK1B_P	INT_HARB1_CHK1B_P	Reserved	Reserved	INT_HARB0REG_CHK1B_P	INT_HARB0_CHK1B_P
44	LR_INT_STATUS11 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_INT_STATUS11 (bit23 -16)							
	Reserved	Reserved	Reserved	INT_MFPSIF_CHK4_P	Reserved	Reserved	INT_MFDMA1_CHK4_P	INT_MFDMA0_CHK4_P
46	LR_INT_STATUS11 (bit15 -08)							
	Reserved	Reserved	INT_HARB3REG_CHK4_P	INT_HARB3_CHK4_P	Reserved	Reserved	INT_HARB2REG_CHK4_P	INT_HARB2_CHK4_P
47	LR_INT_STATUS11 (bit07 -00)							
	Reserved	Reserved	INT_HARB1REG_CHK4_P	INT_HARB1_CHK4_P	Reserved	Reserved	INT_HARB0REG_CHK4_P	INT_HARB0_CHK4_P
48	LR_HARB_RX_PCK_ERR0 (bit31 -24)							
	Reserved	RXSEQ0_QENCERR	RXSEQ0_QPERR	PCI_ECC_DETECT	RXBLK_ECRCERR	Reserved	RXHDBUF_WTPT_PERR	Reserved
49	LR_HARB_RX_PCK_ERR0 (bit23 -16)							
	RXNDBUF_WTPT_PERR	RXHDX_PERR	RXDTWTADR_PERR	RXSEQ0_TCNT_PERR	FCCTL_NDCNT_PERR	FCCTL_NHCNT_PERR	FCCTL_QENCERR	FCCTL_QPERR
4A	LR_HARB_RX_PCK_ERR0 (bit15 -08)							
	RXRAA_PERR	RXRBA_PERR	Reserved	Reserved	RX_PERR	RXNDBUF_INGPERR	RXDTIX_DTB_USYPERR	RXDTIX_WTADRPERR
4B	LR_HARB_RX_PCK_ERR0 (bit07 -00)							
	TAGxTMR_SEQ_PERR	TAGxTMR_ENCERR	TAGxTMR_GPERR	Reserved	RX_PCK_CHK4TOP	RX_PCK_CHK1TOP	RX_PCK_WRN_TOP	Reserved
4C	LR_HARB_TX_PCK_ERR0 (bit31 -24)							
	TXHD_RDPT_PERR	TXNH_RDPT_PERR	TXNH_WTP_T_PERR	TXND_RDPT_PERR	BETY_ERR	CDETY_ERR	CHETY_ERR	NDETY_ERR
4D	LR_HARB_TX_PCK_ERR0 (bit23 -16)							
	NHETY_FRCERR	PDETY_FRCERR	PHETY_FRCERR	QETY_FRCERR	LINKERR	TXSEQ0_QEERR	TXSEQ0_QPERR	Reserved
4E	LR_HARB_TX_PCK_ERR0 (bit15 -08)							
	LINK_SPDERR	LINK_WIDT_HERR	Reserved	TXDTBUF_RDADR_PERR	TXHDOX_PERR	TX0ERR_LT	TXNHIX_PERR	TXNHOX_PERR
4F	LR_HARB_TX_PCK_ERR0 (bit07 -00)							
	LINKERR	TXNDBUF_WTPT_PERR	STOPREQ_PERR	TXERR_GPERR	TXSEQ0_TCNT_PERR	Reserved	XFBCNT_GPERR	XFQCNT_PERR

SSBLOG05-1210

Byte27 = 84 (CHK4) Byte40-7F Detail

(15) Byte28 = 0F (2/4)

	0	1	2	3	4	5	6	7
50	LR_HARB_TX_PCK_ERR1 (bit31 -24)							
	Reserved	TXHDDT_ APERR	TXHDDT_ EPERR	Reserved	ICNT_ NPPERR	NPHDBUF_ PERR	TXDTBUF_ PERR	TXNDBUF_ PERR
51	LR_HARB_TX_PCK_ERR1 (bit23 -16)							
	CHCNT_ PERR	NHCNT_ PERR	PHCNT_ PERR	CDCNT_ PERR	NDCNT_ PERR	PDCNT_ PERR	TXDTBUF_ ADREERR	TXDTBUF_ UNCERR
52	LR_HARB_TX_PCK_ERR1 (bit15 -08)							
	FCTMR_ SEQ_PERR	FCTMR_ SEQ_ ENCERR	FCTMR_ GPERR	FCTMR_ TOERR	NP_FCTMR_ SEQ_ PERR	NP_FCTMR_ SEQ_ ENCERR	NP_FCTMR_ GPERR	NP_FCTMR_ TOERR
53	LR_HARB_TX_PCK_ERR1 (bit07 -00)							
	TRATMR_ GPERR	TRATMR_ SEQ_ ENCERR	TRATMR_ SEQ_ PERR	TRATMR_ TOERR	TX_PCK_ CHK4TOP	TX_PCK_ CHK1TOP	TX_PCK_ WRN_TOP	Reserved
54	LR_HARB_RX_UCK_ERR0 (bit31 -24)							
	RXHDBUF_ RDPT_PERR	RXDDBUF_ ADREERR	RXDDBUF_ UNCOR	RXNHBUF_ RDPT_PERR	RXNDBUF_ RDPT_PERR	RXDTRDADR_ PERR	RXHDOX_ PERR	Reserved
55	LR_HARB_RX_UCK_ERR0 (bit23 -16)							
	RXNHGX_ PERR	RXNHIX_ PERR	TKCNT3_ PERR	RXNDOX_ PERR	Reserved	Reserved	SWTX_ DECERR	SWTX_ DLRC_GERR
56	LR_HARB_RX_UCK_ERR0 (bit15 -08)							
	SWTX_DT_ L_GPERR	SWTX_ ENCERR	SWTX_ HLRC_GERR	Reserved	SWTX_ TOCNT_ GPERR	SWTX_ TOCNT_TO	SWTX_ WDCNT_ GPERR	Reserved
57	LR_HARB_RX_UCK_ERR0 (bit07 -00)							
	ARBSEQ_ QDECERR	ARBSEQ_ QENCERR	ARBSEQ_ QPERR	RXNHBUF_ WTPT_PERR	MRG_ECC_ ADREERR	MRG_ECC_ UNCOR	MRGBUFWT_ ADREERR	MRGBUFRD_ ADREERR
58	LR_HARB_RX_UCK_ERR1 (bit31 -24)							
	MRGBUFD_ CNT_PERR	MRGBUFTMR_ PERR	Reserved	TKCNT1_ PERR	TKCNT2_ PERR	RXNHBUF_ OVER	TKCNT_ QENCERR	TKCNT_ QPERR
59	LR_HARB_RX_UCK_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_HARB_RX_UCK_ERR1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_HARB_RX_UCK_ERR1 (bit07 -00)							
	RG_SEQ_ QENCERR	RG_SEQ_ QPERR	RG_TMR_ PERR	Reserved	Reserved	RX_UCK_ CHK4TOP	RX_UCK_ CHK1TOP	RX_UCK_ WRN_TOP
5C	LR_HARB_TX_UCK_ERR0 (bit31 -24)							
	SWRX_DEC_ ERR	SWRX_ DLRCTIM_ GERR	SWRX_DLRC_ GERR	SWRX_ ENCERR	SWRX_HLRC_ GERR	SWRX_SW_ DT_GPERR	SWRX_SW_ VLD_GERR	SWRX_ TOCNT_ GPERR
5D	LR_HARB_TX_UCK_ERR0 (bit23 -16)							
	SWRX_ TOCNT_TO	SWRX_TX_ ERR_GERR	SWRX_ WDCNT_ GPERR	TXHDBUF_ WTPT_PERR	CDV_RDPT_ PERR	CDV_WTPT_ PERR	MCPICNT_ PERR	MRGICNT_ PERR
5E	LR_HARB_TX_UCK_ERR0 (bit15 -08)							
	TXHDBUF_ HDIN_ GPERR	TXDDBUF_ WTADR_ PERR	TXHDIX_ PERR	LEN_OVER	TXNHCNT_ PERR	SWRX_ REQOFFERR	SWRX_SW_ HD_PERR	DATCNT_ PERR
5F	LR_HARB_TX_UCK_ERR0 (bit07 -00)							
	DIV_ECC_ ADREERR	DIV_ECC_ CRCTERR	DIV_ECC_ UNCOR	CPLSEQ_ QDECERR	CPLSEQ_ QENCERR	CPLSEQ_ QPERR	MRGBUFWT_ ADREERR	MRGBUFRD_ ADREERR

SSBLOG05-1220

Byte27 = 84 (CHK4) Byte40-7F Detail

(15) Byte28 = 0F (3/4)

	0	1	2	3	4	5	6	7
60	LR_HARB_TX_UCK_ERR1 (bit31 -24)							
	Reserved	CPLSEQDTC NT_PERR	TKCNT1_ PERR	TKCNT2_ PERR	TKCNT_ QENCERR	TKCNT_ QPERR	TXDTIX_ DTBUSYPERR	TXDTIX_ WTADRPERR
61	LR_HARB_TX_UCK_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_HARB_TX_UCK_ERR1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_HARB_TX_UCK_ERR1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	TX_UCK_ CHK4TOP	TX_UCK_ CHK1TOP	TX_UCK_ WRN_TOP	Reserved
64	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(15) Byte28 = 0F (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1240

Byte27 = 84 (CHK4) Byte40-7F Detail

(16) Byte28 = 10 (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_CHK3_STATUS000 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_INT_CHK3_STATUS000 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_INT_CHK3_STATUS000 (bit15 -08)							
	Reserved	Reserved	INT_REG2_CHK3_INT_P	INT_REG2_CHK3_P	INT_REG1_CHK3_INT_P	INT_REG1_CHK3_P	INT_REG0_CHK3_INT_P	INT_REG0_CHK3_P
43	LR_INT_CHK3_STATUS000 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	INT_XD_CHK3_INT_P	INT_XD_CHK3_P	INT_DXC1_CHK3_DRAM_P	INT_DXC0_CHK3_DRAM_P
44	LR_XREG_MERRLOG_MP0000 (bit31 -24)							
	RAIF_RGARB_CMP	RAIF_TIMEOUT_WT	RAIF_TIMEOUT_RD	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_XREG_MERRLOG_MP0000 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_XREG_MERRLOG_MP0000 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_XREG_MERRLOG_MP0000 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR_XREG_MERRLOG_MP0000 (bit31 -24)							
	RAIF_RGARB_CMP	RAIF_TIMEOUT_WT	RAIF_TIMEOUT_RD	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_XREG_MERRLOG_MP0000 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_XREG_MERRLOG_MP0000 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	LR_XREG_MERRLOG_MP0000 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR_XREG_HARX_MERR ALOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_XREG_HARX_MERR ALOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_XREG_HARX_MERR ALOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
4F	LR_XREG_HARX_MERR ALOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY_8B	REG4B_BOUNDARY_4B	LENOVR_8B	REG4B_LENOVER_4B

SSBLOG05-1250

Byte27 = 84 (CHK4) Byte40-7F Detail

(16) Byte28 = 10 (2/4)

	0	1	2	3	4	5	6	7
50	LR_XREG_HARX_MERR_FLOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_XREG_HARX_MERR_FLOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR_XREG_HARX_MERR_FLOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
53	LR_XREG_HARX_MERR_FLOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY_8B	REG4B_BOUNDARY_4B	LENOVR_8B	REG4B_LENOVER_4B
54	LR_XREG_HARX_MERR ALOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_XREG_HARX_MERR ALOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_XREG_HARX_MERR ALOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
57	LR_XREG_HARX_MERR ALOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY_8B	REG4B_BOUNDARY_4B	LENOVR_8B	REG4B_LENOVER_4B
58	LR_XREG_HARX_MERR_FLOG (bit31 -24)							
	PCI_BE_ERR	PCI_BYTCNT_PERR	PCI_LADR_PERR	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR_XREG_HARX_MERR_FLOG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_XREG_HARX_MERR_FLOG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	FMT_TYP	BE0	MDL_OVER
5B	LR_XREG_HARX_MERR_FLOG (bit07 -00)							
	Reserved	BYTEACC	LEN_OVER	LEN4KB	BOUNDARY_8B	REG4B_BOUNDARY_4B	LENOVR_8B	REG4B_LENOVER_4B
5C	LR_XREG_MERR_HDLOG0000_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
5D	LR_XREG_MERR_HDLOG0000_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
5E	LR_XREG_MERR_HDLOG0000_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
5F	LR_XREG_MERR_HDLOG0000_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)

SSBLOG05-1260

Byte27 = 84 (CHK4) Byte40-7F Detail

(16) Byte28 = 10 (3/4)

	0	1	2	3	4	5	6	7
60	LR_XREG_MERR_HDLOG0001_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
61	LR_XREG_MERR_HDLOG0001_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
62	LR_XREG_MERR_HDLOG0001_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
63	LR_XREG_MERR_HDLOG0001_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
64	LR_XREG_MERR_HDLOG0002_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
65	LR_XREG_MERR_HDLOG0002_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
66	LR_XREG_MERR_HDLOG0002_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
67	LR_XREG_MERR_HDLOG0002_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
68	LR_XREG_MERR_HDLOG0003_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
69	LR_XREG_MERR_HDLOG0003_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
6A	LR_XREG_MERR_HDLOG0003_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
6B	LR_XREG_MERR_HDLOG0003_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
6C	LR_XREG_MERR_HDLOG0000_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
6D	LR_XREG_MERR_HDLOG0000_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
6E	LR_XREG_MERR_HDLOG0000_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
6F	LR_XREG_MERR_HDLOG0000_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)

SSBLOG05-1270

Byte27 = 84 (CHK4) Byte40-7F Detail

(16) Byte28 = 10 (4/4)

	0	1	2	3	4	5	6	7
70	LR_XREG_MERR_HDLOG0001_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
71	LR_XREG_MERR_HDLOG0001_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
72	LR_XREG_MERR_HDLOG0001_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
73	LR_XREG_MERR_HDLOG0001_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
74	LR_XREG_MERR_HDLOG0002_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
75	LR_XREG_MERR_HDLOG0002_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
76	LR_XREG_MERR_HDLOG0002_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
77	LR_XREG_MERR_HDLOG0002_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
78	LR_XREG_MERR_HDLOG0003_3 (bit31 -24)							
	HED LOG (31)	HED LOG (30)	HED LOG (29)	HED LOG (28)	HED LOG (27)	HED LOG (26)	HED LOG (25)	HED LOG (24)
79	LR_XREG_MERR_HDLOG0003_3 (bit23 -16)							
	HED LOG (23)	HED LOG (22)	HED LOG (21)	HED LOG (20)	HED LOG (19)	HED LOG (18)	HED LOG (17)	HED LOG (16)
7A	LR_XREG_MERR_HDLOG0003_3 (bit15 -08)							
	HED LOG (15)	HED LOG (14)	HED LOG (13)	HED LOG (12)	HED LOG (11)	HED LOG (10)	HED LOG (9)	HED LOG (8)
7B	LR_XREG_MERR_HDLOG0003_3 (bit07 -00)							
	HED LOG (7)	HED LOG (6)	HED LOG (5)	HED LOG (4)	HED LOG (3)	HED LOG (2)	HED LOG (1)	HED LOG (0)
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(17) Byte28 = 11 (1/4)

	0	1	2	3	4	5	6	7
40	LR_XD_CTL_CHK3_MONI0 (bit31 -24)							
	MP31	MP30	MP29	MP28	MP27	MP26	MP25	MP24
41	LR_XD_CTL_CHK3_MONI0 (bit23 -16)							
	MP23	MP22	MP21	MP20	MP19	MP18	MP17	MP16
42	LR_XD_CTL_CHK3_MONI0 (bit15 -08)							
	MP15	MP14	MP13	MP12	MP11	MP10	MP9	MP8
43	LR_XD_CTL_CHK3_MONI0 (bit07 -00)							
	MP7	MP6	MP5	MP4	MP3	MP2	MP1	MP0
44	LR_XD_CTL_CHK3_MONI1 (bit31 -24)							
	MP63	MP62	MP61	MP60	MP59	MP58	MP57	MP56
45	LR_XD_CTL_CHK3_MONI1 (bit23 -16)							
	MP55	MP54	MP53	MP52	MP51	MP50	MP49	MP48
46	LR_XD_CTL_CHK3_MONI1 (bit15 -08)							
	MP47	MP46	MP45	MP44	MP43	MP42	MP41	MP40
47	LR_XD_CTL_CHK3_MONI1 (bit07 -00)							
	MP39	MP38	MP37	MP36	MP35	MP34	MP33	MP32
48	LR_XD_CTL_CHK3_MONI2 (bit31 -24)							
	MP95	MP94	MP93	MP92	MP91	MP90	MP89	MP88
49	LR_XD_CTL_CHK3_MONI2 (bit23 -16)							
	MP87	MP86	MP85	MP84	MP83	MP82	MP81	MP80
4A	LR_XD_CTL_CHK3_MONI2 (bit15 -08)							
	MP79	MP78	MP77	MP76	MP75	MP74	MP73	MP72
4B	LR_XD_CTL_CHK3_MONI2 (bit07 -00)							
	MP71	MP70	MP69	MP68	MP67	MP66	MP65	MP64
4C	LR_XD_CTL_CHK3_MONI3 (bit31 -24)							
	MP127	MP126	MP125	MP124	MP123	MP122	MP121	MP120
4D	LR_XD_CTL_CHK3_MONI3 (bit23 -16)							
	MP119	MP118	MP117	MP116	MP115	MP114	MP113	MP112
4E	LR_XD_CTL_CHK3_MONI3 (bit15 -08)							
	MP111	MP110	MP109	MP108	MP107	MP106	MP105	MP104
4F	LR_XD_CTL_CHK3_MONI3 (bit07 -00)							
	MP103	MP102	MP101	MP100	MP99	MP98	MP97	MP96

SSBLOG05-1290

Byte27 = 84 (CHK4) Byte40-7F Detail

(17) Byte28 = 11 (2/4)

	0	1	2	3	4	5	6	7
50	LR_XD_DTX_TLOG_A0 (bit31 -24)							
	TRASH_LOG_A0	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_XD_DTX_TLOG_A0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR_XD_DTX_TLOG_A0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR_XD_DTX_TLOG_A0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR_XD_DTX_TLOG_A1 (bit31 -24)							
	TRASH_LOG_A1	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_XD_DTX_TLOG_A1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_XD_DTX_TLOG_A1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_XD_DTX_TLOG_A1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR_XD_DTX_TLOG_A2 (bit31 -24)							
	TRASH_LOG_A2	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR_XD_DTX_TLOG_A2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_XD_DTX_TLOG_A2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_XD_DTX_TLOG_A2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_XD_DTX_TLOG_A3 (bit31 -24)							
	TRASH_LOG_A3	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR_XD_DTX_TLOG_A3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR_XD_DTX_TLOG_A3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR_XD_DTX_TLOG_A3 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1300

Byte27 = 84 (CHK4) Byte40-7F Detail

(17) Byte28 = 11 (3/4)

	0	1	2	3	4	5	6	7
60	LR_XD_DTX_TLOG_A4 (bit31 -24)							
	TRASH_LOG_A4	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR_XD_DTX_TLOG_A4 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_XD_DTX_TLOG_A4 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_XD_DTX_TLOG_A4 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR_XD_DTX_TLOG_A5 (bit31 -24)							
	TRASH_LOG_A5	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_XD_DTX_TLOG_A5 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR_XD_DTX_TLOG_A5 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR_XD_DTX_TLOG_A5 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR_XD_DTX_TLOG_A6 (bit31 -24)							
	TRASH_LOG_A6	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR_XD_DTX_TLOG_A6 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR_XD_DTX_TLOG_A6 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR_XD_DTX_TLOG_A6 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	LR_XD_DTX_TLOG_A7 (bit31 -24)							
	TRASH_LOG_A7	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR_XD_DTX_TLOG_A7 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR_XD_DTX_TLOG_A7 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	LR_XD_DTX_TLOG_A7 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1310

Byte27 = 84 (CHK4) Byte40-7F Detail

(17) Byte28 = 11 (4/4)

	0	1	2	3	4	5	6	7
70	LR_XD_RREG4_CHK3_INT (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR_XD_RREG4_CHK3_INT (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	LR_XD_RREG4_CHK3_INT (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR_XD_RREG4_CHK3_INT (bit07 -00)							
	WSETERR	NOHITADRS TOP	DRXLRC	ERXLRC	PNPTO	RCHKS	WCHKS	RREG WR NOHIT
74	LR_XD_RREG4_CHK3_STS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR_XD_RREG4_CHK3_STS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	LR_XD_RREG4_CHK3_STS (bit15 -08)							
	Reserved	EP	CPLSTS	ID	LEN	REQID	NEQCPLD	NEQTAG
77	LR_XD_RREG4_CHK3_STS (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	CADIS	CPLTO	IX	NOHITADR
78	LR_XD_RREG4_SEQSTS (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_XD_RREG4_SEQSTS (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	LR_XD_RREG4_SEQSTS (bit15 -08)							
	ST_N	ST_N	ST_N	ST_N	ST_N	ST_N	ST_N	ST_N
7B	LR_XD_RREG4_SEQSTS (bit07 -00)							
	ST	ST	ST	ST	ST	ST	ST	ST
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1320

Byte27 = 84 (CHK4) Byte40-7F Detail

(18) Byte28 = 12 (1/4)

	0	1	2	3	4	5	6	7
40	LR_LRP_CTL (bit31 -24)							
	START	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_LRP_CTL (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_LRP_CTL (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_LRP_CTL (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	LR_LRP_STS (bit31 -24)							
	ACT	ERRSTP	Reserved	ERR	WRN	Reserved	DGWAIT	DGPASS
45	LR_LRP_STS (bit23 -16)							
	SEQ (07:00)							
46	LR_LRP_STS (bit15 -08)							
	Now ADR (15:08)							
47	LR_LRP_STS (bit07 -00)							
	Now ADR (07:00)							
48	LR_LRP_ERR0 (bit31 -24)							
	ALUCMPERR	SEQEERR	TIMOUT	OJUMP	TIMERPERR	X2PERR	X1PERR	X0PERR
49	LR_LRP_ERR0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_LRP_ERR0 (bit15 -08)							
	Reserved	Reserved	CSADECC	CSUCECC	CSODTP	CSOADP	CSIDTP	CSIADP
4B	LR_LRP_ERR0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LR_LRP_ERR1 (bit31 -24)							
	DSO_LRP	DSO_DMA	DSO_XR	DSO_PPCI	Reserved	DSI_LRP	DSI_XR	DSI_PPCI
4D	LR_LRP_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_LRP_ERR1 (bit15 -08)							
	DSOAW3	DSOAW2	DSOAW1	DSOAW0	DSOUNCW3	DSOUNCW2	DSOUNCW1	DSOUNCW0
4F	LR_LRP_ERR1 (bit07 -00)							
	DSOADP	DSODTP	Reserved	DSIADP	DSIDTPW3	DSIDTPW2	DSIDTPW1	DSIDTPW0

SSBLOG05-1330

Byte27 = 84 (CHK4) Byte40-7F Detail

(18) Byte28 = 12 (2/4)

	0	1	2	3	4	5	6	7
50	LR_LRP_PCIRX_ERR (bit31 -24)							
	Reserved	Reserved	FPCI_RX_WOV_ERR	FPCI_RX_DLRC_ERR	FPCI_RX_BE_ERR	FPCI_RX_MEMDPERR	FPCI_RX_ECCERR	FPCI_RX_ADRECCERR
51	LR_LRP_PCIRX_ERR (bit23 -16)							
	Reserved	Reserved	FPCI_RX_RAMOPERR	FPCI_RX_RAMINPERR	FPCI_RX_TXADRPERR	Reserved	FPCI_RX_SEQDECERR	FPCI_RX_RDTOERR
52	LR_LRP_PCIRX_ERR (bit15 -08)							
	Reserved	FPCI_RX14_REQ	FPCI_RX13_VLD	FPCI_RX12_DLRCTIM	FPCI_RX11_PCCNT	FPCI_RX10_NPCNT	FPCI_RX9_HED	FPCI_RX8_DLRC_NEQ
53	LR_LRP_PCIRX_ERR (bit07 -00)							
	FPCI_RX7_HLRC_NEQ	FPCI_RX6_DT	FPCI_RX5_TOCNT	FPCI_RX4_TOCNT_TO	FPCI_RX3_WDCNT	FPCI_RX2_ENC_ERR	FPCI_RX1_DEC_ERR	Reserved
54	LR_LRP_PCITX_ERR (bit31 -24)							
	EN_ECCWRN	Reserved	EN_WOVERR	EN_DLRCERR	EN_BEERR	EN_MEMDPERR	EN_ECCERR	EN_ADRECCERR
55	LR_LRP_PCITX_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_LRP_PCITX_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_LRP_PCITX_ERR (bit07 -00)							
	Reserved	FPCI_TX_LENERR	Reserved	FPCI_TX_H423PERR	Reserved	Reserved	FPCI_TX_RDDTPERR	FPCI_TX_RDADRPERR
58	LR_LRP_FCTL_ERR (bit31 -24)							
	CSLD_LRCERR	CSLD_XFRCNTERR2	CSLD_SEQERROR	CSLD_TOCNTPERR	CSLD_TIMEOUTERR	CSLD_XFRCNTPERR	CSLD_X2KCNTPERR	CSLD_CSADRPERR
59	LR_LRP_FCTL_ERR (bit23 -16)							
	CSLD_CSDTPERR	CSLD_CSREQB_ADRPERR	CSLD_CSREQ_ADRPERR	CSLD_DXCSDXBF_ERR	CSLD_DXCSDXBF_ERR	Reserved	Reserved	Reserved
5A	LR_LRP_FCTL_ERR (bit15 -08)							
	XRDS_RDDTERR	XRDS_ADRERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_LRP_FCTL_ERR (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_LRP_FIF_DXLRP_ERR (bit31 -24)							
	FIF_DXBF_RDCPLERR	FIF_DXBF_DTOUTPERR	FIF_DXBF_DTINPERR	FIF_DXBF_SEQOUT	FIF_DXBF_BEERR	FIF_DXBF_ADRERR	FIF_DXBF_SEQCLR2	FIF_DXBF_SEQCLR3
5D	LR_LRP_FIF_DXLRP_ERR (bit23 -16)							
	FIF_DXBF_TX7_HLRC_NEQ	FIF_DXBF_TX6_DT	FIF_DXBF_TX5_TOCNT	FIF_DXBF_TX4_TOCNT_TO	FIF_DXBF_TX3_WDCNT	FIF_DXBF_TX2_ENC_ERR	FIF_DXBF_TX1_DEC_ERR	FIF_DXBF_TX8_DLRC_NEQ
5E	LR_LRP_FIF_DXLRP_ERR (bit15 -08)							
	Reserved	FIF_DXBF_RX14_REQ	FIF_DXBF_RX13_VLD	FIF_DXBF_RX12_DLRCTIM	FIF_DXBF_RX11_PCCNT	FIF_DXBF_RX10_NPCNT	FIF_DXBF_RX9_HED	FIF_DXBF_RX8_DLRC_NEQ
5F	LR_LRP_FIF_DXLRP_ERR (bit07 -00)							
	FIF_DXBF_RX7_HLRC_NEQ	FIF_DXBF_RX6_DT	FIF_DXBF_RX5_TOCNT	FIF_DXBF_RX4_TOCNT_TO	FIF_DXBF_RX3_WDCNT	FIF_DXBF_RX2_ENC_ERR	FIF_DXBF_RX1_DEC_ERR	Reserved

SSBLOG05-1340

Byte27 = 84 (CHK4) Byte40-7F Detail

(18) Byte28 = 12 (3/4)

	0	1	2	3	4	5	6	7
60	LR_LRP_FIF_XRPCI_ERR (bit31 -24)							
	FIF_XR_AD ROUTPERR	FIF_XR_ DTOUTPERR	FIF_XR_ DTINPERR	FIF_XR_ SEQOUT	FIF_XR_ BEPERR	FIF_XR_ SEQCLR2	FIF_XR_ STSPERR	Reserved
61	LR_LRP_FIF_XRPCI_ERR (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_LRP_FIF_XRPCI_ERR (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_LRP_FIF_XRPCI_ERR (bit07 -00)							
	FIF_FOP_ ADRPERR	FIF_FOP_ WDTP	FIF_FOP_ WBEP	FIF_FOP_ RDTP	FIF_FOP_ WRSELERR	FIF_FOP_ RDSELERR	FIF_FOP_ WRCMPERR	FIF_FOP_ SEQSTERR
64	LR_LRPDMA_FDMA_ERR_STS0 (bit31 -24)							
	FDR ERR (7)	FDR ERR (6)	FDR ERR (5)	FDR ERR (4)	FDR ERR (3)	FDR ERR (2)	FDR ERR (1)	FDR ERR (0)
65	LR_LRPDMA_FDMA_ERR_STS0 (bit23 -16)							
	FCR ERR (7)	FCR ERR (6)	FCR ERR (5)	FCR ERR (4)	FCR ERR (3)	FCR ERR (2)	FCR ERR (1)	FCR ERR (0)
66	LR_LRPDMA_FDMA_ERR_STS0 (bit15 -08)							
	FBR ERR (7)	FBR ERR (6)	FBR ERR (5)	FBR ERR (4)	FBR ERR (3)	FBR ERR (2)	FBR ERR (1)	FBR ERR (0)
67	LR_LRPDMA_FDMA_ERR_STS0 (bit07 -00)							
	FAR ERR (7)	FAR ERR (6)	FAR ERR (5)	FAR ERR (4)	FAR ERR (3)	FAR ERR (2)	FAR ERR (1)	FAR ERR (0)
68	LR_LRPDMA_FDMA_ERR_STS1 (bit31 -24)							
	FAT ERR (7)	FAT ERR (6)	FAT ERR (5)	FAT ERR (4)	FAT ERR (3)	FAT ERR (2)	FAT ERR (1)	FAT ERR (0)
69	LR_LRPDMA_FDMA_ERR_STS1 (bit23 -16)							
	FAD ERR2 (7)	FAD ERR2 (6)	FAD ERR2 (5)	FAD ERR2 (4)	FAD ERR2 (3)	FAD ERR2 (2)	FAD ERR2 (1)	FAD ERR2 (0)
6A	LR_LRPDMA_FDMA_ERR_STS1 (bit15 -08)							
	FAD ERR (7)	FAD ERR (6)	FAD ERR (5)	FAD ERR (4)	FAD ERR (3)	FAD ERR (2)	FAD ERR (1)	FAD ERR (0)
6B	LR_LRPDMA_FDMA_ERR_STS1 (bit07 -00)							
	FAC ERR (7)	FAC ERR (6)	FAC ERR (5)	FAC ERR (4)	FAC ERR (3)	FAC ERR (2)	FAC ERR (1)	FAC ERR (0)
6C	LR_LRPDMA_FDMA_ERR_STS2 (bit31 -24)							
	FBT ERR (7)	FBT ERR (6)	FBT ERR (5)	FBT ERR (4)	FBT ERR (3)	FBT ERR (2)	FBT ERR (1)	FBT ERR (0)
6D	LR_LRPDMA_FDMA_ERR_STS2 (bit23 -16)							
	FBD ERR2 (7)	FBD ERR2 (6)	FBD ERR2 (5)	FBD ERR2 (4)	FBD ERR2 (3)	FBD ERR2 (2)	FBD ERR2 (1)	FBD ERR2 (0)
6E	LR_LRPDMA_FDMA_ERR_STS2 (bit15 -08)							
	FBD ERR (7)	FBD ERR (6)	FBD ERR (5)	FBD ERR (4)	FBD ERR (3)	FBD ERR (2)	FBD ERR (1)	FBD ERR (0)
6F	LR_LRPDMA_FDMA_ERR_STS2 (bit07 -00)							
	FBC ERR (7)	FBC ERR (6)	FBC ERR (5)	FBC ERR (4)	FBC ERR (3)	FBC ERR (2)	FBC ERR (1)	FBC ERR (0)

SSBLOG05-1350

Byte27 = 84 (CHK4) Byte40-7F Detail

(18) Byte28 = 12 (4/4)

	0	1	2	3	4	5	6	7
70	LR_LRPDMA_FDMA_ERR_STS3 (bit31 -24)							
	FCT ERR (7)	FCT ERR (6)	FCT ERR (5)	FCT ERR (4)	FCT ERR (3)	FCT ERR (2)	FCT ERR (1)	FCT ERR (0)
71	LR_LRPDMA_FDMA_ERR_STS3 (bit23 -16)							
	FCD ERR2 (7)	FCD ERR2 (6)	FCD ERR2 (5)	FCD ERR2 (4)	FCD ERR2 (3)	FCD ERR2 (2)	FCD ERR2 (1)	FCD ERR2 (0)
72	LR_LRPDMA_FDMA_ERR_STS3 (bit15 -08)							
	FCD ERR (7)	FCD ERR (6)	FCD ERR (5)	FCD ERR (4)	FCD ERR (3)	FCD ERR (2)	FCD ERR (1)	FCD ERR (0)
73	LR_LRPDMA_FDMA_ERR_STS3 (bit07 -00)							
	FCC ERR (7)	FCC ERR (6)	FCC ERR (5)	FCC ERR (4)	FCC ERR (3)	FCC ERR (2)	FCC ERR (1)	FCC ERR (0)
74	LR_LRPDMA_FDMA_ERR_STS4 (bit31 -24)							
	FDT ERR (7)	FDT ERR (6)	FDT ERR (5)	FDT ERR (4)	FDT ERR (3)	FDT ERR (2)	FDT ERR (1)	FDT ERR (0)
75	LR_LRPDMA_FDMA_ERR_STS4 (bit23 -16)							
	FDD ERR2 (7)	FDD ERR2 (6)	FDD ERR2 (5)	FDD ERR2 (4)	FDD ERR2 (3)	FDD ERR2 (2)	FDD ERR2 (1)	FDD ERR2 (0)
76	LR_LRPDMA_FDMA_ERR_STS4 (bit15 -08)							
	FDD ERR (7)	FDD ERR (6)	FDD ERR (5)	FDD ERR (4)	FDD ERR (3)	FDD ERR (2)	FDD ERR (1)	FDD ERR (0)
77	LR_LRPDMA_FDMA_ERR_STS4 (bit07 -00)							
	FDC ERR (7)	FDC ERR (6)	FDC ERR (5)	FDC ERR (4)	FDC ERR (3)	FDC ERR (2)	FDC ERR (1)	FDC ERR (0)
78	LR_LRPDMA_FDMA_ERR_STS5 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR_LRPDMA_FDMA_ERR_STS5 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	LR_LRPDMA_FDMA_ERR_STS5 (bit15 -08)							
	PAR ERR (7)	PAR ERR (6)	PAR ERR (5)	PAR ERR (4)	PAR ERR (3)	PAR ERR (2)	PAR ERR (1)	PAR ERR (0)
7B	LR_LRPDMA_FDMA_ERR_STS5 (bit07 -00)							
	CSD ERR (7)	CSD ERR (6)	CSD ERR (5)	CSD ERR (4)	CSD ERR (3)	CSD ERR (2)	CSD ERR (1)	CSD ERR (0)
7C	LR_LRPDMA_FDMA_ERR_STS6 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	LR_LRPDMA_FDMA_ERR_STS6 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	LR_LRPDMA_FDMA_ERR_STS6 (bit15 -08)							
	EAC ERR (15)	EAC ERR (14)	EAC ERR (13)	EAC ERR (12)	EAC ERR (11)	EAC ERR (10)	EAC ERR (9)	EAC ERR (8)
7F	LR_LRPDMA_FDMA_ERR_STS6 (bit07 -00)							
	EAC ERR (7)	EAC ERR (6)	EAC ERR (5)	EAC ERR (4)	EAC ERR (3)	EAC ERR (2)	EAC ERR (1)	EAC ERR (0)

SSBLOG05-1360

Byte27 = 84 (CHK4) Byte40-7F Detail

(19) Byte28 = 13 (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_STATUS1 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	INT_SWIF_CHK1B
41	LR_INT_STATUS1 (bit23 -16)							
	Reserved	Reserved	INT_PARB1REG_CHK1B	INT_PARB1_CHK1B	Reserved	Reserved	INT_PARB0REG_CHK1B	INT_PARB0_CHK1B
42	LR_INT_STATUS1 (bit15 -08)							
	Reserved	Reserved	INT_HARB3REG_CHK1B_P	INT_HARB3_CHK1B_P	Reserved	Reserved	INT_HARB2REG_CHK1B_P	INT_HARB2_CHK1B_P
43	LR_INT_STATUS1 (bit07 -00)							
	Reserved	Reserved	INT_HARB1REG_CHK1B_P	INT_HARB1_CHK1B_P	Reserved	Reserved	INT_HARB0REG_CHK1B_P	INT_HARB0_CHK1B_P
44	LR_INT_STATUS10 (bit31 -24)							
	FRC_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_INT_STATUS10 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_INT_STATUS10 (bit15 -08)							
	Reserved	INT_XD_CHK4(3)	Reserved	Reserved	INT_XD_CHK4(2)	Reserved	Reserved	Reserved
47	LR_INT_STATUS10 (bit07 -00)							
	Reserved	INT_XD_CHK4(1)	INT_PARB1REG_CHK4	INT_PARB1_CHK4	Reserved	INT_XD_CHK4(0)	INT_PARB0REG_CHK4	INT_PARB0_CHK4
48	LR_PARB_RX_PCK_ERR0 (bit31 -24)							
	Reserved	RXSEQ0_QENCERR	RXSEQ0_QPERR	PCI_ECC_DETECT	RXBLK_ECRCERR	Reserved	RXHDBUF_WTPT_PERR	Reserved
49	LR_PARB_RX_PCK_ERR0 (bit23 -16)							
	RXNDBUF_WTPT_PERR	RXHDX_PERR	RXDTWTADR_PERR	RXSEQ0_TCNT_PERR	FCCTL_NDCNT_PERR	FCCTL_NHCNT_PERR	FCCTL_QENCERR	FCCTL_QPERR
4A	LR_PARB_RX_PCK_ERR0 (bit15 -08)							
	RXRAA_PERR	RXRBA_PERR	Reserved	Reserved	RX_PERR	RXNDBUF_I_NGPERR	RXDTIX_DT_BUSYPERR	RXDTIX_W_TADRPERR
4B	LR_PARB_RX_PCK_ERR0 (bit07 -00)							
	TAGxTMR_SEQ_PERR	TAGxTMR_ENCERR	TAGxTMR_GPERR	Reserved	RX_PCK_CHK4TOP	RX_PCK_CHK1TOP	RX_PCK_WRN_TOP	Reserved
4C	LR_PARB_TX_PCK_ERR0 (bit31 -24)							
	TXHD_RDPT_PERR	TXNH_RDPT_PERR	TXNH_WTPT_PERR	TXND_RDPT_PERR	BETY_ERR	CDETY_ERR	CHETY_ERR	NDETY_ERR
4D	LR_PARB_TX_PCK_ERR0 (bit23 -16)							
	NHETY_FRCERR	PDETY_FRCERR	PHETY_FRCERR	QETY_FRCERR	LINKERR	TXSEQ0_QEERR	TXSEQ0_QPERR	Reserved
4E	LR_PARB_TX_PCK_ERR0 (bit15 -08)							
	LINK_SPDERR	LINK_WIDTHERR	Reserved	TXDTBUF_RDADR_PERR	TXHDOX_PERR	TX0ERR_LT	TXNHIX_PERR	TXNHOX_PERR
4F	LR_PARB_TX_PCK_ERR0 (bit07 -00)							
	LINKERR	TXNDBUF_WTPT_PERR	STOPREQ_PERR	TXERR_GPERR	TXSEQ0_TCNT_PERR	Reserved	XFBCNT_GPERR	XFQCNT_PERR

SSBLOG05-1370

Byte27 = 84 (CHK4) Byte40-7F Detail

(19) Byte28 = 13 (2/4)

	0	1	2	3	4	5	6	7
50	LR_PARB_TX_PCK_ERR1 (bit31 -24)							
	Reserved	TXHDDT_ APERR	TXHDDT_ EPERR	Reserved	ICNT_ NPPERR	NPHDBUF_ PERR	TXDTBUF_ PERR	TXNDBUF_ PERR
51	LR_PARB_TX_PCK_ERR1 (bit23 -16)							
	CHCNT_ PERR	NHCNT_ PERR	PHCNT_ PERR	CDCNT_ PERR	NDCNT_ PERR	PDCNT_ PERR	TXDTBUF_ ADREERR	TXDTBUF_ UNCERR
52	LR_PARB_TX_PCK_ERR1 (bit15 -08)							
	FCTMR_ SEQ_PERR	FCTMR_ SEQ_ ENCERR	FCTMR_ GPERR	FCTMR_ TOERR	NP_FCTMR_ SEQ_ PERR	NP_FCTMR_ SEQ_ ENCERR	NP_FCTMR_ GPERR	NP_FCTMR_ _TOERR
53	LR_PARB_TX_PCK_ERR1 (bit07 -00)							
	TRATMR_ GPERR	TRATMR_ SEQ_ ENCERR	TRATMR_ SEQ_ PERR	TRATMR_ TOERR	TX_PCK_ CHK4TOP	TX_PCK_ CHK1TOP	TX_PCK_ WRN_TOP	Reserved
54	LR_PARB_RX_UCK_ERR0 (bit31 -24)							
	RXHDBUF_ RDPT_PERR	RXDDBUF_ ADREERR	RXDDBUF_ UNCOR	RXNHBUF_ RDPT_PERR	RXNDBUF_ RDPT_PERR	RXDTRDADR_ PERR	RXHDOX_ PERR	Reserved
55	LR_PARB_RX_UCK_ERR0 (bit23 -16)							
	RXNHGX_ PERR	RXNHIX_ PERR	TKCNT3_ PERR	RXNDOX_ PERR	Reserved	Reserved	SWTX_ DECERR	SWTX_ DLRC_GERR
56	LR_PARB_RX_UCK_ERR0 (bit15 -08)							
	SWTX_ DT_L_GPERR	SWTX_ ENCERR	SWTX_ HLRC_GERR	Reserved	SWTX_ TOCNT_ GPERR	SWTX_ TOCNT_TO	SWTX_ WDCNT_ GPERR	Reserved
57	LR_PARB_RX_UCK_ERR0 (bit07 -00)							
	ARBSEQ_ QDECERR	ARBSEQ_ QENCERR	ARBSEQ_ QPERR	RXNHBUF_ WTPT_PERR	MRG_ECC_ ADREERR	MRG_ECC_ UNCOR	MRGBUFWT_ ADREERR	MRGBUFWD_ ADREERR
58	LR_PARB_RX_UCK_ERR1 (bit31 -24)							
	MRGBUFD_ CNT_PERR	MRGBUFTMR_ PERR	Reserved	TKCNT1_ PERR	TKCNT2_ PERR	RXNHBUF_ OVER	TKCNT_ QENCERR	TKCNT_ QPERR
59	LR_PARB_RX_UCK_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_PARB_RX_UCK_ERR1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_PARB_RX_UCK_ERR1 (bit07 -00)							
	RG_SEQ_ QENCERR	RG_SEQ_ QPERR	RG_TMR_ PERR	Reserved	Reserved	RX_UCK_ CHK4TOP	RX_UCK_ CHK1TOP	RX_UCK_ WRN_TOP
5C	LR_PARB_TX_UCK_ERR0 (bit31 -24)							
	SWRX_ DECERR	SWRX_ DLRC_TIM_ GERR	SWRX_ DLRC_GERR	SWRX_ ENCERR	SWRX_ HLRC_GERR	SWRX_SW_ DT_GPERR	SWRX_SW_ VLD_GERR	SWRX_ TOCNT_ GPERR
5D	LR_PARB_TX_UCK_ERR0 (bit23 -16)							
	SWRX_ TOCNT_TO	SWRX_TX_ ERR_GERR	SWRX_ WDCNT_ GPERR	TXHDBUF_ WTPT_PERR	CDV_RDPT_ PERR	CDV_WTPT_ PERR	MCPICNT_ PERR	MRGICNT_ PERR
5E	LR_PARB_TX_UCK_ERR0 (bit15 -08)							
	TXHDBUF_ HDIN_ GPERR	TXDDBUF_ WTADR_ PERR	TXHDIX_ PERR	LEN_OVER	TXNHCNT_ PERR	SWRX_ REQOFFERR	SWRX_SW_ HD_PERR	DATCNT_ PERR
5F	LR_PARB_TX_UCK_ERR0 (bit07 -00)							
	DIV_ECC_ ADREERR	DIV_ECC_ CRCTERR	DIV_ECC_ UNCOR	CPLSEQ_ QDECERR	CPLSEQ_ QENCERR	CPLSEQ_ QPERR	MRGBUFD_ ADREERR	MRGBUFD_ ADREERR

SSBLOG05-1380

Byte27 = 84 (CHK4) Byte40-7F Detail

(19) Byte28 = 13 (3/4)

	0	1	2	3	4	5	6	7
60	LR_PARB_TX_UCK_ERR1 (bit31 -24)							
	Reserved	CPLSEQDTCNT_PERR	TKCNT1_PERR	TKCNT2_PERR	TKCNT_QENCERR	TKCNT_QPERR	TXDTIX_DTBUSYPERR	TXDTIX_WTADRPERR
61	LR_PARB_TX_UCK_ERR1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_PARB_TX_UCK_ERR1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_PARB_TX_UCK_ERR1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	TX_UCK_CHK4TOP	TX_UCK_CHK1TOP	TX_UCK_WRN_TOP	Reserved
64	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(19) Byte28 = 13 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1400

Byte27 = 84 (CHK4) Byte40-7F Detail

(20) Byte28 = 14 (1/4)

	0	1	2	3	4	5	6	7
40	LR_XD_CTL_CHK4_MSG (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	LR_XD_CTL_CHK4_MSG (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	LR_XD_CTL_CHK4_MSG (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	LR_XD_CTL_CHK4_MSG (bit07 -00)							
	DEV1 FUNC3	DEV1 FUNC2	DEV1 FUNC1	DEV1 FUNC0	DEV0 FUNC3	DEV0 FUNC2	DEV0 FUNC1	DEV0 FUNC0
44	LR_XD_CTL_CHK4_TRASH (bit31 -24)							
	MOW_COT	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR_XD_CTL_CHK4_TRASH (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR_XD_CTL_CHK4_TRASH (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_XD_CTL_CHK4_TRASH (bit07 -00)							
	DEV1 FUNC3	DEV1 FUNC2	DEV1 FUNC1	DEV1 FUNC0	DEV0 FUNC3	DEV0 FUNC2	DEV0 FUNC1	DEV0 FUNC0
48	LR_XD_CTL_CHK4_MONI (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	LR_XD_CTL_CHK4_MONI (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	LR_XD_CTL_CHK4_MONI (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	M0C0TRASH
4B	LR_XD_CTL_CHK4_MONI (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	TR OWN MOD	MSG	Reserved
4C	LR_XD_DTX TLOG_B0 (bit31 -24)							
	TRASH_LOG _B0	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	LR_XD_DTX TLOG_B0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	LR_XD_DTX TLOG_B0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR_XD_DTX TLOG_B0 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1410

Byte27 = 84 (CHK4) Byte40-7F Detail

(20) Byte28 = 14 (2/4)

	0	1	2	3	4	5	6	7
50	LR_XD_DTX_TLOG_B1 (bit31 -24)							
	TRASH_LOG_B1	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_XD_DTX_TLOG_B1 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	LR_XD_DTX_TLOG_B1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR_XD_DTX_TLOG_B1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR_XD_DTX_TLOG_B2 (bit31 -24)							
	TRASH_LOG_B2	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_XD_DTX_TLOG_B2 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	LR_XD_DTX_TLOG_B2 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	LR_XD_DTX_TLOG_B2 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR_XD_DTX_TLOG_B3 (bit31 -24)							
	TRASH_LOG_B3	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	LR_XD_DTX_TLOG_B3 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	LR_XD_DTX_TLOG_B3 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	LR_XD_DTX_TLOG_B3 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	LR_XD_DTX_TLOG_B4 (bit31 -24)							
	TRASH_LOG_B4	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	LR_XD_DTX_TLOG_B4 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR_XD_DTX_TLOG_B4 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR_XD_DTX_TLOG_B4 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1420

Byte27 = 84 (CHK4) Byte40-7F Detail

(20) Byte28 = 14 (3/4)

	0	1	2	3	4	5	6	7
60	LR_XD_DTX_TLOG_B5 (bit31 -24)							
	TRASH_LOG_B5	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	LR_XD_DTX_TLOG_B5 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR_XD_DTX_TLOG_B5 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR_XD_DTX_TLOG_B5 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR_XD_DTX_TLOG_B6 (bit31 -24)							
	TRASH_LOG_B6	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	LR_XD_DTX_TLOG_B6 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	LR_XD_DTX_TLOG_B6 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	LR_XD_DTX_TLOG_B6 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	LR_XD_DTX_TLOG_B7 (bit31 -24)							
	TRASH_LOG_B7	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	LR_XD_DTX_TLOG_B7 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	LR_XD_DTX_TLOG_B7 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	LR_XD_DTX_TLOG_B7 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	LR_INT_STATUS10 (bit31 -24)							
	FRC_ERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	LR_INT_STATUS10 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	LR_INT_STATUS10 (bit15 -08)							
	Reserved	INT_XD_CHK4(3)	Reserved	Reserved	INT_XD_CHK4(2)	Reserved	Reserved	Reserved
6F	LR_INT_STATUS10 (bit07 -00)							
	Reserved	INT_XD_CHK4(1)	INT_PARB1REG_CHK4	INT_PARB1_CHK4	Reserved	INT_XD_CHK4(0)	INT_PARB0REG_CHK4	INT_PARB0_CHK4

SSBLOG05-1430

Byte27 = 84 (CHK4) Byte40-7F Detail

(20) Byte28 = 14 (4/4)

	0	1	2	3	4	5	6	7
70	LR_INT_LRP_STATUS0 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	LR_INT_LRP_STATUS0 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	LR_INT_LRP_STATUS0 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	PC1_TEMPER ATURE_ERR	PC0_TEMPER ATURE_ERR	PC1_ REPLACE_ SYS_ERR	PC0_ REPLACE_ SYS_ERR
73	LR_INT_LRP_STATUS0 (bit07 -00)							
	FC7_ SYS_ERR	FC6_ SYS_ERR	FC5_ SYS_ERR	FC4_ SYS_ERR	FC3_ SYS_ERR	FC2_ SYS_ERR	FC1_ SYS_ERR	FC0_ SYS_ERR
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1440

Byte27 = 84 (CHK4) Byte40-7F Detail

(21) Byte28 = 15 (1/4)

	0	1	2	3	4	5	6	7
40	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
41	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
42	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
43	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
44	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
45	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
46	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
47	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
48	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
49	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
4A	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
4B	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
4C	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
4D	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
4E	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
4F	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							

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Byte27 = 84 (CHK4) Byte40-7F Detail

(21) Byte28 = 15 (2/4)

	0	1	2	3	4	5	6	7
50	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
51	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
52	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
53	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
54	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
55	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
56	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
57	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
58	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
59	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
5A	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
5B	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
5C	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
5D	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
5E	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
5F	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							

Byte27 = 84 (CHK4) Byte40-7F Detail

(21) Byte28 = 15 (3/4)

	0	1	2	3	4	5	6	7
60	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
61	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
62	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
63	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
64	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
65	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
66	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
67	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
68	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
69	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
6A	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
6B	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
6C	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
6D	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
6E	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
6F	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							

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Byte27 = 84 (CHK4) Byte40-7F Detail

(21) Byte28 = 15 (4/4)

	0	1	2	3	4	5	6	7
70	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
71	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
72	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
73	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
74	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
75	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
76	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
77	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
78	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
79	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
7A	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
7B	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							
7C	LR_LRP_DSMEM (bit31 -24)							
	LR_LRP_DSMEM							
7D	LR_LRP_DSMEM (bit23 -16)							
	LR_LRP_DSMEM							
7E	LR_LRP_DSMEM (bit15 -08)							
	LR_LRP_DSMEM							
7F	LR_LRP_DSMEM (bit07 -00)							
	LR_LRP_DSMEM							

Byte27 = 84 (CHK4) Byte40-7F Detail

(22) Byte28 = 1F (1/4)

	0	1	2	3	4	5	6	7
40	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
42	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
43	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(22) Byte28 = 1F (2/4)

	0	1	2	3	4	5	6	7
50	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(22) Byte28 = 1F (3/4)

	0	1	2	3	4	5	6	7
60	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 84 (CHK4) Byte40-7F Detail

(22) Byte28 = 1F (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-1520

Byte27 = 89 & Byte0D Bit2 = 1 (Main-PK CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV#high (Logical DKC#)							
12	LDEV#low (LDEV# in DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Inf valid	Err code						
21	Segment Number							
22	Filemark	ECM	ILI	Reserved	Sense Key			
23	Information Bytes							
24								
25								
26								
27	Format (x'8')				Message (x'9')			
28	Subcode (See following pages)							
29	Detail Information (See following pages)							
2A								
2F								
30								
31								
32	Reserved							
33								
34	Module ID 1							
35	Routine ID 1							
36	Module ID 2							
37	Routine ID 2							
38	Master Path Number							
39	SCSI Initiator ID							
3A	SCSI Target ID							
3B	Logical Unit Number							
3C	SCSI Command Code							
3D	Processor Number							
3E	LDEV#							
3F								
40	Detail Information (See following pages)							
7F								

Byte27 = 89 (Main-PK CHK2) Byte28-31 Detail

(1) Byte28 = X0/X1/X2/X3/X4/X5

	0	1	2	3	4	5	6	7
28	PK_TYP0	PK_TYP1	PK_TYP2	MICRO_TOV	SubCode(x'X0' : DMA0 Error) SubCode(x'X1' : DMA1 Error) SubCode(x'X2' : DMA2 Error) SubCode(x'X3' : DMA3 Error) SubCode(x'X4' : MFDMA0) SubCode(x'X5' : MFDMA1)			
29	LR_INT_INTSTS0 (b31 – 24)							
	INT_CHK1B	LRP4_CHK1B	LRP3-0_CHK1B	SWIF_CHK1B	PARB1_CHK1B	PARB0_CHK1B	HARB3_CHK1B	HARB2_CHK1B
2A	LR_INT_INTSTS0 (b23 – 16)							
	HARB1_CHK1B	HARB0_CHK1B	XD_CHK1B	FMT_CHK1B	DX_CHK1B	DRR_CHK1B	DMAMB_CHK1B	DMA_CHK1B
2B	LR_INT_INTSTS0 (b15 – 08)							
	REG_CHK1B	SMBUS_CHK1B	FIVE1	FIVE0	CHK4	CHK3	CHK2	WARN
2C	LR_INT_INTSTS0 (b07 – 00)							
	Reserved	Reserved	MBOX INT	LRP4 INT	LRP3 INT	LRP2 INT	LRP1 INT	LRP0 INT
2D	LR_INT_STATUS8 (b31 – 24)							
	FRC_ERR	Reserved	INT_MFDM_A1_CHK2_P	INT_MFDM_A0_CHK2_P	Reserved	Reserved	INT_DRR1_CHK2	INT_DRR0_CHK2
2E	LR_INT_STATUS8 (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	INT_DMA3_CHK2	INT_DMA2_CHK2	INT_DMA1_CHK2	INT_DMA0_CHK2
2F	LR_INT_STATUS8 (b15 – 08)							
	INT_MFDM_A1_CTL_CHK2_P(7)	INT_MFDM_A1_CTL_CHK2_P(6)	INT_MFDM_A1_CTL_CHK2_P(5)	INT_MFDM_A1_CTL_CHK2_P(4)	INT_MFDM_A1_CTL_CHK2_P(3)	INT_MFDM_A1_CTL_CHK2_P(2)	INT_MFDM_A1_CTL_CHK2_P(1)	INT_MFDM_A1_CTL_CHK2_P(0)
30	LR_INT_STATUS8 (b07 – 00)							
	INT_MFDM_A0_CTL_CHK2_P(7)	INT_MFDM_A0_CTL_CHK2_P(6)	INT_MFDM_A0_CTL_CHK2_P(5)	INT_MFDM_A0_CTL_CHK2_P(4)	INT_MFDM_A0_CTL_CHK2_P(3)	INT_MFDM_A0_CTL_CHK2_P(2)	INT_MFDM_A0_CTL_CHK2_P(1)	INT_MFDM_A0_CTL_CHK2_P(0)
31	LR_INT_LR_STATUS (b31 – 24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID

Byte27 = 89 (Main-PK CHK2) Byte28-31 Detail

(2) Byte28 = X8/XC

	0	1	2	3	4	5	6	7
28	PK_TYP0	PK_TYP1	PK_TYP2	MICRO_TOV	SubCode (x'X8' : HSN Path Error at Async_DMA) SubCode (x'XC' : HSN Path Error at MFDMA)			
29	LR_INT_INTSTS0 (b31 – 24)							
	INT_CHK1B	LRP4_CHK1B	LRP3-0_CHK1B	SWIF_CHK1B	PARB1_CHK1B	PARB0_CHK1B	HARB3_CHK1B	HARB2_CHK1B
2A	LR_INT_INTSTS0 (b23 – 16)							
	HARB1_CHK1B	HARB0_CHK1B	XD_CHK1B	FMT_CHK1B	DX_CHK1B	DRR_CHK1B	DMAMB_CHK1B	DMA_CHK1B
2B	LR_INT_INTSTS0 (b15 – 08)							
	REG_CHK1B	SMBUS_CHK1B	FIVE1	FIVE0	CHK4	CHK3	CHK2	WARN
2C	LR_INT_INTSTS0 (b07 – 00)							
	Reserved	Reserved	MBOX INT	LRP4 INT	LRP3 INT	LRP2 INT	LRP1 INT	LRP0 INT
2D	LR_INT_STATUS8 (b15 – 08)							
	INT_MFDM_A1_CTL_CHK2_P(7)	INT_MFDM_A1_CTL_CHK2_P(6)	INT_MFDM_A1_CTL_CHK2_P(5)	INT_MFDM_A1_CTL_CHK2_P(4)	INT_MFDM_A1_CTL_CHK2_P(3)	INT_MFDM_A1_CTL_CHK2_P(2)	INT_MFDM_A1_CTL_CHK2_P(1)	INT_MFDM_A1_CTL_CHK2_P(0)
2E	LR_INT_STATUS8 (b07 – 00)							
	INT_MFDM_A0_CTL_CHK2_P(7)	INT_MFDM_A0_CTL_CHK2_P(6)	INT_MFDM_A0_CTL_CHK2_P(5)	INT_MFDM_A0_CTL_CHK2_P(4)	INT_MFDM_A0_CTL_CHK2_P(3)	INT_MFDM_A0_CTL_CHK2_P(2)	INT_MFDM_A0_CTL_CHK2_P(1)	INT_MFDM_A0_CTL_CHK2_P(0)
2F	LR_INT_STATUS1 (b15 – 08)							
	Reserved	Reserved	INT_HARB3_REG_CHK1B_P	INT_HARB3_CHK1B_P	Reserved	Reserved	INT_HARB2_REG_CHK1B_P	INT_HARB2_CHK1B_P
30	LR_INT_STATUS1 (b07 – 00)							
	Reserved	Reserved	INT_HARB1REG_CHK1B_P	INT_HARB1_CHK1B_P	Reserved	Reserved	INT_HARB0REG_CHK1B_P	INT_HARB0_CHK1B_P
31	LR_INT_LR_STATUS (b31 – 24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03 (1/4)

	0	1	2	3	4	5	6	7
40	LR_INT_LR_STATUS (bit31 -24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID
41	LR_INT_LR_STATUS (bit23 -16)							
	MP_CHK_CODE (7)	MP_CHK_CODE (6)	MP_CHK_CODE (5)	MP_CHK_CODE (4)	MP_CHK_CODE (3)	MP_CHK_CODE (2)	MP_CHK_CODE (1)	MP_CHK_CODE (0)
42	LR_INT_LR_STATUS (bit15 -08)							
	LSI_REV (7)	LSI_REV (6)	LSI_REV (5)	LSI_REV (4)	LSI_REV (3)	LSI_REV (2)	LSI_REV (1)	LSI_REV (0)
43	LR_INT_LR_STATUS (bit07 -00)							
	ADPMODE	ADPMODE	ADPMODE	ADPMODE	ADPMODE	ADPMODE	ADPMODE	ADPMODE
44	LR_DMA_ERR_ST13 (bit31 -24)							
	ERR_SEQ_DCP	PCHK_P_C_ATAG_S	PCHK_P_G_ATAG_S	PCHK_P_DL_3B	PCHK_P_S_ADR_S	PCHK_P_D1_ADR_S	PCHK_P_D2_ADR_S	PCHK_P_OUT_DEKKEY
45	LR_DMA_ERR_ST13 (bit23 -16)							
	PCHK_P_OUT_IVKEY	PCHK_P_W0016_OUT_D	PCHK_P_W1770_OUT_D	PCHK_P_W80C0_OUT_D	PCHK_P_WC1D1_OUT_D	PCHK_P_OUT_DEKEXPKEY	PCHK_START_GPTIM	Reserved
46	LR_DMA_ERR_ST13 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR_DMA_ERR_ST13 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR_DMA_ERR_ST14 (bit31 -24)							
	PCHK_INC_BP	PCHK_P_IN	PCHK_BF_PTR_CNT	PCHK_P_DL_3B	PCHK_CUR_ON	PCHK_CMP_CUR_ON	PCHK_BM_R_SBN	PCHK_BMPTN_FD
49	LR_DMA_ERR_ST14 (bit23 -16)							
	PCHK_CMP_SEL_R	PCHK_RDL_2B_8BB_T3	PCHK_CMP_BFBDR_DIV	PCHK_R_RD_L_3B_8BB	PCHK_CMP_LAST_REQ	PCHK_CUR_B_DL_2B_8BB	PCHK_S_TL_2B_8BB_D1	PCHK_CFW_DL_2B_8BB
4A	LR_DMA_ERR_ST14 (bit15 -08)							
	PCHK_CMP_CFW_END	PCHK_HD_TAG_CNT	PCHK_P_S_ADR_CNT	PCHK_HD_D	PCHK_HD_FOR	PCHK_STL_FOR	PCHK_DTL	PCHK_SEL_4Kto512_RDL
4B	LR_DMA_ERR_ST14 (bit07 -00)							
	PCHK_TMR0_TOCNT	PCHK_TMR1_TOCNT	PCHK_TMR2_TOCNT	PCHK_TMR3_TOCNT	PCHK_TMR4_TOCNT	PCHK_TMR5_TOCNT	PCHK_TMR6_TOCNT	PCHK_TMR7_TOCNT
4C	LR_DMA_ERR_ST15 (bit31 -24)							
	ERR_TMR0_SEQERR	ERR_TMR1_SEQERR	ERR_TMR2_SEQERR	ERR_TMR3_SEQERR	ERR_TMR4_SEQERR	ERR_TMR5_SEQERR	ERR_TMR6_SEQERR	ERR_TMR7_SEQERR
4D	LR_DMA_ERR_ST15 (bit23 -16)							
	Reserved	Reserved	ERR_SEQ_SIT	PCHK_E03_WDCNT	PCHK_E05_TOCNT	PCHK_E06_DT	ERR_E07_HLRC_NEQ	Reserved
4E	LR_DMA_ERR_ST15 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	TO_E04_TO	ERR_RX_ERRSTS
4F	LR_DMA_ERR_ST15 (bit07 -00)							
	TO_TMR0_OVER	TO_TMR1_OVER	TO_TMR2_OVER	TO_TMR3_OVER	TO_TMR4_OVER	TO_TMR5_OVER	TO_TMR6_OVER	TO_TMR7_OVER

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03 (2/4)

	0	1	2	3	4	5	6	7
50	LR_DMA_ERR_ST16 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	LR_DMA_ERR_ST16 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PCHK_CMP_TM_X_C_CLN	PCHK_CHGL
52	LR_DMA_ERR_ST16 (bit15 -08)							
	ERR_SEQ_DDS	ERR_SEQ_DDM	ERR_SEQ_DUD	ERR_SEQ_IVG	ERR_SEQ_CDA	ERR_SEQ_CIC	ERR_SEQ_BS	Reserved
53	LR_DMA_ERR_ST16 (bit07 -00)							
	PCHK_P_IN_FOR	PCHK_RHP_EXP_CNT	PCHK_CMP_FLG_SC	PCHK_R_CHDL_2B_8BB	PCHK_CMP_TM_CDL_NL	PCHK_R_SDL_3B_8BB	PCHK_OD	PCHK_FD
54	LR_DMA_ERR_ST17 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	LR_DMA_ERR_ST17 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ERR_SEQ_SIR	PCHK_E03_WDCNT	PCHK_E05_TOCNT
56	LR_DMA_ERR_ST17 (bit15 -08)							
	TO_E4	PCHK_E06_DT	ERR_E07_HLRC_NEQ	ERR_E08_DLRC_NEQ	PCHK_E09_HED	PCHK_E10_NPCNT	PCHK_E11_PCCNT	ERR_E12_DLRCTIM
57	LR_DMA_ERR_ST17 (bit07 -00)							
	ERR_E13_VLD	ERR_E14_REQ	ERR_HSN_REP	ERR_NOT_CPL_CPLD	ERR_CPLD_NOT_SC	ERR_CPL_NOT_SC	ERR_ERR_EP	ERR_ERR_HED
58	LR_DMA_ERR_ST18 (bit31 -24)							
	PCHK_P_IN	ERR_SEQ_CCG	PCHK_DCC_1B_32BB	PCHK_R_SEGDL	PCHK_E1_E00_CRC	PCHK_E1_E01_GENRTAG	PCHK_E1_E02_T10CRC	Reserved
59	LR_DMA_ERR_ST18 (bit23 -16)							
	PCHK_ENC_DAT_D	PCHK_ENC_DIF_D	ERR_ENC_DATA_NQL	ERR_ENC_DAT_BEN_NQL	ERR_KUD_BEN_NQL	ERR_KUD_ATAG1_NQL	PCHK_SEL_FF_OP_V	PCHK_SEL_FF_OP_OUT_V
5A	LR_DMA_ERR_ST18 (bit15 -08)							
	ERR_IV_ERR_7	ERR_IV_ERR_6	ERR_IV_ERR_5	ERR_IV_ERR_4	ERR_IV_ERR_3	ERR_IV_ERR_2	ERR_IV_ERR_1	ERR_IV_ERR_0
5B	LR_DMA_ERR_ST18 (bit07 -00)							
	ERR_ENC_ERR_7	ERR_ENC_ERR_6	ERR_ENC_ERR_5	ERR_ENC_ERR_4	ERR_ENC_ERR_3	ERR_ENC_ERR_2	ERR_ENC_ERR_1	ERR_ENC_ERR_0
5C	LR_DMA_ERR_ST19 (bit31 -24)							
	PCHK_P_IN	Reserved	PCHK_DCC_1B_32BB	PCHK_R_SEGDL	PCHK_E1_E00_CRC	PCHK_E1_E01_GENRTAG	PCHK_E1_E02_T10CRC	PCHK_E_LRC
5D	LR_DMA_ERR_ST19 (bit23 -16)							
	PCHK_DEC_DAT	PCHK_P_RTAG_LA	ERR_DEC_DAT_NQL	ERR_DEC_DAT_BEN_NQL	ERR_KUD_BEN_NQL	ERR_SEQ_CCG	ERR_KUD_ATAG1_NQL	Reserved
5E	LR_DMA_ERR_ST19 (bit15 -08)							
	ERR_KUD_ERR_7	ERR_KUD_ERR_6	ERR_KUD_ERR_5	ERR_KUD_ERR_4	ERR_KUD_ERR_3	ERR_KUD_ERR_2	ERR_KUD_ERR_1	ERR_KUD_ERR_0
5F	LR_DMA_ERR_ST19 (bit07 -00)							
	ERR_DEC_ERR_7	ERR_DEC_ERR_6	ERR_DEC_ERR_5	ERR_DEC_ERR_4	ERR_DEC_ERR_3	ERR_DEC_ERR_2	ERR_DEC_ERR_1	ERR_DEC_ERR_0

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03 (3/4)

	0	1	2	3	4	5	6	7
60	LR_DMA_ERR_ST20 (bit31 -24)							
	PCHK_P_IN0	PCHK_P_IN1	Reserved	PCHK_SEL_ATA01_BEN	PCHK_SEL_ATA01	PCHK_SEL_DATA_SIZE	PCHK_SEL_WEND_V	PCHK_SEL_CUR_IV_V
61	LR_DMA_ERR_ST20 (bit23 -16)							
	PCHK_LA	PCHK_SBC	PCHK_DEKEXPKEY	PCHK_DOC_1B_32BB	ERR_IV_CHK	PCHK_TV	PCHK_IVN	PCHK_IV0
62	LR_DMA_ERR_ST20 (bit15 -08)							
	PCHK_IV1	PCHK_DDSDT_DIF	PCHK_DDSDT0	PCHK_DAT_BEN_D	PCHK_DDSDT1	PCHK_DDSDT2	PCHK_TV	PCHK_IVKEY
63	LR_DMA_ERR_ST20 (bit07 -00)							
	PCHK_DEKKEY	PCHK_IV_D	Reserved	PCHK_DCC_1B_32BB	PCHK_R_SEGDL	PCHK_E00_CRC_OUT_512	PCHK_OV_G_RTAG_512	E02_T10CRC_512
64	LR_DMA_ERR_ST21 (bit31 -24)							
	PCHK_E1_0_CRC_4K	PCHK_OV_G_RTAG_4K	PCHK_E1_2_T10CRC_4K	PCHK_OV_C_RTAG	PCHK_OCRC	PCHK_ERR3_R_OCRC	PCHK_P_C_RTAG_INC	PCHK_RTAG_LA_INC
65	LR_DMA_ERR_ST21 (bit23 -16)							
	PCHK_GEN_E_LRC	PCHK_CHK_E_LRC	PCHK_GENDT_LRC4B	PCHK_BLKLEN_T10_L	PCHK_E1_00_DBV_BNCNT	PCHK_E1_01_DBV_BLCNT	PCHK_E1_02_DBV_LRC	PCHK_E1_03_DBV_TLRC
66	LR_DMA_ERR_ST21 (bit15 -08)							
	PCHK_E1_04_DBV_HD	PCHK_DPB_IND	PCHK_DDS_DT_32B_D	PCHK_SEGDL_PMM	PCHK_DPB_W_ADR	PCHK_DPB_R_ADR_T	PCHK_DPB_Q_D2	PCHK_DDM_SBP
67	LR_DMA_ERR_ST21 (bit07 -00)							
	PCHK_DBF_IND	PCHK_DDA_DT	PCHK_DDM_DT0	PCHK_DDM_DT1	PCHK_BF_P_TR_CNT	PCHK_BAD_R_CNT	ERR_UNCRE_RR	ERR_ADRE_RR
68	LR_DMA_ERR_ST23 (bit31 -24)							
	PCHK_P_IN0	PCHK_P_IN1	PCHK_SEL_SB_BDR	PCHK_CMP_R_DDL_3B_8BB	PCHK_R_BDL_2B_8BB	PCHK_CMP_R_BDL_2B_8BB	PCHK_R_RDL_2B_8BB	PCHK_CMP_R_RDL
69	LR_DMA_ERR_ST23 (bit23 -16)							
	PCHK_REQ_DLEN_L2b	ERR_SEQ_CIC	PCHK_BP	PCHK_BADR	PCHK_DT_D0_ADR	PCHK_DT_D2	PCHK_HD_TAG_CNT	PCHK_HD_DADR
6A	LR_DMA_ERR_ST23 (bit15 -08)							
	PCHK_HDR_LEN_4BB_D	PCHK_RH_32B_D	PCHK_RHP_CMD_CNT	PCHK_RHP_EXP_CNT	PCHK_RH_EXP	PCHK_RDL_1B_32BB	PCHK_OSN	PCHK_DDC_SBP
6B	LR_DMA_ERR_ST23 (bit07 -00)							
	PCHK_TMR0_CNT	PCHK_TMR1_CNT	PCHK_TMR2_CNT	PCHK_TMR3_CNT	PCHK_TMR4_CNT	PCHK_TMR5_CNT	PCHK_TMR6_CNT	PCHK_TMR7_CNT
6C	LR_DMA_ERR_ST24 (bit31 -24)							
	ERR_SEQ_DDC	PCHK_OCRC	PCHK_E1_E00_FDBKDT	PCHK_E1_E02_CHKRTAG	PCHK_P_RTAG_LA_INC	PCHK_LA	PCHK_DT_D3	PCHK_CMP_CHG_TC
6D	LR_DMA_ERR_ST24 (bit23 -16)							
	ERR_UNCRE_RR	ERR_ADRERR	PCHK_EXP_DT_SLRC	PCHK_LA	PCHK_CMP_CHG_DC	PCHK_CMP_CHG_SC	ERR_LA_ATA0_NQL	ERR_LA_ATA01_NQL
6E	LR_DMA_ERR_ST24 (bit15 -08)							
	ERR_LA_RTAG0_NQL	ERR_LA_RTAG1_NQL	ERR_LA_RTAG2_NQL	ERR_LA_RTAG3_NQL	ERR_LA_SLRC_NQL	ERR_OCRC_NQL	ERR_E3_E0C_CRC_NQL	ERR_E3_E0B_ATA01_NQL
6F	LR_DMA_ERR_ST24 (bit07 -00)							
	ERR_E3_04_RTAG0_NQL	ERR_E3_05_RTAG0_NQL	ERR_E3_06_RTAG0_NQL	ERR_E3_07_RTAG0_NQL	ERR_E3_08_EXL_WRSEQ	ERR_E3_08_EXF_WRSEQ	ERR_E3_08_WRSEQ	ERR_E3_E0A_ATA0_NQL

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(1) Byte28 = 00/01/02/03 (4/4)

	0	1	2	3	4	5	6	7
70	LR_DMA_ERR_ST25 (bit31 -24)							
	ERR_TMR0_SEQERR	ERR_TMR1_SEQERR	ERR_TMR2_SEQERR	ERR_TMR3_SEQERR	ERR_TMR4_SEQERR	ERR_TMR5_SEQERR	ERR_TMR6_SEQERR	ERR_TMR7_SEQERR
71	LR_DMA_ERR_ST25 (bit23 -16)							
	Reserved	Reserved	ERR_SEQ_SIT	PCHK_E03_WDCNT	PCHK_E05_TOCNT	PCHK_E06_DT	ERR_E07_HLRC_NEQ	ERR_E08_DLRC_NEQ
72	LR_DMA_ERR_ST25 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	TO_E04_TO	ERR_RX_ERRSTS
73	LR_DMA_ERR_ST25 (bit07 -00)							
	TO_TMR0_OVER	TO_TMR1_OVER	TO_TMR2_OVER	TO_TMR3_OVER	TO_TMR4_OVER	TO_TMR5_OVER	TO_TMR6_OVER	TO_TMR7_OVER
74	LR_DMA_ERR_ST26 (bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR_DMA_ERR_ST26 (bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	ERR_SEQ_SIR	PCHK_E03_WDCNT	PCHK_E05_TOCNT
76	LR_DMA_ERR_ST26 (bit15 -08)							
	TO_E4	PCHK_E06_DT	ERR_E07_HLRC_NEQ	ERR_E08_DLRC_NEQ	PCHK_E09_HED	PCHK_E10_NPCNT	PCHK_E11_PCCNT	ERR_E12_DLRCTIM
77	LR_DMA_ERR_ST26 (bit07 -00)							
	ERR_E13_VLD	ERR_E14_REQ	ERR_HSN_REP	ERR_NOT_CPL_CPLD	ERR_CPLD_NOT_SC	ERR_CPL_NOT_SC	ERR_ERR_EP	ERR_ERR_HED
78	LR_DMA_ERR_ST27 (bit31 -24)							
	PCHK_P_IN0	PCHK_P_IN1	PCHK_SEL_SB_BDR	PCHK_CMP_R_DDL_3B_8BB	PCHK_R_BDL_2B_8BB	PCHK_CMP_R_BDL_2B_8BB	PCHK_R_RDL_2B_8BB	PCHK_CMP_R_RDL
79	LR_DMA_ERR_ST27 (bit23 -16)							
	PCHK_REQ_DLEN_L2b	ERR_SEQ_CIC	PCHK_BP	PCHK_BADR	PCHK_DT_D0_ADR	PCHK_DT_D2	PCHK_HD_TAG_CNT	PCHK_HD_DADR
7A	LR_DMA_ERR_ST27 (bit15 -08)							
	PCHK_HDR_LEN_4BB_D	PCHK_RH_32B_D	PCHK_RHP_CMD_CNT	PCHK_RHP_EXP_CNT	PCHK_RH_EXP	PCHK_RDL_1B_32BB	PCHK_OSN	PCHK_DDC_SBP
7B	LR_DMA_ERR_ST27 (bit07 -00)							
	PCHK_TMR0_CNT	PCHK_TMR1_CNT	PCHK_TMR2_CNT	PCHK_TMR3_CNT	PCHK_TMR4_CNT	PCHK_TMR5_CNT	PCHK_TMR6_CNT	PCHK_TMR7_CNT
7C	LR_DMA_ERR_ST28 (bit31 -24)							
	ERR_SEQ_DDC	PCHK_OCRC	PCHK_E1_E00_FDBKDT	PCHK_E1_E02_CHKRTAG	PCHK_P_RT_AG_LA_INC	PCHK_LA	PCHK_DT_D3	PCHK_CMP_CHG_TC
7D	LR_DMA_ERR_ST28 (bit23 -16)							
	ERR_UNCRERR	ERR_ADRERR	PCHK_EXP_DT_SLRC	PCHK_LA	PCHK_CMP_CHG_DC	PCHK_CMP_CHG_SC	ERR_LA_ATAG0_NQL	ERR_LA_ATAG1_NQL
7E	LR_DMA_ERR_ST28 (bit15 -08)							
	ERR_LA_RT_AG0_NQL	ERR_LA_RT_AG1_NQL	ERR_LA_RT_AG2_NQL	ERR_LA_RT_AG3_NQL	ERR_LA_SL_RC_NQL	ERR_OCRC_NQL	ERR_E3_E0C_CRC_NQL	ERR_E3_E0B_ATAG1_NQL
7F	LR_DMA_ERR_ST28 (bit07 -00)							
	ERR_E3_04_RTAG0_NQL	ERR_E3_05_RTAG0_NQL	ERR_E3_06_RTAG0_NQL	ERR_E3_07_RTAG0_NQL	ERR_E3_08_EXL_WRSEQ	ERR_E3_08_EXF_WRSEQ	ERR_E3_08_WRSEQ	ERR_E3_E0A_ATAG0_NQL

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(2) Byte28 = 04/05/0C (1/4)

	0	1	2	3	4	5	6	7
40	MFDMA_CCW_PRM_W0_B3_B2 (b15 – 08)							
	MFDMA_CCW_PRM_W0_B3_B2							
41	MFDMA_CCW_PRM_W0_B3_B2 (b07 – 00)							
	MFDMA_CCW_PRM_W0_B3_B2							
42	MFDMA_CCW_PRM_W1_B1_B0 (b15 – 08)							
	MFDMA_CCW_PRM_W1_B1_B0							
43	MFDMA_CCW_PRM_W1_B1_B0 (b07 – 00)							
	MFDMA_CCW_PRM_W1_B1_B0							
44	MFDMA_CCW_PRM_W2 (b31 – 24)							
	MFDMA_CCW_PRM_W2							
45	MFDMA_CCW_PRM_W2 (b23 – 16)							
	MFDMA_CCW_PRM_W2							
46	MFDMA_CCW_PRM_W2 (b15 – 08)							
	MFDMA_CCW_PRM_W2							
47	MFDMA_CCW_PRM_W2 (b07 – 00)							
	MFDMA_CCW_PRM_W2							
48	MFDMA_CCW_PRM_W3 (b31 – 24)							
	MFDMA_CCW_PRM_W3							
49	MFDMA_CCW_PRM_W3 (b23 – 16)							
	MFDMA_CCW_PRM_W3							
4A	MFDMA_CCW_PRM_W3 (b15 – 08)							
	MFDMA_CCW_PRM_W3							
4B	MFDMA_CCW_PRM_W3 (b07 – 00)							
	MFDMA_CCW_PRM_W3							
4C	MFDMA_CCW_PRM_W4 (b31 – 24)							
	MFDMA_CCW_PRM_W4							
4D	MFDMA_CCW_PRM_W4 (b23 – 16)							
	MFDMA_CCW_PRM_W4							
4E	MFDMA_CCW_PRM_W4 (b15 – 08)							
	MFDMA_CCW_PRM_W4							
4F	MFDMA_CCW_PRM_W4 (b07 – 00)							
	MFDMA_CCW_PRM_W4							

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(2) Byte28 = 04/05/0C (2/4)

	0	1	2	3	4	5	6	7
50	MFDMA_CCW_PRM_W5 (b31 – 24)							
	MFDMA_CCW_PRM_W5							
51	MFDMA_CCW_PRM_W5 (b23 – 16)							
	MFDMA_CCW_PRM_W5							
52	MFDMA_CCW_PRM_W5 (b15 – 08)							
	MFDMA_CCW_PRM_W5							
53	MFDMA_CCW_PRM_W5 (b07 – 00)							
	MFDMA_CCW_PRM_W5							
54	MFDMA_CCW_PRM_W6 (b31 – 24)							
	MFDMA_CCW_PRM_W6							
55	MFDMA_CCW_PRM_W6 (b23 – 16)							
	MFDMA_CCW_PRM_W6							
56	MFDMA_CCW_PRM_W6 (b15 – 08)							
	MFDMA_CCW_PRM_W6							
57	MFDMA_CCW_PRM_W6 (b07 – 00)							
	MFDMA_CCW_PRM_W6							
58	MFDMA_FLD_PRM_W0 (b31 – 24)							
	MFDMA_FLD_PRM_W0							
59	MFDMA_FLD_PRM_W0 (b23 – 16)							
	MFDMA_FLD_PRM_W0							
5A	MFDMA_FLD_PRM_W0 (b15 – 08)							
	MFDMA_FLD_PRM_W0							
5B	MFDMA_FLD_PRM_W0 (b07 – 00)							
	MFDMA_FLD_PRM_W0							
5C	MFDMA_FLD_PRM_W1 (b31 – 24)							
	MFDMA_FLD_PRM_W1							
5D	MFDMA_FLD_PRM_W1 (b23 – 16)							
	MFDMA_FLD_PRM_W1							
5E	MFDMA_FLD_PRM_W1 (b15 – 08)							
	MFDMA_FLD_PRM_W1							
5F	MFDMA_FLD_PRM_W1 (b07 – 00)							
	MFDMA_FLD_PRM_W1							

SSBLOG05-1610

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(2) Byte28 = 04/05/0C (3/4)

	0	1	2	3	4	5	6	7
60	MFDMA_FLD_PRM_W2 (b31 – 24)							
	MFDMA_FLD_PRM_W2							
61	MFDMA_FLD_PRM_W2 (b23 – 16)							
	MFDMA_FLD_PRM_W2							
62	MFDMA_FLD_PRM_W2 (b15 – 08)							
	MFDMA_FLD_PRM_W2							
63	MFDMA_FLD_PRM_W2 (b07 – 00)							
	MFDMA_FLD_PRM_W2							
64	MFDMA_FLD_PRM_W3 (b31 – 24)							
	MFDMA_FLD_PRM_W3							
65	MFDMA_FLD_PRM_W3 (b23 – 16)							
	MFDMA_FLD_PRM_W3							
66	MFDMA_FLD_PRM_W3 (b15 – 08)							
	MFDMA_FLD_PRM_W3							
67	MFDMA_FLD_PRM_W3 (b07 – 00)							
	MFDMA_FLD_PRM_W3							
68	MFDMA_FLD_PRM_W4 (b31 – 24)							
	MFDMA_FLD_PRM_W4							
69	MFDMA_FLD_PRM_W4 (b23 – 16)							
	MFDMA_FLD_PRM_W4							
6A	MFDMA_FLD_PRM_W4 (b15 – 08)							
	MFDMA_FLD_PRM_W4							
6B	MFDMA_FLD_PRM_W4 (b07 – 00)							
	MFDMA_FLD_PRM_W4							
6C	MFDMA_FLD_PRM_W5 (b31 – 24)							
	MFDMA_FLD_PRM_W5							
6D	MFDMA_FLD_PRM_W5 (b23 – 16)							
	MFDMA_FLD_PRM_W5							
6E	MFDMA_FLD_PRM_W5 (b15 – 08)							
	MFDMA_FLD_PRM_W5							
6F	MFDMA_FLD_PRM_W5 (b07 – 00)							
	MFDMA_FLD_PRM_W5							

SSBLOG05-1620

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(2) Byte28 = 04/05/0C (4/4)

	0	1	2	3	4	5	6	7
70	MFDMA_FLD_PRM_W6 (b31 – 24)							
	MFDMA_FLD_PRM_W6							
71	MFDMA_FLD_PRM_W6 (b23 – 16)							
	MFDMA_FLD_PRM_W6							
72	MFDMA_FLD_PRM_W6 (b15 – 08)							
	MFDMA_FLD_PRM_W6							
73	MFDMA_FLD_PRM_W6 (b07 – 00)							
	MFDMA_FLD_PRM_W6							
74	MFDMA_FLD_PRM_W7 (b31 – 24)							
	MFDMA_FLD_PRM_W7							
75	MFDMA_FLD_PRM_W7 (b23 – 16)							
	MFDMA_FLD_PRM_W7							
76	MFDMA_FLD_PRM_W7 (b15 – 08)							
	MFDMA_FLD_PRM_W7							
77	MFDMA_FLD_PRM_W7 (b07 – 00)							
	MFDMA_FLD_PRM_W7							
78	MFDMA_FLD_PRM_W12 (b31 – 24)							
	MFDMA_FLD_PRM_W12							
79	MFDMA_FLD_PRM_W12 (b23 – 16)							
	MFDMA_FLD_PRM_W12							
7A	MFDMA_FLD_PRM_W12 (b15 – 08)							
	MFDMA_FLD_PRM_W12							
7B	MFDMA_FLD_PRM_W12 (b07 – 00)							
	MFDMA_FLD_PRM_W12							
7C	MFDMA_FLD_PRM_W13 (b31 – 24)							
	MFDMA_FLD_PRM_W13							
7D	MFDMA_FLD_PRM_W13 (b23 – 16)							
	MFDMA_FLD_PRM_W13							
7E	MFDMA_FLD_PRM_W13 (b15 – 08)							
	MFDMA_FLD_PRM_W13							
7F	MFDMA_FLD_PRM_W13 (b07 – 00)							
	MFDMA_FLD_PRM_W13							

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(3) Byte28 = 08 (1/4)

	0	1	2	3	4	5	6	7
40	DM_DMAAnSTS_W0 (b31 – 24)							
	DM_DMAAnSTS_W0							
41	DM_DMAAnSTS_W0 (b23 – 16)							
	DM_DMAAnSTS_W0							
42	DM_DMAAnSTS_W0 (b15 – 08)							
	DM_DMAAnSTS_W0							
43	DM_DMAAnSTS_W0 (b07 – 00)							
	DM_DMAAnSTS_W0							
44	DM_DMAAnSTS_W1 (b31 – 24)							
	DM_DMAAnSTS_W1							
45	DM_DMAAnSTS_W1 (b23 – 16)							
	DM_DMAAnSTS_W1							
46	DM_DMAAnSTS_W1 (b15 – 08)							
	DM_DMAAnSTS_W1							
47	DM_DMAAnSTS_W1 (b07 – 00)							
	DM_DMAAnSTS_W1							
48	DM_DMAAnSTS_W2 (b31 – 24)							
	DM_DMAAnSTS_W2							
49	DM_DMAAnSTS_W2 (b23 – 16)							
	DM_DMAAnSTS_W2							
4A	DM_DMAAnSTS_W2 (b15 – 08)							
	DM_DMAAnSTS_W2							
4B	DM_DMAAnSTS_W2 (b07 – 00)							
	DM_DMAAnSTS_W2							
4C	DM_DMAAnSTS_W3 (b31 – 24)							
	DM_DMAAnSTS_W3							
4D	DM_DMAAnSTS_W3 (b23 – 16)							
	DM_DMAAnSTS_W3							
4E	DM_DMAAnSTS_W3 (b15 – 08)							
	DM_DMAAnSTS_W3							
4F	DM_DMAAnSTS_W3 (b07 – 00)							
	DM_DMAAnSTS_W3							

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Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(3) Byte28 = 08 (2/4)

	0	1	2	3	4	5	6	7
50	DM_DMAAnSTS_W4 (b31 – 24)							
	DM_DMAAnSTS_W4							
51	DM_DMAAnSTS_W4 (b23 – 16)							
	DM_DMAAnSTS_W4							
52	DM_DMAAnSTS_W4 (b15 – 08)							
	DM_DMAAnSTS_W4							
53	DM_DMAAnSTS_W4 (b07 – 00)							
	DM_DMAAnSTS_W4							
54	DM_DMAAnSTS_W5 (b31 – 24)							
	DM_DMAAnSTS_W5							
55	DM_DMAAnSTS_W5 (b23 – 16)							
	DM_DMAAnSTS_W5							
56	DM_DMAAnSTS_W5 (b15 – 08)							
	DM_DMAAnSTS_W5							
57	DM_DMAAnSTS_W5 (b07 – 00)							
	DM_DMAAnSTS_W5							
58	DM_DMAAnSTS_W6 (b31 – 24)							
	DM_DMAAnSTS_W6							
59	DM_DMAAnSTS_W6 (b23 – 16)							
	DM_DMAAnSTS_W6							
5A	DM_DMAAnSTS_W6 (b15 – 08)							
	DM_DMAAnSTS_W6							
5B	DM_DMAAnSTS_W6 (b07 – 00)							
	DM_DMAAnSTS_W6							
5C	DM_DMAAnSTS_W7 (b31 – 24)							
	DM_DMAAnSTS_W7							
5D	DM_DMAAnSTS_W7 (b23 – 16)							
	DM_DMAAnSTS_W7							
5E	DM_DMAAnSTS_W7 (b15 – 08)							
	DM_DMAAnSTS_W7							
5F	DM_DMAAnSTS_W7 (b07 – 00)							
	DM_DMAAnSTS_W7							

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(3) Byte28 = 08 (3/4)

	0	1	2	3	4	5	6	7
60	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
66	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
67	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
68	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6C	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6D	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6E	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6F	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 89 (Main-PK CHK2) Byte40-7F Detail

(3) Byte28 = 08 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
72	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) (1/2)

0	1	2	3	4	5	6	7	
00	Time stamp							
01								
02								
03								
04	Format Message							
05	Processor ID							
06	System error code							
07								
08	JCB#							
09								
0A								
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device number				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'A')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number(*1)				Message code(x'0')			
35	SSID of self storage system (low)							
36	Symptom code (x'FF8A')							
37	(This information is not fixed until DKC reports SSB to HOST.)							

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Byte27 = 8A (DKA CHK2) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Detail information (See following pages)							
7F								

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(1) Byte28 = 40/41/42/43/50/51/52/53

	0	1	2	3	4	5	6	7
28	Subcode (x'40' or x'41' or x'42' or x'43') Subcode (x'50' or x'51' or x'52' or x'53')				DKA CHK2 DMA Error DKA CHK2 DMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2_INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(2) Byte28 = 44/54

	0	1	2	3	4	5	6	7
28	Subcode (x'44') Subcode (x'54')				DKA CHK2 MDMA Error DKA CHK2 MDMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b31 – 24)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	CHK2_INT0(b23 – 16)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
30	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(3) Byte28 = 48/4c/58/5c

	0	1	2	3	4	5	6	7
28	Subcode (x'48' or x'4c') Subcode (x'58' or x'5c')				DKA CHK2 DMA Path Error DKA CHK2 DMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOW N	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOW N
30	DMA Number							
	DMA0 = 0x00							
	DMA1 = 0x01							
	DMA2 = 0x02							
31	DMA3 = 0x03							
	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

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Byte27 = 8A (DKA CHK2) Byte28-31 Detail

(4) Byte28 = 49/4d/59/5d

	0	1	2	3	4	5	6	7
28	Subcode (x'49' or x'4d') Subcode (x'59' or x'5d')				DKA CHK2 MDMA Path Error DKA CHK2 MDMA Timeout			
29	LR: INT_STATUS(b31 – 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
2A	LR: INT_STATUS(b23 – 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
2B	LR: INT_STATUS(b15 – 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
2C	LR: INT_STATUS(b07 – 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
2D	CHK2_INT0(b15 – 08)							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	DRR1 CHK2	DRR0 CHK2
2E	CHK2_INT0(b07 – 00)							
	(RSV)	(RSV)	(RSV)	DMAA CHK2	DMA3 CHK2	DMA2 CHK2	DMA1 CHK2	DMA0 CHK2
2F	CHK4_INT1(b23 – 16)							
	(RSV)	HAT1 CHK4	HAR1 CHK4	HA1 LINKDOWN	(RSV)	HAT0 CHK4	HAR0 CHK4	HA0 LINKDOWN
30	DMA Number							
	MDMA = 0x04							
31	LR_STATUS(b28 – 21)							
	MODULE ID(0)	LSI NUM(6)	LSI NUM(5)	LSI NUM(4)	LSI NUM(3)	LSI NUM(2)	LSI NUM(1)	LR MODE

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT_STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT_STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT_STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT_STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: DMACTL_DCSQ_ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: DMACTL_DCSQ_ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: DMACTL_DCSQ_ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: DMACTL_DCSQ_ERRMPN(bit07 -00)							
	DCSQ ERRMPN EN	Reserved	Reserved	Reserved	DCSQ ERRMPN EN(3)	DCSQ ERRMPN EN(2)	DCSQ ERRMPN EN(1)	DCSQ ERRMPN EN(0)
48	LR: DMACTL_ERR_ST0(bit31 -24)							
	DMA CSEQ ERR	Reserved	DMA_NQ PIDX PERR	DMA_NQ CIDX PERR	DMA_YQ PIDX PERR	DMA_YQ CIDX PERR	DMA QSEL PERR	DMA POP PERR
49	LR: DMACTL_ERR_ST0(bit23 -16)							
	STS0 PERR	STS0 SBADR PERR	STS0 SBTAG PERR	Reserved	STS1 PERR	STS1 SBADR PERR	STS1 SBTAG PERR	Reserved
4A	LR: DMACTL_ERR_ST0(bit15 -08)							
	STS SBQWP PERR	STS SBQRP PERR	LSINO ERR	DMA PBF0 PERR	DMA PBF1 PERR	DMA QCNT PERR	DMA NQ OVER	DMA YQ OVER
4B	LR: DMACTL_ERR_ST0(bit07 -00)							
	STSBSEQ ERR	STSPATH ERR	Reserved	Reserved	HTIM PERR	HTIM TO	HTIM STATCNT PERR	Reserved
4C	LR: DMABUF_ERR_ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
4D	LR: DMABUF_ERR_ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
4E	LR: DMABUF_ERR_ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	LR: DMABUF_ERR_ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF_ERR_ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
51	LR: DMABUF_ERR_ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
52	LR: DMABUF_ERR_ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
53	LR: DMABUF_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF_ERR_ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
55	LR: DMABUF_ERR_ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
56	LR: DMABUF_ERR_ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
57	LR: DMABUF_ERR_ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
58	LR: DMABUF_ERR_ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
59	LR: DMABUF_ERR_ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5A	LR: DMABUF_ERR_ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	DBH ENCPRM PERR	DBH IVLACNT PERR	DBH KEY34 PERR	DBH FBPRM PERR	DBH DBFDL PERR
5B	LR: DMABUF_ERR_ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	AES TWVERR (5)	AES TWVERR (4)	AES TWVERR (3)	AES TWVERR (2)	AES TWVERR (1)	AES TWVERR (0)
5C	LR: DMABUF_ERR_ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
5D	LR: DMABUF_ERR_ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5E	LR: DMABUF_ERR_ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
5F	LR: DMABUF_ERR_ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF_ERR_ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
61	LR: DMABUF_ERR_ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
62	LR: DMABUF_ERR_ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
63	LR: DMABUF_ERR_ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
64	LR: DMABUF_ERR_ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF_ERR_ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF_ERR_ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF_ERR_ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF_ERR_ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF_ERR_ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF_ERR_ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF_ERR_ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DXBF_IF_CTL_ERR_ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
6D	LR: DXBF_IF_CTL_ERR_ST0 (bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
6E	LR: DXBF_IF_CTL_ERR_ST0 (bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
6F	LR: DXBF_IF_CTL_ERR_ST0 (bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(1) Byte28 = 40/41/42/43/50/51/52/53 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF_IF_CTL_ERR_ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
71	LR: DXBF_IF_CTL_ERR_ST1 (bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF_IF_CTL_ERR_ST1 (bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	LR: DXBF_IF_CTL_ERR_ST1 (bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	LR: DXBF_IF_CTL_ERR_ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	LR: DXBF_IF_CTL_ERR_ST2 (bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
76	LR: DXBF_IF_CTL_ERR_ST2 (bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
77	LR: DXBF_IF_CTL_ERR_ST2 (bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
78	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (1/4)

	0	1	2	3	4	5	6	7
40	LR: INT_STATUS(b31 - 24)							
	FOP1 ERR	FOP0 ERR	FOP1 STOP	FOP0 STOP	MBOX1 INT	MBOX0 INT	FDMA1 ERR	FDMA0 ERR
41	LR: INT_STATUS(b23 - 16)							
	HA1 ERR	HA0 ERR	PA1 ERR	PA0 ERR	BRG1 ERR	BRG0 ERR	LA1	LA0
42	LR: INT_STATUS(b15 - 08)							
	FIVE1 MCK	FIVE0 MCK	PPR1 MSG	PPR0 MSG	PA ERR	DA ERR	DRR ERR	PCDX ERR
43	LR: INT_STATUS(b07 - 00)							
	XREG ERR	XD/INT ERR	DMA ERR	SMBUS ERR	MF ERR	PPR ERR	CHK2	WARNING
44	LR: MDMACTL_PFCSQ_ERRMPN(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	LR: MDMACTL_PFCSQ_ERRMPN(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	LR: MDMACTL_PFCSQ_ERRMPN(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	LR: MDMACTL_PFCSQ_ERRMPN(bit07 -00)							
	PFCSQ ERRMPN EN	Reserved	Reserved	Reserved	PFCSQ ERRMPN EN(3)	PFCSQ ERRMPN EN(2)	PFCSQ ERRMPN EN(1)	PFCSQ ERRMPN EN (0)
48	LR: DMACTL_TTRDY(bit31 -00)							
49								
4A								
4B								
4C	LR: DMABUF_MULACT(bit31 -00)							
4D								
4E								
4F								

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (2/4)

	0	1	2	3	4	5	6	7
50	LR: DMABUF_ERR_ST0(bit31 -24)							
	RSVPRM0 PERR	RSVPRM1 PERR	HSN SPLITDL PERR	DBDX SPLDL PERR	SEG SPLDL PERR	DBFSQ ERR	ZLEN PERR	SEGDL ZERO ERR
51	LR: DMABUF_ERR_ST0(bit23 -16)							
	SPMWP PERR	SPMRP PERR	DBXRP PERR	DBHRP PERR	SPDX SPLPRM PERR	SPHN SPLPRM PERR	SPLSQ ERR	Reserved
52	LR: DMABUF_ERR_ST0(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
53	LR: DMABUF_ERR_ST0(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	LR: DMABUF_ERR_ST1(bit31 -24)							
	DFI0 DF0WA PERR	DFI0 DF0WD PERR	DFI0 DF1WA PERR	DFI0 DF1WD PERR	DFI0 DF2WA PERR	DFI0 DF2WD PERR	DFI0 DF3WA PERR	DFI0 DF3WD PERR
55	LR: DMABUF_ERR_ST1(bit23 -16)							
	DFI1 DF0WA PERR	DFI1 DF0WD PERR	DFI1 DF1WA PERR	DFI1 DF1WD PERR	DFI1 DF2WA PERR	DFI1 DF2WD PERR	DFI1 DF3WA PERR	DFI1 DF3WD PERR
56	LR: DMABUF_ERR_ST1(bit15 -08)							
	DFI2 DF0WA PERR	DFI2 DF0WD PERR	DFI2 DF1WA PERR	DFI2 DF1WD PERR	DFI2 DF2WA PERR	DFI2 DF2WD PERR	DFI2 DF3WA PERR	DFI2 DF3WD PERR
57	LR: DMABUF_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
58	LR: DMABUF_ERR_ST2(bit31 -24)							
	DFB0 DBDT PERR	DBF0 CMWDT PERR	DBF0 WPPERR	DBF0 RPPERR	DBF1 WPPERR	DBF1 RPPERR	DBF2 WPPERR	DBF2 RPPERR
59	LR: DMABUF_ERR_ST2(bit23 -16)							
	DBF0 UNCR ERR0	DBF0 ADR ERR0	DBF0 UNCR ERR1	DBF0 ADR ERR1	DBF0 UNCR ERR2	DBF0 ADR ERR2	DBF0 UNCR ERR3	DBF0 ADR ERR3
5A	LR: DMABUF_ERR_ST2(bit15 -08)							
	DBF1 UNCR ERR0	DBF1 ADR ERR0	DBF1 UNCR ERR1	DBF1 ADR ERR1	DBF1 UNCR ERR2	DBF1 ADR ERR2	DBF1 UNCR ERR3	DBF1 ADR ERR3
5B	LR: DMABUF_ERR_ST2(bit07 -00)							
	DBF2 UNCR ERR0	DBF2 ADR ERR0	DBF2 UNCR ERR1	DBF2 ADR ERR1	DBF2 UNCR ERR2	DBF2 ADR ERR2	DBF2 UNCR ERR3	DBF2 ADR ERR3
5C	LR: DMABUF_ERR_ST3(bit31 -24)							
	DBH 2WADR ERR	DBH 2WCLS ERR	DBH 2WNCM1 ERR	DBH 2WNCM2 ERR	DBH NOADR ERR	DBH PATH ERR	DBH PAIRMOD ERR	Reserved
5D	LR: DMABUF_ERR_ST3(bit23 -16)							
	DBHSQ ERR	DBH TXDBFNO PERR	DBH RXDBFNO PERR	DBH HSNMULP PERR	DBH HSNMULC PERR	DBH SPLDL PERR	DBH MLCNV ERR	DBH HSNDH25 PERR
5E	LR: DMABUF_ERR_ST3(bit15 -08)							
	DBH HSNADR PERR	DBH HSNCMD PERR	DBH PRM PERR	Reserved	Reserved	Reserved	DBH FBPRM PERR	DBH DBFDL PERR
5F	LR: DMABUF_ERR_ST3(bit07 -00)							
	DBH DBFWSUM PERR	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (3/4)

	0	1	2	3	4	5	6	7
60	LR: DMABUF_ERR_ST4(bit31 -24)							
	DHRSQ ERR	DHR RWST ERR	DHR HSNMUL PERR	DHR HSNMULE PERR	DHR HSNST1 PERR	DHR HSNST2 PERR	DHR ENCMD PERR	Reserved
61	LR: DMABUF_ERR_ST4(bit23 -16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
62	LR: DMABUF_ERR_ST4(bit15 -08)							
	DBDXSQ ERR	DBDXSQ CRTBNO PERR	DBDXSQ DBFDL PERR	Reserved	Reserved	Reserved	Reserved	Reserved
63	LR: DMABUF_ERR_ST4(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	LR: DMABUF_ERR_ST5(bit31 -24)							
	DRWSQ0 ERR	DRW0 PPATH ERR	DRW0 ENCMD PERR	DRW0 FCTG PERR	DRW0 IFSIG ERR	Reserved	Reserved	Reserved
65	LR: DMABUF_ERR_ST5(bit23 -16)							
	DRWSQ1 ERR	DRW1 PPATH ERR	DRW1 ENCMD PERR	DRW1 FCTG PERR	DRW1 IFSIG ERR	Reserved	Reserved	Reserved
66	LR: DMABUF_ERR_ST5(bit15 -08)							
	DRWSQ2 ERR	DRW2 PPATH ERR	DRW2 ENCMD PERR	DRW2 FCTG PERR	DRW2 IFSIG ERR	Reserved	Reserved	Reserved
67	LR: DMABUF_ERR_ST5(bit07 -00)							
	DRWSQ3 ERR	DRW3 PPATH ERR	DRW3 ENCMD PERR	DRW3 FCTG PERR	DRW3 IFSIG ERR	Reserved	Reserved	Reserved
68	LR: DMABUF_ERR_ST6(bit31 -24)							
	DRWSQ4 ERR	DRW4 PPATH ERR	DRW4 ENCMD PERR	DRW4 FCTG PERR	DRW4 IFSIG ERR	Reserved	Reserved	Reserved
69	LR: DMABUF_ERR_ST6(bit23 -16)							
	DRWSQ5 ERR	DRW5 PPATH ERR	DRW5 ENCMD PERR	DRW5 FCTG PERR	DRW5 IFSIG ERR	Reserved	Reserved	Reserved
6A	LR: DMABUF_ERR_ST6(bit15 -08)							
	DRWSQ6 ERR	DRW6 PPATH ERR	DRW6 ENCMD PERR	DRW6 FCTG PERR	DRW6 IFSIG ERR	Reserved	Reserved	Reserved
6B	LR: DMABUF_ERR_ST6(bit07 -00)							
	DRWSQ7 ERR	DRW7 PPATH ERR	DRW7 ENCMD PERR	DRW7 FCTG PERR	DRW7 IFSIG ERR	Reserved	Reserved	Reserved
6C	LR: DMABUF_ERR_ST7(bit31 -24)							
	DRWSQ8 ERR	DRW8 PPATH ERR	DRW8 ENCMD PERR	DRW8 FCTG PERR	DRW8 IFSIG ERR	Reserved	Reserved	Reserved
6D	LR: DMABUF_ERR_ST7(bit23 -16)							
	DRWSQ9 ERR	DRW9 PPATH ERR	DRW9 ENCMD PERR	DRW9 FCTG PERR	DRW9 IFSIG ERR	Reserved	Reserved	Reserved
6E	LR: DMABUF_ERR_ST7(bit15 -08)							
	DRWSQA ERR	DRWA PPATH ERR	DRWA ENCMD PERR	DRWA FCTG PERR	DRWA IFSIG ERR	Reserved	Reserved	Reserved
6F	LR: DMABUF_ERR_ST7(bit07 -00)							
	DRWSQB ERR	DRWB PPATH ERR	DRWB ENCMD PERR	DRWB FCTG PERR	DRWB IFSIG ERR	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(2) Byte28 = 44/54 (4/4)

	0	1	2	3	4	5	6	7
70	LR: DXBF_IF_CTL_ERR_ST0(bit31 -24)							
	PBCSEQ ERR	PADR PERR	PBI CNT PERR	XD PARA ERR	PBF DT PERRH	PBF DT PERRL	PBF LRCERR	PDLST ERR
71	LR: DXBF_IF_CTL_ERR_ST0(bit23 -16)							
	DBCSEQ ERR	DADR PERR	DLEN PERR	DCYCLE PERR	Reserved	Reserved	Reserved	Reserved
72	LR: DXBF_IF_CTL_ERR_ST0(bit15 -08)							
	RSBLKL CNT PERR	RSBLKH CNT PERR	RCYCLE CNT PERR	RDBF DT PERR	RDLST ERR	Reserved	Reserved	Reserved
73	LR: DXBF_IF_CTL_ERR_ST0(bit07 -00)							
	WSBLKL CNT PERR	RSBLKH CNT PERR	WCYCLE CNT PERR	DWSBLKL CNT PERR	DWSBLKH CNT PERR	DWSBLKL CNT PERR	DWDT PERR	Reserved
74	LR: DXBF_IF_CTL_ERR_ST1(bit31 -24)							
	IDXARB ERR	DXCIADR PERR	DXCILEN PERR	NSERNO PERR	CSERNO PERR	Reserved	Reserved	Reserved
75	LR: DXBF_IF_CTL_ERR_ST1(bit23 -16)							
	ACPT PERR	ACTT PERR	ACP TO	ACT TO	Reserved	Reserved	Reserved	Reserved
76	LR: DXBF_IF_CTL_ERR_ST1(bit15 -08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
77	LR: DXBF_IF_CTL_ERR_ST1(bit07 -00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	LR: DXBF_IF_CTL_ERR_ST2(bit31 -24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	LR: DXBF_IF_CTL_ERR_ST2(bit23 -16)							
	DXBFARB ROPERR	DXBFARB WOPERR	DXBFARB COPERR	Reserved	Reserved	Reserved	DXBFARB ROERR PERR	DXBFARB WOERR PERR
7A	LR: DXBF_IF_CTL_ERR_ST2(bit15 -08)							
	DXBFARB ROERR (7)	DXBFARB ROERR (6)	DXBFARB ROERR (5)	DXBFARB ROERR (4)	DXBFARB ROERR (3)	DXBFARB ROERR (2)	DXBFARB ROERR (1)	DXBFARB ROERR (0)
7B	LR: DXBF_IF_CTL_ERR_ST2(bit07 -00)							
	DXBFARB WOERR (7)	DXBFARB WOERR (6)	DXBFARB WOERR (5)	DXBFARB WOERR (4)	DXBFARB WOERR (3)	DXBFARB WOERR (2)	DXBFARB WOERR (1)	DXBFARB WOERR (0)
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

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Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH0_HSNRX_ERR_ST0(b31 - 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH0_HSNRX_ERR_ST0(b23 - 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMAPRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH0_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH0_HSNRX_ERR_ST0(b07 - 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH0_HSNTX_ERR_ST0(b31 - 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH0_HSNTX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH0_HSNTX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH0_HSNTX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH0_HSNTXR_ERR_ST0(b31 - 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH0_HSNTXR_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH0_HSNTXR_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH0_HSNTXR_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	LM: DMA PARM WORD0(bit31 - 00)							
4D								
4E								
4F								

SSBLOG05-1820

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1830

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (3/4)

59	Byte23 15/15/00/00 (0/1)							
60	LM: DMA PARM WORD6(bit31 - 00)							
61								
62								
63								
64	LM: DMA PARM WORD7(bit31 - 00)							
65								
66								
67								
68	LM: DMA STS WORD0(bit31 - 24)							
	XFREND	ABORT	OWN ERR	OTHER ERR	ERR2	ERR3	ERR1	Reserved
69	LM: DMA STS WORD0(bit23 - 16)							
	PID(7:0)							
6A	LM: DMA STS WORD0(bit15 - 08)							
	MPN(3:0)				REQID(11:8)			
6B	LM: DMA STS WORD0(bit07 - 00)							
	REQID(7:0)							
6C	LM: DMA STS WORD3(bit31 - 24)							
	LA WRSEQ ERR	LOGICAL PATH ERR	LA CMPERR (RD)	SLRC ERR (RD)	LA CMPERR (WT)	SLRC ERR (WT)	DECODE DATA SLRCERR	T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)							
	T10CRC CRCERR	OCRC ERR	EXTEND WRSEQ ERR	HSN TIMEOUT	DE-DUPLI CATION PTNERR	Reserved	USE P1 PATH	USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)							
	SEGDL(15:08)							
6F	LM: DMA STS WORD3(bit07 - 00)							
	SEGDL(07:00)							

SSBLOG05-1840

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(3) Byte28 = 48/49/58/59 (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1850

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (1/4)

	0	1	2	3	4	5	6	7
40	LR: DH1_HSNRX_ERR_ST0(b31 - 24)							
	PRDTD1 PERR	LACHK PERR	LAGEN PERR	T10CRC GEN PERR	OCRC GEN PERR	PRDTD2 PERR	PLA PERR	RBYTE CNT PERR
41	LR: DH1_HSNRX_ERR_ST0(b23 - 16)							
	RDLST ERR	RHD PERR	RSTS PERR	DMAPRM PERR	HRXSEQ ERR	RACTOFT PERR	RACTOF TO	Reserved
42	LR: DH1_HSNRX_ERR_ST0(b15 - 08)							
	HSNARB ROCPERR	HSNARB RODPERR	HSNARB HCRC ERR	HSNARB PCRC ERR	HSNARB RX HOTLINE	Reserved	Reserved	HSNARB ROERR PERR
43	LR: DH1_HSNRX_ERR_ST0(b07 - 00)							
	HSNARB ROERR (7)	HSNARB ROERR (6)	HSNARB ROERR (5)	HSNARB ROERR (4)	HSNARB ROERR (3)	HSNARB ROERR (2)	HSNARB ROERR (1)	HSNARB ROERR (0)
44	LR: DH1_HSNTX_ERR_ST0(b31 - 24)							
	HTXSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ PERR	Reserved	Reserved
45	LR: DH1_HSNTX_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
46	LR: DH1_HSNTX_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
47	LR: DH1_HSNTX_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	LR: DH1_HSNTXR_ERR_ST0(b31 - 24)							
	HTXRSEQ ERR	TACTONT PERR	TACTOFT PERR	TACTON TO	TACTOFF TO	MBDREQ ERR	Reserved	Reserved
49	LR: DH1_HSNTXR_ERR_ST0(b23 - 16)							
	HSNARB TOPERR	HSNARB TX HLERR	Reserved	Reserved	Reserved	Reserved	Reserved	HSNARB TOERR PERR
4A	LR: DH1_HSNTXR_ERR_ST0(b15 - 08)							
	HSNARB TOERR (7)	HSNARB TOERR (6)	HSNARB TOERR (5)	HSNARB TOERR (4)	HSNARB TOERR (3)	HSNARB TOERR (2)	HSNARB TOERR (1)	HSNARB TOERR (0)
4B	LR: DH1_HSNTXR_ERR_ST0(b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C 4D 4E 4F	LM: DMA PARM WORD0(bit31 -00)							

SSBLOG05-1860

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (2/4)

	0	1	2	3	4	5	6	7
50	LM: DMA PARM WORD1(bit31 - 00)							
51								
52								
53								
54	LM: DMA PARM WORD2(bit31 - 00)							
55								
56								
57								
58	LM: DMA PARM WORD4(bit31 - 00)							
59								
5A								
5B								
5C	LM: DMA PARM WORD5(bit31 - 00)							
5D								
5E								
5F								

SSBLOG05-1870

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (3/4)

(7)	Byte23	12	11	10	9	8	7	6	5	4	3	2	1	0
60	LM: DMA PARM WORD6(bit31 - 00)													
61														
62														
63														
64	LM: DMA PARM WORD7(bit31 - 00)													
65														
66														
67														
68	LM: DMA STS WORD0(bit31 - 24)													
	XFREND	ABORT		OWN ERR		OTHER ERR		ERR2		ERR3		ERR1		Reserved
69	LM: DMA STS WORD0(bit23 - 16)													
	PID(7:0)													
6A	LM: DMA STS WORD0(bit15 - 08)													
	MPN(3:0)								REQID(11:8)					
6B	LM: DMA STS WORD0(bit07 - 00)													
	REQID(7:0)													
6C	LM: DMA STS WORD3(bit31 - 24)													
	LA WRSEQ ERR	LOGICAL PATH ERR		LA CMPERR (RD)		SLRC ERR (RD)		LA CMPERR (WT)		SLRC ERR (WT)		DECODE DATA SLRCERR		T10CRC UDT CMPERR
6D	LM: DMA STS WORD3(bit23 - 16)													
	T10CRC CRCERR	OCRC ERR		EXTEND WRSEQ ERR		HSN TIMEOUT		DE-DUPLI CATION PTNERR		Reserved		USE P1 PATH		USE P0 PATH
6E	LM: DMA STS WORD3(bit15 - 08)													
	SEGDL(15:08)													
6F	LM: DMA STS WORD3(bit07 - 00)													
	SEGDL(07:00)													

Byte27 = 8A (DKA CHK2) Byte40-7F Detail

(4) Byte28 = 4c/4d/5c/5d (4/4)

	0	1	2	3	4	5	6	7
70	LM : HSNSTS1 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
71	LM : HSNSTS1 (bit55 -48)							
	LSI#							Reserved
72	LM : HSNSTS1 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
73	LM : HSNSTS1 (bit39 -32)							
	LSI INFO(7:0)							
74	LM : HSNSTS1 (bit31 -00)							
75								
76								
77								
78	LM : HSNSTS2 (bit63 -56)							
	ERR	WARNING	PATH ERR	Reserved	MODULE NUMBER(3:0)			
79	LM : HSNSTS2 (bit55 -48)							
	LSI#							Reserved
7A	LM : HSNSTS2 (bit47 -40)							
	Reserved	Reserved	Reserved	PATH#				
7B	LM : HSNSTS2 (bit39 -32)							
	LSI INFO(7:0)							
7C	LM : HSNSTS2 (bit31 -00)							
7D								
7E								
7F								

SSBLOG05-1890**Byte27 = 8B (DRR CHK2)**

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (Logical DKC in LDEV#)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU Physical Device#				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'B')			
28	Subcode (See following pages)							
29	Detail information (See following pages)							
31								
32	Module ID							
33	Routine ID							
34	P/K ID				Message code(x'0')			
35	SSID of self storage system (low)							
36	Symptom code (x'FF8B')							
37	(This information is not available before SSB reported to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	(Reserved)							
3D	Cylinder address							
3E								
3F	Head address							
40	Detail information (See following pages)							
7F								

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(1) Byte28 = 00/01

	0	1	2	3	4	5	6	7
28	PK_TYP0	PK_TYP1	PK_TYP2	MICRO_TOV	SubCode (x'00' : DRR0 Error) SubCode (x'01' : DRR1 Error)			
29	LR_INT_INTSTS0 (b31 – 24)							
	INT_CHK1B	LRP4_CHK1B	LRP3-0_CHK1B	SWIF_CHK1B	PARB1_CHK1B	PARB0_CHK1B	HARB3_CHK1B	HARB2_CHK1B
2A	LR_INT_INTSTS0 (b23 – 16)							
	HARB1_CHK1B	HARB0_CHK1B	XD_CHK1B	FMT_CHK1B	DX_CHK1B	DRR_CHK1B	DMAMB_CHK1B	DMA_CHK1B
2B	LR_INT_INTSTS0 (b15 – 08)							
	REG_CHK1B	SMBUS_CHK1B	FIVE1	FIVE0	CHK4	CHK3	CHK2	WARN
2C	LR_INT_INTSTS0 (b07 – 00)							
	Reserved	Reserved	MBOX INT	LRP4 INT	LRP3 INT	LRP2 INT	LRP1 INT	LRP0 INT
2D	LR_INT_STATUS8 (b31 – 24)							
	FRC_ERR	Reserved	INT_MFDM_A1_CHK2_P	INT_MFDM_A0_CHK2_P	Reserved	Reserved	INT_DRR1_CHK2	INT_DRR0_CHK2
2E	LR_INT_STATUS8 (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	INT_DMA3_CHK2	INT_DMA2_CHK2	INT_DMA1_CHK2	INT_DMA0_CHK2
2F	LR_INT_STATUS8 (b15 – 08)							
	INT_MFDM_A1_CTL_C_HK2_P(7)	INT_MFDM_A1_CTL_C_HK2_P(6)	INT_MFDM_A1_CTL_C_HK2_P(5)	INT_MFDM_A1_CTL_C_HK2_P(4)	INT_MFDM_A1_CTL_C_HK2_P(3)	INT_MFDM_A1_CTL_C_HK2_P(2)	INT_MFDM_A1_CTL_C_HK2_P(1)	INT_MFDM_A1_CTL_C_HK2_P(0)
30	LR_INT_STATUS8 (b07 – 00)							
	INT_MFDM_A0_CTL_C_HK2_P(7)	INT_MFDM_A0_CTL_C_HK2_P(6)	INT_MFDM_A0_CTL_C_HK2_P(5)	INT_MFDM_A0_CTL_C_HK2_P(4)	INT_MFDM_A0_CTL_C_HK2_P(3)	INT_MFDM_A0_CTL_C_HK2_P(2)	INT_MFDM_A0_CTL_C_HK2_P(1)	INT_MFDM_A0_CTL_C_HK2_P(0)
31	LR_INT_LR_STATUS (b31 – 24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(2) Byte28 = 08

	0	1	2	3	4	5	6	7
28	PK_TYP0	PK_TYP1	PK_TYP2	MICRO_TOV	SubCode (x'08' : HNS Path Error at DRR)			
29	LR_INT_INTSTS0 (b31 – 24)							
	INT_CHK1B	LRP4_CHK1B	LRP3-0_CHK1B	SWIF_CHK1B	PARB1_CHK1B	PARB0_CHK1B	HARB3_CHK1B	HARB2_CHK1B
2A	LR_INT_INTSTS0 (b23 – 16)							
	HARB1_CHK1B	HARB0_CHK1B	XD_CHK1B	FMT_CHK1B	DX_CHK1B	DRR_CHK1B	DMAMB_CHK1B	DMA_CHK1B
2B	LR_INT_INTSTS0 (b15 – 08)							
	REG_CHK1B	SMBUS_CHK1B	FIVE1	FIVE0	CHK4	CHK3	CHK2	WARN
2C	LR_INT_INTSTS0 (b07 – 00)							
	Reserved	Reserved	MBOX INT	LRP4 INT	LRP3 INT	LRP2 INT	LRP1 INT	LRP0 INT
2D	LR_INT_STATUS8 (b31 – 24)							
	FRC_ERR	Reserved	INT_MFDMA1_CHK2_P	INT_MFDMA0_CHK2_P	Reserved	Reserved	INT_DRR1_CHK2	INT_DRR0_CHK2
2E	LR_INT_STATUS8 (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	INT_DMA3_CHK2	INT_DMA2_CHK2	INT_DMA1_CHK2	INT_DMA3_CHK2
2F	LR_INT_STATUS1 (b15 – 08)							
	Reserved	Reserved	INT_HARB3REG_CHK1B_P	INT_HARB3_CHK1B_P	Reserved	Reserved	INT_HARB2REG_CHK1B_P	INT_HARB2_CHK1B_P
30	LR_INT_STATUS1 (b07 – 00)							
	Reserved	Reserved	INT_HARB1REG_CHK1B_P	INT_HARB1_CHK1B_P	Reserved	Reserved	INT_HARB0REG_CHK1B_P	INT_HARB0_CHK1B_P
31	LR_INT_LR_STATUS (b31 – 24)							
	Reserved	Reserved	Reserved	MOID	PKID	PKID	PKID	PKID

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01 (1/4)

	0	1	2	3	4	5	6	7
40	LR_DRR_ERR1_ST00 (b31 – 24)							
	ERR1 ST00 (31)	ERR1 ST00 (30)	ERR1 ST00 (29)	ERR1 ST00 (28)	ERR1 ST00 (27)	ERR1 ST00 (26)	ERR1 ST00 (25)	ERR1 ST00 (24)
41	LR_DRR_ERR1_ST00 (b23 – 16)							
	ERR1 ST00 (23)	ERR1 ST00 (22)	ERR1 ST00 (21)	ERR1 ST00 (20)	ERR1 ST00 (19)	ERR1 ST00 (18)	ERR1 ST00 (17)	ERR1 ST00 (16)
42	LR_DRR_ERR1_ST00 (b15 – 08)							
	ERR1 ST00 (15)	ERR1 ST00 (14)	ERR1 ST00 (13)	ERR1 ST00 (12)	ERR1 ST00 (11)	ERR1 ST00 (10)	ERR1 ST00 (9)	ERR1 ST00 (8)
43	LR_DRR_ERR1_ST00 (b07 – 00)							
	ERR1 ST00 (7)	ERR1 ST00 (6)	ERR1 ST00 (5)	ERR1 ST00 (4)	ERR1 ST00 (3)	ERR1 ST00 (2)	ERR1 ST00 (1)	ERR1 ST00 (0)
44	LR_DRR_ERR1_ST01 (b31 – 24)							
	ERR1 ST01 (31)	ERR1 ST01 (30)	ERR1 ST01 (29)	ERR1 ST01 (28)	ERR1 ST01 (27)	ERR1 ST01 (26)	ERR1 ST01 (25)	ERR1 ST01 (24)
45	LR_DRR_ERR1_ST01 (b23 – 16)							
	ERR1 ST01 (23)	ERR1 ST01 (22)	ERR1 ST01 (21)	ERR1 ST01 (20)	ERR1 ST01 (19)	ERR1 ST01 (18)	ERR1 ST01 (17)	ERR1 ST01 (16)
46	LR_DRR_ERR1_ST01 (b15 – 08)							
	ERR1 ST01 (15)	ERR1 ST01 (14)	ERR1 ST01 (13)	ERR1 ST01 (12)	ERR1 ST01 (11)	ERR1 ST01 (10)	ERR1 ST01 (9)	ERR1 ST01 (8)
47	LR_DRR_ERR1_ST01 (b07 – 00)							
	ERR1 ST01 (7)	ERR1 ST01 (6)	ERR1 ST01 (5)	ERR1 ST01 (4)	ERR1 ST01 (3)	ERR1 ST01 (2)	ERR1 ST01 (1)	ERR1 ST01 (0)
48	LR_DRR_ERR1_ST02 (b31 – 24)							
	ERR1 ST02 (31)	ERR1 ST02 (30)	ERR1 ST02 (29)	ERR1 ST02 (28)	ERR1 ST02 (27)	ERR1 ST02 (26)	ERR1 ST02 (25)	ERR1 ST02 (24)
49	LR_DRR_ERR1_ST02 (b23 – 16)							
	ERR1 ST02 (23)	ERR1 ST02 (22)	ERR1 ST02 (21)	ERR1 ST02 (20)	ERR1 ST02 (19)	ERR1 ST02 (18)	ERR1 ST02 (17)	ERR1 ST02 (16)
4A	LR_DRR_ERR1_ST02 (b15 – 08)							
	ERR1 ST02 (15)	ERR1 ST02 (14)	ERR1 ST02 (13)	ERR1 ST02 (12)	ERR1 ST02 (11)	ERR1 ST02 (10)	ERR1 ST02 (9)	ERR1 ST02 (8)
4B	LR_DRR_ERR1_ST02 (b07 – 00)							
	ERR1 ST02 (7)	ERR1 ST02 (6)	ERR1 ST02 (5)	ERR1 ST02 (4)	ERR1 ST02 (3)	ERR1 ST02 (2)	ERR1 ST02 (1)	ERR1 ST02 (0)
4C	LR_DRR_ERR1_ST03 (b31 – 24)							
	ERR1 ST03 (31)	ERR1 ST03 (30)	ERR1 ST03 (29)	ERR1 ST03 (28)	ERR1 ST03 (27)	ERR1 ST03 (26)	ERR1 ST03 (25)	ERR1 ST03 (24)
4D	LR_DRR_ERR1_ST03 (b23 – 16)							
	ERR1 ST03 (23)	ERR1 ST03 (22)	ERR1 ST03 (21)	ERR1 ST03 (20)	ERR1 ST03 (19)	ERR1 ST03 (18)	ERR1 ST03 (17)	ERR1 ST03 (16)
4E	LR_DRR_ERR1_ST03 (b15 – 08)							
	ERR1 ST03 (15)	ERR1 ST03 (14)	ERR1 ST03 (13)	ERR1 ST03 (12)	ERR1 ST03 (11)	ERR1 ST03 (10)	ERR1 ST03 (9)	ERR1 ST03 (8)
4F	LR_DRR_ERR1_ST03 (b07 – 00)							
	ERR1 ST03 (7)	ERR1 ST03 (6)	ERR1 ST03 (5)	ERR1 ST03 (4)	ERR1 ST03 (3)	ERR1 ST03 (2)	ERR1 ST03 (1)	ERR1 ST03 (0)

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01 (2/4)

	0	1	2	3	4	5	6	7
50	LR_DRR_ERR1_ST04 (b31 – 24)							
	ERR1 ST04 (31)	ERR1 ST04 (30)	ERR1 ST04 (29)	ERR1 ST04 (28)	ERR1 ST04 (27)	ERR1 ST04 (26)	ERR1 ST04 (25)	ERR1 ST04 (24)
51	LR_DRR_ERR1_ST04 (b23 – 16)							
	ERR1 ST04 (23)	ERR1 ST04 (22)	ERR1 ST04 (21)	ERR1 ST04 (20)	ERR1 ST04 (19)	ERR1 ST04 (18)	ERR1 ST04 (17)	ERR1 ST04 (16)
52	LR_DRR_ERR1_ST04 (b15 – 08)							
	ERR1 ST04 (15)	ERR1 ST04 (14)	ERR1 ST04 (13)	ERR1 ST04 (12)	ERR1 ST04 (11)	ERR1 ST04 (10)	ERR1 ST04 (9)	ERR1 ST04 (8)
53	LR_DRR_ERR1_ST04 (b07 – 00)							
	ERR1 ST04 (7)	ERR1 ST04 (6)	ERR1 ST04 (5)	ERR1 ST04 (4)	ERR1 ST04 (3)	ERR1 ST04 (2)	ERR1 ST04 (1)	ERR1 ST04 (0)
54	LR_DRR_ERR1_ST05 (b31 – 24)							
	ERR1 ST05 (31)	ERR1 ST05 (30)	ERR1 ST05 (29)	ERR1 ST05 (28)	ERR1 ST05 (27)	ERR1 ST05 (26)	ERR1 ST05 (25)	ERR1 ST05 (24)
55	LR_DRR_ERR1_ST05 (b23 – 16)							
	ERR1 ST05 (23)	ERR1 ST05 (22)	ERR1 ST05 (21)	ERR1 ST05 (20)	ERR1 ST05 (19)	ERR1 ST05 (18)	ERR1 ST05 (17)	ERR1 ST05 (16)
56	LR_DRR_ERR1_ST05 (b15 – 08)							
	ERR1 ST05 (15)	ERR1 ST05 (14)	ERR1 ST05 (13)	ERR1 ST05 (12)	ERR1 ST05 (11)	ERR1 ST05 (10)	ERR1 ST05 (9)	ERR1 ST05 (8)
57	LR_DRR_ERR1_ST05 (b07 – 00)							
	ERR1 ST05 (7)	ERR1 ST05 (6)	ERR1 ST05 (5)	ERR1 ST05 (4)	ERR1 ST05 (3)	ERR1 ST05 (2)	ERR1 ST05 (1)	ERR1 ST05 (0)
58	LR_DRR_ERR1_ST06 (b31 – 24)							
	ERR1 ST06 (31)	ERR1 ST06 (30)	ERR1 ST06 (29)	ERR1 ST06 (28)	ERR1 ST06 (27)	ERR1 ST06 (26)	ERR1 ST06 (25)	ERR1 ST06 (24)
59	LR_DRR_ERR1_ST06 (b23 – 16)							
	ERR1 ST06 (23)	ERR1 ST06 (22)	ERR1 ST06 (21)	ERR1 ST06 (20)	ERR1 ST06 (19)	ERR1 ST06 (18)	ERR1 ST06 (17)	ERR1 ST06 (16)
5A	LR_DRR_ERR1_ST06 (b15 – 08)							
	ERR1 ST06 (15)	ERR1 ST06 (14)	ERR1 ST06 (13)	ERR1 ST06 (12)	ERR1 ST06 (11)	ERR1 ST06 (10)	ERR1 ST06 (9)	ERR1 ST06 (8)
5B	LR_DRR_ERR1_ST06 (b07 – 00)							
	ERR1 ST06 (7)	ERR1 ST06 (6)	ERR1 ST06 (5)	ERR1 ST06 (4)	ERR1 ST06 (3)	ERR1 ST06 (2)	ERR1 ST06 (1)	ERR1 ST06 (0)
5C	LR_DRR_ERR1_ST07 (b31 – 24)							
	ERR1 ST07 (31)	ERR1 ST07 (30)	ERR1 ST07 (29)	ERR1 ST07 (28)	ERR1 ST07 (27)	ERR1 ST07 (26)	ERR1 ST07 (25)	ERR1 ST07 (24)
5D	LR_DRR_ERR1_ST07 (b23 – 16)							
	ERR1 ST07 (23)	ERR1 ST07 (22)	ERR1 ST07 (21)	ERR1 ST07 (20)	ERR1 ST07 (19)	ERR1 ST07 (18)	ERR1 ST07 (17)	ERR1 ST07 (16)
5E	LR_DRR_ERR1_ST07 (b15 – 08)							
	ERR1 ST07 (15)	ERR1 ST07 (14)	ERR1 ST07 (13)	ERR1 ST07 (12)	ERR1 ST07 (11)	ERR1 ST07 (10)	ERR1 ST07 (9)	ERR1 ST07 (8)
5F	LR_DRR_ERR1_ST07 (b07 – 00)							
	ERR1 ST07 (7)	ERR1 ST07 (6)	ERR1 ST07 (5)	ERR1 ST07 (4)	ERR1 ST07 (3)	ERR1 ST07 (2)	ERR1 ST07 (1)	ERR1 ST07 (0)

SSBLOG05-1940

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01 (3/4)

	0	1	2	3	4	5	6	7
60	DR_DRR0STS_W0 (b31 – 24)				DR_DRR0STS_W0			
61	DR_DRR0STS_W0 (b23 – 16)				DR_DRR0STS_W0			
62	DR_DRR0STS_W0 (b15 – 08)				DR_DRR0STS_W0			
63	DR_DRR0STS_W0 (b07 – 00)				DR_DRR0STS_W0			
64	DR_DRR0STS_W1 (b31 – 24)				DR_DRR0STS_W1			
65	DR_DRR0STS_W1 (b23 – 16)				DR_DRR0STS_W1			
66	DR_DRR0STS_W1 (b15 – 08)				DR_DRR0STS_W1			
67	DR_DRR0STS_W1 (b07 – 00)				DR_DRR0STS_W1			
68	DR_DRR0STS_W2 (b31 – 24)				DR_DRR0STS_W2			
69	DR_DRR0STS_W2 (b23 – 16)				DR_DRR0STS_W2			
6A	DR_DRR0STS_W2 (b15 – 08)				DR_DRR0STS_W2			
6B	DR_DRR0STS_W2 (b07 – 00)				DR_DRR0STS_W2			
6C	DR_DRR0STS_W3 (b31 – 24)				DR_DRR0STS_W3			
6D	DR_DRR0STS_W3 (b23 – 16)				DR_DRR0STS_W3			
6E	DR_DRR0STS_W3 (b15 – 08)				DR_DRR0STS_W3			
6F	DR_DRR0STS_W3 (b07 – 00)				DR_DRR0STS_W3			

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 = 00/01 (4/4)

	0	1	2	3	4	5	6	7
70	DR_DRR0STS_W4 (b31 – 24)				DR_DRR0STS_W4			
71	DR_DRR0STS_W4 (b23 – 16)				DR_DRR0STS_W4			
72	DR_DRR0STS_W4 (b15 – 08)				DR_DRR0STS_W4			
73	DR_DRR0STS_W4 (b07 – 00)				DR_DRR0STS_W4			
74	DR_DRR0STS_W5 (b31 – 24)				DR_DRR0STS_W5			
75	DR_DRR0STS_W5 (b23 – 16)				DR_DRR0STS_W5			
76	DR_DRR0STS_W5 (b15 – 08)				DR_DRR0STS_W5			
77	DR_DRR0STS_W5 (b07 – 00)				DR_DRR0STS_W5			
78	DR_DRR0STS_W6 (b31 – 24)				DR_DRR0STS_W6			
79	DR_DRR0STS_W6 (b23 – 16)				DR_DRR0STS_W6			
7A	DR_DRR0STS_W6 (b15 – 08)				DR_DRR0STS_W6			
7B	DR_DRR0STS_W6 (b07 – 00)				DR_DRR0STS_W6			
7C	DR_DRR0STS_W7 (b31 – 24)				DR_DRR0STS_W7			
7D	DR_DRR0STS_W7 (b23 – 16)				DR_DRR0STS_W7			
7E	DR_DRR0STS_W7 (b15 – 08)				DR_DRR0STS_W7			
7F	DR_DRR0STS_W7 (b07 – 00)				DR_DRR0STS_W7			

SSBLOG05-1960

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 08 (1/4)

	0	1	2	3	4	5	6	7
40	DR_DRRnSTS_W0 (b31 – 24)				DR_DRRnSTS_W0			
41	DR_DRRnSTS_W0 (b23 – 16)				DR_DRRnSTS_W0			
42	DR_DRRnSTS_W0 (b15 – 08)				DR_DRRnSTS_W0			
43	DR_DRRnSTS_W0 (b07 – 00)				DR_DRRnSTS_W0			
44	DR_DRRnSTS_W1 (b31 – 24)				DR_DRRnSTS_W1			
45	DR_DRRnSTS_W1 (b23 – 16)				DR_DRRnSTS_W1			
46	DR_DRRnSTS_W1 (b15 – 08)				DR_DRRnSTS_W1			
47	DR_DRRnSTS_W1 (b07 – 00)				DR_DRRnSTS_W1			
48	DR_DRRnSTS_W2 (b31 – 24)				DR_DRRnSTS_W2			
49	DR_DRRnSTS_W2 (b23 – 16)				DR_DRRnSTS_W2			
4A	DR_DRRnSTS_W2 (b15 – 08)				DR_DRRnSTS_W2			
4B	DR_DRRnSTS_W2 (b07 – 00)				DR_DRRnSTS_W2			
4C	DR_DRRnSTS_W3 (b31 – 24)				DR_DRRnSTS_W3			
4D	DR_DRRnSTS_W3 (b23 – 16)				DR_DRRnSTS_W3			
4E	DR_DRRnSTS_W3 (b15 – 08)				DR_DRRnSTS_W3			
4F	DR_DRRnSTS_W3 (b07 – 00)				DR_DRRnSTS_W3			

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 08 (2/4)

	0	1	2	3	4	5	6	7
50	DR_DRRnSTS_W4 (b31 – 24)							
	DR_DRRnSTS_W4							
51	DR_DRRnSTS_W4 (b23 – 16)							
	DR_DRRnSTS_W4							
52	DR_DRRnSTS_W4 (b15 – 08)							
	DR_DRRnSTS_W4							
53	DR_DRRnSTS_W4 (b07 – 00)							
	DR_DRRnSTS_W4							
54	DR_DRRnSTS_W5 (b31 – 24)							
	DR_DRRnSTS_W5							
55	DR_DRRnSTS_W5 (b23 – 16)							
	DR_DRRnSTS_W5							
56	DR_DRRnSTS_W5 (b15 – 08)							
	DR_DRRnSTS_W5							
57	DR_DRRnSTS_W5 (b07 – 00)							
	DR_DRRnSTS_W5							
58	DR_DRRnSTS_W6 (b31 – 24)							
	DR_DRRnSTS_W6							
59	DR_DRRnSTS_W6 (b23 – 16)							
	DR_DRRnSTS_W6							
5A	DR_DRRnSTS_W6 (b15 – 08)							
	DR_DRRnSTS_W6							
5B	DR_DRRnSTS_W6 (b07 – 00)							
	DR_DRRnSTS_W6							
5C	DR_DRRnSTS_W7 (b31 – 24)							
	DR_DRRnSTS_W7							
5D	DR_DRRnSTS_W7 (b23 – 16)							
	DR_DRRnSTS_W7							
5E	DR_DRRnSTS_W7 (b15 – 08)							
	DR_DRRnSTS_W7							
5F	DR_DRRnSTS_W7 (b07 – 00)							
	DR_DRRnSTS_W7							

SSBLOG05-1980

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 08 (3/4)

	0	1	2	3	4	5	6	7
60	LR_DRR_RXB_HSNSTS0H (b31 – 24)							
	RXB HSNSTS #0 (63)	RXB HSNSTS #0 (62)	RXB HSNSTS #0 (61)	RXB HSNSTS #0 (60)	RXB HSNSTS #0 (59)	RXB HSNSTS #0 (58)	RXB HSNSTS #0 (57)	RXB HSNSTS #0 (56)
61	LR_DRR_RXB_HSNSTS0H (b23 – 16)							
	RXB HSNSTS #0 (55)	RXB HSNSTS #0 (54)	RXB HSNSTS #0 (53)	RXB HSNSTS #0 (52)	RXB HSNSTS #0 (51)	RXB HSNSTS #0 (50)	RXB HSNSTS #0 (49)	RXB HSNSTS #0 (48)
62	LR_DRR_RXB_HSNSTS0H (b15 – 08)							
	RXB HSNSTS #0 (47)	RXB HSNSTS #0 (46)	RXB HSNSTS #0 (45)	RXB HSNSTS #0 (44)	RXB HSNSTS #0 (43)	RXB HSNSTS #0 (42)	RXB HSNSTS #0 (41)	RXB HSNSTS #0 (40)
63	LR_DRR_RXB_HSNSTS0H (b07 – 00)							
	RXB HSNSTS #0 (39)	RXB HSNSTS #0 (38)	RXB HSNSTS #0 (37)	RXB HSNSTS #0 (36)	RXB HSNSTS #0 (35)	RXB HSNSTS #0 (34)	RXB HSNSTS #0 (33)	RXB HSNSTS #0 (32)
64	LR_DRR_RXB_HSNSTS0L (b31 – 24)							
	RXB HSNSTS #0 (31)	RXB HSNSTS #0 (30)	RXB HSNSTS #0 (29)	RXB HSNSTS #0 (28)	RXB HSNSTS #0 (27)	RXB HSNSTS #0 (26)	RXB HSNSTS #0 (25)	RXB HSNSTS #0 (24)
65	LR_DRR_RXB_HSNSTS0L (b23 – 16)							
	RXB HSNSTS #0 (23)	RXB HSNSTS #0 (22)	RXB HSNSTS #0 (21)	RXB HSNSTS #0 (20)	RXB HSNSTS #0 (19)	RXB HSNSTS #0 (18)	RXB HSNSTS #0 (17)	RXB HSNSTS #0 (16)
66	LR_DRR_RXB_HSNSTS0L (b15 – 08)							
	RXB HSNSTS #0 (15)	RXB HSNSTS #0 (14)	RXB HSNSTS #0 (13)	RXB HSNSTS #0 (12)	RXB HSNSTS #0 (11)	RXB HSNSTS #0 (10)	RXB HSNSTS #0 (9)	RXB HSNSTS #0 (8)
67	LR_DRR_RXB_HSNSTS0L (b07 – 00)							
	RXB HSNSTS #0 (7)	RXB HSNSTS #0 (6)	RXB HSNSTS #0 (5)	RXB HSNSTS #0 (4)	RXB HSNSTS #0 (3)	RXB HSNSTS #0 (2)	RXB HSNSTS #0 (1)	RXB HSNSTS #0 (0)
68	LR_DRR_TXB0_HSNSTS0H (b31 – 24)							
	TXB0 HSNSTS #0 (63)	TXB0 HSNSTS #0 (62)	TXB0 HSNSTS #0 (61)	TXB0 HSNSTS #0 (60)	TXB0 HSNSTS #0 (59)	TXB0 HSNSTS #0 (58)	TXB0 HSNSTS #0 (57)	TXB0 HSNSTS #0 (56)
69	LR_DRR_TXB0_HSNSTS0H (b23 – 16)							
	TXB0 HSNSTS #0 (55)	TXB0 HSNSTS #0 (54)	TXB0 HSNSTS #0 (53)	TXB0 HSNSTS #0 (52)	TXB0 HSNSTS #0 (51)	TXB0 HSNSTS #0 (50)	TXB0 HSNSTS #0 (49)	TXB0 HSNSTS #0 (48)
6A	LR_DRR_TXB0_HSNSTS0H (b15 – 08)							
	TXB0 HSNSTS #0 (47)	TXB0 HSNSTS #0 (46)	TXB0 HSNSTS #0 (45)	TXB0 HSNSTS #0 (44)	TXB0 HSNSTS #0 (43)	TXB0 HSNSTS #0 (42)	TXB0 HSNSTS #0 (41)	TXB0 HSNSTS #0 (40)
6B	LR_DRR_TXB0_HSNSTS0H (b07 – 00)							
	TXB0 HSNSTS #0 (39)	TXB0 HSNSTS #0 (38)	TXB0 HSNSTS #0 (37)	TXB0 HSNSTS #0 (36)	TXB0 HSNSTS #0 (35)	TXB0 HSNSTS #0 (34)	TXB0 HSNSTS #0 (33)	TXB0 HSNSTS #0 (32)
6C	LR_DRR_TXB0_HSNSTS0L (b31 – 24)							
	TXB0 HSNSTS #0 (31)	TXB0 HSNSTS #0 (30)	TXB0 HSNSTS #0 (29)	TXB0 HSNSTS #0 (28)	TXB0 HSNSTS #0 (27)	TXB0 HSNSTS #0 (26)	TXB0 HSNSTS #0 (25)	TXB0 HSNSTS #0 (24)
6D	LR_DRR_TXB0_HSNSTS0L (b23 – 16)							
	TXB0 HSNSTS #0 (23)	TXB0 HSNSTS #0 (22)	TXB0 HSNSTS #0 (21)	TXB0 HSNSTS #0 (20)	TXB0 HSNSTS #0 (19)	TXB0 HSNSTS #0 (18)	TXB0 HSNSTS #0 (17)	TXB0 HSNSTS #0 (16)
6E	LR_DRR_TXB0_HSNSTS0L (b15 – 08)							
	TXB0 HSNSTS #0 (15)	TXB0 HSNSTS #0 (14)	TXB0 HSNSTS #0 (13)	TXB0 HSNSTS #0 (12)	TXB0 HSNSTS #0 (11)	TXB0 HSNSTS #0 (10)	TXB0 HSNSTS #0 (9)	TXB0 HSNSTS #0 (8)
6F	LR_DRR_TXB0_HSNSTS0L (b07 – 00)							
	TXB0 HSNSTS #0 (7)	TXB0 HSNSTS #0 (6)	TXB0 HSNSTS #0 (5)	TXB0 HSNSTS #0 (4)	TXB0 HSNSTS #0 (3)	TXB0 HSNSTS #0 (2)	TXB0 HSNSTS #0 (1)	TXB0 HSNSTS #0 (0)

SSBLOG05-1990

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 08 (4/4)

	0	1	2	3	4	5	6	7
70	LR_DRR_TXB1_HSNSTS0H (b31 – 24)							
	TXB1 HSNSTS #0 (63)	TXB1 HSNSTS #0 (62)	TXB1 HSNSTS #0 (61)	TXB1 HSNSTS #0 (60)	TXB1 HSNSTS #0 (59)	TXB1 HSNSTS #0 (58)	TXB1 HSNSTS #0 (57)	TXB1 HSNSTS #0 (56)
71	LR_DRR_TXB1_HSNSTS0H (b23 – 16)							
	TXB1 HSNSTS #0 (55)	TXB1 HSNSTS #0 (54)	TXB1 HSNSTS #0 (53)	TXB1 HSNSTS #0 (52)	TXB1 HSNSTS #0 (51)	TXB1 HSNSTS #0 (50)	TXB1 HSNSTS #0 (49)	TXB1 HSNSTS #0 (48)
72	LR_DRR_TXB1_HSNSTS0H (b15 – 08)							
	TXB1 HSNSTS #0 (47)	TXB1 HSNSTS #0 (46)	TXB1 HSNSTS #0 (45)	TXB1 HSNSTS #0 (44)	TXB1 HSNSTS #0 (43)	TXB1 HSNSTS #0 (42)	TXB1 HSNSTS #0 (41)	TXB1 HSNSTS #0 (40)
73	LR_DRR_TXB1_HSNSTS0H (b07 – 00)							
	TXB1 HSNSTS #0 (39)	TXB1 HSNSTS #0 (38)	TXB1 HSNSTS #0 (37)	TXB1 HSNSTS #0 (36)	TXB1 HSNSTS #0 (35)	TXB1 HSNSTS #0 (34)	TXB1 HSNSTS #0 (33)	TXB1 HSNSTS #0 (32)
74	LR_DRR_TXB1_HSNSTS0L (b31 – 24)							
	TXB1 HSNSTS #0 (31)	TXB1 HSNSTS #0 (30)	TXB1 HSNSTS #0 (29)	TXB1 HSNSTS #0 (28)	TXB1 HSNSTS #0 (27)	TXB1 HSNSTS #0 (26)	TXB1 HSNSTS #0 (25)	TXB1 HSNSTS #0 (24)
75	LR_DRR_TXB1_HSNSTS0L (b23 – 16)							
	TXB1 HSNSTS #0 (23)	TXB1 HSNSTS #0 (22)	TXB1 HSNSTS #0 (21)	TXB1 HSNSTS #0 (20)	TXB1 HSNSTS #0 (19)	TXB1 HSNSTS #0 (18)	TXB1 HSNSTS #0 (17)	TXB1 HSNSTS #0 (16)
76	LR_DRR_TXB1_HSNSTS0L (b15 – 08)							
	TXB1 HSNSTS #0 (15)	TXB1 HSNSTS #0 (14)	TXB1 HSNSTS #0 (13)	TXB1 HSNSTS #0 (12)	TXB1 HSNSTS #0 (11)	TXB1 HSNSTS #0 (10)	TXB1 HSNSTS #0 (9)	TXB1 HSNSTS #0 (8)
77	LR_DRR_TXB1_HSNSTS0L (b07 – 00)							
	TXB1 HSNSTS #0 (7)	TXB1 HSNSTS #0 (6)	TXB1 HSNSTS #0 (5)	TXB1 HSNSTS #0 (4)	TXB1 HSNSTS #0 (3)	TXB1 HSNSTS #0 (2)	TXB1 HSNSTS #0 (1)	TXB1 HSNSTS #0 (0)
78	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7B	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	Reserved (b31 – 24)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved (b23 – 16)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved (b15 – 08)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved (b07 – 00)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

SSBLOG05-2000

Byte27 = 8C (SVP Failure : SVP(PC)) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'C')			
28								
29	Error information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (*1)							
31								
32								
33	Error information (See following pages)							
34								
35								
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address (x'0000')							
3E								
3F	Head address							

Byte27 = 8C (SVP Failure : SVP(PC)) (2/2)

	0	1	2	3	4	5	6	7
40	Error information (See following pages)							
7F								

*1: Error type code

- x'0000' : Logical inconsistency
- x'0001' : HEAP related error
- x'0002' : File related error
- x'0003' : LAN failure
- x'0004' : SSVP failure
- x'0005' : Windows failure
- x'0006' : CUDG detected error
- x'0009' : BOOT detected error
- x'000A' : BOOT detected error
- x'000B' : Router failure
- x'000C' : Disable SVP reboot
- x'000D' : Stop SSB overwrite
- x'000E' : Stop SSBS overwrite
- x'000F' : Stop SSBS overwrite
- x'0015' : Battery failure
- x'0030' : Delay of time
- x'0031' : Warning of a stack
- x'0032' : Single task error
- x'0033' : SVP launcher error
- x'0034' : SVP reboot
- x'0035' : SVP reboot (WSAENOBUFF)
- x'0036' : Standby SVP fail
- x'0037' : SVP fail over
- x'0038' : SVP change refusal
- x'0039' : Ip address setting error
- x'003A' : Subnet mask setting error
- x'003B' : Virus detection error
- x'003C' : Virus detection error
- x'003D' : Virus detection error
- x'003E' : SVP environment error
- x'003F' : Situation observation error
- x'0040' : Dummy PS/Off starting (normal)

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

- (1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure / Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error / SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over / SVP change refusal / Ip address setting error / Subnet mask setting error / Virus detection error / SVP environment error / Situation observation error / Dummy PS/Off starting (normal)

	0	1	2	3	4	5	6	7
28	Failed AP code (*1)							
29								
2A	Reserved							
2B								
2F	Error type code (*2)							
30								
31	Error code							
32								
33								
34								
35								
40	Failed Window class							
41								
47	Failed function name							
48								
5F	Failed line number							
60								
61	Error code							
62								
63								
64								
65	Reserved							
66								
67								
7F								

*1: Failed AP code

x'0000' : Dump task

x'0001' : DKU inline

x'0002' : Microprogram change (online)

x'0003' : Information

x'0004' : LOGOUT

x'0005' : PATH inline

x'0006' : Back Ground

x'0007' : DIAG

x'0008' : Install

x'0009' : Online

x'000A' : Micro-program change (off-line)

x'000B' : Special package change

x'000C' : Common function for communication

x'00FF' : Others

***2: Error type code**

x'0000' : Logical inconsistency
x'0001' : HEAP related error
x'0002' : File related error
x'0003' : LAN failure
x'0005' : Windows failure
x'000A' : SVP message
x'000B' : Router failure
x'000C' : Disable SVP reboot
x'000D' : Stop SSB overwrite
x'000E' : Stop SSBS overwrite
x'000F' : Flash memory failure
x'0015' : Battery failure
x'0030' : Delay of time
x'0031' : Warning of a stack
x'0032' : Single task error
x'0033' : SVP launcher error
x'0034' : SVP reboot
x'0035' : SVP reboot(WSAENOBUFF)
x'0036' : Standby SVP fail
x'0037' : SVP fail over
x'0038' : SVP change refusal
x'0039' : Ip address setting error
x'003A' : Subnet mask setting error
x'003B' : Virus detection error
x'003C' : Virus detection error
x'003D' : Virus detection error
x'003E' : Virus detection error
x'003F' : Situation observation error
x'0040' : Dummy PS/Off starting (normal)

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Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(2) SSVP Failure

	0	1	2	3	4	5	6	7
28	Failed AP code (x'00FF')							
29								
2A	Reserved							
2B								
2F	Error type code (x'0004': SSVP Failure)							
30								
31	Reserved							
32								
33	SSVP error code							
34								
35								
40	SSVP IMPL Sequence							
41	Detect Code							
42	Part Code							
43								
44								
45								
46								
47								
48	Detail Information							
49								
4A								
4B								
4C								
4D								
4E								
4F								
50	Reserved							
51								
52								
53								
54								
55								
56								
57								
7F								

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Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(3) CUDG detected error

	0	1	2	3	4	5	6	7
28	'CUDG ERR' (x'43 55 44 47 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0006: CUDG detected error)							
31								
32								
33	Error code (x'00000000')							
34								
35								
40	Error occurred time (year)							
41	Error occurred time (month)							
42	Error occurred time (day)							
43	Error occurred time (hour)							
44	Error occurred time (minute)							
45	Error occurred time (second)							
46	Report MPID							
47	Test object							
48	Test type (x'01' : CUDG3, x'10' : FCDG3)							
49	Not used							
4A	Test contents							
4B	Test contents							
4C	Error code							
4D	Error code							
4E	Not used							
4F	Not used							
50	Cache Module Group No. (slave 1)							
51	Cache Module Group No. (slave 1)							
52	Cache Module Group No. (slave 1)							
53	Cache Module Group No. (slave 1)							
54	Cache Module Group No. (slave 2)							
55	Cache Module Group No. (slave 2)							
56	Cache Module Group No. (slave 2)							
57	Cache Module Group No. (slave 2)							
58	PCB Location (Main Platter)							
59	PCB Location (Main Platter)							
5A	PCB Location (Main Platter)							
5B	PCB Location (Cache Platter 1)							
5C	PCB Location (Cache Platter 1)							
5D	PCB Location (Cache Platter 2)							
5E	PCB Location (Cache Platter 2)							
5F	RUN Option							
60	Additional information (32byte) (*1)							
61								
62								
63								
64								
65								
66								
7F								

*1: Additional information (for FCDG3 only)

	0	1	2	3	4	5	6	7
60	P Path number (x'00' ~ x'03')							
61	Reserved							
62								
63								
64	Record type (x'10' : HTP IMPL diagnostic)							
65	HTP Map (0x01 = HTP0, 0x02 = HTP1, 0x04 = HTP2, 0x08 = HTP3, 0x10 = HTP4, 0x20 = HTP5, 0x40 = HTP6, 0x80 = HTP7)							
66	LR number (x'00' ~ x'1F', x'FF' : indefinite)							
67	Port Map (0x80=Port0, 0x40=Port1, 0x20=Port2, 0x10=Port3, 0x08=Port4, 0x04=Port5, 0x02=Port6, 0x01=Port7)							
68	Error code (*2)							
69	Process type (*3)							
6A	Error status1 (Record type = x'10': See (*4))							
6B								
6C	Error status2							
6E								
6F								
70								
71								
72								
73								
74								
7F								

*2: Error code

Code	Meaning	Function	Remarks
0A	FCDG3' load error	FCDG3'	Make status in loading process
10	Memory clear and test error	FCDG3'	
18	FCDG3 MP detected error	FCDG3	Detail information in error status 1
1A	FCDG3 load error	FCDG3	Make status in loading process
20	SCAN detected error	All	MP detected error

*3: Process type

Code	Process	Remarks
01	FCDG3'	Set loading error status when a loading error occurred.
02	FCDG3	Set loading error status when a loading error occurred.
03	Loading	

*4: Error status1 (Record type (Byte 64) = x'10' : FCDG3)

Code	Meaning
0101	FCDG3' Waiting for end of FIVE diagnosis timeout
0104	FCDG3' MCHK Detected in FIVE diagnosis (except Soft MCHK)
0105	FCDG3' CHK1B Detected
010f	FCDG3' Invalid Parameter(s) Received
0110	FCDG3' FIVE diagnosis result, Error detected in preliminary diagnosis
0111	FCDG3' FIVE diagnosis result, Error detected in Memory diagnosis (Fixed data)
0112	FCDG3' Error detected in Memory diagnosis (Sequential access)
0113	FCDG3' FIVE diagnosis result, Error detected in Memory diagnosis (Inc data)
0114	FCDG3' FIVE diagnosis result, Error detected in Memory diagnosis (random data)
0115	FCDG3' FIVE diagnosis result, Failure detected in CS access diagnosis
0120	FCDG3' Time-Out error in FIVE inside memory initialization
0170	FCDG3' Invalid Parameter(s) received in FIVE inside memory initialization
0171	FCDG3' Invalid PCB Indicated
0181	FCDG3' Error of except MCHK detected in FIVE diagnosis
01A1	FCDG3' CHK4 Detected
01C0	FCDG3' FIVE diagnosis result, Correctable error detected
01D0	FCDG3' Register read error
01D1	FCDG3' Register write error
08xx	FCDG3 FIVE processor diagnosis
12xx	FCDG3 LINK diagnosis
13xx	FCDG3 Frame diagnosis
14xx	FCDG3 SFP information acquisition
15xx	FCDG3 CAM function diagnosis
21xx	FCDG3 FIVE data transport diagnosis (MS request)
22xx	FCDG3 FIVE data transport diagnosis (burst)
23xx	FCDG3 WS direct space diagnosis
24xx	Indirect access Write/Read Test
25xx	Scan space Write/Read Test
30xx	FCDG3 diagnosis
32xx	FCDG3' Load area diagnosis

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xx: 02	MPCK Detected	70	Invalid Parameter(s) Received
03	FIVE diagnosis result is Other Errors	71	Invalid PCB Received
04	Hard MCHK Detected	72	Invalid Package Speed Detected
20	Time-out	76	Invalid SFP Speed
21	Frame transmission Time-out	77	SFP4G/PK8G Error
22	Frame reception Time-out	78	SFP8G/PK4G Error
24	Loop Count Over	80	FIVE MCK Detected
30	CHK1B Detected	81	FIVE CHK1B or CHK4 Detected
40	Compare Error	A1	CHK4 Detected
61	Store burst (STB) transmission Error	C0	Correctable Error
62	Fetch burst (FTB) transmission Error	D0	Register read error
63	Store (ST) transmission Error Detected	D1	Register write error
64	Fetch (FT) transmission Error Detected	D2	MCH_DMA error
		E0	LRP Support (MailBOX) error
		E1	Virtual Queue clear error
		E2	Port Initial-setting error
		F0	ULP Flame transmission Queue is full

Byte27 = 8C (SVP Failure : SVP(PC)) Error information detail

(4) BOOT detected error

	0	1	2	3	4	5	6	7
28	'BOOT ERR' (x'42 4F 4F 54 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0009' : BOOT detected error)							
31								
32	Error code (x'00000000' X: Processor number)							
33								
34								
35								
40	Error detection point							
41								
42								
43								
4F	Reserved							
50								
51								
52								
53	Error code for loading error							
54								
55								
56								
57	Error code of internal interface							
58								
59								
5A								
5B	Error code of initialization function (*1)							
5C								
5D								
5E								
5F	Reserved							
60								
61								
7F								

*1: Error code of initialization function.

x'31000041' : Forcible start with jumper pin.

x'40000041' : Load module ID in FM is incorrect.

(FM writing was failed or load module is incorrect)

x'42000041' : Sum check error of FM.

(Failure occurred at FM writing or input / output error of FM occurred)

x'48000041' : This MP has the micro-programs which type is different from the PCB name.

(In the MP-Install procedure, the package may have been set which type is different from one that is specified in the define configuration.)

x'56000041' : Sum check error of the load module.

(SVP Failure occurred)

others : Loading for the load module impossible.

(The load module was incorrect)

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Byte27 = 8D (Power Failure) Common information (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'D')			
28	Hardware information (See following pages)							
29								
30								
31								
32	Not used							
33								
34	PCB							
35	SSID of self storage system (low order)							
36	Symptom code (x'FF8D' : LDEV type = DKU87I/x'EF8D' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

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Byte27 = 8D (Power Failure) Common information (2/2)

	0	1	2	3	4	5	6	7
40	Micro control information							
7F								

Byte27 = 8D (Power Failure) Byte28-31 detail (1/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'40' : DKC Power Failure)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Power Failure Hardware Information							
	ACDCALARM value							
2B	Micro Information							
	Power Failure Status Management Information							
2C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8D (Power Failure) Byte28-31 detail (2/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'80' : DKU/HDU Power Failure)							
29	Power supply disorder location							
	DKU#							
2A	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2B	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8D (Power Failure) Byte28-31 detail (3/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'90' : DKA detected DKU/HDU Power Recover)							
29	Power supply disorder location							
	DKU#							
2A	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2B	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

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Byte27 = 8E & Byte34 = X0 & Byte28 ≠ F0 (CHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID (*1)							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	Controller No.	DKU Physical Device#				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40								
41	Not used							
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								

*1: Failed processor number for CHK1

Byte27 = 8E (CHK1)

(1) Byte28 = 20

	0	1	2	3	4	5	6	7
28	Subcode (x'20')							
29	MP Number							
	MP Number							
2A	IA32_MC9_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2B	IA32_MC9_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 00
2C	IA32_MC10_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2D	IA32_MC10_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 00
2E	IA32_MC5_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2F	IA32_MC9_STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
30	IA32_MC9_STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
31	IA32_MC9_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 00

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Byte27 = 8E (CHK1)

(2) Byte28 = 21

	0	1	2	3	4	5	6	7
28	Subcode (x'21')							
29	MP Number							
	MP Number							
2A	IA32_MC4 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2B	IA32_MC5 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2C	IA32_MC6 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2D	IA32_MC11 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2E	GFERRST:B3							
	GFER _31	GFER _30	GFER _29	GFER _28	GFER _27	GFER _26	GFER _25	GFER _24
2F	GFERRST:B2							
	GFER _23	GFER _22	GFER _21	GFER _20	GFER _19	GFER _18	GFER _17	GFER _16
30	GFERRST:B1							
	GFER _15	GFER _14	GFER _13	GFER _12	GFER _11	GFER _10	GFER _09	GFER _08
31	GFERRST:B0							
	GFER _07	GFER _06	GFER _05	GFER _04	GFER _03	GFER _02	GFER _01	GFER _00

Byte27 = 8E (CHK1)

(3) Byte28 = 22 or 23 or 24 or 25

	0	1	2	3	4	5	6	7
28	Subcode (x'22' or x'23' or x'24' or x'25')							
29	MP Number							
	MP Number							
2A	NMI STAT1							
	NMI STAT 17	NMI STAT 16	NMI STAT 15	NMI STAT 14	NMI STAT 13	NMI STAT 12	NMI STAT 11	NMI STAT 10
2B	NMI STAT0							
	NMI STAT 07	NMI STAT 06	NMI STAT 05	NMI STAT 04	NMI STAT 03	NMI STAT 02	NMI STAT 01	NMI STAT 00
2C	OTHER ISTAT1							
	OTHER ISTAT17	OTHER ISTAT16	OTHER ISTAT15	OTHER ISTAT14	OTHER ISTAT13	OTHER ISTAT12	OTHER ISTAT11	OTHER ISTAT10
2D	OTHER ISTAT0							
	OTHER ISTAT07	OTHER ISTAT06	OTHER ISTAT05	OTHER ISTAT04	OTHER ISTAT03	OTHER ISTAT02	OTHER ISTAT01	OTHER ISTAT00
2E	GFERRST:B3							
	GFER _31	GFER _30	GFER _29	GFER _28	GFER _27	GFER _26	GFER _25	GFER _31
2F	GFERRST:B2							
	GFER _23	GFER _22	GFER _21	GFER _20	GFER _19	GFER _18	GFER _17	GFER _23
30	GFERRST:B1							
	GFER _15	GFER _14	GFER _13	GFER _12	GFER _11	GFER _10	GFER _09	GFER _15
31	GFERRST:B0							
	GFER _07	GFER _06	GFER _05	GFER _04	GFER _03	GFER _02	GFER _01	GFER _07

Byte27 = 8E (CHK1)

(4) Byte28 = 30

	0	1	2	3	4	5	6	7
28	Subcode (x'30')							
29	MP Number							
	MP Number							
2A	GFERRST:B3							
	GFER _31	GFER _30	GFER _29	GFER _28	GFER _27	GFER _26	GFER _25	GFER _24
2B	GFERRST:B2							
	GFER _23	GFER _22	GFER _21	GFER _20	GFER _19	GFER _18	GFER _17	GFER _16
2C	GFERRST:B1							
	GFER _15	GFER _14	GFER _13	GFER _12	GFER _11	GFER _10	GFER _09	GFER _08
2D	GFERRST:B0							
	GFER _07	GFER _06	GFER _05	GFER _04	GFER _03	GFER _02	GFER _01	GFER _00
2E	GNERRST:B3							
	GNER _31	GNER _30	GNER _29	GNER _28	GNER _27	GNER _26	GNER _25	GNER _24
2F	GNERRST:B2							
	GNER _23	GNER _22	GNER _21	GNER _20	GNER _19	GNER _18	GNER _17	GNER _16
30	GNERRST:B1							
	GNER _15	GNER _14	GNER _13	GNER _12	GNER _11	GNER _10	GNER _09	GNER _08
31	GNERRST:B0							
	GNER _07	GNER _06	GNER _05	GNER _04	GNER _03	GNER _02	GNER _01	GNER _00

Byte27 = 8E (CHK1)

(5) Byte28 = 40

	0	1	2	3	4	5	6	7
28	Subcode (x'40')							
29	MP Number							
	MP Number							
2A	IA32_MC9_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2B	IA32_MC9_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 00
2C	IA32_MC10_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
2D	IA32_MC10_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 00
2E	IA32_MC5_STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	VAL
2F	IA32_MC9_STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
30	IA32_MC9_STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
31	IA32_MC9_STATUS B0							
	MCA ErCode 07	MCA ErCode 06	MCA ErCode 05	MCA ErCode 04	MCA ErCode 03	MCA ErCode 02	MCA ErCode 01	MCA ErCode 07

Byte27 = 8E (CHK1)

(6) Byte28 = 41 or 42 or 43

	0	1	2	3	4	5	6	7
28	Subcode (x'41' or x'42' or x'43')							
29	MP Number							
	MP Number							
2A	INT0 ISTAT1							
	I STATE 17	I STATE 16	I STATE 15	I STATE 14	I STATE 13	I STATE 12	I STATE 11	I STATE 10
2B	INT0 ISTAT0							
	I STATE 07	I STATE 06	I STATE 05	I STATE 04	I STATE 03	I STATE 02	I STATE 01	I STATE 00
2C								
	0	0	0	0	0	0	0	0
2D								
	0	0	0	0	0	0	0	0
2E	GNERRST:B3							
	GNER _31	GNER _30	GNER _29	GNER _28	GNER _27	GNER _26	GNER _25	GNER _24
2F	GNERRST:B2							
	GNER _23	GNER _22	GNER _21	GNER _20	GNER _19	GNER _18	GNER _17	GNER _16
30	GNERRST:B1							
	GNER _15	GNER _14	GNER _13	GNER _12	GNER _11	GNER _10	GNER _09	GNER _08
31	GNERRST:B0							
	GNER _07	GNER _06	GNER _05	GNER _04	GNER _03	GNER _02	GNER _01	GNER _00

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Byte27 = 8E (CHK1)

(7) Byte28 = 44 or 45

	0	1	2	3	4	5	6	7
28	Subcode (x'44' or x'45')							
29	MP Number							
	MP Number							
2A	INT0 ISTAT1							
	I STATE _17	I STATE _16	I STATE _15	I STATE _14	I STATE _13	I STATE _12	I STATE _11	I STATE _10
2B	INT0 ISTAT0							
	I STATE _07	I STATE _06	I STATE _05	I STATE _04	I STATE _03	I STATE _02	I STATE _01	I STATE _00
2C	IA32 THERM STATUS							
	Therm Thres _1 Log	Therm Thres _1 Status	Out of Spec Status_Log	Out of Spec Status	PROC# or FRCEPR# _Log	PROC# or FRCEPR# _Event	Thermal _Status Log	Thermal _Status
2D	ESR							
	Illegal Reg _Adr	Recvd Illegal _Vector	Send Illegal _Vector	Reserved	Receive _Acpt _Error	Send Acpt _Error	Receive Chk SUM Error	Send Chk SUM Error
2E	GNERRST:B3							
	GNER _31	GNER _30	GNER _29	GNER _28	GNER _27	GNER _26	GNER _25	GNER _24
2F	GNERRST:B2							
	GNER _23	GNER _22	GNER _21	GNER _20	GNER _19	GNER _18	GNER _17	GNER _16
30	GNERRST:B1							
	GNER _15	GNER _14	GNER _13	GNER _12	GNER _11	GNER _10	GNER _09	GNER _08
31	GNERRST:B0							
	GNER _07	GNER _06	GNER _05	GNER _04	GNER _03	GNER _02	GNER _01	GNER _00

Byte27 = 8E (CHK1)

(8) Byte28 = 47

	0	1	2	3	4	5	6	7
28	Subcode (x'47')							
29	MP Number							
	MP Number							
2A	P4 PEXLNKSTS:B1							
	BANDWIDTH STATUS	BW MNG STATUS	LINK ACTIVE	SCCON	LNKTRG	Reserved	NLNKWD 5	NLNKWD 4
2B	P4 PEXLNKSTS:B0							
	NLNKWD 3	NLNKWD 2	NLNKWD 1	NLNKWD 0	LNKSPD 3	LNKSPD 2	LNKSPD 1	LNKSPD 0
2C	P5 PEXLNKSTS:B1							
	BANDWIDTH STATUS	BW MNG STATUS	LINK ACTIVE	SCCON	LNKTRG	Reserved	NLNKWD 5	NLNKWD 4
2D	P5 PEXLNKSTS:B0							
	NLNKWD 3	NLNKWD 2	NLNKWD 1	NLNKWD 0	LNKSPD 3	LNKSPD 2	LNKSPD 1	LNKSPD 0
2E	P6 PEXLNKSTS:B1							
	BANDWIDTH STATUS	BW MNG STATUS	LINK ACTIVE	SCCON	LNKTRG	Reserved	NLNKWD 5	NLNKWD 4
2F	P6 PEXLNKSTS:B0							
	NLNKWD 3	NLNKWD 2	NLNKWD 1	NLNKWD 0	LNKSPD 3	LNKSPD 2	LNKSPD 1	NLNKWD 3
30	P7 PEXLNKSTS:B1							
	BANDWIDTH STATUS	BW MNG STATUS	LINK ACTIVE	SCCON	LNKTRG	Reserved	NLNKWD 5	NLNKWD 4
31	P7 PEXLNKSTS:B0							
	NLNKWD 3	NLNKWD 2	NLNKWD 1	NLNKWD 0	LNKSPD 3	LNKSPD 2	LNKSPD 1	LNKSPD 0

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Byte27 = 8E (CHK1)

(9) Byte28 = 50 or 51 or 52

	0	1	2	3	4	5	6	7
28	Subcode (x'50' or x'51' or x'52')							
29	MP Number							
	MP Number							
2A	INT0 ISTAT1							
	I STATE 17	I STATE 16	I STATE 15	I STATE 14	I STATE 13	I STATE 12	I STATE 11	I STATE 10
2B	INT0 ISTAT0							
	I STATE 07	I STATE 06	I STATE 05	I STATE 04	I STATE 03	I STATE 02	I STATE 01	I STATE 00
2C	INT0 MSICNT							
	C0_IRR	C0_MSICNT 6	C0_MSICNT 5	C0_MSICNT 4	C0_MSICNT 3	C0_MSICNT 2	C0_MSICNT 1	C0_MSICNT 0
2D								
	0	0	0	0	0	0	0	0
2E	GNERRST:B3							
	GNER _31	GNER _30	GNER _29	GNER _28	GNER _27	GNER _26	GNER _25	GNER _24
2F	GNERRST:B2							
	GNER _23	GNER _22	GNER _21	GNER _20	GNER _19	GNER _18	GNER _17	GNER _16
30	GNERRST:B1							
	GNER _15	GNER _14	GNER _13	GNER _12	GNER _11	GNER _10	GNER _09	GNER _08
31	GNERRST:B0							
	GNER _07	GNER _06	GNER _05	GNER _04	GNER _03	GNER _02	GNER _01	GNER _00

Byte27 = 8E (CHK1)

(10) Byte28 = 80 or 81

	0	1	2	3	4	5	6	7
28	Subcode (x'80' or x'81')							
29	MP_Number							
	MP_Number							
2A								
	0	0	0	0	0	0	0	0
2B								
	0	0	0	0	0	0	0	0
2C								
	0	0	0	0	0	0	0	0
2D	Error MCE No.							
2E	IA32 MCi STATUS:B7							
	STATUS _31	STATUS _30	STATUS _29	STATUS _28	STATUS _27	STATUS _26	STATUS _25	STATUS _24
2F	IA32 MCi STATUS:B6							
	STATUS _31	STATUS _30	STATUS _29	STATUS _28	STATUS _27	STATUS _26	STATUS _25	STATUS _24
30	IA32 MCi STATUS:B1							
	STATUS _31	STATUS _30	STATUS _29	STATUS _28	STATUS _27	STATUS _26	STATUS _25	STATUS _24
31	IA32 MCi STATUS:B0							
	STATUS _31	STATUS _30	STATUS _29	STATUS _28	STATUS _27	STATUS _26	STATUS _25	STATUS _24

Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 or 40 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
	'01'							
41								
	0	0	0	0	0	0	0	0
42	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	GFERRST:B3							
	GFER 31	GFER 30	GFER 29	GFER 28	GFER 27	GFER 26	GFER 25	GFER 24
45	GFERRST:B2							
	GFER 23	GFER 22	GFER 21	GFER 20	GFER 19	GFER 18	GFER 17	GFER 16
46	GFERRST:B1							
	GFER 15	GFER 14	GFER 13	GFER 12	GFER 11	GFER 10	GFER 09	GFER 08
47	GFERRST:B0							
	GFER 07	GFER 06	GFER 05	GFER 04	GFER 03	GFER 02	GFER 01	GFER 00
48	GNERRST:B3							
	GNER 31	GNER 30	GNER 29	GNER 28	GNER 27	GNER 26	GNER 25	GNER 24
49	GNERRST:B2							
	GNER 23	GNER 22	GNER 21	GNER 20	GNER 19	GNER 18	GNER 17	GNER 16
4A	GNERRST:B1							
	GNER 15	GNER 14	GNER 13	GNER 12	GNER 11	GNER 10	GNER 09	GNER 08
4B	GNERRST:B0							
	GNER 07	GNER 06	GNER 05	GNER 04	GNER 03	GNER 02	GNER 01	GNER 00
4C	IA32_MC9_STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
4D	IA32_MC9_STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
4E	IA32_MC9_STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
4F	IA32_MC9_STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
50	IA32_MC9_STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
51	IA32_MC9_STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
52	IA32_MC9_ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
53	IA32_MC9_ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32

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Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 or 40 (2/3)

	0	1	2	3	4	5	6	7
54	IA32 MC9 ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
55	IA32 MC9 ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
56	IA32 MC9 ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
57	IA32 MC9 ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0
58	IA32 MC9 MISK B5							
	Info 38	Info 37	Info 36	Info 35	Info 34	Info 33	Info 32	Info 31
59	IA32 MC9 MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23
5A	IA32 MC9 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
5B	IA32 MC9 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
5C	IA32 MC9 MISK B1							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
5D	IA32 MC9 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0
5E	IA32 MC10 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
5F	IA32 MC10 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
60	IA32 MC10 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
61	IA32 MC10 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
62	IA32 MC10 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
63	IA32 MC10 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
64	IA32 MC10 ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
65	IA32 MC10 ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
66	IA32 MC10 ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
67	IA32 MC10 ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
68	IA32 MC10 ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
69	IA32 MC10 ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0

Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 or 40 (3/3)

	0	1	2	3	4	5	6	7
6A	IA32 MC10 MISK B5							
	Info 38	Info 37	Info 36	Info 35	Info 34	Info 33	Info 32	Info 31
6B	IA32 MC10 MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23
6C	IA32 MC10 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
6D	IA32 MC10 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
6E	IA32 MC10 MISK I							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
6F	IA32 MC10 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0
70	IA32 MC5 STATUS B6							
	AR	Corr_Err_Sts 1	Corr_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
71	IA32 MC5 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
72	IA32 MC5 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
73	IA32 MC5 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
74	IA32 MC5 ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
75	IA32 MC5 ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
76	IA32 MC5 ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
77	IA32 MC5 ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
78	IA32 MC5 ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
79	IA32 MC5 ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0
7A	IA32 MC5 MISK B5							
	Info 38	Info 37	Info 36	Info 35	Info 34	Info 33	Info 32	Info 31
7B	IA32 MC5 MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23
7C	IA32 MC5 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
7D	IA32 MC5 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
7E	IA32 MC5 MISK I							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
7F	IA32 MC5 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
41	‘02’							
42	0	0	0	0	0	0	0	0
43	I STATE0							
44	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
45	I STATE1							
46	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
47	IA32 MC4 STATUS B3							
48	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
49	IA32 MC4 STATUS B2							
50	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
51	IA32 MC4 STATUS B1							
52	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
53	IA32 MC4 STATUS B0							
54	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
55	IA32 MC5 STATUS B6							
56	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
57	IA32 MC5 STATUS B5							
58	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
59	IA32 MC5 STATUS B4							
60	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
61	IA32 MC5 STATUS B3							
62	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
63	IA32 MC5 STATUS B2							
64	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
65	IA32 MC5 STATUS B1							
66	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
67	IA32 MC5 STATUS B0							
68	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
69	IA32 MC5 ADDR B5							
70	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
71	IA32 MC5 ADDR B4							
72	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
73	IA32 MC5 ADDR B3							
74	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
75	IA32 MC5 ADDR B2							
76	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
77	IA32 MC5 ADDR B1							
78	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
79	IA32 MC5 ADDR B0							
80	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 (2/3)

	0	1	2	3	4	5	6	7
55	IA32 MC5 MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23
56	IA32 MC5 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
57	IA32 MC5 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
58	IA32 MC5 MISK 1							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
59	IA32 MC5 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0
5A	IA32 MC6 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
5B	IA32 MC6 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
5C	IA32 MC6 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
5D	IA32 MC6 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
5E	IA32 MC6 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
5F	IA32 MC6 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
60	IA32 MC6 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
61	IA32 MC6 ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
62	IA32 MC6 ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
63	IA32 MC6 ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
64	IA32 MC6 ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
65	IA32 MC6 ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
66	IA32 MC6 ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0
67	IA32 MC6 MISK B7							
	Info 54	Info 53	Info 52	Info 51	Info 50	Info 49	Info 48	Info 47
68	IA32 MC6 MISK B6							
	Info 46	Info 45	Info 44	Info 43	Info 42	Info 41	Info 40	Info 39
69	IA32 MC6 MISK B5							
	Info 38	Info 37	Info 36	Info 35	Info 34	Info 33	Info 32	Info 31
6A	IA32 MC6 MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23

SSBLOG05-2310

Byte27 = 8E (CHK1) Byte40-7F Detail

(2) Byte28 = 21 (3/3)

	0	1	2	3	4	5	6	7
6B	IA32 MC6 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
6C	IA32 MC6 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
6D	IA32 MC6 MISK 1							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
6E	IA32 MC6 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0
6F	IA32 MC11 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
70	IA32 MC11 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
71	IA32 MC11 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
72	IA32 MC11 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
73	IA32 MC11 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
74	IA32 MC11 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
75	IA32 MC11 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
76	IA32 MC11 ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
77	IA32 MC11 ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
78	IA32 MC11 ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
79	IA32 MC11 ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
7A	IA32 MC11 ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
7B	IA32 MC11 ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0
7C	IA32 MC11 MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
7D	IA32 MC11 MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
7E	IA32 MC11 MISK 1							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
7F	IA32 MC11 MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0

SSBLOG05-2320

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 22 or 23 or 24 or 25 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
41	'03'							
42	0	0	0	0	0	0	0	0
	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	PLDERR0							
	PLDERR_ 07	PLDERR_ 06	PLDERR_ 05	PLDERR_ 04	PLDERR_ 03	PLDERR_ 02	PLDERR_ 01	PLDERR_ 00
45	PLDERR1							
	PLDERR_ 17	PLDERR_ 16	PLDERR_ 15	PLDERR_ 14	PLDERR_ 13	PLDERR_ 12	PLDERR_ 11	PLDERR_ 10
46								
47	0	0	0	0	0	0	0	0
48	0	0	0	0	0	0	0	0
	GNERRST:B3							
	GNER 31	GNER 30	GNER 29	GNER 28	GNER 27	GNER 26	GNER 25	GNER 24
49	GNERRST:B2							
	GNER 23	GNER 22	GNER 21	GNER 20	GNER 19	GNER 18	GNER 17	GNER 16
4A	GNERRST:B1							
	GNER 15	GNER 14	GNER 13	GNER 12	GNER 11	GNER 10	GNER 09	GNER 08
4B	GNERRST:B0							
	GNER 07	GNER 06	GNER 05	GNER 04	GNER 03	GNER 02	GNER 01	GNER 00
4C	IA32 MC4 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4D	IA32 MC4 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
4E	IA32 MC4 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
4F	IA32 MC4 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
50	IA32 MC4 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
51	IA32 MC4 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
52	IA32 MC4 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
53	IA32 MC4 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
54	IA32 MC5 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
55	IA32 MC5 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 22 or 23 or 24 or 25 (2/3)

	0	1	2	3	4	5	6	7
56	IA32 MC5 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
57	IA32 MC5 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
58	IA32 MC5 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
59	IA32 MC5 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
5A	IA32 MC5 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
5B	IA32 MC5 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
5C	IA32 MC6 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5D	IA32 MC6 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
5E	IA32 MC6 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
5F	IA32 MC6 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
60	IA32 MC6 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
61	IA32 MC6 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
62	IA32 MC6 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
63	IA32 MC6 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
64	IA32 MC9 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
65	IA32 MC9 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
66	IA32 MC9 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
67	IA32 MC9 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
68	IA32 MC9 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
69	IA32 MC9 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
6A	IA32 MC9 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8

Byte27 = 8E (CHK1) Byte40-7F Detail

(3) Byte28 = 22 or 23 or 24 or 25 (3/3)

	0	1	2	3	4	5	6	7
6B	IA32 MC9 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
6C	IA32 MC10 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
6D	IA32 MC10 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
6E	IA32 MC10 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
6F	IA32 MC10 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other Info 5	Other Info 4	Other Info 3	Other Info 2	Other Info 1	Other Info 0
70	IA32 MC10 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
71	IA32 MC10 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
72	IA32 MC10 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
73	IA32 MC10 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
74	IA32 MC11 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
75	IA32 MC11 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
76	IA32 MC11 STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
77	IA32 MC11 STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other Info 5	Other Info 4	Other Info 3	Other Info 2	Other Info 1	Other Info 0
78	IA32 MC11 STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
79	IA32 MC11 STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
7A	IA32 MC11 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
7B	IA32 MC11 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7C	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 30 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
	'04'							
41	IA32 MC0 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
42	IA32 MC0 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
43	IA32 MC0 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
44	IA32 MC1 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
45	IA32 MC1 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
46	IA32 MC1 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
47	IA32 MC2 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
48	IA32 MC3 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
49	IA32 MC3 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
4A	IA32 MC3 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
4B	IA32 MC4 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4C	IA32 MC4 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
4D	IA32 MC4 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
4E	IA32 MC5 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4F	IA32 MC5 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
50	IA32 MC5 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
51	IA32 MC6 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
52	IA32 MC6 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
53	IA32 MC6 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
54	IA32 MC9 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
55	IA32 MC9 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 30 (2/3)

	0	1	2	3	4	5	6	7
56	IA32 MC9 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
57	IA32 MC9 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
58	IA32 MC10 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
59	IA32 MC10 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
5A	IA32 MC10 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
5B	IA32 MC10 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
5C	IA32 MC11 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5D	IA32 MC11 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
5E	IA32 MC11 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
5F	IA32 MC11 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
60	IA32 MC12 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
61	IA32 MC12 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
62	IA32 MC12 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
63	IA32 MC12 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
64	IA32 MC13 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
65	IA32 MC13 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
66	IA32 MC13 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
67	IA32 MC13 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
68	IA32 MC14 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
69	IA32 MC14 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
6A	IA32 MC14 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8

Byte27 = 8E (CHK1) Byte40-7F Detail

(4) Byte28 = 30 (3/3)

	0	1	2	3	4	5	6	7
6B	IA32 MC14 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
6C	IA32 MC15 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
6D	IA32 MC15 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
6E	IA32 MC15 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
6F	IA32 MC15 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
70	IA32 MC16 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
71	IA32 MC16 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
72	IA32 MC16 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
73	IA32 MC16 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
74	IA32 MC17 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
75	IA32 MC17 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
76	IA32 MC17 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
77	IA32 MC17 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
78	IA32 MC18 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
79	IA32 MC18 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
7A	IA32 MC18 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
7B	IA32 MC18 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7C	IA32 MC19 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
7D	IA32 MC19 STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
7E	IA32 MC19 STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
7F	IA32 MC19 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 or 43 or 47 or 51 or 52 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
	'05'							
41								
	0	0	0	0	0	0	0	0
42	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44								
	0	0	0	0	0	0	0	0
45	P0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	PCIENFE	Reserved
46	P4_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	PCIENFE	Reserved
47	P5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	PCIENFE	Reserved
48	P6_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	PCIENFE	Reserved
49	P7_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	PCIENFE	Reserved
4A	P0_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	RPLNRLOV
4B	P0_CORERRSTS B0							
	BADDLLP	BADTLTP	Reserved	Reserved	Reserved	Reserved	Reserved	RECEIVERERR
4C	P4_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	RPLNRLOV
4D	P4_CORERRSTS B0							
	BADDLLP	BADTLTP	Reserved	Reserved	Reserved	Reserved	Reserved	RECEIVERERR
4E	P5_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	RPLNRLOV
4F	P5_CORERRSTS B0							
	BADDLLP	BADTLTP	Reserved	Reserved	Reserved	Reserved	Reserved	RECEIVERERR

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 or 43 or 47 or 51 or 52 (2/3)

	0	1	2	3	4	5	6	7
50	P6 CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
51	P6 CORERRSTS B0							
	BADDLLP	BADTLPL	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLPL
52	P7 CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
53	P7 CORERRSTS B0							
	BADDLLP	BADTLPL	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLPL
54	P0 UNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	P0 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	RCVOVF	UNEXPCTCS
56	P0 UNCERRSTS B1							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	Reserved	Reserved
57	P0 UNCERRSTS B0							
	Reserved	Reserved	SPRISDOWN	LNKPRTER	Reserved	Reserved	Reserved	Reserved
58	P4 UNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	P4 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	RCVOVF	UNEXPCTCS
5A	P4 UNCERRSTS B1							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	Reserved	Reserved
5B	P4 UNCERRSTS B0							
	Reserved	Reserved	SPRISDOWN	LNKPRTER	Reserved	Reserved	Reserved	Reserved
5C	P5 UNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	P5 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	RCVOVF	UNEXPCTCS
5E	P5 UNCERRSTS B1							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	Reserved	Reserved
5F	P5 UNCERRSTS B0							
	Reserved	Reserved	SPRISDOWN	LNKPRTER	Reserved	Reserved	Reserved	Reserved
60	P6 UNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	P6 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	RCVOVF	UNEXPCTCS
62	P6 UNCERRSTS B1							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	Reserved	Reserved
63	P6 UNCERRSTS B0							
	Reserved	Reserved	SPRISDOWN	LNKPRTER	Reserved	Reserved	Reserved	Reserved
64	P7 UNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	P7 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	RCVOVF	UNEXPCTCS
66	P7 UNCERRSTS B1							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	Reserved	Reserved
67	P7 UNCERRSTS B0							
	Reserved	Reserved	SPRISDOWN	LNKPRTER	Reserved	Reserved	Reserved	Reserved
68								
	0	0	0	0	0	0	0	0
69	P0 XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LBWCHANGE
6A	P4 XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LBWCHANGE
6B	P5 XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LBWCHANGE

Byte27 = 8E (CHK1) Byte40-7F Detail

(5) Byte28 = 41 or 43 or 47 or 51 or 52 (3/3)

	0	1	2	3	4	5	6	7
6C	P6_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LBWCHANGE
6D	P7_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LBWCHANGE
6E	P0_XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OBPOISNDDT	MSIWRERR
6F	P0_XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	FIFOPE	Reserved
70	P4_XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OBPOISNDDT	MSIWRERR
71	P4_XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	FIFOPE	Reserved
72	P5_XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OBPOISNDDT	MSIWRERR
73	P5_XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	FIFOPE	Reserved
74	P6_XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OBPOISNDDT	MSIWRERR
75	P6_XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	FIFOPE	Reserved
76	P7_XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OBPOISNDDT	MSIWRERR
77	P7_XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	FIFOPE	Reserved
78	IRPP0ERRST B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
79	IRPP0ERRST B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	IRPP0ERRST B1							
	Reserved	PE	OVFLOW	Reserved	Reserved	UNEXPRES	Reserved	Reserved
7B	IRPP0ERRST B0							
	Reserved	Reserved	Reserved	CSRACSERR	WCUNCERR	POISONED	WCCORERR	Reserved
7C	IRPP1ERRST B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7D	IRPP1ERRST B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7E	IRPP1ERRST B1							
	Reserved	PE	OVFLOW	Reserved	Reserved	UNEXPRES	Reserved	Reserved
7F	IRPP1ERRST B0							
	Reserved	Reserved	Reserved	CSRACSERR	WCUNCERR	POISONED	WCCORERR	Reserved

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Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 50 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
	'06'							
41								
	0	0	0	0	0	0	0	0
42	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	P4_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	P5_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	P6_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
47	P7_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	P4_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	Reserved	Reserved
49	P5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	Reserved	Reserved
4A	P6_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	Reserved	Reserved
4B	P7_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	PCIECE	Reserved	Reserved
4C	P4_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
4D	P4_CORERRSTS B0							
	BADDLLP	BADTLP	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLP
4E	P5_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
4F	P5_CORERRSTS B0							
	BADDLLP	BADTLP	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLP
50	P6_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
51	P6_CORERRSTS B0							
	BADDLLP	BADTLP	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLP
52	P7_CORERRSTS B1							
	Reserved	Reserved	SDVNFERR	RPLYTO	Reserved	Reserved	Reserved	Reserved
53	P7_CORERRSTS B0							
	BADDLLP	BADTLP	Reserved	Reserved	Reserved	Reserved	BADDLLP	BADTLP
54	P4_UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	Reserved	Reserved
55	P4_UNCERRSTS B1/0							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	SPRISDOWN	LNKPRTErr
56	P5_UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	Reserved	Reserved
57	P5_UNCERRSTS B1/0							
	CMPABORT	COMPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	SPRISDOWN	LNKPRTErr

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 50 (2/3)

	0	1	2	3	4	5	6	7
58	P6 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	Reserved	Reserved
59	P6 UNCERRSTS B1/0							
	CMPABORT	COMPPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	SPRISDOWN	LNKPRTERR
5A	P7 UNCERRSTS B2							
	Reserved	Reserved	ACSVIOLAT	RCVUR	Reserved	MALTLP	Reserved	Reserved
5B	P7 UNCERRSTS B1/0							
	CMPABORT	COMPPTO	FCPRTCLERR	POISENDTLP	Reserved	Reserved	SPRISDOWN	LNKPRTERR
5C	P4 XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	P4 XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	Reserved	UR
5E	P5 XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5F	P5 XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	Reserved	UR
60	P6 XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	P6 XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	Reserved	UR
62	P7 XPUNCERRSTS B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
63	P7 XPUNCERRSTS B0							
	Reserved	UR	CA	SENTUR	SENTCA	Reserved	Reserved	UR
64	DMAUNCERRSTS B1							
	Reserved	Reserved	Reserved	Syndrome	Reserved	RDADDECERR	Reserved	Reserved
65	DMAUNCERRSTS B0							
	RDCMPHDERR	Reserved	Reserved	CFGPE	INTHWPE	POISONEDDT	Reserved	Reserved
66	C0 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
67	C0 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
68	C0 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
69	C1 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
6A	C1 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
6B	C1 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
6C	C2 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
6D	C2 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
6E	C2 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
6F	C3 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR

Byte27 = 8E (CHK1) Byte40-7F Detail

(6) Byte28 = 42 or 50 (3/3)

	0	1	2	3	4	5	6	7
70	C3 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
71	C3 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
72	C4 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCCNTERR	XORQERR	XOWPERR
73	C4 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
74	C4 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
75	C5 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCCNTERR	XORQERR	XOWPERR
76	C5 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
77	C5 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
78	C6 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCCNTERR	XORQERR	XOWPERR
79	C6 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
7A	C6 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
7B	C7 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCCNTERR	XORQERR	XOWPERR
7C	C7 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
7D	C7 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
7E								
	0	0	0	0	0	0	0	0
7F								
	0	0	0	0	0	0	0	0

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Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 44 or 45 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
41	'07'							
42	0	0	0	0	0	0	0	0
	I STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	GFERRST:B3							
	GFER 31	GFER 30	GFER 29	GFER 28	GFER 27	GFER 26	GFER 25	GFER 24
45	GFERRST:B2							
	GFER 23	GFER 22	GFER 21	GFER 20	GFER 19	GFER 18	GFER 17	GFER 16
46	GFERRST:B1							
	GFER 15	GFER 14	GFER 13	GFER 12	GFER 11	GFER 10	GFER 09	GFER 08
47	GFERRST:B0							
	GFER 07	GFER 06	GFER 05	GFER 04	GFER 03	GFER 02	GFER 01	GFER 00
48	DMAUNCERRSTS B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	DMAUNCERRSTS B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	DMAUNCERRSTS B1							
	Reserved	Reserved	Reserved	Syndrome	Reserved	RDADDECERR	Reserved	Reserved
4B	DMAUNCERRSTS B0							
	RDCMPHDERR	Reserved	Reserved	CFGPE	INTHWPE	POISONEDDT	Reserved	Reserved
4C	C0 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	C0 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
4E	C0 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
4F	C0 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
50	C1 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	C1 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
52	C1 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
53	C1 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
54	C2 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	C2 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
56	C2 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
57	C2 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 44 or 45 (2/3)

	0	1	2	3	4	5	6	7
58	C3 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	C3 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
5A	C3 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
5B	C3 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
5C	C4 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	C4 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
5E	C4 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
5F	C4 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
60	C5 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	C5 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
62	C5 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
63	C5 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
64	C6 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	C6 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
66	C6 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
67	C6 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
68	C7 CHANERR INT B3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	C7 CHANERR INT B2							
	Reserved	Reserved	Reserved	Reserved	Reserved	DESCNTERR	XORQERR	XOWPERR
6A	C7 CHANERR INT B1							
	UNAFFILERR	Reserved	INTCFGERR	CMPADRERR	DESCLENERR	DESCCTLERR	WRDTERR	RDDTERR
6B	C7 CHANERR INT B0							
	DTPE	CDTPE	CHANCMDER	CHAINADRER	DESCERR	NDESCERR	DESTADRERR	SRCADRERR
6C	IA32 MC0 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
6D	IA32 MC1 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
6E	IA32 MC2 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
6F	IA32 MC3 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0

Byte27 = 8E (CHK1) Byte40-7F Detail

(7) Byte28 = 44 or 45 (3/3)

	0	1	2	3	4	5	6	7
70	IA32 MC4 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
71	IA32 MC5 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
72	IA32 MC6 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
73	IA32 MC9 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
74	IA32 MC10 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
75	IA32 MC11 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
76	IA32 MC12 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
77	IA32 MC13 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
78	IA32 MC14 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
79	IA32 MC15 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7A	IA32 MC16 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7B	IA32 MC17 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7C	IA32 MC18 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7D	IA32 MC19 STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0
7E								
	0	0	0	0	0	0	0	0
7F								
	0	0	0	0	0	0	0	0

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Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 80 or 81 (1/3)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT FORMAT NUMBER							
41	'08'							
42	0	0	0	0	0	0	0	0
	I_STATE0							
	I_STATE 07	I_STATE 06	I_STATE 05	I_STATE 04	I_STATE 03	I_STATE 02	I_STATE 01	I_STATE 00
43	I_STATE1							
	I_STATE 17	I_STATE 16	I_STATE 15	I_STATE 14	I_STATE 13	I_STATE 12	I_STATE 11	I_STATE 10
44	GFERRST:B3							
	GFER 31	GFER 30	GFER 29	GFER 28	GFER 27	GFER 26	GFER 31	GFER 30
45	GFERRST:B2							
	GFER 23	GFER 22	GFER 21	GFER 20	GFER 19	GFER 18	GFER 23	GFER 22
46	GFERRST:B1							
	GFER 15	GFER 14	GFER 13	GFER 12	GFER 11	GFER 10	GFER 15	GFER 14
47	GFERRST:B0							
	GFER 07	GFER 06	GFER 05	GFER 04	GFER 03	GFER 02	GFER 07	GFER 06
48	GNERRST:B3							
	GNER 31	GNER 30	GNER 29	GNER 28	GNER 27	GNER 26	GNER 25	GNER 24
49	GNERRST:B2							
	GNER 23	GNER 22	GNER 21	GNER 20	GNER 19	GNER 18	GNER 17	GNER 16
4A	GNERRST:B1							
	GNER 15	GNER 14	GNER 13	GNER 12	GNER 11	GNER 10	GNER 09	GNER 08
4B	GNERRST:B0							
	GNER 07	GNER 06	GNER 05	GNER 04	GNER 03	GNER 02	GNER 01	GNER 00
4C	IA32 MC0 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4D	IA32 MC1 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4E	IA32 MC2 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
4F	IA32 MC3 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
50	IA32 MC4 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
51	IA32 MC5 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
52	IA32 MC6 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
53	IA32 MC9 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag

Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 80 or 81 (2/3)

	0	1	2	3	4	5	6	7
54	IA32 MC10 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
55	IA32 MC11 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
56	IA32 MC12 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
57	IA32 MC13 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
58	IA32 MC14 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
59	IA32 MC15 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5A	IA32 MC16 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5B	IA32 MC17 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5C	IA32 MC18 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5D	IA32 MC19 STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
5E								
	0	0	0	0	0	0	0	0
5F								
	0	0	0	0	0	0	0	0
60	IA32 MCi STATUS B7							
	VAL	OVER	UC	Error enabled	MISCV	ADDRV	PCC	Signaling Flag
61	IA32 MCi STATUS B6							
	AR	Th_Err_Sts 1	Th_Err_Sts 0	Corr_Err_Cnt 14	Corr_Err_Cnt 13	Corr_Err_Cnt 12	Corr_Err_Cnt 11	Corr_Err_Cnt 10
62	IA32 MCi STATUS B5							
	Corr_Err_Cnt 9	Corr_Err_Cnt 8	Corr_Err_Cnt 7	Corr_Err_Cnt 6	Corr_Err_Cnt 5	Corr_Err_Cnt 4	Corr_Err_Cnt 3	Corr_Err_Cnt 2
63	IA32 MCi STATUS B4							
	Corr_Err_Cnt 1	Corr_Err_Cnt 0	Other_Info 5	Other_Info 4	Other_Info 3	Other_Info 2	Other_Info 1	Other_Info 0
64	IA32 MCi STATUS B3							
	MSCOD 15	MSCOD 14	MSCOD 13	MSCOD 12	MSCOD 11	MSCOD 10	MSCOD 9	MSCOD 8
65	IA32 MCi STATUS B2							
	MSCOD 7	MSCOD 6	MSCOD 5	MSCOD 4	MSCOD 3	MSCOD 2	MSCOD 1	MSCOD 0
66	IA32 MCi STATUS B1							
	MCACOD 15	MCACOD 14	MCACOD 13	MCACOD 12	MCACOD 11	MCACOD 10	MCACOD 9	MCACOD 8
67	IA32 MCi STATUS B0							
	MCACOD 7	MCACOD 6	MCACOD 5	MCACOD 4	MCACOD 3	MCACOD 2	MCACOD 1	MCACOD 0

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Byte27 = 8E (CHK1) Byte40-7F Detail

(8) Byte28 = 80 or 81 (3/3)

	0	1	2	3	4	5	6	7
68	IA32 MCi ADDR B7							
	0	0	0	0	0	0	0	0
69	IA32 MCi ADDR B6							
	0	0	0	0	0	0	0	0
6A	IA32 MCi ADDR B5							
	0	0	MCEADR 45	MCEADR 44	MCEADR 43	MCEADR 42	MCEADR 41	MCEADR 40
6B	IA32 MCi ADDR B4							
	MCEADR 39	MCEADR 38	MCEADR 37	MCEADR 36	MCEADR 35	MCEADR 34	MCEADR 33	MCEADR 32
6C	IA32 MCi ADDR B3							
	MCEADR 31	MCEADR 30	MCEADR 29	MCEADR 28	MCEADR 27	MCEADR 26	MCEADR 25	MCEADR 24
6D	IA32 MCi ADDR B2							
	MCEADR 23	MCEADR 22	MCEADR 21	MCEADR 20	MCEADR 19	MCEADR 18	MCEADR 17	MCEADR 16
6E	IA32 MCi ADDR B1							
	MCEADR 15	MCEADR 14	MCEADR 13	MCEADR 12	MCEADR 11	MCEADR 10	MCEADR 9	MCEADR 8
6F	IA32 MCi ADDR B0							
	MCEADR 7	MCEADR 6	MCEADR 5	MCEADR 4	MCEADR 3	MCEADR 2	MCEADR 1	MCEADR 0
70	IA32 MCi MISK B7							
	Info 54	Info 53	Info 52	Info 51	Info 50	Info 49	Info 48	Info 47
71	IA32 MCi MISK B6							
	Info 46	Info 45	Info 44	Info 43	Info 42	Info 41	Info 40	Info 39
72	IA32 MCi MISK B5							
	Info 38	Info 37	Info 36	Info 35	Info 34	Info 33	Info 32	Info 31
73	IA32 MCi MISK B4							
	Info 30	Info 29	Info 28	Info 27	Info 26	Info 25	Info 24	Info 23
74	IA32 MCi MISK B3							
	Info 22	Info 21	Info 20	Info 19	Info 18	Info 17	Info 16	Info 15
75	IA32 MCi MISK B2							
	Info 14	Info 13	Info 12	Info 11	Info 10	Info 9	Info 8	Info 7
76	IA32 MCi MISK B1							
	Info 6	Info 5	Info 4	Info 3	Info 2	Info 1	Info 0	ADRMD 2
77	IA32 MCi MISK B0							
	ADRMD 1	ADRMD 0	Rec_Err_LSB 5	Rec_Err_LSB 4	Rec_Err_LSB 3	Rec_Err_LSB 2	Rec_Err_LSB 1	Rec_Err_LSB 0
78								
	0	0	0	0	0	0	0	0
79								
	0	0	0	0	0	0	0	0
7A								
	0	0	0	0	0	0	0	0
7B								
	0	0	0	0	0	0	0	0
7C								
	0	0	0	0	0	0	0	0
7D								
	0	0	0	0	0	0	0	0
7E								
	0	0	0	0	0	0	0	0
7F								
	0	0	0	0	0	0	0	0

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Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	Controller No.	DKU Physical Device#				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40								
41	Not used							
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								

*1: Failed processor number for WCHK

SSBLOG05-2510

Byte27 = 8E & Byte34: bit4-7 = 1 (Selective Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type		Processor ID					
07	System error code							
08								
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used(x'0')							
29	DCN status (ldcnstsls)							
	Force device busy	Channel connect request	Command chain execute	Path reserve	Stack status	Sense pending	Long Busy (Command process)	Processor connect
2A	DCN status (ldcnstsls)							
	Single path mode	Gurant path mode	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	Garant path MP
2B	DCN flag 1 (ldcnflglc)							
	RST NTF/ RST ALG SSB pending	Pin Data pending	(RSV)	(RSV)	ODE pending	RSQ pending	SCI/SCE pending	PCH pending
2C	LCT initiation cause (lctsfcs)							
	0:Ready 1:Not ready	0:Disable 1:Enable	0:No 64SNS 1:64SNS pending	0: Mount 1:No mount	0:No RST ALG LPN 1:Exist	0:No SYS ALG LPN 1:Exist	0:No Stack 1:Exist	0:No Wait SNS 1:Exist
2D	LCT initiation cause (lctsfcs)							
	0:No Wait-RI 1:Exist	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	0:Others 1:DEV BSY JOB

Byte27 = 8E & Byte34: bit4-7 = 1 (Selective Reset) (2/3)

	0	1	2	3	4	5	6	7
2E	Command interface flag (lctfigc)							
	Pending exist	SCE(RSP)	(RSV)	(RSV)	ODE pending	RSG pending	SCI pending	PCH pending
2F	Command code							
30	Internal SSB log number							
31								
32	Related reset detailed log number							
33								
34	PCB number				Message code (x'1')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT(DKA x'00'))							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								
60								
61	wchklmpbitmap (from LCT)							
62								
63								

SSBLOG05-2530

Byte27 = 8E & Byte34: bit4-7 = 1 (Selective Reset) (3/3)

	0	1	2	3	4	5	6	7
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	EXITFLAG AREA							
6D								
6E								
6F								
70	DCN (wldcn1l)							
71								
72								
73								
74	DCN (wldcn2l)							
75								
76								
77								
78	RIW							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

SSBLOG05-2540

Byte27 = 8E & Byte34: bit4-7 = 3 (wait SENSE Timeout) (1/2)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used(x'00')							
29	LDEV#							
2A	24 Byte type	Basic SSB (*1)			32 Byte type	Basic SSB (*1)		
2B								
2C						Exception code (*1)		
2D		F/M (*1)						
2E		System error code (*1)				System error code (*1)		
2F								
30	Reset internal SSB number							
31								
32	Detail log number for RESET							
33								
34	PCB number				Message code (x'3')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							

*1: Timeout SSB information

SSBLOG05-2550

Byte27 = 8E & Byte34: bit4-7 = 3 (wait SENSE Timeout) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used (x'00')							
41								
42								
43								
44								
45								
46								
47								
48								
4F								
50								
51								
52								
53								
54								
55								
56	LPN							
57								
58	Not used (x'00')							
5F								
60	Timeout SSB							
61								
62								
63								
64								
65								
66								
67								
68								
6F								
70								
7F								

SSBLOG05-2560

Byte27 = 8E & Byte34: bit4-7 = 5 (CHK1B Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Related internal SSB log number (for error support)							
29								
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (Interruption source address)							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Related RESET detail log number							
33								
34	PCB number				Message code (x'5')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							

SSBLOG05-2570

Byte27 = 8E & Byte34: bit4-7 = 5 (CHK1B Reset) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2580

Byte27 = 8E & Byte34: bit4-7 = 5 (CHK1B Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	DKCMAIN program counter							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

SSBLOG05-2590

Byte27 = 8E & Byte34: bit4-7 = 6 (DKC Internal Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW(Interruption source address)							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'6')			
35	SSID (Low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST)							

SSBLOG05-2600

Byte27 = 8E & Byte34: bit4-7 = 6 (DKC Internal Reset) (2/3)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2610

Byte27 = 8E & Byte34: bit4-7 = 6 (DKC Internal Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
74								
77	DKCMAIN program counter							
78								
	SLV-DCN (slvdcnr)							
7B								
7C								
7F								

SSBLOG05-2620

Byte27 = 8E & Byte34: bit4-7 = 8 (Force Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (Interruption source address)							
2D								
2E								
2F								

SSBLOG05-2630

Byte27 = 8E & Byte34: bit4-7 = 8 (Force Reset) (2/3)

	0	1	2	3	4	5	6	7
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'8')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2640

Byte27 = 8E & Byte34: bit4-7 = 8 (Force Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchkmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	DKCMAIN program counter							
7B	SLV-DCN (slvdcnr)							
7C								
7F								

SSBLOG05-2650

Byte27 = 8E & Byte34: bit4-7 = 9 (Logical Inconsistency Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05	Format Message							
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14								
15	RDEV#							
16	SSB log number							
17								
18								
19	Related failure log number							
1A								
1B								
1C	Related SSB log number							
1D								
1E								
1F	SCB (x'37')							
20								
21								
22	Basic SSB							
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29								
2A								
2B	Old PSW (Interruption source address)							
2C								
2D								
2E								
2F								

Byte27 = 8E & Byte34: bit4-7 = 9 (Logical Inconsistency Reset) (2/3)

	0	1	2	3	4	5	6	7
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'9')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

SSBLOG05-2670

Byte27 = 8E & Byte34: bit4-7 = 9 (Logical Inconsistency Reset) (3/3)

	0	1	2	3	4	5	6	7
60	wchkmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN (wldcn11)							
71								
72								
73	DCN (wldcn21)							
74								
75								
76	DKCMAIN program counter							
77								
78								
79	SLV-DCN (slvdcnr)							
7A								
7B								
7C								
7D								
7E								
7F								

SSBLOG05-2680

Byte27 = 8F (Logical Inconsistency) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'F')			
28	Module ID							
29	Routine ID							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								

SSBLOG05-2690**Byte27 = 8F (Logical Inconsistency) (2/2)**

	0	1	2	3	4	5	6	7
34	PCB number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type =DKU86I)							
37	(This information is not fixed unit DKC reports SSB to HOST)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-2700

Byte27 = 9F (Trace data/Log data) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'9')				Message (x'F')			
28	Module ID (*2)							
29	Routine ID (*2)							
2A	Error information							
2F								
30								
31								
32								
33								
34	PCB number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF9F')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

Byte27 = 9F (Trace data/Log data) (2/2)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

*1:

- (1) The pair of system error code x'AE5A' (Drive not Ready) and x'A772' are reported even when one of the following processes has completed successfully. These indicate the PDEV spin down executed correctly and do not mean the error condition. Please ignore them.
 - PDEV Replace
 - DKU Inline execution at Installation or at PDEV expansion.
- (2) System error code x'AE5B' (Unit Attention) is reported even after one of the following processes has completed successfully. It indicates the PDEV was reset correctly and does not mean the error condition. Please ignore it.
 - DKA Replace
 - DKA expansion
 - PDEV expansion
 - MP PCB micro-program exchange
 - PDEVmicro-program exchange

*2: The details information of every Module ID & Routine ID

- Module ID & Routine ID=0xC3A5: The Specified R-VOL of TC-MF is still Online from the HOST.
(Add Pair operation from the host was rejected)

Check the SSB.LOG on SVP of the specified MCU. SSB containing EC=C3A5 on the specified R-VOL number should be logged.

(1) Analyze the SSB.LOG

Refer to the case of Byte27=0X(X≠8/A) for the way of tracing an on-line host.

But, only the Byte position of BitMap information is different. Refer to Byte100(0x64)-103(0x67) of SSB for the BitMap information.

*3: System error code x'507F' is LOG which shows that the function of MODE277 is not effective. PS OFF/ON is required in order to confirm the function of MODE277.

*4: In case of Mode 449 ON at the primary storage system of UR, if the secondary storage system is powered off or the path between primary and secondary storage system is blocked, error code DB00 might be displayed on primary storage system.
These error codes, which are generated when the communication between primary and secondary storage system fails, doesn't indicate any failures.
Please ignore them.

SSBLOG05-2720

Byte27 = EC (Environment Monitor Detected DKC Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'E')				Message (x'C')			
28	Error detection code (*1)							
29	Error detection code (*2)							
2A	Error reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
33								
34	PCB number (don't care)				PCB number (don't care)			
35	SSID (low) of self storage system (don't care)							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = EC (Environment Monitor Detected DKC Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
7F								

*1: Error Detection Code

Code	Detection
10	DKCMN1 (DKC-0)
20	DKCMN2 (DKC-0)
11	DKCMN1 (DKC-1)
21	DKCMN2 (DKC-1)
30	SSVP
00	Others

*2: Error Component Code

Code	Component
10	Logic 1 (DKC-0)
11	Logic 2 (DKC-0)
12	Logic 1 (DKC-1)
13	Logic 2 (DKC-1)
A0	DKCMN1 (DKC-0)
A1	DKCMN2 (DKC-0)
A2	SSVP
A4	DKCMN1 (DKC-1)
A5	DKCMN2 (DKC-1)
A3	others

*3: Error Reason Code (1/2)

Code	Reason
10	Outside (intake) temperature alarm (high temperature) is detected.
11	Outside (intake) temperature warning (high temperature) is detected.
12	Outside (intake) temperature warning (low temperature) is detected.
13	Internal temperature alarm is detected.
14	Internal temperature warning 2 is detected.
15	Internal temperature warning 1 is detected.
20	Logic voltage alarm is detected.
40	DKCPS-x0 abnormality is detected. (x: DKC Module#)
41	DKCPS-x1 abnormality is detected. (x: DKC Module#)
42	DKCPS-x2 abnormality is detected. (x: DKC Module#)
43	DKCPS-x3 abnormality is detected. (x: DKC Module#)

*3: Error Reason Code (2/2)

Code	Reason
60	DKCPS-x0 input voltage abnormality (x: DKC Module#)
61	DKCPS-x1 input voltage abnormality (x: DKC Module#)
62	DKCPS-x2 input voltage abnormality (x: DKC Module#)
63	DKCPS-x3 input voltage abnormality (x: DKC Module#)
70	Low rotation speed of DKCFAN-x0 is detected. (x: DKC Module#)
71	Low rotation speed of DKCFAN-x1 is detected. (x: DKC Module#)
72	Low rotation speed of DKCFAN-x2 is detected. (x: DKC Module#)
73	Low rotation speed of DKCFAN-x3 is detected. (x: DKC Module#)
74	Low rotation speed of DKCFAN-x4 is detected. (x: DKC Module#)
75	Low rotation speed of DKCFAN-x5 is detected. (x: DKC Module#)
76	Low rotation speed of DKCFAN-x6 is detected. (x: DKC Module#)
77	Low rotation speed of DKCFAN-x7 is detected. (x: DKC Module#)
78	Low rotation speed of DKCFAN-x8 is detected. (x: DKC Module#)
79	Low rotation speed of DKCFAN-x9 is detected. (x: DKC Module#)
85	SVP (BASIC) SVP RAS Switch#1 remains. (SVP PS ON/OFF INH SW)
86	SVP (OPTION) SVP RAS Switch#1 remains. (SVP PS ON/OFF INH SW)
90	Environment monitor register access error.
9A	DKCMN Ready off.
9B	DKCMN version disagreement.
A0	Logic voltage alarm disagreement is detected.
A2	Abnormal temperature disagreement of the DKC outside is detected.
A3	Temperature alarm disagreement in DKC is detected.
A4	Temperature warning disagreement in DKC is detected.
A5	PSOFFREQ control signal disagreement is detected.
A6	PSOFFOK control signal disagreement is detected.
A7	SYSON control signal disagreement is detected.
A8	DKCPS-x0 control signal disagreement is detected. (x: DKC Module#)
A9	DKCPS-x1 control signal disagreement is detected. (x: DKC Module#)
AA	DKCPS-x2 control signal disagreement is detected. (x: DKC Module#)
AB	DKCPS-x3 control signal disagreement is detected. (x: DKC Module#)
AC	MN communication abnormality is detected.
AD	DKCPANEL connection abnormality is detected.
AE	PCIADP/PCICON connection abnormality is detected.
AF	MN abnormality is detected.
B0	MN status disagreement is detected.
B1	Communication abnormality between MN is detected.
B2	PS Status disagreement is detected.
B4	EPO unmatched.
B6	MODCON unconnection
B7	MODCON connection
C0	DKC ALARM LED light on
E3	Duplex SVP Setup fail.

SSBLOG05-2750

Byte27 = EE (Environment Monitor Detected DKU Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Time stamp							
05	System error code							
06								
07	JCB#							
08								
09	REQ ID							
0A								
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD		
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'37')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'E')				Message (x'E')			
28	Error Detection code (*1)							
29	Error Component code (*2)							
2A	Error Reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number (don't care)				Message code (don't care)			
35	SSID (low) of self storage system (don't care)							
36	Symptom code (x'FFEE')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

SSBLOG05-2760

Byte27 = EE (Environment Monitor Detected DKU Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
44								
45								
46								
47								
48								
49								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
80								
81								
82								
83								
84								
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96								
97								
98								
99								
100								
101								
102								
103								
104								
105								
106								
107								
108								
109								
110								
111								
112								
113								
114								
115								
116								
117								
118								
119								
120								
121								
122								
123								
124								
125								
126								
127								

***1: Error Detection Code**

Code	Detection
80	SSWxx0-HP0
82	SSWxx0-LP1
90	SSWxx1-HP0
92	SSWxx1-LP1
A0	SSWxx2-HP0
A2	SSWxx2-LP1
B0	SSWxx3-HP0
B2	SSWxy3-LP1

***2: Error Component Code**

Code	Detection	Code	Component	Code	Component
00	HDU-000/004	30	HDU-030/034	60	HDU-060/064
01	HDU-001/005	31	HDU-031/035	61	HDU-061/065
02	HDU-002/006	32	HDU-032/036	62	HDU-062/066
03	HDU-003/007	33	HDU-033/037	63	HDU-063/067
04	HDU-100/104	34	HDU-130/134	64	HDU-160/164
05	HDU-101/105	35	HDU-131/135	65	HDU-161/165
06	HDU-102/106	36	HDU-132/136	66	HDU-162/166
07	HDU-103/107	37	HDU-133/137	67	HDU-163/167
10	HDU-010/014	40	HDU-040/044	70	HDU-070/074
11	HDU-011/015	41	HDU-041/045	71	HDU-071/075
12	HDU-012/016	42	HDU-042/046	72	HDU-072/076
13	HDU-013/017	43	HDU-043/047	73	HDU-073/077
14	HDU-110/114	44	HDU-140/144	74	HDU-170/174
15	HDU-111/115	45	HDU-141/145	75	HDU-171/175
16	HDU-112/116	46	HDU-142/146	76	HDU-172/176
17	HDU-113/117	47	HDU-143/147	77	HDU-173/177
20	HDU-020/024	50	HDU-050/054		
21	HDU-021/025	51	HDU-051/055		
22	HDU-022/026	52	HDU-052/056		
23	HDU-023/027	53	HDU-053/057		
24	HDU-120/124	54	HDU-150/154		
25	HDU-121/125	55	HDU-151/155		
26	HDU-122/126	56	HDU-152/156		
27	HDU-123/127	57	HDU-153/157		

***3: Error Reason Code**

Code	Reason
50	DKU PS_0 abnormality has been detected.
51	DKU PS_1 abnormality has been detected.
52	DKU PS_2 abnormality has been detected.
53	DKU PS_3 abnormality has been detected.
C0	DKU FAN_0 abnormality has been detected.
C1	DKU FAN_1 abnormality has been detected.
C4	DKU FAN_0 fuse disconnection has been detected.
C5	DKU FAN_0 fuse disconnection has been detected.
D0	HDD PWR_0 abnormality has been detected.
D1	HDD PWR_1 abnormality has been detected.
D2	HDD PWR_2 abnormality has been detected.
D3	HDD PWR_3 abnormality has been detected.
F0	SSW alarm disagreement.
F1	SSW access error abnormality (Cluster 1 Standard) has been detected.
F2	SSW access error abnormality (Cluster 2 Standard) has been detected.
F3	SSW access error abnormality (Cluster 1 High performance) has been detected.
F4	SSW access error abnormality (Cluster 2 High performance) has been detected.

SSBLOG05-2780

Byte27 = F0 (Operation Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'35')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'0')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (*1)							
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF0')							
37								

SSBLOG05-2790**Byte27 = F0 (Operation Terminated) (2/2)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'1D')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
7F								

(*1) Hardware level

Bit 0 : Serial channel hardware level	
For bit0 = 0	For bit0 = 1
Bit 1 : Not used Bit 2 ~ 3 : SP number Bit 4 ~ 5 : CHLs per Cluster 00 : 4 01 : 8 10 : Not used 11 : Not used Bit 6 : NVS 0 : Without NVS 1 : With NVS Bit 7 : Not used Bit 8 ~ 10 : Cache size 000 : Non cache 001 : 256MB 010 : 512MB 011 : 768MB 100 : 1024MB 101 : 1280MB 110 : 1536MB 111 : Over 1537MB Bit 11 ~ 13 : Cluster hardware level Bit 14 ~ 15 : Cache/NVS hardware level	Bit 1 : Not used Bit 2 ~ 3 : SP number Bit 4 ~ 5 : CHLs per Cluster 0000 : Parallel = 4, Serial = 0 0001 : Parallel = 8, Serial = 0 0010 : Parallel = 4, Serial = 2 0100 : Parallel = 4, Serial = 4 0110 : Parallel = 0, Serial = 2 1000 : Parallel = 0, Serial = 4 1010 : Parallel = 0, Serial = 8 1100 : Parallel = 0, Serial = 8 Bit 8 : DUAL FRAME 0 : DUAL FRAME 1 : MODULAR POWER Bit 9 ~ 11 : Cache size 000 : Non cache 001 : 256MB 010 : 512MB 011 : 768MB 100 : 1024MB 101 : 1280MB 110 : 1536MB 111 : Over 1536MB Bit 12 ~ 13 : Cluster hardware level Bit 14 ~ 15 : Cache/NVS hardware level

SSBLOG05-2800

Byte27 = F1 (Micro-program Detected Cache Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID (NOTE)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'1')			
28	Queue transition in progress (Side A)	Queue transition in progress (Side B)	Free SGCB queue operation in progress	Free SLCB queue operation in progress	Free GRPT queue operation in progress	Not used		
29	Not used							
	VDEV number							
2A	Queue type							
	VDEV number		Slot number					
2B	Queue number							
	Slot number							
2C	Queue number							
	Slot number							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

*1: When byte 28, bit2-4 = 000, upper value is valid.
When byte 28, bit2-4 ≠ 000, lower value is valid.

SSBLOG05-2810**Byte27 = F1 (Micro-program Detected Cache Failure) (2/2)**

	0	1	2	3	4	5	6	7
30	Not used							
31	Module ID							
33	Routine ID							
32	PCB number				Error code			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF1')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-2820

Byte27 = F2 (CACHE ERR)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Host Information							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'2')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
2C								
2D	SSID for Other Storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Hardware information (See following pages)							
31	Module ID							
32	Routine ID							
33	PKID				Message code(x'0')			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF2')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Hardware information (See following pages)							
7F								

Byte27 = F2 (CACHE ERR) Byte28 - 2C, 30 Detail

	0	1	2	3	4	5	6	7
28	Subcode							
	x'1' = C30/C31/C32/C33/C70/C71/C72/C73 (CACHE error) (CHK2) x'2' = D (CHK3)				Not used '0'	Not used '0'	Not used '0'	MSW register LOG error
29	Transfer information							
	Transfer P/K Type x'1' = CHF x'2' = DKA x'4' = CHM x'5' = CMPK (MSW) x'6' = MP-PK				Act mode x'1' = Nomal write x'2' = Read x'3' = 2 Cache write x'5' = Atomic write			
2A	Transfer Master P/K board number							
	CHF/CHM/DKA: x'00' ~ x'0B', x'10' ~ x'1B'							
	MP-PK: x'00' ~ x'0F'							
	CM-PK: x'00' ~ x'07'							
2B	Using path code0 (CHK2)							
	Not used '0'				Command1 Access CACHE board number CM-PK: x'00' ~ x'07'			
2C	Not used '0'							
2D								
2E								
2F								
30	Transfer Master Path				Not used '0'			
	x'0' = Not used '0'							
	x'1' = MPA							
	x'3' = DMA0							
	x'4' = DMA1							
	x'5' = DMA2							
	x'6' = DMA3							
	x'9' = DRR0							
	x'A' = DRR1							
	x'D' = MFDMA							

Byte27 = F2 (CACHE ERR) Byte40 - 4F Detail

	0	1	2	3	4	5	6	7
40	Command1 HSN Status byte0							
	ERROR	WARNING	Module ID		LSI ID			
41	Command1 HSN Status byte1							
	Function ID							
42	Command1 HSN Status byte2							
	Micro Error	HARD Error	Not used	Not used	Format ID			
43	Command1 HSN Status byte3							
	MPA Error							
44	Command1 HSN Status byte4							
	Command1 HSN Status byte4							
45	Command1 HSN Status byte5							
	Command1 HSN Status byte5							
46	Command1 HSN Status byte6							
	Command1 HSN Status byte6							
47	Command1 HSN Status byte7							
	Command1 HSN Status byte7							
48	DMA_EXTERNAL_ERROR3(DMA)/ DRR_EXTERNAL_ERROR3(DRR)/ ERRSTS9(MFDMA)							
	DMA Status byte16/ DRR Status byte16/ MFDMA Status36							
49	DMA_EXTERNAL_ERROR3(DMA)/ DRR_EXTERNAL_ERROR3(DRR)/ ERRSTS9(MFDMA)							
	DMA Status byte16/ DRR Status byte16/ MFDMA Status37							
4A	DMA_EXTERNAL_ERROR3(DMA)/ DRR_EXTERNAL_ERROR3(DRR)/ ERRSTS9(MFDMA)							
	DMA Status byte16/ DRR Status byte16/ MFDMA Status38							
4B	DMA_EXTERNAL_ERROR3(DMA)/ DRR_EXTERNAL_ERROR3(DRR)/ ERRSTS9(MFDMA)							
	DMA Status byte16/ DRR Status byte16/ MFDMA Status39							
4C	Not used '0'							
4D	Not used '0'							
4E	Not used '0'							
4F	Not used '0'							

Byte27 = F2 (CACHE ERR) Byte50 - 63 Detail

	0	1	2	3	4	5	6	7
50	MSW::MCTL0 CMA_RG0_BODERR Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	MEMREG1 BODERR	MEMREG0 BODERR
51	MSW::MCTL0 CMA_RG0_BODERR Byte1							
	Not used	MWB0 WBODERR	MWB0 WBODERR	MWB1 WBODERR	MWB1 WBODERR	MWDALN0 BODERR	Not used	MEMCTL BODERR
52	MSW::MCTL0 CMA_RG0_BODERR Byte0							
	MWDCTL BODERR	MRDCTL BODERR	MRB0 WBODERR	MRB0 RBODERR	MRB1 WBODERR	MRB1 RBODERR	RTNCTL0 BODERR	RTNCTL0 BODERR
53	MSW::MCTL1 CMA_RG0_BODERR Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	MEMREG1 BODERR	MEMREG0 BODERR
54	MSW::MCTL1 CMA_RG0_BODERR Byte1							
	Not used	MWB0 WBODERR	MWB0 RBODERR	MWB1 WBODERR	MWB1 RBODERR	MWDALN0 BODERR	Not used	MEMCTL BODERR
55	MSW::MCTL1 CMA_RG0_BODERR Byte0							
	MWDCTL BODERR	MRDCTL BODERR	MRB0 WBODERR	MRB0 RBODERR	MRB1 WBODERR	MRB1 RBODERR	RTNCTL0 BODERR	RTNCTL0 BODERR
56	MSW::MCTL2 CMA_RG0_BODERR Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	MEMREG1 BODERR	MEMREG0 BODERR
57	MSW::MCTL2 CMA_RG0_BODERR Byte1							
	Not used	MWB0 WBODERR	MWB0 RBODERR	MWB1 WBODERR	MWB1 RBODERR	MWDALN0 BODERR	Not used	MEMCTL BODERR
58	MSW::MCTL2 CMA_RG0_BODERR Byte0							
	MWDCTL BODERR	MRDCTL BODERR	MRB0 WBODERR	MRB0 RBODERR	MRB1 WBODERR	MRB1 RBODERR	RTNCTL0 BODERR	RTNCTL0 BODERR
59	MSW::MCTL3 CMA_RG0_BODERR Byte2							
	Not used	Not used	Not used	Not used	Not used	Not used	MEMREG1 BODERR	MEMREG0 BODERR
5A	MSW::MCTL3 CMA_RG0_BODERR Byte1							
	Not used	MWB0 WBODERR	MWB0 RBODERR	MWB1 WBODERR	MWB1 RBODERR	MWDALN0 BODERR	Not used	MEMCTL BODERR
5B	MSW::MCTL3 CMA_RG0_BODERR Byte0							
	MWDCTL BODERR	MRDCTL BODERR	MRB0 WBODERR	MRB0 RBODERR	MRB1 WBODERR	MRB1 RBODERR	RTNCTL0 BODERR	RTNCTL0 BODERR
5C	MSW::MCTL0 CMA_RG0_ANYERR Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MEMCTL ANYERR
5D	MSW::MCTL0 CMA_RG0_ANYERR Byte0							
	MWDCTL ANYERR	MRDCTL ANYERR	Not used	Not used	Not used	Not used	RTNCTL0 ANYERR	RTNCTL0 ANYERR
5E	MSW::MCTL1 CMA_RG0_ANYERR Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MEMCTL ANYERR
5F	MSW::MCTL1 CMA_RG0_ANYERR Byte0							
	MWDCTL ANYERR	MRDCTL ANYERR	Not used	Not used	Not used	Not used	RTNCTL0 ANYERR	RTNCTL0 ANYERR
60	MSW::MCTL2 CMA_RG0_ANYERR Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MEMCTL ANYERR
61	MSW::MCTL2 CMA_RG0_ANYERR Byte0							
	MWDCTL ANYERR	MRDCTL ANYERR	Not used	Not used	Not used	Not used	RTNCTL0 ANYERR	RTNCTL0 ANYERR
62	MSW::MCTL3 CMA_RG0_ANYERR Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MEMCTL ANYERR
63	MSW::MCTL3 CMA_RG0_ANYERR Byte0							
	MWDCTL ANYERR	MRDCTL ANYERR	Not used	Not used	Not used	Not used	RTNCTL0 ANYERR	RTNCTL0 ANYERR

Byte27 = F2 (CACHE ERR) Byte64 - 6F Detail

- (1) If MSWSTS : (b4 = '1') then CM0 or CM4 error
- (2) If MSWSTS : (b5 = '1') then CM1 or CM5 error
- (3) If MSWSTS : (b6 = '1') then CM2 or CM6 error
- (4) If MSWSTS : (b7 = '1') then CM3 or CM7 error

	0	1	2	3	4	5	6	7
64	MSW::MCTLx CMA_MEM_ANYERR_STS Byte0							
	CMyMEMSIG ERR	CMxMEMSIG ERR	ANYINV CMDERR	RNK OVRERR	ACS ERR	DIMMINV ERR	DSCBSY ERR	MEMNOT RDYERR
65	MSW::MCTLx CMA_MWD_ANYERR_STS Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	ANYWR SLRCERR
66	MSW::MCTLx CMA_RTN_ANYERR_STS(VC0) Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	ANYINV CMDERR	RD SLRCERR
67	MSW::MCTLx CMA_RTN_ANYERR_STS(VC1) Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	ANYINV CMDERR	RD SLRCERR
68	MSW::MCTLx CMA_MRD_ANYERR_STS Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RDTMG ERR
69	MSW::MCTLx CMA_MRD_ANYERR_STS Byte2							
	CMyRANK3 RWR UNCERR	CMyRANK2 RWR UNCERR	CMyRANK1 RWR UNCERR	CMyRANK0 RWR UNCERR	CMxRANK3 RWR UNCERR	CMxRANK2 RWR UNCERR	CMxRANK1 RWR UNCERR	CMxRANK0 RWR UNCERR
6A	MSW::MCTLx CMA_MRD_ANYERR_STS Byte1							
	CMyRANK3 WR UNCERR	CMyRANK2 WR UNCERR	CMyRANK1 WR UNCERR	CMyRANK0 WR UNCERR	CMxRANK3 WR UNCERR	CMxRANK2 WR UNCERR	CMxRANK1 WR UNCERR	CMxRANK0 WR UNCERR
6B	MSW::MCTLx CMA_MRD_ANYERR_STS Byte0							
	CMyRANK3 RD UNCERR	CMyRANK2 RD UNCERR	CMyRANK1 RD UNCERR	CMyRANK0 RD UNCERR	CMxRANK3 RD UNCERR	CMxRANK2 RD UNCERR	CMxRANK1 RD UNCERR	CMxRANK0 RD UNCERR
6C	MSW::MCTLx CMA_MEM_DIMM_CFG Byte1							
	Not used	CMy RANK 4rank	CMy RANK 2rank	CMy RANK 1rank	Not used	CMy SIZE 4Gb	CMy SIZE 2Gb	CMy SIZE 1Gb
6D	MSW::MCTLx CMA_MEM_DIMM_CFG Byte0							
	Not used	CMx RANK 4rank	CMx RANK 2rank	CMx RANK 1rank	Not used	CMx SIZE 4Gb	CMx SIZE 2Gb	CMx SIZE 1Gb
6E	MSW::MCTLx CMA_MEM_DIMM_ENB Byte1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CMy ENB
6F	MSW::MCTLx CMA_MEM_DIMM_ENB Byte0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CMx ENB

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Byte27 = F2 (CACHE ERR) Byte70 - 7F Detail

	0	1	2	3	4	5	6	7
70	Access mode Byte0							
71	Access mode Byte1							
72	Access mode Byte2							
73	Access mode Byte3							
74	Access mode Byte4							
75	Access mode Byte5							
76	Access mode Byte6							
77	Access mode Byte7							
78	Access Address Byte0							
79	Access Address Byte1							
7A	Access Address Byte2							
7B	Access Address Byte3							
7C	Access Address Byte4							
7D	Access Address Byte5							
7E	Access Address Byte6							
7F	Access Address Byte7							

Byte27 = F5 (Remote Copy/global-active device Paths Removed/Restored) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type		Processor ID					
07								
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'5')			
28	Reason code (*1)							
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C	RCU Sequence No.							
2D								
2E								
2F	MCU Manufacture code/Factory code							
30								
31	MCU Sequence No.							
32								
33								
34	SSID of self storage system							
35								
36	Symptom code (x'38')							
37	Symptom code (x'87')							

*1: Reason code

x'01' : One or more remote copy/global-active device paths removed unexpectedly

x'02' : One or more remote copy/global-active device paths restored

x'03' : All remote copy/global-active device paths removed unexpectedly

Byte27 = F5 (Remote Copy/global-active device Paths Removed/Restored) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control							
39	Fault pair duplex	Program action code						
3A		Configuration information						
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-2900

Byte27 = F6 (CFW Access not Authorized) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'6')			
28	Reason code (x'01': CFW ID unmatched)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (Invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF6')							
37								

SSBLOG05-2910

Byte27 = F6 (CFW Access not Authorized) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Not used							
7F								

SSBLOG05-2920

Byte27 = FA (NVS Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID (NOTE)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'A')			
28	Reason code (x'02': NVS failure)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (Invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFFA')							
37								

SSBLOG05-2930**Byte27 = FA (NVS Terminated) (2/2)**

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Not used					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Key code (for reconnection)							
41	Format/Message (for reconnection)							
42	Module ID (for reconnection)							
43	Routine ID (for reconnection)							
44								
	Not used							
7F								

SSBLOG05-2940

Byte27 = FB (TrueCopy for Mainframe Pair suspend) (1/2)

	0	1	2	3	4	5	6	7
00 	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (NOTE)					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'B')			
28	R-VOL Suspended	R-VOL failed	Reason code (*1)					
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C								
2D	RCU Sequence No.							
2E								
2F	MCU Manufacture code/Factory code							
30								
31								
32	MCU Sequence No.							
33								
34	SSID of self storage system							
35								

***1: Reason code**

x'04'-x'2F' : Reserved

x'30' : Pair suspended. MCU device write error.

x'31' : Pair suspended. RCU storage system error/or MCU storage system error.

x'32' : Pair suspended. RCU device communication error.

x'33' : Pair suspended (Critical device status). All write command is rejected until the pair is re-established.

x'34' : Pair suspended. RCU device is not ready (intervention required).

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Byte27 = FB (TrueCopy for Mainframe Pair suspend) (2/2)

	0	1	2	3	4	5	6	7
36	Symptom code (x'FE')							
37	Symptom code (same byte #28)							
38	Log and message control							
39	Fault pair duplex	Program action code						
3A		Configuration information						
3B								
3C	Case of Byte3B	Bit3=0:	Not used (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					
40	Error information							
7F								

SSBLOG05-2960

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP No.2 ¹	SP No.2 ⁰	CTRL No.	Device address				
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'F')				Message (x'F')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Module ID							
33	Routine ID							
34	PKID				Message code(x'0')			
35	SSID of self storage system							
36	Symptom code (x'FFFF')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Hardware information (See following pages)							
7F								

SSBLOG05-2970

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 or 04 : DXBF correctable error (1/4)

	0	1	2	3	4	5	6	7
28	Subcode : x'01' or x'04' : DXBF correctable error							
29	MPID (Reported SSB)							
2A	Not used	Not used	Not used	MOID	PKID	PKID	PKID	PKID
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	WARNING							
	WRN_EC0	WRN_EC1	WRN_EC2	WRN_EC3	WRN_EC4	WRN_EC5	WRN_EC6	WRN_EC7
41	ECC_LOG_EN_MON							
	WLOG_EN(0)	WLOG_EN(1)	WLOG_EN(2)	WLOG_EN(3)	ELOG_EN(0)	ELOG_EN(1)	ELOG_EN(2)	ELOG_EN(3)
42	Not used (x'00')							
43	Not used (x'00')							
44	ECC_WAR_MON (byte3)							
	WAR_LOG1 (7)	WAR_LOG1 (6)	WAR_LOG1 (5)	WAR_LOG1 (4)	WAR_LOG1 (3)	WAR_LOG1 (2)	WAR_LOG1 (1)	WAR_LOG1 (0)
45	ECC_WAR_MON (byte2)							
	WAR_LOG2 (7)	WAR_LOG2 (6)	WAR_LOG2 (5)	WAR_LOG2 (4)	WAR_LOG2 (3)	WAR_LOG2 (2)	WAR_LOG2 (1)	WAR_LOG2 (0)
46	ECC_WAR_MON (byte1)							
	WAR_LOG3 (7)	WAR_LOG3 (6)	WAR_LOG3 (5)	WAR_LOG3 (4)	WAR_LOG3 (3)	WAR_LOG3 (2)	WAR_LOG3 (1)	WAR_LOG3 (0)
47	ECC_WAR_MON (byte0)							
	WAR_LOG4 (7)	WAR_LOG4 (6)	WAR_LOG4 (5)	WAR_LOG4 (4)	WAR_LOG4 (3)	WAR_LOG4 (2)	WAR_LOG4 (1)	WAR_LOG4 (0)
48	ECC_WRNADR_MON1 (byte3)							
	WLOG_ADR1(31)	WLOG_ADR1(30)	WLOG_ADR1(29)	WLOG_ADR1(28)	WLOG_ADR1(27)	WLOG_ADR1(26)	WLOG_ADR1(25)	WLOG_ADR1(24)
49	ECC_WRNADR_MON1 (byte2)							
	WLOG_ADR1(23)	WLOG_ADR1(22)	WLOG_ADR1(21)	WLOG_ADR1(20)	WLOG_ADR1(19)	WLOG_ADR1(18)	WLOG_ADR1(17)	WLOG_ADR1(16)
4A	ECC_WRNADR_MON1 (byte1)							
	WLOG_ADR1(15)	WLOG_ADR1(14)	WLOG_ADR1(13)	WLOG_ADR1(12)	WLOG_ADR1(11)	WLOG_ADR1(10)	WLOG_ADR1(9)	WLOG_ADR1(8)
4B	ECC_WRNADR_MON1 (byte0)							
	WLOG_ADR1(7)	WLOG_ADR1(6)	WLOG_ADR1(5)	WLOG_ADR1(4)	WLOG_ADR1(3)	WLOG_ADR1(2)	WLOG_ADR1(1)	WLOG_ADR1(0)
4C	ECC_WRNADR_MON2 (byte3)							
	WLOG_ADR2(31)	WLOG_ADR2(30)	WLOG_ADR2(29)	WLOG_ADR2(28)	WLOG_ADR2(27)	WLOG_ADR2(26)	WLOG_ADR2(25)	WLOG_ADR2(24)
4D	ECC_WRNADR_MON2 (byte2)							
	WLOG_ADR2(23)	WLOG_ADR2(22)	WLOG_ADR2(21)	WLOG_ADR2(20)	WLOG_ADR2(19)	WLOG_ADR2(18)	WLOG_ADR2(17)	WLOG_ADR2(16)
4E	ECC_WRNADR_MON2 (byte1)							
	WLOG_ADR2(15)	WLOG_ADR2(14)	WLOG_ADR2(13)	WLOG_ADR2(12)	WLOG_ADR2(11)	WLOG_ADR2(10)	WLOG_ADR2(9)	WLOG_ADR2(8)
4F	ECC_WRNADR_MON2 (byte0)							
	WLOG_ADR2(7)	WLOG_ADR2(6)	WLOG_ADR2(5)	WLOG_ADR2(4)	WLOG_ADR2(3)	WLOG_ADR2(2)	WLOG_ADR2(1)	WLOG_ADR2(0)

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 or 04 : DXBF correctable error (2/4)

	0	1	2	3	4	5	6	7
50	ECC_WRNADR_MON3 (byte3)							
	WLOG_ADR3(31)	WLOG_ADR3(30)	WLOG_ADR3(29)	WLOG_ADR3(28)	WLOG_ADR3(27)	WLOG_ADR3(26)	WLOG_ADR3(25)	WLOG_ADR3(24)
51	ECC_WRNADR_MON3 (byte2)							
	WLOG_ADR3(23)	WLOG_ADR3(22)	WLOG_ADR3(21)	WLOG_ADR3(20)	WLOG_ADR3(19)	WLOG_ADR3(18)	WLOG_ADR3(17)	WLOG_ADR3(16)
52	ECC_WRNADR_MON3 (byte1)							
	WLOG_ADR3(15)	WLOG_ADR3(14)	WLOG_ADR3(13)	WLOG_ADR3(12)	WLOG_ADR3(11)	WLOG_ADR3(10)	WLOG_ADR3(9)	WLOG_ADR3(8)
53	ECC_WRNADR_MON3 (byte0)							
	WLOG_ADR3(7)	WLOG_ADR3(6)	WLOG_ADR3(5)	WLOG_ADR3(4)	WLOG_ADR3(3)	WLOG_ADR3(2)	WLOG_ADR3(1)	WLOG_ADR3(0)
54	ECC_WRNADR_MON4 (byte3)							
	WLOG_ADR4(31)	WLOG_ADR4(30)	WLOG_ADR4(29)	WLOG_ADR4(28)	WLOG_ADR4(27)	WLOG_ADR4(26)	WLOG_ADR4(25)	WLOG_ADR4(24)
55	ECC_WRNADR_MON4 (byte2)							
	WLOG_ADR4(23)	WLOG_ADR4(22)	WLOG_ADR4(21)	WLOG_ADR4(20)	WLOG_ADR4(19)	WLOG_ADR4(18)	WLOG_ADR4(17)	WLOG_ADR4(16)
56	ECC_WRNADR_MON4 (byte1)							
	WLOG_ADR4(15)	WLOG_ADR4(14)	WLOG_ADR4(13)	WLOG_ADR4(12)	WLOG_ADR4(11)	WLOG_ADR4(10)	WLOG_ADR4(9)	WLOG_ADR4(8)
57	ECC_WRNADR_MON4 (byte0)							
	WLOG_ADR4(7)	WLOG_ADR4(6)	WLOG_ADR4(5)	WLOG_ADR4(4)	WLOG_ADR4(3)	WLOG_ADR4(2)	WLOG_ADR4(1)	WLOG_ADR4(0)
58	ECC_SYN_MON1 (byte3)							
	ECC_SYND_EC0_1(15)	ECC_SYND_EC0_1(14)	ECC_SYND_EC0_1(13)	ECC_SYND_EC0_1(12)	ECC_SYND_EC0_1(11)	ECC_SYND_EC0_1(10)	ECC_SYND_EC0_1(9)	ECC_SYND_EC0_1(8)
59	ECC_SYN_MON1 (byte2)							
	ECC_SYND_EC0_1(7)	ECC_SYND_EC0_1(6)	ECC_SYND_EC0_1(5)	ECC_SYND_EC0_1(4)	ECC_SYND_EC0_1(3)	ECC_SYND_EC0_1(2)	ECC_SYND_EC0_1(1)	ECC_SYND_EC0_1(0)
5A	ECC_SYN_MON1 (byte1)							
	ECC_SYND_EC0_2(15)	ECC_SYND_EC0_2(14)	ECC_SYND_EC0_2(13)	ECC_SYND_EC0_2(12)	ECC_SYND_EC0_2(11)	ECC_SYND_EC0_2(10)	ECC_SYND_EC0_2(9)	ECC_SYND_EC0_2(8)
5B	ECC_SYN_MON1 (byte0)							
	ECC_SYND_EC0_2(7)	ECC_SYND_EC0_2(6)	ECC_SYND_EC0_2(5)	ECC_SYND_EC0_2(4)	ECC_SYND_EC0_2(3)	ECC_SYND_EC0_2(2)	ECC_SYND_EC0_2(1)	ECC_SYND_EC0_2(0)
5C	ECC_SYN_MON2 (byte3)							
	ECC_SYND_EC0_3(15)	ECC_SYND_EC0_3(14)	ECC_SYND_EC0_3(13)	ECC_SYND_EC0_3(12)	ECC_SYND_EC0_3(11)	ECC_SYND_EC0_3(10)	ECC_SYND_EC0_3(9)	ECC_SYND_EC0_3(8)
5D	ECC_SYN_MON2 (byte2)							
	ECC_SYND_EC0_3(7)	ECC_SYND_EC0_3(6)	ECC_SYND_EC0_3(5)	ECC_SYND_EC0_3(4)	ECC_SYND_EC0_3(3)	ECC_SYND_EC0_3(2)	ECC_SYND_EC0_3(1)	ECC_SYND_EC0_3(0)
5E	ECC_SYN_MON2 (byte1)							
	ECC_SYND_EC0_4(15)	ECC_SYND_EC0_4(14)	ECC_SYND_EC0_4(13)	ECC_SYND_EC0_4(12)	ECC_SYND_EC0_4(11)	ECC_SYND_EC0_4(10)	ECC_SYND_EC0_4(9)	ECC_SYND_EC0_4(8)
5F	ECC_SYN_MON2 (byte0)							
	ECC_SYND_EC0_4(7)	ECC_SYND_EC0_4(6)	ECC_SYND_EC0_4(5)	ECC_SYND_EC0_4(4)	ECC_SYND_EC0_4(3)	ECC_SYND_EC0_4(2)	ECC_SYND_EC0_4(1)	ECC_SYND_EC0_4(0)
60	WARNING							
	WRN_EC0	WRN_EC1	WRN_EC2	WRN_EC3	WRN_EC4	WRN_EC5	WRN_EC6	WRN_EC7
61	ECC_LOG_EN_MON							
	WLOG_EN(0)	WLOG_EN(1)	WLOG_EN(2)	WLOG_EN(3)	ELOG_EN(0)	ELOG_EN(1)	ELOG_EN(2)	ELOG_EN(3)
62	Not used (x'00')							
63	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 or 04 : DXBF correctable error (3/4)

	0	1	2	3	4	5	6	7
64	ECC_WAR_MON (byte3)							
	WAR_LOG1 (7)	WAR_LOG1 (6)	WAR_LOG1 (5)	WAR_LOG1 (4)	WAR_LOG1 (3)	WAR_LOG1 (2)	WAR_LOG1 (1)	WAR_LOG1 (0)
65	ECC_WAR_MON (byte2)							
	WAR_LOG2 (7)	WAR_LOG2 (6)	WAR_LOG2 (5)	WAR_LOG2 (4)	WAR_LOG2 (3)	WAR_LOG2 (2)	WAR_LOG2 (1)	WAR_LOG2 (0)
66	ECC_WAR_MON (byte1)							
	WAR_LOG3 (7)	WAR_LOG3 (6)	WAR_LOG3 (5)	WAR_LOG3 (4)	WAR_LOG3 (3)	WAR_LOG3 (2)	WAR_LOG3 (1)	WAR_LOG3 (0)
67	ECC_WAR_MON (byte0)							
	WAR_LOG4 (7)	WAR_LOG4 (6)	WAR_LOG4 (5)	WAR_LOG4 (4)	WAR_LOG4 (3)	WAR_LOG4 (2)	WAR_LOG4 (1)	WAR_LOG4 (0)
68	ECC_WRNADR_MON1 (byte3)							
	WLOG_AD R1(31)	WLOG_ADR 1(30)	WLOG_ADR 1(29)	WLOG_ADR 1(28)	WLOG_ADR 1(27)	WLOG_ADR 1(26)	WLOG_ADR 1(25)	WLOG_AD R1(24)
69	ECC_WRNADR_MON1 (byte2)							
	WLOG_AD R1(23)	WLOG_ADR 1(22)	WLOG_ADR 1(21)	WLOG_ADR 1(20)	WLOG_ADR 1(19)	WLOG_ADR 1(18)	WLOG_ADR 1(17)	WLOG_AD R1(16)
6A	ECC_WRNADR_MON1 (byte1)							
	WLOG_AD R1(15)	WLOG_ADR 1(14)	WLOG_ADR 1(13)	WLOG_ADR 1(12)	WLOG_ADR 1(11)	WLOG_ADR 1(10)	WLOG_ADR 1(9)	WLOG_AD R1(8)
6B	ECC_WRNADR_MON1 (byte0)							
	WLOG_AD R1(7)	WLOG_ADR 1(6)	WLOG_ADR 1(5)	WLOG_ADR 1(4)	WLOG_ADR 1(3)	WLOG_ADR 1(2)	WLOG_ADR 1(1)	WLOG_AD R1(0)
6C	ECC_WRNADR_MON2 (byte3)							
	WLOG_AD R2(31)	WLOG_ADR 2(30)	WLOG_ADR 2(29)	WLOG_ADR 2(28)	WLOG_ADR 2(27)	WLOG_ADR 2(26)	WLOG_ADR 2(25)	WLOG_AD R2(24)
6D	ECC_WRNADR_MON2 (byte2)							
	WLOG_AD R2(23)	WLOG_ADR 2(22)	WLOG_ADR 2(21)	WLOG_ADR 2(20)	WLOG_ADR 2(19)	WLOG_ADR 2(18)	WLOG_ADR 2(17)	WLOG_AD R2(16)
6E	ECC_WRNADR_MON2 (byte1)							
	WLOG_AD R2(15)	WLOG_ADR 2(14)	WLOG_ADR 2(13)	WLOG_ADR 2(12)	WLOG_ADR 2(11)	WLOG_ADR 2(10)	WLOG_ADR 2(9)	WLOG_AD R2(8)
6F	ECC_WRNADR_MON2 (byte0)							
	WLOG_AD R2(7)	WLOG_ADR 2(6)	WLOG_ADR 2(5)	WLOG_ADR 2(4)	WLOG_ADR 2(3)	WLOG_ADR 2(2)	WLOG_ADR 2(1)	WLOG_AD R2(0)
70	ECC_WRNADR_MON3 (byte3)							
	WLOG_AD R3(31)	WLOG_ADR 3(30)	WLOG_ADR 3(29)	WLOG_ADR 3(28)	WLOG_ADR 3(27)	WLOG_ADR 3(26)	WLOG_ADR 3(25)	WLOG_AD R3(24)
71	ECC_WRNADR_MON3 (byte2)							
	WLOG_AD R3(23)	WLOG_ADR 3(22)	WLOG_ADR 3(21)	WLOG_ADR 3(20)	WLOG_ADR 3(19)	WLOG_ADR 3(18)	WLOG_ADR 3(17)	WLOG_AD R3(16)
72	ECC_WRNADR_MON3 (byte1)							
	WLOG_AD R3(15)	WLOG_ADR 3(14)	WLOG_ADR 3(13)	WLOG_ADR 3(12)	WLOG_ADR 3(11)	WLOG_ADR 3(10)	WLOG_ADR 3(9)	WLOG_AD R3(8)
73	ECC_WRNADR_MON3 (byte0)							
	WLOG_AD R3(7)	WLOG_ADR 3(6)	WLOG_ADR 3(5)	WLOG_ADR 3(4)	WLOG_ADR 3(3)	WLOG_ADR 3(2)	WLOG_ADR 3(1)	WLOG_AD R3(0)
74	ECC_WRNADR_MON4 (byte3)							
	WLOG_AD R4(31)	WLOG_ADR 4(30)	WLOG_ADR 4(29)	WLOG_ADR 4(28)	WLOG_ADR 4(27)	WLOG_ADR 4(26)	WLOG_ADR 4(25)	WLOG_AD R4(24)
75	ECC_WRNADR_MON4 (byte2)							
	WLOG_AD R4(23)	WLOG_ADR 4(22)	WLOG_ADR 4(21)	WLOG_ADR 4(20)	WLOG_ADR 4(19)	WLOG_ADR 4(18)	WLOG_ADR 4(17)	WLOG_AD R4(16)
76	ECC_WRNADR_MON4 (byte1)							
	WLOG_AD R4(15)	WLOG_ADR 4(14)	WLOG_ADR 4(13)	WLOG_ADR 4(12)	WLOG_ADR 4(11)	WLOG_ADR 4(10)	WLOG_ADR 4(9)	WLOG_AD R4(8)
77	ECC_WRNADR_MON4 (byte0)							
	WLOG_AD R4(7)	WLOG_ADR 4(6)	WLOG_ADR 4(5)	WLOG_ADR 4(4)	WLOG_ADR 4(3)	WLOG_ADR 4(2)	WLOG_ADR 4(1)	WLOG_AD R4(0)

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 or 04 : DXBF correctable error (4/4)

	0	1	2	3	4	5	6	7
78	ECC_SYN_MON1 (byte3)							
	ECC_SYND_EC0_1(15)	ECC_SYND_EC0_1(14)	ECC_SYND_EC0_1(13)	ECC_SYND_EC0_1(12)	ECC_SYND_EC0_1(11)	ECC_SYND_EC0_1(10)	ECC_SYND_EC0_1(9)	ECC_SYND_EC0_1(8)
79	ECC_SYN_MON1 (byte2)							
	ECC_SYND_EC0_1(7)	ECC_SYND_EC0_1(6)	ECC_SYND_EC0_1(5)	ECC_SYND_EC0_1(4)	ECC_SYND_EC0_1(3)	ECC_SYND_EC0_1(2)	ECC_SYND_EC0_1(1)	ECC_SYND_EC0_1(0)
7A	ECC_SYN_MON1 (byte1)							
	ECC_SYND_EC0_2(15)	ECC_SYND_EC0_2(14)	ECC_SYND_EC0_2(13)	ECC_SYND_EC0_2(12)	ECC_SYND_EC0_2(11)	ECC_SYND_EC0_2(10)	ECC_SYND_EC0_2(9)	ECC_SYND_EC0_2(8)
7B	ECC_SYN_MON1 (byte0)							
	ECC_SYND_EC0_2(7)	ECC_SYND_EC0_2(6)	ECC_SYND_EC0_2(5)	ECC_SYND_EC0_2(4)	ECC_SYND_EC0_2(3)	ECC_SYND_EC0_2(2)	ECC_SYND_EC0_2(1)	ECC_SYND_EC0_2(0)
7C	ECC_SYN_MON2 (byte3)							
	ECC_SYND_EC0_3(15)	ECC_SYND_EC0_3(14)	ECC_SYND_EC0_3(13)	ECC_SYND_EC0_3(12)	ECC_SYND_EC0_3(11)	ECC_SYND_EC0_3(10)	ECC_SYND_EC0_3(9)	ECC_SYND_EC0_3(8)
7D	ECC_SYN_MON2 (byte2)							
	ECC_SYND_EC0_3(7)	ECC_SYND_EC0_3(6)	ECC_SYND_EC0_3(5)	ECC_SYND_EC0_3(4)	ECC_SYND_EC0_3(3)	ECC_SYND_EC0_3(2)	ECC_SYND_EC0_3(1)	ECC_SYND_EC0_3(0)
7E	ECC_SYN_MON2 (byte1)							
	ECC_SYND_EC0_4(15)	ECC_SYND_EC0_4(14)	ECC_SYND_EC0_4(13)	ECC_SYND_EC0_4(12)	ECC_SYND_EC0_4(11)	ECC_SYND_EC0_4(10)	ECC_SYND_EC0_4(9)	ECC_SYND_EC0_4(8)
7F	ECC_SYN_MON2 (byte0)							
	ECC_SYND_EC0_4(7)	ECC_SYND_EC0_4(6)	ECC_SYND_EC0_4(5)	ECC_SYND_EC0_4(4)	ECC_SYND_EC0_4(3)	ECC_SYND_EC0_4(2)	ECC_SYND_EC0_4(1)	ECC_SYND_EC0_4(0)

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(2) Byte28 = 02 : Cache Memory correctable error (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'02' : Cache Memory correctable error							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Warning CM0,CM4 status (Byte3)							MCTL0 RAM Warning
	Not used (x'00')							
41	Warning CM0,CM4 status (Byte2)							
	CM4 RANK3 Rewrite Warning	CM4 RANK2 Rewrite Warning	CM4 RANK1 Rewrite Warning	CM4 RANK0 Rewrite Warning	CM0 RANK3 Rewrite Warning	CM0 RANK2 Rewrite Warning	CM0 RANK1 Rewrite Warning	CM0 RANK0 Rewrite Warning
42	Warning CM0,CM4 status (Byte1)							
	CM4 RANK3 Write Warning	CM4 RANK2 Write Warning	CM4 RANK1 Write Warning	CM4 RANK0 Write Warning	CM0 RANK3 Write Warning	CM0 RANK2 Write Warning	CM0 RANK1 Write Warning	CM0 RANK0 Write Warning
43	Warning CM0,CM4 status (Byte0)							
	CM4 RANK3 Read Warning	CM4 RANK2 Read Warning	CM4 RANK1 Read Warning	CM4 RANK0 Read Warning	CM0 RANK3 Read Warning	CM0 RANK2 Read Warning	CM0 RANK1 Read Warning	CM0 RANK0 Read Warning
44	Correctable error count of CM0,CM4(Byte3)							
	CM4 RANK3 Correctable Error Count				CM4 RANK2 Correctable Error Count			
45	Correctable error count of CM0,CM4(Byte2)							
	CM4 RANK1 Correctable Error Count				CM4 RANK0 Correctable Error Count			
46	Correctable error count of CM0,CM4(Byte1)							
	CM0 RANK3 Correctable Error Count				CM0 RANK2 Correctable Error Count			
47	Correctable error count of CM0,CM4(Byte0)							
	CM0 RANK1 Correctable Error Count				CM0 RANK0 Correctable Error Count			

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(2) Byte28 = 02 : Cache Memory correctable error (2/3)

	0	1	2	3	4	5	6	7
48	Warning CM1,CM5 status (Byte3)							MCTL1 RAM Warning
	Not used (x'00')							
49	Warning CM1,CM5 status (Byte2)							
	CM5 RANK3 Rewrite Warning	CM5 RANK2 Rewrite Warning	CM5 RANK1 Rewrite Warning	CM5 RANK0 Rewrite Warning	CM1 RANK3 Rewrite Warning	CM1 RANK2 Rewrite Warning	CM1 RANK1 Rewrite Warning	CM1 RANK0 Rewrite Warning
4A	Warning CM1,CM5 status (Byte1)							
	CM5 RANK3 Write Warning	CM5 RANK2 Write Warning	CM5 RANK1 Write Warning	CM5 RANK0 Write Warning	CM1 RANK3 Write Warning	CM1 RANK2 Write Warning	CM1 RANK1 Write Warning	CM1 RANK0 Write Warning
4B	Warning CM1,CM5 status (Byte0)							
	CM5 RANK3 Read Warning	CM5 RANK2 Read Warning	CM5 RANK1 Read Warning	CM5 RANK0 Read Warning	CM1 RANK3 Read Warning	CM1 RANK2 Read Warning	CM1 RANK1 Read Warning	CM1 RANK0 Read Warning
4C	Correctable error count of CM1,CM4(Byte3)							
	CM5 RANK3 Correctable Error Count				CM5 RANK2 Correctable Error Count			
4D	Correctable error count of CM1,CM4(Byte2)							
	CM5 RANK1 Correctable Error Count				CM5 RANK0 Correctable Error Count			
4E	Correctable error count of CM1,CM4(Byte1)							
	CM1 RANK3 Correctable Error Count				CM1 RANK2 Correctable Error Count			
4F	Correctable error count of CM1,CM4(Byte0)							
	CM1 RANK1 Correctable Error Count				CM1 RANK0 Correctable Error Count			
50	Warning CM2,CM6 status (Byte3)							MCTL2 RAM Warning
	Not used (x'00')							
51	Warning CM2,CM6 status (Byte2)							
	CM6 RANK3 Rewrite Warning	CM5 RANK2 Rewrite Warning	CM6 RANK1 Rewrite Warning	CM6 RANK0 Rewrite Warning	CM2 RANK3 Rewrite Warning	CM2 RANK2 Rewrite Warning	CM2 RANK1 Rewrite Warning	CM2 RANK0 Rewrite Warning
52	Warning CM2,CM6 status (Byte1)							
	CM6 RANK3 Write Warning	CM5 RANK2 Write Warning	CM6 RANK1 Write Warning	CM6 RANK0 Write Warning	CM2 RANK3 Write Warning	CM2 RANK2 Write Warning	CM2 RANK1 Write Warning	CM2 RANK0 Write Warning
53	Warning CM2,CM6 status (Byte0)							
	CM6 RANK3 Read Warning	CM6 RANK2 Read Warning	CM6 RANK1 Read Warning	CM6 RANK0 Read Warning	CM2 RANK3 Read Warning	CM2 RANK2 Read Warning	CM2 RANK1 Read Warning	CM2 RANK0 Read Warning
54	Correctable error count of CM2,CM6(Byte3)							
	CM6 RANK3 Correctable Error Count				CM6 RANK2 Correctable Error Count			
55	Correctable error count of CM2,CM6(Byte2)							
	CM6 RANK1 Correctable Error Count				CM6 RANK0 Correctable Error Count			
56	Correctable error count of CM2,CM6(Byte1)							
	CM2 RANK3 Correctable Error Count				CM2 RANK2 Correctable Error Count			
57	Correctable error count of CM2,CM6(Byte0)							
	CM2 RANK1 Correctable Error Count				CM2 RANK0 Correctable Error Count			

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(2) Byte28 = 02 : Cache Memory correctable error (3/3)

	0	1	2	3	4	5	6	7
58	Warning CM3,CM6 status (Byte3)							MCTL3 RAM Warning
	Not used (x'00')							
59	Warning CM3,CM6 status (Byte2)							
	CM6 RANK3 Rewrite Warning	CM6 RANK2 Rewrite Warning	CM6 RANK1 Rewrite Warning	CM6 RANK0 Rewrite Warning	CM3 RANK3 Rewrite Warning	CM3 RANK2 Rewrite Warning	CM3 RANK1 Rewrite Warning	CM3 RANK0 Rewrite Warning
5A	Warning CM3,CM6 status (Byte1)							
	CM6 RANK3 Write Warning	CM6 RANK2 Write Warning	CM5 RANK1 Write Warning	CM6 RANK0 Write Warning	CM3 RANK3 Write Warning	CM3 RANK2 Write Warning	CM3 RANK1 Write Warning	CM3 RANK0 Write Warning
5B	Warning CM3,CM6 status (Byte0)							
	CM6 RANK3 Read Warning	CM6 RANK2 Read Warning	CM6 RANK1 Read Warning	CM6 RANK0 Read Warning	CM3 RANK3 Read Warning	CM3 RANK2 Read Warning	CM3 RANK1 Read Warning	CM3 RANK0 Read Warning
5C	Correctable error count of CM3,CM6(Byte3)							
	CM6 RANK3 Correctable Error Count				CM6 RANK2 Correctable Error Count			
5D	Correctable error count of CM1,CM6(Byte2)							
	CM6 RANK1 Correctable Error Count				CM6 RANK0 Correctable Error Count			
5E	Correctable error count of CM3,CM6(Byte1)							
	CM3 RANK3 Correctable Error Count				CM3 RANK2 Correctable Error Count			
5F	Correctable error count of CM3,CM6(Byte0)							
	CM3 RANK1 Correctable Error Count				CM3 RANK0 Correctable Error Count			
60	Syndrome Bit Log0 (Byte3)							
61	Syndrome Bit Log0 (Byte2)							
62	Syndrome Bit Log0 (Byte1)							
63	Syndrome Bit Log0 (Byte0)							
64	Syndrome Bit Log1 (Byte3)							
65	Syndrome Bit Log1 (Byte2)							
66	Syndrome Bit Log1 (Byte1)							
67	Syndrome Bit Log1 (Byte0)							
68	Syndrome Block Log							
69	Not used (x'00')							
6A	Not used (x'00')							
6B	Not used (x'00')							
6C	Packet Header Log (Requeqter ID) (Byte1)							
6D	Packet Header Log (Requeqter ID) (Byte0)							
6E	Packet Header Log (Destination ID) (Byte1)							
6F	Packet Header Log (Destination ID) (Byte0)							
70	Packet Header Log (Command) (Byte1)							
71	Packet Header Log (Command) (Byte0)							
72	Packet Header Log (Data Length) (Byte1)							
73	Packet Header Log (Data Length) (Byte0)							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Packet Header Log (Dstination Address) (Byte4)							
78	Packet Header Log (Dstination Address) (Byte3)							
79	Packet Header Log (Dstination Address) (Byte2)							
7A	Packet Header Log (Dstination Address) (Byte1)							
7B	Packet Header Log (Dstination Address) (Byte0)							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 81 : LR correctable error (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'81' : LR correctable error							
29	MPID (Reported SSB)							
2A	Not used	Not used	Not used	MOID	PKID	PKID	PKID	PKID
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	INT_STATUS4 (Byte3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	INT_STATUS4 (Byte2)							
	Not used	Not used	Not used	Not used	Not used	INT_MFPSIF_WRN_P	INT_MFDMA1_WRN_P	INT_MFDM_A0_WRN_P
42	INT_STATUS4 (Byte1)							
	INT_MFDMA1_CTL_WRN_P (7)	INT_MFDMA1_CTL_WRN_P (6)	INT_MFDMA1_CTL_WRN_P (5)	INT_MFDMA1_CTL_WRN_P (4)	INT_MFDMA1_CTL_WRN_P (3)	INT_MFDMA1_CTL_WRN_P (2)	INT_MFDMA1_CTL_WRN_P (1)	INT_MFDMA1_CTL_WRN_P (0)
43	INT_STATUS4 (Byte0)							
	INT_MFDMA0_CTL_WRN_P (7)	INT_MFDMA0_CTL_WRN_P (6)	INT_MFDMA0_CTL_WRN_P (5)	INT_MFDMA0_CTL_WRN_P (4)	INT_MFDMA0_CTL_WRN_P (3)	INT_MFDMA0_CTL_WRN_P (2)	INT_MFDMA0_CTL_WRN_P (1)	INT_MFDMA0_CTL_WRN_P (0)
44	INT_STATUS5 (Byte3)							
	FRC_ERR	Not used	INT_FMC_WRN_P	INT_LRP4_WRN	INT_LRP3_WRN	INT_LRP2_WRN	INT_LRP1_WRN	INT_LRP0_WRN
45	INT_STATUS5 (Byte2)							
	Not used	Not used	Not used	INT_FDMA4_WRN	INT_FDMA3_WRN	INT_FDMA2_WRN	INT_FDMA1_WRN	INT_FDMA0_WRN
46	INT_STATUS5 (Byte1)							
	Not used	Not used	INT_DRR1_WRN	INT_DRR0_WRN	Not used	Not used	INT_DMAMB1_WRN	INT_DMAMB1_WRN
47	INT_STATUS5 (Byte0)							
	Not used	Not used	Not used	Not used	INT_DMA3_WRN	INT_DMA3_WRN	INT_DMA3_WRN	INT_DMA3_WRN
48	INT_STATUS6 (Byte1)							
	Not used	Not used	INT_PARB1_WRN	INT_PARB0_WRN	INT_HARB3_WRN_P	INT_HARB2_WRN_P	INT_HARB1_WRN_P	INT_HARB0_WRN_P
49	INT_STATUS6 (Byte0)							
	Not used	Not used	INT_PARB1R EG_WRN	INT_PARB0R EG_WRN	INT_HARB3R EG_WRN_P	INT_HARB2R EG_WRN_P	INT_HARB1R EG_WRN_P	INT_HARB0R EG_WRN_P
4A	INT_STATUS7 (Byte3)							
	DC_UPDOWN_WRN	TX_FAULT	PCI CORE WARN	Not used	Not used	Not used	Not used	INT_FMT_WRN_P
4B	INT_STATUS7 (Byte0)							
	Not used	Not used	INT_DXA1_WRN	INT_DXA0_WRN	INT_DXC1_M ICRO	INT_DXC0_M ICRO	INT_DXC1_WRN_N_DRAM_P	INT_DXC0_WRN_N_DRAM_P

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 81 : LR correctable error (2/5)

	0	1	2	3	4	5	6	7
4C	LR_MF_HSN_RX_WARNSTS8 (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	F_PRAM_CO R_ERR	C_PRAM_C OR_ERR
4D	WRN_ST00 (byte2)							
	NPBF3 CORRECT	NPBF2 CORRECT	NPBF1 CORRECT	NPBF0 CORRECT	YPBF3 CORRECT	YPBF2 CORRECT	YPBF1 CORRECT	YPBF0 CORRECT
4E	WRN_ST00 (byte1)							
	MBF CORRECT	Not used	Not used	Not used	Not used	Not used	Not used	Not used
4F	WRN_ST00 (byte0)							
	NPBF0 CORRECT	NPBF1 CORRECT	Not used	Not used	Not used	Not used	Not used	Not used
50	LR_XARBREG_PREGE_WRN_STS [PARB1] (byte3)							
	Not used	Not used	RX0_STATU S_ERR	TLM_RX0_E RR	Not used	Not used	Not used	TLM_TX0_ ERR
51	LR_XARBREG_PREGE_WRN_STS [PARB1] (byte2)							
	Not used	Not used	TLM_FCPTC _ERR	TLM_ROVF_ ERR	TLM_PTLT_ ERR	TLM_MFTLT_ _ERR	TLM_ECRC_ ERR	TLM_UR_E RR
52	LR_XARBREG_PREGE_WRN_STS [PARB1] (byte1)							
	Not used	Not used	DLM_SURPRI SE_DW_ERR	DLM_PTC_E RR	DLM_TLP_E RR	DLM_DLLP_ ERR	DLM_REPL AY_RO_ERR	DLM_REPLA Y_TO_ERR
53	LR_XARBREG_PREGE_WRN_STS [PARB1] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIE VER_ERR
54	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte0)							
	Not used	Not used	Not used	SPEED_GEN 3_ERR	PLM_LTSSM _ERR	PLM_LINKTR AINING_ERR	DLM_VCO_E N_ERR	DLM_DL_U P_ERR
55	LR_XARBREG_PREGE_ZPEXW_ERR [PARB1] (byte3)							
	TLM_RAM_R X0_AADR_PA RITY_ERROR	TLM_RAM_RX 0_BADR_PARI TY_ERROR	TLM_RAM_TX 0_AADR_PARI TY_ERROR	TLM_RAM_TX 0_BADR_PARI TY_ERROR	DLM_RAM ADR_PARIT Y_ERROR	Not used	Not used	Not used
56	LR_XARBREG_PREGE_ZPEXW_ERR [PARB1] (byte1)							
	ZPEX_DLM _ECCERRO RREP	ZPEX_DLM _ECCERROR _NOR	ZPEX_TLM _ECCERROR _TX0	ZPEX_TLM _ECCERROR _RX0	ZPEX_TLM_PA RITY_ERROR ALLOCATED	ZPEX_TLM PARITY_ER ROR_RX0	ZPEX_TLM PARITY_ER ROR_STOP	ZPEX_TLM PARITY_E RROR_TX0
57	LR_XARBREG_PREGE_ZPEXW_ERR [PARB1] (byte0)							
	ZPEX_DLM _ECCRECO VERY_REP	ZPEX_DLM _ECCRECOV ERY_NOR	ZPEX_TLM _ECCRECOV ERY_TX0	ZPEX_TLM _ECCRECOV ERY_RX0	Not used	Not used	ZPEX_ERRI NJECCRECO VERY	ZPEX_ERRI NJECCERR
58	LR_XARBREG_PREGE_WRN_STS [PARB0] (byte3)							
	Not used	Not used	RX0_STATU S_ERR	TLM_RX0_E RR	Not used	Not used	Not used	TLM_TX0_ ERR
59	LR_XARBREG_PREGE_WRN_STS [PARB0] (byte2)							
	Not used	Not used	TLM_FCPTC _ERR	TLM_ROVF_ ERR	TLM_PTLT_ ERR	TLM_MFTLT_ _ERR	TLM_ECRC_ ERR	TLM_UR_E RR
5A	LR_XARBREG_PREGE_WRN_STS [PARB0] (byte1)							
	Not used	Not used	DLM_SURPRI SE_DW_E RR	DLM_PTC_E RR	DLM_TLP_E RR	DLM_DLLP_ ERR	DLM_REPL AY_RO_ERR	DLM_REPL AY_TO_ER R
5B	LR_XARBREG_PREGE_WRN_STS [PARB0] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIE VER_ERR

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 81 : LR correctable error (3/5)

	0	1	2	3	4	5	6	7
5C	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte0)							
	Not used	Not used	Not used	SPEED_GEN_3_ERR	PLM_LTSSM_ERR	PLM_LINKT_RAINING_ERR	DLM_VC0_E_N_ERR	DLM_DL_U_P_ERR
5D	LR_XARBREG_PREG_ZPEXW_ERR [HARB2] (byte3)							
	TLM_RAM_RX0_AADR_PARITY_ERROR	TLM_RAM_RX0_BADR_PARITY_ERROR	TLM_RAM_TX0_AADR_PARITY_ERROR	TLM_RAM_TX0_BADR_PARITY_ERROR	DLM_RAM_ADR_PARITY_ERROR	Not used	Not used	Not used
5E	LR_XARBREG_PREG_ZPEXW_ERR [HARB2] (byte1)							
	ZPEX_DLM_ECCERROR_RREP	ZPEX_DLM_ECCERROR_NOR	ZPEX_TLM_ECCERROR_TX0	ZPEX_TLM_ECCERROR_RX0	ZPEX_TLM_PARITY_ERROR_ALLOCATED	ZPEX_TLM_PARITY_ERROR_RX0	ZPEX_TLM_PARITY_ERROR_STOP	ZPEX_TLM_PARITY_ERROR_TX0
5F	LR_XARBREG_PREG_ZPEXW_ERR [HARB2] (byte0)							
	ZPEX_DLM_ECCRECOVERY_REP	ZPEX_DLM_ECCRECOVERY_NOR	ZPEX_TLM_ECCRECOVERY_TX0	ZPEX_TLM_ECCRECOVERY_RX0	Not used	Not used	ZPEX_ERRI_NJECCRECOVERY	ZPEX_ERRI_NJECCERR
60	LR_XARBREG_PREG_WRN_STS [PARB3] (byte3)							
	Not used	Not used	RX0_STATUS_ERR	TLM_RX0_ERR	Not used	Not used	Not used	TLM_TX0_ERR
61	LR_XARBREG_PREG_WRN_STS [PARB3] (byte2)							
	Not used	Not used	TLM_FCPTC_ERR	TLM_ROVF_ERR	TLM_PTL_P_ERR	TLM_MFTLP_ERR	TLM_ECRC_ERR	TLM_UR_ERR
62	LR_XARBREG_PREG_WRN_STS [PARB3] (byte1)							
	Not used	Not used	DLM_SURPRISE_DW_ERR	DLM_PTC_ERR	DLM_TLP_ERR	DLM_DLLP_ERR	DLM_REPLAY_RO_ERR	DLM_REPLAY_TO_ERR
63	LR_XARBREG_PREG_WRN_STS [PARB3] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIEVER_ERR
64	LR_XARBREG_PREGL_WRN_STS [PARB3] (byte0)							
	Not used	Not used	Not used	SPEED_GEN_3_ERR	PLM_LTSSM_ERR	PLM_LINKT_RAINING_ERR	DLM_VC0_E_N_ERR	DLM_DL_U_P_ERR
65	LR_XARBREG_PREG_ZPEXW_ERR [PARB3] (byte3)							
	TLM_RAM_RX0_AADR_PARITY_ERROR	TLM_RAM_RX0_BADR_PARITY_ERROR	TLM_RAM_TX0_AADR_PARITY_ERROR	TLM_RAM_TX0_BADR_PARITY_ERROR	DLM_RAM_ADR_PARITY_ERROR	Not used	Not used	Not used
66	LR_XARBREG_PREG_ZPEXW_ERR [PARB3] (byte1)							
	ZPEX_DLM_ECCERROR_RREP	ZPEX_DLM_ECCERROR_NOR	ZPEX_TLM_ECCERROR_TX0	ZPEX_TLM_ECCERROR_RX0	ZPEX_TLM_PARITY_ERROR_ALLOCATED	ZPEX_TLM_PARITY_ERROR_RX0	ZPEX_TLM_PARITY_ERROR_STOP	ZPEX_TLM_PARITY_ERROR_TX0
67	LR_XARBREG_PREG_ZPEXW_ERR [PARB3] (byte0)							
	ZPEX_DLM_ECCRECOVERY_REP	ZPEX_DLM_ECCRECOVERY_NOR	ZPEX_TLM_ECCRECOVERY_TX0	ZPEX_TLM_ECCRECOVERY_RX0	Not used	Not used	ZPEX_ERRI_NJECCRECOVERY	ZPEX_ERRI_NJECCERR
68	LR_XARBREG_PREG_WRN_STS [PARB2] (byte3)							
	Not used	Not used	RX0_STATUS_ERR	TLM_RX0_ERR	Not used	Not used	Not used	TLM_TX0_ERR
69	LR_XARBREG_PREG_WRN_STS [PARB2] (byte2)							
	Not used	Not used	TLM_FCPTC_ERR	TLM_ROVF_ERR	TLM_PTL_P_ERR	TLM_MFTLP_ERR	TLM_ECRC_ERR	TLM_UR_ERR
6A	LR_XARBREG_PREG_WRN_STS [PARB2] (byte1)							
	Not used	Not used	DLM_SURPRISE_DW_ERR	DLM_PTC_ERR	DLM_TLP_ERR	DLM_DLLP_ERR	DLM_REPLAY_RO_ERR	DLM_REPLAY_TO_ERR
6B	LR_XARBREG_PREG_WRN_STS [PARB2] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIEVER_ERR

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 81 : LR correctable error (4/5)

	0	1	2	3	4	5	6	7
6C	LR_XARBREG_PREGL_WRN_STS [PARB2] (byte0)							
	Not used	Not used	Not used	SPEED_GEN3_ERR	PLM_LTSSM_ERR	PLM_LINKTR_AINING_ERR	DLM_VC0_EN_ERR	DLM_DL_UP_ERR
6D	LR_XARBREG_PREGL_WRN_STS [PARB2] (byte3)							
	TLM_RAM_RX0_AADR_PARITY_ERROR	TLM_RAM_RX0_BADR_PARITY_ERROR	TLM_RAM_TX0_AADR_PARITY_ERROR	TLM_RAM_TX0_BADR_PARITY_ERROR	DLM_RAM_ADR_PARITY_ERROR	Not used	Not used	Not used
6E	LR_XARBREG_PREGL_WRN_STS [PARB2] (byte1)							
	ZPEX_DLM_ECCERROR_RREP	ZPEX_DLM_ECCERROR_NOR	ZPEX_TLM_ECCERROR_TX0	ZPEX_TLM_ECCERROR_RX0	ZPEX_TLM_PARITY_ERROR_ALLOCATED	ZPEX_TLM_PARITY_ERROR_RX0_STOP	ZPEX_TLM_PARITY_ERROR_STOP	ZPEX_TLM_PARITY_ERROR_TX0
6F	LR_XARBREG_PREGL_WRN_STS [PARB2] (byte0)							
	ZPEX_DLM_ECCRECOVERY_REP	ZPEX_DLM_ECCRECOVERY_NOR	ZPEX_TLM_ECCRECOVERY_TX0	ZPEX_TLM_ECCRECOVERY_RX0	Not used	Not used	ZPEX_ERRI_NJECCRECOVERY	ZPEX_ERRI_NJECCERR
70	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte3)							
	Not used	Not used	RX0_STATUS_ERR	TLM_RX0_ERR	Not used	Not used	Not used	TLM_TX0_ERR
71	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte2)							
	Not used	Not used	TLM_FCPTC_ERR	TLM_ROVF_ERR	TLM_PTLT_ERR	TLM_MFTLT_ERR	TLM_ECRC_ERR	TLM_UR_ERR
72	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte1)							
	Not used	Not used	DLM_SURPRISE_DW_ERR	DLM_PTC_ERR	DLM_TLP_ERR	DLM_DLLP_ERR	DLM_REPLAY_RO_ERR	DLM_REPLAY_TO_ERR
73	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIEVER_ERR
74	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte0)							
	Not used	Not used	Not used	SPEED_GEN3_ERR	PLM_LTSSM_ERR	PLM_LINKTR_AINING_ERR	DLM_VC0_EN_ERR	DLM_DL_UP_ERR
75	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte3)							
	TLM_RAM_RX0_AADR_PARITY_ERROR	TLM_RAM_RX0_BADR_PARITY_ERROR	TLM_RAM_TX0_AADR_PARITY_ERROR	TLM_RAM_TX0_BADR_PARITY_ERROR	DLM_RAM_ADR_PARITY_ERROR	Not used	Not used	Not used
76	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte1)							
	ZPEX_DLM_ECCERROR_RREP	ZPEX_DLM_ECCERROR_NOR	ZPEX_TLM_ECCERROR_TX0	ZPEX_TLM_ECCERROR_RX0	ZPEX_TLM_PARITY_ERROR_ALLOCATED	ZPEX_TLM_PARITY_ERROR_RX0_STOP	ZPEX_TLM_PARITY_ERROR_STOP	ZPEX_TLM_PARITY_ERROR_TX0
77	LR_XARBREG_PREGL_WRN_STS [PARB1] (byte0)							
	ZPEX_DLM_ECCRECOVERY_REP	ZPEX_DLM_ECCRECOVERY_NOR	ZPEX_TLM_ECCRECOVERY_TX0	ZPEX_TLM_ECCRECOVERY_RX0	Not used	Not used	ZPEX_ERRI_NJECCRECOVERY	ZPEX_ERRI_NJECCERR
78	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte3)							
	Not used	Not used	RX0_STATUS_ERR	TLM_RX0_ERR	Not used	Not used	Not used	TLM_TX0_ERR
79	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte2)							
	Not used	Not used	TLM_FCPTC_ERR	TLM_ROVF_ERR	TLM_PTLT_ERR	TLM_MFTLT_ERR	TLM_ECRC_ERR	TLM_UR_ERR
7A	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte1)							
	Not used	Not used	DLM_SURPRISE_DW_ERR	DLM_PTC_ERR	DLM_TLP_ERR	DLM_DLLP_ERR	DLM_REPLAY_RO_ERR	DLM_REPLAY_TO_ERR
7B	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PLM_RECIEVER_ERR

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(3) Byte28 = 81 : LR correctable error (5/5)

	0	1	2	3	4	5	6	7
7C	LR_XARBREG_PREGL_WRN_STS [PARB0] (byte0)							
	Not used	Not used	Not used	SPEED_GEN3_ERR	PLM_LTSSM_ERR	PLM_LINKTRAINING_ERR	DLM_VC0_EN_ERR	DLM_DL_UP_ERR
7D	LR_XARBREG_PREGE_ZPEXW_ERR [HARB0] (byte3)							
	TLM_RAM_RX0_AADR_PARITY_ERROR	TLM_RAM_RX0_BADR_PARITY_ERROR	TLM_RAM_TX0_AADR_PARITY_ERROR	TLM_RAM_TX0_BADR_PARITY_ERROR	DLM_RAM_ADR_PARITY_ERROR	Not used	Not used	Not used
7E	LR_XARBREG_PREGE_ZPEXW_ERR [HARB0] (byte1)							
	ZPEX_DLM_ECCERROR_RREP	ZPEX_DLM_ECCERROR_NOR	ZPEX_TLM_ECCERROR_TX0	ZPEX_TLM_ECCERROR_RX0	ZPEX_TLM_PARITY_ERROR_ALLOCATED	ZPEX_TLM_PARITY_ERROR_RX0	ZPEX_TLM_PARITY_ERROR_STOP	ZPEX_TLM_PARITY_ERROR_TX0
7F	LR_XARBREG_PREGE_ZPEXW_ERR [HARB0] (byte0)							
	ZPEX_DLM_ECCRECOVERY_REP	ZPEX_DLM_ECCRECOVERY_NOR	ZPEX_TLM_ECCRECOVERY_TX0	ZPEX_TLM_ECCRECOVERY_RX0	Not used	Not used	ZPEX_ERRI_NJECCRECOVERY	ZPEX_ERRI_NJECCERR

SSBLOG05-3090

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (1/19)

(4-1) Byte7F = 01 : MSW correctable error LOG1 (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : MSW correctable error							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	MSW:: Warning Status(Byte3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	MSW:: Warning Status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
42	MSW:: Warning Status(Byte1)							
	DBG Warning	CRG Warning	I2C Warning	IDIF Warning	MCIF Warning	DSCTL Warning	BRAM Warning	BPORT Warning
43	MSW:: Warning Status(Byte0)							
	CRAM Warning	MCTL Warning	IPORT Warning	PPORT Warning	XPORT Warning	EPORT Warning	RCTL Warning	SW Warning
44	MSW:: EPORT0 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
45	MSW:: EPORT1 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
46	MSW:: EPORT2 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
47	MSW:: EPORT3 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
48	MSW:: EPORT4 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
49	MSW:: EPORT5 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable

SSBLOG05-3100

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (2/19)

(4-1) Byte7F = 01 : MSW correctable error LOG1 (2/5)

	0	1	2	3	4	5	6	7
4A	MSW:: EPORT6 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
4B	MSW:: EPORT7 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
4C	MSW:: PPORT0 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
4D	MSW:: PPORT1 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
4E	MSW:: PPORT2 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
4F	MSW:: PPORT3 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
50	MSW:: PPORT4 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
51	MSW:: PPORT5 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
52	MSW:: PPORT6 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
53	MSW:: PPORT7 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
54	MSW:: PPORT8 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
55	MSW:: PPORT9 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
56	MSW:: PPORTA PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
57	MSW:: PPORTB PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable

SSBLOG05-3110

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (3/19)

(4-1) Byte7F = 01 : MSW correctable error LOG1 (3/5)

	0	1	2	3	4	5	6	7
58	MSW:: XPORT0 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
59	MSW:: XPORT1 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5A	MSW:: XPORT2 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5B	MSW:: XPORT3 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5C	MSW:: IPORT0 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5D	MSW:: IPORT1 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5E	MSW:: BPORT0 PCIe Warning							
	Receiver Error Status	Bad DLLP Status	Bad TLP Status	Replay Timeout Status	Not used	Normal Buffer Correctable	Retry Buffer Correctable	ERRINJ Block Correctable
5F	Not used (x'00')							
60	MSW::EPORT0 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
61	MSW::EPORT0 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
62	MSW::EPORT0 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
63	Not used (x'00')							
64	MSW::EPORT1 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
65	MSW::EPORT1 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
66	MSW::EPORT1 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
67	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (4/19)

(4-1) Byte7F = 01 : MSW correctable error LOG1 (4/5)

	0	1	2	3	4	5	6	7
68	MSW::EPORT2 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
69	MSW::EPORT2 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6A	MSW::EPORT2 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
6B	Not used (x'00')							
6C	MSW::EPORT3 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6D	MSW::EPORT3 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6E	MSW::EPORT3 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
6F	Not used (x'00')							
70	MSW::EPORT4 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
71	MSW::EPORT4 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
72	MSW::EPORT4 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
73	Not used (x'00')							
74	MSW::EPORT5 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
75	MSW::EPORT5 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
76	MSW::EPORT5 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
77	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (5/19)

(4-1) Byte7F = 01 : MSW correctable error LOG1 (5/5)

	0	1	2	3	4	5	6	7
78	MSW::EPORT6 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
79	MSW::EPORT6 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
7A	MSW::EPORT6 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
7B	Not used (x'00')							
7C	MSW::EPORT7 EPG_EGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
7D	MSW::EPORT7 SPG_SGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
7E	MSW::EPORT7 MPA SNP RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Data Correctable	Header Correctable
7F	x '01'							

SSBLOG05-3140

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (6/19)

(4-2) Byte7F = 02 : MSW correctable error LOG2 (1/4)

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : MSW correctable error							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	MSW::PPORT0 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
41	MSW::PPORT0 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
42	MSW::PPORT1 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
43	MSW::PPORT1 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
44	MSW::PPORT2 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
45	MSW::PPORT2 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
46	MSW::PPORT3 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
47	MSW::PPORT3 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
48	MSW::PPORT4 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
49	MSW::PPORT4 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable

SSBLOG05-3150

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (7/19)

(4-2) Byte7F = 02 : MSW correctable error LOG2 (2/4)

	0	1	2	3	4	5	6	7
4A	MSW::PPORT5 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4B	MSW::PPORT5 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4C	MSW::PPORT6 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4D	MSW::PPORT6 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4E	MSW::PPORT7 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4F	MSW::PPORT7 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
50	MSW::PPORT8 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
51	MSW::PPORT8 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
52	MSW::PPORT9 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
53	MSW::PPORT9 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
54	MSW::PPORTA XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
55	MSW::PPORTA XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
56	MSW::PPORTB XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
57	MSW::PPORTB XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable

SSBLOG05-3160

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (8/19)

(4-2) Byte7F = 02 : MSW correctable error LOG2 (3/4)

	0	1	2	3	4	5	6	7
58	MSW::XPORT0 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
59	MSW::XPORT0 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5A	MSW::XPORT0 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5B	MSW::XPORT0 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5C	MSW::XPORT1 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5D	MSW::XPORT1 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5E	MSW::XPORT1 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5F	MSW::XPORT1 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
60	MSW::XPORT2 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
61	MSW::XPORT2 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
62	MSW::XPORT2 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
63	MSW::XPORT2 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
64	MSW::XPORT3 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
65	MSW::XPORT3 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable

SSBLOG05-3170

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (9/19)

(4-2) Byte7F = 02 : MSW correctable error LOG2 (4/4)

	0	1	2	3	4	5	6	7
66	MSW::XPORT3 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
67	MSW::XPORT3 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
68	MSW::IPORT0 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
69	MSW::IPORT0 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6A	MSW::IPORT0 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6B	MSW::IPORT0 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6C	MSW::IPORT1 XPG XGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6D	MSW::IPORT1 XPG XGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6E	MSW::IPORT1 SXPG SXGTBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
6F	MSW::IPORT1 SXPG SXGRBF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
70 7E	Not used (x'00')							
7F	x '02'							

SSBLOG05-3180

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (10/19)

(4-3) Byte7F = 03 : MSW correctable error LOG3 (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : MSW correctable error							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	MSW::MCTL0 RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
41	MSW::MCTL0 RWTBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
42	MSW::MCTL0 TXBUF RAM Correctable(VC0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
43	MSW::MCTL0 TXBUF RAM Correctable(VC1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
44	MSW::MCTL1 RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
45	MSW::MCTL1 RWTBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
46	MSW::MCTL1 TXBUF RAM Correctable(VC0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
47	MSW::MCTL1 TXBUF RAM Correctable(VC1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
48	MSW::MCTL2 RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
49	MSW::MCTL2 RWTBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable

SSBLOG05-3190

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (11/19)

(4-3) Byte7F = 03 : MSW correctable error LOG3 (2/5)

	0	1	2	3	4	5	6	7
4A	MSW::MCTL2 TXBUF RAM Correctable(VC0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4B	MSW::MCTL2 TXBUF RAM Correctable(VC1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4C	MSW::MCTL3 RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4D	MSW::MCTL3 RWTBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4E	MSW::MCTL3 TXBUF RAM Correctable(VC0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
4F	MSW::MCTL3 TXBUF RAM Correctable(VC1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
50	MSW::CRAM RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
51	MSW::CRAM Correctable							
	RAM0 Correctable	RAM1 Correctable	RAM2 Correctable	RAM3 Correctable	RAM4 Correctable	RAM5 Correctable	RAM6 Correctable	RAM7 Correctable
52	MSW::CRAM TXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
53	Not used (x'00')							
54	MSW::DSCTL0 LOGBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
55	MSW::DSCTL1 LOGBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
56	MSW::DSCTL2 LOGBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
57	MSW::DSCTL3 LOGBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
58	MSW::DSCTL4 LOGBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (12/19)

(4-3) Byte7F = 03 : MSW correctable error LOG3 (3/5)

	0	1	2	3	4	5	6	7
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	MSW::BRAM RXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5D	MSW::BRAM Correctable							
	RAM0 Correctable	RAM1 Correctable	RAM2 Correctable	RAM3 Correctable	RAM4 Correctable	RAM5 Correctable	RAM6 Correctable	RAM7 Correctable
5E	MSW::BRAM TXBUF RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	RAM Correctable
5F	Not used (x'00')							
60	MSW::EPORT0 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
61	MSW::EPORT1 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
62	MSW::EPORT2 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
63	MSW::EPORT3 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
64	MSW::EPORT4 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
65	MSW::EPORT5 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
66	MSW::EPORT6 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
67	MSW::EPORT7 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
68	MSW::PPORT0 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
69	MSW::PPORT1 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable

SSBLOG05-3210

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (13/19)

(4-3) Byte7F = 03 : MSW correctable error LOG3 (4/5)

	0	1	2	3	4	5	6	7
6A	MSW::PPOINT2 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
6B	MSW::PPOINT3 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
6C	MSW::PPOINT4 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
6D	MSW::PPOINT5 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
6E	MSW::PPOINT6 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
6F	MSW::PPOINT7 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
70	MSW::PPOINT8 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
71	MSW::PPOINT9 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
72	MSW::PPOINTA PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
73	MSW::PPOINTB PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
74	MSW::XPORT0 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
75	MSW::XPORT1 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
76	MSW::XPORT2 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
77	MSW::XPORT3 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable

SSBLOG05-3220

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (14/19)

(4-3) Byte7F = 03 : MSW correctable error LOG3 (5/5)

	0	1	2	3	4	5	6	7
78	MSW::IPORT0 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
78	MSW::IPORT1 PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
78	MSW::BPORT PIF333 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	RXBUF RAM Correctable	RXBUF RAM Correctable
7B 7E 7F	Not used (x'00')							
	x '03'							

SSBLOG05-3230

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (15/19)

(4-4) Byte7F = 04 : MSW correctable error LOG4 (1/5)

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : MSW correctable error							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	MSW::EPORT0 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
41	MSW::EPORT1 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
42	MSW::EPORT2 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
43	MSW::EPORT3 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
44	MSW::EPORT4 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
45	MSW::EPORT5 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
46	MSW::EPORT6 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
47	MSW::EPORT7 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
48	MSW::PPORT0 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
49	MSW::PPORT1 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
4A	MSW::PPORT2 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable

SSBLOG05-3240

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (16/19)

(4-4) Byte7F = 04 : MSW correctable error LOG4 (2/5)

	0	1	2	3	4	5	6	7
4B	MSW::PPORT3 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
4C	MSW::PPORT4 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
4D	MSW::PPORT5 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
4E	MSW::PPORT6 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
4F	MSW::PPORT7 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
50	MSW::PPORT8 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
51	MSW::PPORT9 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
52	MSW::PPORTA PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
53	MSW::PPORTB PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
54	MSW::XPORT0 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
55	MSW::XPORT1 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
56	MSW::XPORT2 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
57	MSW::XPORT3 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
58	MSW::IPORT0 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable

SSBLOG05-3250

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (17/19)

(4-4) Byte7F = 04 : MSW correctable error LOG4 (3/5)

	0	1	2	3	4	5	6	7
59	MSW::IPIRT1 PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
5A	MSW::BPORT PIF500 RX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	PCIRIF RAM Correctable
5B 5F	Not used (x'00')							
60	MSW::EPORT0 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
61	MSW::EPORT1 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
62	MSW::EPORT2 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
63	MSW::EPORT3 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
64	MSW::EPORT4 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
65	MSW::EPORT5 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
66	MSW::EPORT6 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
67	MSW::EPORT7 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
68	MSW::PPIRT0 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
69	MSW::PPIRT1 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
6A	MSW::PPIRT2 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
6B	MSW::PPIRT3 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable

SSBLOG05-3260

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (18/19)

(4-4) Byte7F = 04 : MSW correctable error LOG4 (4/5)

	0	1	2	3	4	5	6	7
6C	MSW::PPORT4 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
6D	MSW::PPORT5 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
6E	MSW::PPORT6 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
6F	MSW::PPORT7 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
70	MSW::PPORT8 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
71	MSW::PPORT9 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
72	MSW::PPORTA PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
73	MSW::PPORTB PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
74	MSW::XPORT0 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
75	MSW::XPORT1 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
76	MSW::XPORT2 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
77	MSW::XPORT3 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
78	MSW::IPORT0 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
79	MSW::IPORT1 PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable

SSBLOG05-3270

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handing support) Hardware detail information

(4) Byte28 = 83 : MSW correctable error (19/19)

(4-4) Byte7F = 04 : MSW correctable error LOG4 (5/5)

	0	1	2	3	4	5	6	7
7A	MSW::BPORT PIF500 TX RAM Correctable							
	Not used	Not used	Not used	Not used	Not used	TLN RAM Correctable	TXBUF RAM Correctable	TXBUF RAM Correctable
7B 7E	Not used (x'00')							
7F	x '04'							

SSBLOG05-3280

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(5) Byte28 = 40 : Abnormal DC Voltage control warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'40' : Abnormal DC Voltage control warning							
29	MPID (Reported SSB)							
2A	Register access result BYTE (00:No error, 01:Error detected)							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	The Value of Threshold counter for the warning (CHA/DKA_1PA/1PG) (Module1(1PG):bit[0-3], Module0(1PA):bit[4-7])							
41	The Value of Threshold counter for the warning (CHA/DKA_1PB/1PH) (Module1(1PH):bit[0-3], Module0(1PB):bit[4-7])							
42	The Value of Threshold counter for the warning (CHA_1PC/1PJ) (Module1(1PJ):bit[0-3], Module0(1PC):bit[4-7])							
43	The Value of Threshold counter for the warning (CHA_1PD/1PK) (Module1(1PK):bit[0-3], Module0(1PD):bit[4-7])							
44	The Value of Threshold counter for the warning (CHA_1PE/1PL) (Module1(1PL):bit[0-3], Module0(1PE):bit[4-7])							
45	The Value of Threshold counter for the warning (CHA_1PF/1PM) (Module1(1PM):bit[0-3], Module0(1PF):bit[4-7])							
46	The Value of Threshold counter for the warning (CMPK_1CA/1CC) (Module1(1CC):bit[0-3], Module0(1CA):bit[4-7])							
47	The Value of Threshold counter for the warning (CMPK_1CB/1CD) (Module1(1CD):bit[0-3], Module0(1CB):bit[4-7])							
48	The Value of Threshold counter for the warning (MPPK_1MA/1MC) (Module1(1MC):bit[0-3], Module0(1MA):bit[4-7])							
49	The Value of Threshold counter for the warning (MPPK_1MB/1MD) (Module1(1MD):bit[0-3], Module0(1MB):bit[4-7])							
4A	The Value of Threshold counter for the warning (MPPK_1PE/1PL) (Module1(1PL):bit[0-3], Module0(1PE):bit[4-7])							
4B	The Value of Threshold counter for the warning (MPPK_1PF/1PM) (Module1(1PM):bit[0-3], Module0(1PF):bit[4-7])							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	The Value of Threshold counter for the warning (CHA/DKA_2PA/2PG) (Module2(2PG):bit[0-3], Module0(2PA):bit[4-7])							
51	The Value of Threshold counter for the warning (CHA/DKA_2PB/2PH) (Module2(2PH):bit[0-3], Module0(2PB):bit[4-7])							
52	The Value of Threshold counter for the warning (CHA_2PC/2PJ) (Module2(2PJ):bit[0-3], Module0(2PC):bit[4-7])							
53	The Value of Threshold counter for the warning (CHA_2PD/2PK) (Module2(2PK):bit[0-3], Module0(2PD):bit[4-7])							
54	The Value of Threshold counter for the warning (CHA_2PE/2PL) (Module2(2PL):bit[0-3], Module0(2PE):bit[4-7])							
55	The Value of Threshold counter for the warning (CHA_2PF/2PM) (Module2(2PM):bit[0-3], Module0(2PF):bit[4-7])							
56	The Value of Threshold counter for the warning (CMPK_2CA/2CC) (Module2(2CC):bit[0-3], Module0(2CA):bit[4-7])							
57	The Value of Threshold counter for the warning (CMPK_2CB/2CD) (Module2(2CD):bit[0-3], Module0(2CB):bit[4-7])							
58	The Value of Threshold counter for the warning (MPPK_2MA/2MC) (Module2(2MC):bit[0-3], Module0(2MA):bit[4-7])							
59	The Value of Threshold counter for the warning (MPPK_2MB/2MD) (Module2(2MD):bit[0-3], Module0(2MB):bit[4-7])							
5A	The Value of Threshold counter for the warning (MPPK_2PE/2PL) (Module2(2PL):bit[0-3], Module0(2PE):bit[4-7])							
5B	The Value of Threshold counter for the warning (MPPK_2PF/2PM) (Module2(2PM):bit[0-3], Module0(2PF):bit[4-7])							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(5) Byte28 = 40 : Abnormal DC Voltage control warning (2/3)

	0	1	2	3	4	5	6	7
60	Voltage control register information (CHA/DKA_1PA/1PG)							
	DC_CTL_EN (UP_SET) (1PG)	DC_CTL_EN (DWN_SET) (1PG)	DC_CTL_ UP_MONI (1PG)	DC_CTL_ DWN_MONI (1PG)	DC_CTL_EN (UP_SET) (1PA)	DC_CTL_EN (DWN_SET) (1PA)	DC_CTL_ UP_MONI (1PA)	DC_CTL_ DWN_MONI (1PA)
61	Voltage control register information (CHA/DKA_1PB/1PH)							
	DC_CTL_EN (UP_SET) (1PH)	DC_CTL_EN (DWN_SET) (1PH)	DC_CTL_ UP_MONI (1PH)	DC_CTL_ DWN_MONI (1PH)	DC_CTL_EN (UP_SET) (1PB)	DC_CTL_EN (DWN_SET) (1PB)	DC_CTL_ UP_MONI (1PB)	DC_CTL_ DWN_MONI (1PB)
62	Voltage control register information (CHA_1PC/1PJ)							
	DC_CTL_EN (UP_SET) (1PJ)	DC_CTL_EN (DWN_SET) (1PJ)	DC_CTL_ UP_MONI (1PJ)	DC_CTL_ DWN_MONI (1PJ)	DC_CTL_EN (UP_SET) (1PC)	DC_CTL_EN (DWN_SET) (1PC)	DC_CTL_ UP_MONI (1PC)	DC_CTL_ DWN_MONI (1PC)
63	Voltage control register information (CHA_1PD/1PK)							
	DC_CTL_EN (UP_SET) (1PK)	DC_CTL_EN (DWN_SET) (1PK)	DC_CTL_ UP_MONI (1PK)	DC_CTL_ DWN_MONI (1PK)	DC_CTL_EN (UP_SET) (1PD)	DC_CTL_EN (DWN_SET) (1PD)	DC_CTL_ UP_MONI (1PD)	DC_CTL_ DWN_MONI (1PD)
64	Voltage control register information (CHA_1PE/1PL)							
	DC_CTL_EN (UP_SET) (1PL)	DC_CTL_EN (DWN_SET) (1PL)	DC_CTL_ UP_MONI (1PL)	DC_CTL_ DWN_MONI (1PL)	DC_CTL_EN (UP_SET) (1PE)	DC_CTL_EN (DWN_SET) (1PE)	DC_CTL_ UP_MONI (1PE)	DC_CTL_ DWN_MONI (1PE)
65	Voltage control register information (CHA_1PF/1PM)							
	DC_CTL_EN (UP_SET) (1PM)	DC_CTL_EN (DWN_SET) (1PM)	DC_CTL_ UP_MONI (1PM)	DC_CTL_ DWN_MONI (1PM)	DC_CTL_EN (UP_SET) (1PF)	DC_CTL_EN (DWN_SET) (1PF)	DC_CTL_ UP_MONI (1PF)	DC_CTL_ DWN_MONI (1PF)
66	Voltage control register information (CMPK_1CA/1CC)							
	DC_CTL_EN (UP_SET) (1CC)	DC_CTL_EN (DWN_SET) (1CC)	DC_CTL_ UP_MONI (1CC)	DC_CTL_ DWN_MONI (1CC)	DC_CTL_EN (UP_SET) (1CA)	DC_CTL_EN (DWN_SET) (1CA)	DC_CTL_ UP_MONI (1CA)	DC_CTL_ DWN_MONI (1CA)
67	Voltage control register information (CMPK_1CB/1CD)							
	DC_CTL_EN (UP_SET) (1CD)	DC_CTL_EN (DWN_SET) (1CD)	DC_CTL_ UP_MONI (1CD)	DC_CTL_ DWN_MONI (1CD)	DC_CTL_EN (UP_SET) (1CB)	DC_CTL_EN (DWN_SET) (1CB)	DC_CTL_ UP_MONI (1CB)	DC_CTL_ DWN_MONI (1CB)
68	Voltage control register information (MPPK_1MA/1MC)							
	Not used (x'00')	DC_CTL_EN (1MC)	DC_CTL_ UP_MONI (1MC)	DC_CTL_ DWN_MONI (1MC)	Not used (x'00')	DC_CTL_EN (1MA)	DC_CTL_ UP_MONI (1MA)	DC_CTL_ DWN_MONI (1MA)
69	Voltage control register information (MPPK_1MB/1MD)							
	Not used (x'00')	DC_CTL_EN (1MD)	DC_CTL_ UP_MONI (1MD)	DC_CTL_ DWN_MONI (1MD)	Not used (x'00')	DC_CTL_EN (1MB)	DC_CTL_ UP_MONI (1MB)	DC_CTL_ DWN_MONI (1MB)
6A	Voltage control register information (MPPK_1PE/1PL)							
	Not used (x'00')	DC_CTL_EN (1PL)	DC_CTL_ UP_MONI (1PL)	DC_CTL_ DWN_MONI (1PL)	Not used (x'00')	DC_CTL_EN (1PE)	DC_CTL_ UP_MONI (1PE)	DC_CTL_ DWN_MONI (1PE)
6B	Voltage control register information (MPPK_1PF/1PM)							
	Not used (x'00')	DC_CTL_EN (1PM)	DC_CTL_ UP_MONI (1PM)	DC_CTL_ DWN_MONI (1PM)	Not used (x'00')	DC_CTL_EN (1PF)	DC_CTL_ UP_MONI (1PF)	DC_CTL_ DWN_MONI (1PF)
6C	Not used (x'00')							
6D	Not used (x'00')							
6E	Not used (x'00')							
6F	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(5) Byte28 = 40 : Abnormal DC Voltage control warning (3/3)

	0	1	2	3	4	5	6	7
70	Voltage control register information (CHA/DKA_2PA/2PG)							
	DC_CTL_EN (UP_SET) (2PG)	DC_CTL_EN (DWN_SET) (2PG)	DC_CTL_ UP_MONI (2PG)	DC_CTL_ DWN_MONI (2PG)	DC_CTL_EN (UP_SET) (2PA)	DC_CTL_EN (DWN_SET) (2PA)	DC_CTL_ UP_MONI (2PA)	DC_CTL_ DWN_MONI (2PA)
71	Voltage control register information (CHA/DKA_2PB/2PH)							
	DC_CTL_EN (UP_SET) (2PH)	DC_CTL_EN (DWN_SET) (2PH)	DC_CTL_ UP_MONI (2PH)	DC_CTL_ DWN_MONI (2PH)	DC_CTL_EN (UP_SET) (2PB)	DC_CTL_EN (DWN_SET) (2PB)	DC_CTL_ UP_MONI (2PB)	DC_CTL_ DWN_MONI (2PB)
72	Voltage control register information (CHA_2PC/2PJ)							
	DC_CTL_EN (UP_SET) (2PJ)	DC_CTL_EN (DWN_SET) (2PJ)	DC_CTL_ UP_MONI (2PJ)	DC_CTL_ DWN_MONI (2PJ)	DC_CTL_EN (UP_SET) (2PC)	DC_CTL_EN (DWN_SET) (2PC)	DC_CTL_ UP_MONI (2PC)	DC_CTL_ DWN_MONI (2PC)
73	Voltage control register information (CHA_2PD/2PK)							
	DC_CTL_EN (UP_SET) (2PK)	DC_CTL_EN (DWN_SET) (2PK)	DC_CTL_ UP_MONI (2PK)	DC_CTL_ DWN_MONI (2PK)	DC_CTL_EN (UP_SET) (2PD)	DC_CTL_EN (DWN_SET) (2PD)	DC_CTL_ UP_MONI (2PD)	DC_CTL_ DWN_MONI (2PD)
74	Voltage control register information (CHA_2PE/2PL)							
	DC_CTL_EN (UP_SET) (2PL)	DC_CTL_EN (DWN_SET) (2PL)	DC_CTL_ UP_MONI (2PL)	DC_CTL_ DWN_MONI (2PL)	DC_CTL_EN (UP_SET) (2PE)	DC_CTL_EN (DWN_SET) (2PE)	DC_CTL_ UP_MONI (2PE)	DC_CTL_ DWN_MONI (2PE)
75	Voltage control register information (CHA_2PF/2PM)							
	DC_CTL_EN (UP_SET) (2PM)	DC_CTL_EN (DWN_SET) (2PM)	DC_CTL_ UP_MONI (2PM)	DC_CTL_ DWN_MONI (2PM)	DC_CTL_EN (UP_SET) (2PF)	DC_CTL_EN (DWN_SET) (2PF)	DC_CTL_ UP_MONI (2PF)	DC_CTL_ DWN_MONI (2PF)
76	Voltage control register information (CMPK_2CA/2CC)							
	DC_CTL_EN (UP_SET) (2CC)	DC_CTL_EN (DWN_SET) (2CC)	DC_CTL_ UP_MONI (2CC)	DC_CTL_ DWN_MONI (2CC)	DC_CTL_EN (UP_SET) (2CA)	DC_CTL_EN (DWN_SET) (2CA)	DC_CTL_ UP_MONI (2CA)	DC_CTL_ DWN_MONI (2CA)
77	Voltage control register information (CMPK_2CB/2CD)							
	DC_CTL_EN (UP_SET) (2CD)	DC_CTL_EN (DWN_SET) (2CD)	DC_CTL_ UP_MONI (2CD)	DC_CTL_ DWN_MONI (2CD)	DC_CTL_EN (UP_SET) (2CB)	DC_CTL_EN (DWN_SET) (2CB)	DC_CTL_ UP_MONI (2CB)	DC_CTL_ DWN_MONI (2CB)
78	Voltage control register information (MPPK_2MA/2MC)							
	Not used (x'00')	DC_CTL_EN (2MC)	DC_CTL_ UP_MONI (2MC)	DC_CTL_ DWN_MONI (2MC)	Not used (x'00')	DC_CTL_EN (2MA)	DC_CTL_ UP_MONI (2MA)	DC_CTL_ DWN_MONI (2MA)
79	Voltage control register information (MPPK_2MB/2MD)							
	Not used (x'00')	DC_CTL_EN (2MD)	DC_CTL_ UP_MONI (2MD)	DC_CTL_ DWN_MONI (2MD)	Not used (x'00')	DC_CTL_EN (2MB)	DC_CTL_ UP_MONI (2MB)	DC_CTL_ DWN_MONI (2MB)
7A	Voltage control register information (MPPK_2PE/2PL)							
	Not used (x'00')	DC_CTL_EN (2PL)	DC_CTL_ UP_MONI (2PL)	DC_CTL_ DWN_MONI (2PL)	Not used (x'00')	DC_CTL_EN (2PE)	DC_CTL_ UP_MONI (2PE)	DC_CTL_ DWN_MONI (2PE)
7B	Voltage control register information (MPPK_2PF/2PM)							
	Not used (x'00')	DC_CTL_EN (2PM)	DC_CTL_ UP_MONI (2PM)	DC_CTL_ DWN_MONI (2PM)	Not used (x'00')	DC_CTL_EN (2PF)	DC_CTL_ UP_MONI (2PF)	DC_CTL_ DWN_MONI (2PF)
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(6) Byte28 = 41 : Abnormal CEMODE warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'41' : Abnormal CEMODE warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	PK status							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
41	UPDOWN							
	Reserved	Reserved	Down(Moni)	Up(Moni)	Reserved	Reserved	Down(set)	Up(set)
42	Not used (x'00')							
43	Not used (x'00')							
44	Not used (x'00')							
45	Not used (x'00')							
46	Not used (x'00')							
47	Not used (x'00')							
48	Not used (x'00')							
49	Not used (x'00')							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(7) Byte28 = 42 : Abnormal CEDT warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'42' : Abnormal CEDT warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	PK status							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
41	Not used (x'00')							
42	Not used (x'00')							
43	Not used (x'00')							
44	Not used (x'00')							
45	Not used (x'00')							
46	Not used (x'00')							
47	Not used (x'00')							
48	Not used (x'00')							
49	Not used (x'00')							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
51	Not used (x'00')							
52	Not used (x'00')							
53	Not used (x'00')							
54	Not used (x'00')							
55	Not used (x'00')							
56	Not used (x'00')							
57	Not used (x'00')							
58	Not used (x'00')							
59	Not used (x'00')							
5A	Not used (x'00')							
5B	Not used (x'00')							
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							
60	Not used (x'00')							
7F								

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(8) Byte28 = C0 : SFP (8/16G Fibre) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C0' : SFP (8/16G Fibre) warning							
29	MPID (Reported SSB)							
2A	DC_UPDOWN_WRN	TX_FAULT	PCI CORE WARN	Not used	Not used	Not used	Not used	INT_FMT_WRN_P
2B	Tx_Fault detection port number							
2C	LR TX_FAULT_PK temperature warning							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR TX_FAULT_PK temperature detection port0							
41	LR TX_FAULT_PK temperature detection port0							
42	LR TX_FAULT_PK temperature detection port1							
43	LR TX_FAULT_PK temperature detection port1							
44	Device address "A0", Data address "6"							
45	Device address "A0", Data address "10"							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5D	Device address "A0", Data address "73"							
5E	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(8) Byte28 = C0 : SFP (8/16G Fibre) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = C2 : SFP (8/16G CHM) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C2' : SFP (8/16G CHM) warning							
29	MPID (Reported SSB)							
2A	DC_UPDOWN_WRN	TX_FAULT	PCI CORE WARN	Not used	Not used	Not used	Not used	INT_FMT_WRN_P
2B	Tx_Fault detection port number							
2C	LR TX_FAULT_PK temperature warning							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR TX_FAULT_PK temperature detection port0							
41	Not used (x'00')							
42	LR TX_FAULT_PK temperature detection port1							
43	Not used (x'00')							
44	Device address "A0", Data address "6"							
45	Device address "A0", Data address "10"							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5D	Device address "A0", Data address "73"							
5E	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(9) Byte28 = C2 : SFP (8/16G CHM) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(10) Byte28 = C5 : SFP (10G Either) warning (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'C5' : SFP (10G Either) warning							
29	MPID (Reported SSB)							
2A	DC_UPDOWN_WRN	TX_FAULT	PCI CORE WARN	Not used	Not used	Not used	Not used	INT_FMT_WRN_P
2B	Tx_Fault detection port number							
2C	LR TX_FAULT_PK temperature warning							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	LR TX_FAULT_PK temperature detection port0							
41	LR TX_FAULT_PK temperature detection port0							
42	LR TX_FAULT_PK temperature detection port1							
43	LR TX_FAULT_PK temperature detection port1							
44	Device address "A0", Data address "3"							
45	Not used (x'00')							
46	Device address "A0", Data address "40"							
47	Device address "A0", Data address "41"							
48	Device address "A0", Data address "42"							
49	Device address "A0", Data address "43"							
4A	Device address "A0", Data address "44"							
4B	Device address "A0", Data address "45"							
4C	Device address "A0", Data address "46"							
4D	Device address "A0", Data address "47"							
4E	Device address "A0", Data address "48"							
4F	Device address "A0", Data address "49"							
50	Device address "A0", Data address "50"							
51	Device address "A0", Data address "51"							
52	Device address "A0", Data address "52"							
53	Device address "A0", Data address "53"							
54	Device address "A0", Data address "54"							
55	Device address "A0", Data address "55"							
56	Device address "A0", Data address "60"							
57	Device address "A0", Data address "61"							
58	Device address "A0", Data address "68"							
59	Device address "A0", Data address "69"							
5A	Device address "A0", Data address "70"							
5B	Device address "A0", Data address "71"							
5C	Device address "A0", Data address "72"							
5D	Device address "A0", Data address "73"							
5E	Device address "A0", Data address "74"							
5F	Device address "A0", Data address "75"							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(10) Byte28 = C5 : SFP (10G Either) warning (2/2)

	0	1	2	3	4	5	6	7
60	Device address "A0", Data address "76"							
61	Device address "A0", Data address "77"							
62	Device address "A0", Data address "78"							
63	Device address "A0", Data address "79"							
64	Device address "A0", Data address "80"							
65	Device address "A0", Data address "81"							
66	Device address "A0", Data address "82"							
67	Device address "A0", Data address "83"							
68	Device address "A0", Data address "96"							
69	Device address "A0", Data address "97"							
6A	Device address "A2", Data address "112"							
6B	Device address "A2", Data address "113"							
6C	Device address "A2", Data address "114"							
6D	Device address "A2", Data address "115"							
6E	Device address "A2", Data address "116"							
6F	Device address "A2", Data address "117"							
70	Device address "A2", Data address "118"							
71	Device address "A2", Data address "119"							
72	Device address "A2", Data address "128"							
73	Device address "A2", Data address "129"							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3390

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(11) Byte28 = 45 : Battery warning (1/4)

(11-1) Byte4F = 00 : Battery warning LOG1 (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'45' : Battery warning							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Parts status(Byte3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	Parts status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	CM Backup Configuration warning	CM Backup warning	Battery Warning
42	Parts status(Byte1)							
	MSW Board Error	MSW Any Error	MSW Warning	Not used	Not used	Not used	Not used	Not used
43	Parts status(Byte0)							
	BRG_P warning	BRG_S warning	SH Micon warning	BKM warning	SSD warning	Battery Controller warning	Battery warning	Battery Charge
44	Battery status(Byte3)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
45	Battery status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
46	Battery status(Byte1)							
	Battery Full	Not used	Not used	Not used	Not used	Not used	Not used	Battery Half
47	Battery status(Byte0)							
	Not used	Not used	Not used	Battery Quarter	Not used	Not used	Battery Empty	Not used
48	Parts firm exchange status(Byte3)							
	SPROM Erase Fail	SPROM Erase Completion	CMPK Degeneration Fail	CMPK Degeneration Completion	Not used	SSD Erase Fail	Not used	SSD Erase Completion
49	Parts firm exchange status(Byte2)							
	Not used	BKM Exchange Fail	BKM Exchange Done	BKM Exchange Ready	BATC_FIRM Exchange Fail	Not used	SSD_FIRM Exchange Fail	SH_FIRM Exchange Fail
4A	Parts firm exchange status(Byte1)							
	BATC_FIRM Exchange Done	Not used	SSD_FIRM Exchange Done	SH_FIRM Exchange Done	BATC_FIRM Exchange Busy	Not used	SSD_FIRM Exchange Busy	SH_FIRM Exchange Busy
4B	Parts firm exchange status(Byte0)							
	Not used	SSD Exchange Fail	SSD Exchange Done	SSD Exchange Ready	Not used	Battery Exchange Fail	Battery Exchange Done	Battery Exchange Ready

SSBLOG05-3400

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(11) Byte28 = 45 : Battery warning (2/4)

(11-1) Byte4F = 00 : Battery warning LOG1 (2/2)

	0	1	2	3	4	5	6	7
4C	BKM CONFIG							
	Not Used	Not Used	Not Used	Not Used	Not Used	Not Used	BKML Set	BKMS Set
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	x '00'							
50	Battery controller LOG00							
51	Battery controller LOG01							
52	Battery controller LOG02							
53	Battery controller LOG03							
54	Battery controller LOG04							
55	Battery controller LOG05							
56	Battery controller LOG06							
57	Battery controller LOG07							
58	Battery controller LOG08							
59	Battery controller LOG09							
5A	Battery controller LOG10							
5B	Battery controller LOG11							
5C	Battery controller LOG12							
5D	Battery controller LOG13							
5E	Battery controller LOG14							
5F	Battery controller LOG15							
60	Battery controller LOG16							
61	Battery controller LOG17							
62	Battery controller LOG18							
63	Battery controller LOG19							
64	Battery controller LOG20							
65	Battery controller LOG21							
66	Battery controller LOG22							
67	Battery controller LOG23							
68	Battery controller LOG24							
69	Battery controller LOG25							
6A	Battery controller LOG26							
6B	Battery controller LOG27							
6C	Battery controller LOG28							
6D	Battery controller LOG29							
6E	Battery controller LOG30							
6F	Battery controller LOG31							
70	Battery controller LOG32							
71	Battery controller LOG33							
72	Battery controller LOG34							
73	Battery controller LOG35							
74	Battery controller LOG36							
75	Battery controller LOG37							
76	Battery controller LOG38							
77	Battery controller LOG39							
78	Battery controller LOG40							
79	Battery controller LOG41							
7A	Battery controller LOG42							
7B	Battery controller LOG43							
7C	Battery controller LOG44							
7D	Battery controller LOG45							
7E	Battery controller LOG46							
7F	Battery controller LOG47							

SSBLOG05-3410

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(11) Byte28 = 45 : Battery warning (3/4)

(11-2) Byte4F = 01 : Battery warning LOG2 (1/2)

	0	1	2	3	4	5	6	7
28	Subcode : x'45' : Battery warning							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Parts status(Byte3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
41	Parts status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	CM Backup Configuration warning	CM Backup warning	Battery Warning
42	Parts status(Byte1)							
	MSW Board Error	MSW Any Error	MSW Warning	Not used	Not used	Not used	Not used	Not used
43	Parts status(Byte0)							
	BRG_P warning	BRG_S warning	SH Micon warning	BKM warning	SSD warning	Battery Controller warning	Battery warning	Battery Charge
44	Battery status(Byte3)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
45	Battery status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
46	Battery status(Byte1)							
	Battery Full	Not used	Not used	Not used	Not used	Not used	Not used	Battery Half
47	Battery status(Byte0)							
	Not used	Not used	Not used	Battery Quarter	Not used	Not used	Battery Empty	Not used
48	Parts firm exchange status(Byte3)							
	SPROM Erase Fail	SPROM Erase Completion	CMPK Degeneration Fail	CMPK Degeneration Completion	Not used	SSD Erase Fail	Not used	SSD Erase Completion
49	Parts firm exchange status(Byte2)							
	Not used	BKM Exchange Fail	BKM Exchange Done	BKM Exchange Ready	BATC_FIRM Exchange Fail	Not used	SSD_FIRM Exchange Fail	SH_FIRM Exchange Fail
4A	Parts firm exchange status(Byte1)							
	BATC_FIRM Exchange Done	Not used	SSD_FIRM Exchange Done	SH_FIRM Exchange Done	BATC_FIRM Exchange Busy	Not used	SSD_FIRM Exchange Busy	SH_FIRM Exchange Busy
4B	Parts firm exchange status(Byte0)							
	Not used	SSD Exchange Fail	SSD Exchange Done	SSD Exchange Ready	Not used	Battery Exchange Fail	Battery Exchange Done	Battery Exchange Ready

SSBLOG05-3420

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(11) Byte28 = 45 : Battery warning (4/4)

(11-2) Byte4F = 01 : Battery warning LOG2 (2/2)

	0	1	2	3	4	5	6	7
4C	BKM CONFIG							
	Not Used	Not Used	Not Used	Not Used	Not Used	Not Used	BKML Set	BKMS Set
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	x '01'							
50	Battery controller LOG48							
51	Battery controller LOG49							
52	Battery controller LOG50							
53	Battery controller LOG51							
54	Battery controller LOG52(for Li-ion Battery)							
55	Battery controller LOG53(for Li-ion Battery)							
56	Battery controller LOG54(for Li-ion Battery)							
57	Battery controller LOG55(for Li-ion Battery)							
58	Battery controller LOG56(for Li-ion Battery)							
59	Battery controller LOG57(for Li-ion Battery)							
5A	Battery controller LOG58(for Li-ion Battery)							
5B	Battery controller LOG59(for Li-ion Battery)							
5C	Battery controller LOG60(for Li-ion Battery)							
5D	Battery controller LOG61(for Li-ion Battery)							
5E	Battery controller LOG62(for Li-ion Battery)							
5F	Battery controller LOG63(for Li-ion Battery)							
60	Battery controller LOG64(for Li-ion Battery)							
61	Battery controller LOG65(for Li-ion Battery)							
62	Battery controller LOG66(for Li-ion Battery)							
63	Battery controller LOG67(for Li-ion Battery)							
64	Battery controller LOG68(for Li-ion Battery)							
65	Battery controller LOG69(for Li-ion Battery)							
66	Battery controller LOG70(for Li-ion Battery)							
67	Battery controller LOG71(for Li-ion Battery)							
68	Battery controller LOG72(for Li-ion Battery)							
69	Battery controller LOG73(for Li-ion Battery)							
6A	Battery controller LOG74(for Li-ion Battery)							
6B	Battery controller LOG75(for Li-ion Battery)							
6C	Not used (x'00')							
6D	Not used (x'00')							
6E	Not used (x'00')							
6F	Not used (x'00')							
70	Not used (x'00')							
71	Not used (x'00')							
72	Not used (x'00')							
73	Not used (x'00')							
74	Not used (x'00')							
75	Not used (x'00')							
76	Not used (x'00')							
77	Not used (x'00')							
78	Not used (x'00')							
79	Not used (x'00')							
7A	Not used (x'00')							
7B	Not used (x'00')							
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

SSBLOG05-3430

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(12) Byte28 = 46 : CMBK warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode : x'46' : CMBK warning							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Store / Restore status(Byte3)							
	DIAG Restore Done	DIAG Store Done	Not used	Not used	SSD Inline Done	SSD CUDG5 Done	SSD CUDG4 Done	SSD CUDG3 Done
41	Store / Restore status(Byte2)							
	BKM Replace DIAG Done	BKM Replace DIAG Fail	Not used	Internal DIAG Running	Not used	Not used	Not used	Invalid Command Error
42	Store / Restore status(Byte1)							
	Not used	Not used	Config Done	Config Fail	Restore Done	Restore Busy	Restore Fail	Restore Success
43	Store / Restore status(Byte0)							
	Restore Volatile	Not used	Not used	Not used	Store Done	Store Busy	Store Fail	Store Success
44	Parts status(Byte3)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
45	Parts status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	CM Backup Configuration warning	CM Backup warning	Battery Warning
46	Parts status(Byte1)							
	MSW Board Error	MSW Any Error	MSW Warning	Not used	Not used	Not used	Not used	Not used
47	Parts status(Byte0)							
	BRG_P warning	BRG_S warning	SH Micon warning	BKM warning	SSD warning	Battery Controller warning	Battery warning	Battery Charge
48	Parts firm exchange status(Byte3)							
	SPROM Erase Fail	SPROM Erase Completion	CMPK Degeneration Fail	CMPK Degeneration Completion	Not used	SSD Erase Fail	Not used	SSD Erase Completion
49	Parts firm exchange status(Byte2)							
	Not used	BKM Exchange Fail	BKM Exchange Done	BKM Exchange Ready	BATC_FIRM Exchange Fail	Not used	SSD_FIRM Exchange Fail	SH_FIRM Exchange Fail
4A	Parts firm exchange status(Byte1)							
	BATC_FIRM Exchange Done	Not used	SSD_FIRM Exchange Done	SH_FIRM Exchange Done	BATC_FIRM Exchange Busy	Not used	SSD_FIRM Exchange Busy	SH_FIRM Exchange Busy

SSBLOG05-3440

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(12) Byte28 = 46 : CMBK warning (2/3)

	0	1	2	3	4	5	6	7
4B	Parts firm exchange status(Byte0)							
	Not used	SSD Exchange Fail	SSD Exchange Done	SSD Exchange Ready	Not used	Battery Exchange Fail	Battery Exchange Done	Battery Exchange Ready
4C	SSD CONFIG							
	Not used	Not used	Not used	Not used	Not used	Not used	SSD Capacity 256GB	SSD Capacity 128GB
4D	BKM CONFIG							
	Not Used	Not Used	Not Used	Not Used	Not Used	Not Used	BKML Set	BKMS Set
4E	Store error LOG							
	Restore Error_log CM7	Restore Error_log CM6	Restore Error_log CM5	Restore Error_log CM4	Restore Error_log CM3	Restore Error_log CM2	Restore Error_log CM1	Restore Error_log CM0
4F	Restore error LOG							
	Store Error_log CM7	Store Error_log CM6	Store Error_log CM5	Store Error_log CM4	Store Error_log CM3	Store Error_log CM2	Store Error_log CM1	Store Error_log CM0
50	SH_micro Firm revision (Byte3)							
51	SH_micro Firm revision (Byte2)							
52	SH_micro Firm revision (Byte1)							
53	SH_micro Firm revision (Byte0)							
54	BATC_micro Firm revision (Byte3)							
55	BATC_micro Firm revision (Byte2)							
56	BATC_micro Firm revision (Byte1)							
57	BATC_micro Firm revision (Byte0)							
58	Bridge_Chip error status (PCI express) (Byte3)							
59	Bridge_Chip error status (PCI express) (Byte2)							
5A	Bridge_Chip error status (PCI express) (Byte1)							
5B	Bridge_Chip error status (PCI express) (Byte0)							
5C	Bridge_Chip error status (SATA_P0) (Byte3)							
5D	Bridge_Chip error status (SATA_P0) (Byte2)							
5E	Bridge_Chip error status (SATA_P0) (Byte1)							
5F	Bridge_Chip error status (SATA_P0) (Byte0)							

SSBLOG05-3450

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(12) Byte28 = 46 : CMBK warning (3/3)

	0	1	2	3	4	5	6	7
60	SSD SATA error status (Byte3)							
61	SSD SATA error status (Byte2)							
62	SSD SATA error status (Byte1)							
63	SSD SATA error status (Byte0)							
64	SSD RFIS error status (Byte3)							
65	SSD RFIS error status (Byte2)							
66	SSD RFIS error status (Byte1)							
67	SSD RFIS error status (Byte0)							
68	SH warning log (Byte3)							
69	SH warning log (Byte2)							
6A	SH warning log (Byte1)							
6B	SH warning log (Byte0)							
6C	BKM warning log (Byte3)							
6D	BKM warning log (Byte2)							
6E	BKM warning log (Byte1)							
6F	BKM warning log (Byte0)							
70	SSD model number00							
71	SSD model number01							
72	SSD model number02							
73	SSD model number03							
74	SSD model number04							
75	SSD model number05							
76	SSD model number06							
77	SSD model number07							
78	SSD model number08							
79	SSD model number09							
7A	SSD model number10							
7B	SSD model number11							
7C	SSD model number12							
7D	SSD model number13							
7E	SSD model number14							
7F	SSD model number15							

SSBLOG05-3460

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(13) Byte28 = 48 : MPPK Abnormal temperature warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode : x'48' : MPPK Abnormal temperature warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	IA32 Thermistor status0 :B7							
	Reserved							
41	IA32 Thermistor status1 :B6							
	Reserved							
42	IA32 Thermistor status2 :B5							
	Reserved							
43	IA32 Thermistor status3 :B4							
	Reserved							
44	IA32 Thermistor status4 :B3							
	Reading Valid	Resolution in Degrees Celsius				Reserved	Reserved	Reserved
45	IA32 Thermistor status5 :B2							
	Reserved	Digital Readout						
46	IA32 Thermistor status6 :B1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Thmal Thres #2 Log	Thmal Thres #2 Status
47	IA32 Thermistor status7 :B0							
	Thmal Thres #1 Log	Thmal Thres #1 Status	Out of Spec Status Log	Out of Spec Status	PRCHOT# or FRCEPR# Log	PRCHOT# or FRCEPR# Eve	Thmal Status Log	Thmal Status
48	Not used (x'00')							
49	LM84 (1) Local temperature							
	Local Temp (1)							
4A	Not used (x'00')							
4B	Not used (x'00')							
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							
50	Not used (x'00')							
5F								
60	Not used (x'00')							
7F								

SSBLOG05-3470

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(14) Byte28 = 49 : PK Abnormal temperature warning (1/4)

	0	1	2	3	4	5	6	7
28	Subcode : x'49' : PK Abnormal temperature warning							
29	MPID (Reported SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	CHA/DKA_1PA Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
41	CHA/DKA_1PB Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
42	CHA_1PC Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
43	CHA_1PD Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
44	CHA_1PE Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
45	CHA_1PF Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
46	CMPK_1CA Abnormal temperature register (Module0)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
47	CMPK_1CB Abnormal temperature register (Module0)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
48	MPPK_1MA Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
49	MPPK_1MB Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
4A	MPPK_1PE Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
4B	MPPK_1PF Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
4C	Not used (x'00')							
4D	Not used (x'00')							
4E	Not used (x'00')							
4F	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(14) Byte28 = 49 : PK Abnormal temperature warning (2/4)

	0	1	2	3	4	5	6	7
50	CHA/DKA_2PA Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
51	CHA/DKA_2PB Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
52	CHA_2PC Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
53	CHA_2PD Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
54	CHA_2PE Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
55	CHA_2PF Abnormal temperature register (Module0)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
56	CMPK_2CA Abnormal temperature register (Module0)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
57	CMPK_2CB Abnormal temperature register (Module0)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
58	MPPK_2MA Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
59	MPPK_2MB Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
5A	MPPK_2PE Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
5B	MPPK_2PF Abnormal temperature register (Module0)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
5C	Not used (x'00')							
5D	Not used (x'00')							
5E	Not used (x'00')							
5F	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(14) Byte28 = 49 : PK Abnormal temperature warning (3/4)

	0	1	2	3	4	5	6	7
60	CHA/DKA_1PG Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
61	CHA/DKA_1PH Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
62	CHA_1PJ Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
63	CHA_1PK Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
64	CHA_1PL Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
65	CHA_1PM Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
66	CMPK_1CC Abnormal temperature register (Module1)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
67	CMPK_1CD Abnormal temperature register (Module1)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
68	MPPK_1MC Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
69	MPPK_1MD Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
6A	MPPK_1PL Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
6B	MPPK_1PM Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
6C	Not used (x'00')							
6D	Not used (x'00')							
6E	Not used (x'00')							
6F	Not used (x'00')							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(14) Byte28 = 49 : PK Abnormal temperature warning (4/4)

	0	1	2	3	4	5	6	7
70	CHA/DKA_2PG Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
71	CHA/DKA_2PH Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
72	CHA_2PJ Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
73	CHA_2PK Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
74	CHA_2PL Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
75	CHA_2PM Abnormal temperature register (Module1)							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	PK temperature warning
76	CMPK_2CC Abnormal temperature register (Module1)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
77	CMPK_2CD Abnormal temperature register (Module1)							
	Not used	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MP-PK Mask (Don't care)	MSW Test Pin (Don't care)	MSW Test Pin (Don't care)	PK temperature warning
78	MPPK_2MC Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
79	MPPK_2MD Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7A	MPPK_2PL Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7B	MPPK_2PM Abnormal temperature register (Module1)							
	PSOFFOK	THAWAR	LIDET2	LIDET1	CEMODE	CEDT2	CEDT1	CEDT0
7C	Not used (x'00')							
7D	Not used (x'00')							
7E	Not used (x'00')							
7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(15) Byte28 = 43 : Forced volatile reboot warning (1/1)

	0	1	2	3	4	5	6	7
28	Subcode (x'43' : Forced volatile reboot warning)							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Volatile PIN(CMPK_1CA)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
41	Volatile PIN(CMPK_1CB)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
42	Volatile PIN(CMPK_2CA)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
43	Volatile PIN(CMPK_2CB)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
44	Volatile PIN(CMPK_1CC)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
45	Volatile PIN(CMPK_1CD)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
46	Volatile PIN(CMPK_2CC)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
47	Volatile PIN(CMPK_2CD)							
	ENV alarm	Not used	Not used	Not used	Volatile PIN	Not used	Not used	Not used
48 7F	Not used (x'00')							

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(16) Byte28 = 47 : CMBK Config warning (1/3)

	0	1	2	3	4	5	6	7
28	Subcode (x'47' : CMBK Config warning)							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							

	0	1	2	3	4	5	6	7
40	Store / Restore status(Byte3)							
	DIAG Restore Done	DIAG Store Done	Not used	Not used	SSD Inline Done	SSD CUDG5 Done	SSD CUDG4 Done	SSD CUDG3 Done
41	Store / Restore status(Byte2)							
	BKM Replace DIAG Done	BKM Replace DIAG Fail	Not used	Internal DIAG Running	Not used	Not used	Not used	Invalid Command Error
42	Store / Restore status(Byte1)							
	Not used	Not used	Config Done	Config Fail	Restore Done	Restore Busy	Restore Fail	Restore Success
43	Store / Restore status(Byte0)							
	Restore Volatile	Not used	Not used	Not used	Store Done	Store Busy	Store Fail	Store Success
44	Parts status(Byte2)							
	Not used	Not used	Not used	Not used	Not used	CM Backup Configuration warning	CM Backup warning	Battery Warning
45	Parts status(Byte1)							
	MSW Board Error	MSW Any Error	MSW Warning	Not used	Not used	Not used	Not used	Not used
46	Parts status(Byte0)							
	BRG_P warning	BRG_S warning	SH Micon warning	BKM warning	SSD warning	Battery Controller warning	Battery warning	Battery Charge
47	Not used ("00")							
48	Parts firm exchange status(Byte3)							
	SPROM Erase Fail	SPROM Erase Completion	CMPK Degeneration Fail	CMPK Degeneration Completion	Not used	SSD Erase Fail	Not used	SSD Erase Completion
49	Parts firm exchange status(Byte2)							
	Not used	BKM Exchange Fail	BKM Exchange Done	BKM Exchange Ready	BATC_FIRM Exchange Fail	Not used	SSD_FIRM Exchange Fail	SH_FIRM Exchange Fail
4A	Parts firm exchange status(Byte1)							
	BATC_FIRM Exchange Done	Not used	SSD_FIRM Exchange Done	SH_FIRM Exchange Done	BATC_FIRM Exchange Busy	Not used	SSD_FIRM Exchange Busy	SH_FIRM Exchange Busy
4B	Parts firm exchange status(Byte0)							
	Not used	SSD Exchange Fail	SSD Exchange Done	SSD Exchange Ready	Not used	Battery Exchange Fail	Battery Exchange Done	Battery Exchange Ready

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(16) Byte28 = 47 : CMBK Config warning (2/3)

	0	1	2	3	4	5	6	7
4C	SSD CONFIG							
	Not used	Not used	Not used	Not used	Not used	Not used	SSD Capacity 256GB	SSD Capacity 128GB
4D	BKM CONFIG							
	Not Used	Not Used	Not Used	Not Used	Not Used	Not Used	BKML Set	BKMS Set
4E	Not used ("00")							
4F	Not used ("00")							
50	SH_micro Firm revision (Byte3)							
51	SH_micro Firm revision (Byte2)							
52	SH_micro Firm revision (Byte1)							
53	SH_micro Firm revision (Byte0)							
54	BATC_micro Firm revision (Byte3)							
55	BATC_micro Firm revision (Byte2)							
56	BATC_micro Firm revision (Byte1)							
57	BATC_micro Firm revision (Byte0)							
58	Memory Controller 0 DIMM Configuration0							
	Not used	CM4 4RANK	CM4 2RANK	CM4 1RANK	Not used	CM4 4Gbit	CM4 2Gbit	CM4 1Gbit
59	Memory Controller 0 DIMM Configuration1							
	Not used	CM0 4RANK	CM0 2RANK	CM0 1RANK	Not used	CM0 4Gbit	CM0 2Gbit	CM0 1Gbit
5A	Memory Controller 1 DIMM Configuration0							
	Not used	CM5 4RANK	CM5 2RANK	CM5 1RANK	Not used	CM5 4Gbit	CM5 2Gbit	CM5 1Gbit
5B	Memory Controller 1 DIMM Configuration1							
	Not used	CM1 4RANK	CM1 2RANK	CM1 1RANK	Not used	CM1 4Gbit	CM1 2Gbit	CM1 1Gbit
5C	Memory Controller 3 DIMM Configuration0							
	Not used	CM6 4RANK	CM6 2RANK	CM6 1RANK	Not used	CM6 4Gbit	CM6 2Gbit	CM6 1Gbit
5D	Memory Controller 2 DIMM Configuration1							
	Not used	CM2 4RANK	CM2 2RANK	CM2 1RANK	Not used	CM2 4Gbit	CM2 2Gbit	CM2 1Gbit
5E	Memory Controller 3 DIMM Configuration0							
	Not used	CM7 4RANK	CM7 2RANK	CM7 1RANK	Not used	CM7 4Gbit	CM7 2Gbit	CM7 1Gbit
5F	Memory Controller 3 DIMM Configuration1							
	Not used	CM3 4RANK	CM3 2RANK	CM3 1RANK	Not used	CM3 4Gbit	CM3 2Gbit	CM3 1Gbit

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Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(16) Byte28 = 47 : CMBK Config warning (3/3)

	0	1	2	3	4	5	6	7
60	Memory Controller 0 DIMM Enable0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM4 Enable
61	Memory Controller 0 DIMM Enable1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM0 Enable
62	Memory Controller 1 DIMM Enable0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM5 Enable
63	Memory Controller 1 DIMM Enable1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM1 Enable
64	Memory Controller 2 DIMM Enable0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM6 Enable
65	Memory Controller 2 DIMM Enable1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM2 Enable
66	Memory Controller 3 DIMM Enable0							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM7 Enable
67	Memory Controller 3 DIMM Enable1							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CM3 Enable
68	Not used ("00")							
69	Not used ("00")							
6A	Not used ("00")							
6B	Not used ("00")							
6C	Not used ("00")							
6D	Not used ("00")							
6E	Not used ("00")							
6F	Not used ("00")							
70	SSD model number00							
71	SSD model number01							
72	SSD model number02							
73	SSD model number03							
74	SSD model number04							
75	SSD model number05							
76	SSD model number06							
77	SSD model number07							
78	SSD model number08							
79	SSD model number09							
7A	SSD model number10							
7B	SSD model number11							
7C	SSD model number12							
7D	SSD model number13							
7E	SSD model number14							
7F	SSD model number15							

Byte27 = FF (DXBF / Cache Memory / PCI Express Correctable / Abnormal MODE / SFP / Battery / SSD / BKM / Abnormal Temperature / Forced volatile Warning : Error handling support) Hardware detail information

(17) Byte28 = 11 : MSW PLDIF warning

	0	1	2	3	4	5	6	7
28	Subcode : x'11' : MSW PLDIF Warning							
29	MPID (Reported SSB)							
2A	Module Number Parity Error	LSI Number Parity Error	Module Number		LSI Number			
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	MSW PLDIF status (Byte1)							
	PLDDEC_SEQ_DEC_ERR	PLDDEC_SEQ_ENC_ERR	Not used	Not used	Not used	Not used	Not used	PLDDEC_DT_PERR
43	MSW PLDIF status (Byte0)							
	Not used	PLDIF_SEQ_ERR	Not used	Not used	Not used	Not used	PLD_ERR	PLDIF_CNT_PERR
44	Not used (x'00')							
7F								

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Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08								
09	System error code							
0A								
0B	JCB#							
0C								
0D	REQ ID							
0E	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0F	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
10	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
11	JCB ID							
12	LDEV# high (Logical DKC#)							
13	LDEV# low (LDEV# in Logical DKC)							
14								
15	CDEV# / PORT#							
16	RDEV# / PORT#							
17	SSB log number							
18								
19	Related failure log number							
1A								
1B	Related SIM log number							
1C								
1D	Related SSB log number							
1E								
1F	Reserved							
20	SCB							
21	Basic SSB							
22								
23	Storage control type							
24	Count							
25	Device address							
26	Device type							
27	Content (x'6')				Format (x'3')			
28	Operation byte of LR/LRE command parameter							
29	Auxiliary byte of LR/LRE command parameter							
2A	Byte 2 for LR/LRE command							
2B	Reserved							
2C								
2D	Search parameter of LR command (CCHHR)							
2E	or Record ID of re-execution command (R)							
2F								

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Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (2/2)

	0	1	2	3	4	5	6	7
30	Sector number to re-execute the LR command							
31	TLF for LR/LRE command							
32								
33	Operation byte = 0x3F : LRE External operation byte Others : Reserved							
34	SSID (Low) of self storage system							
35								
36	Symptom code (x'08C1')							
37								
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used							
7F								

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Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'4')			
27	Operation byte of LR/LRE command parameter							
28	Extension operation byte of LR/LRE command parameter							
29								
2A	Head address of extent							
2B	(DX command parameter)							
2C								
2D								
2E								
2F	Path group ID							
30	(byte 1 to 7 of the ID by the last Set Path Group ID command)							
31								
32								
33								
34	SSID (Low) of self storage system							
35								
36	Reference code (x'0212')							
37								

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Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception) (2/2)

	0	1	2	3	4	5	6	7
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used (x'00')							
55								
56	Module ID (detected error)							
57	Routine ID (detected error)							
58	Not used (x'00')							
7F								

SSBLOG05-3600

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): Common information (1/1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'1')			
27	Sector number							
28	Cylinder address							
29								
2A	Head address							
2B								
2C	Record number							
2D	Error displacement							
2E								
2F								
30	Drive serial number (x'0C24' + DKU sequence number)							
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (x'4XY') X: 0: Data check in HA field or R0 field, 1: Data check in C field, 2: Data check in K field,							
37	3: Data check in D field, 5: PA error YY: x'80': Correctable (Recovered), x'C0': Uncorrectable							
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code (x'00')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3610

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 40/41/42/43 (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, slot number							
45								
46								
47	Slot status							
48								
49								
4A	Staging bit map/Dirty bit map							
4B								
4C								
4D								
4E	Current sub block number							
4F								
50								
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56	Not used (x'00')							
7								
7F								

***1: EXIT FLAG**

x'08': CHL I/F CHK2 time over	x'40': 'Staging' requested
x'0D': EOF	x'41': 'Waiting free segment' requested
x'0E': DSB = 0E requested	x'42': 'Time over of waiting for staging' requested
x'0F': Logical erro	x'43': 'Staging for PA mismatch' requested
x'1E': Make sense only	x'45': 'Destaging' requested
x'1F': Logical error (make sense only)	x'46': 'High priority destaging' requested(30 slot)
x'30': Selective reset requested	x'47': 'Write through process' requested
	x'4B': 'DSB = 4E retry' requested (data transfer)
	x'4E': 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F': STG requested (Read or STG in progress)

***2: Slot failure**

x'80': Sector write error	x'08': No decision
x'40': Sector read error	
x'20': ECC/LRC error	
x'10': Incomplete data	

***3: Obtained information flag**

bit 0-1: Block type	bit2-3: Side	bit4: Segment	bit5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception):For Byte36 = 45 (1/1)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, slot number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	PACC in CBUF							
55	PAHF in CBUF							
56								
57	Current CC in JCB							
58								
59	Current HH in JCB							
5A								
5B	Not used (x'00')							
5C								
7F								

*1: EXIT FLAG

x'08':	CHL I/F CHK2 time over	x'40':	'Staging' requested
x'0D':	EOF	x'41':	'Waiting free segment' requested
x'0E':	DSB = 0E requested	x'42':	'Time over of waiting for staging' requested
x'0F':	Logical error	x'43':	'Staging for PA mismatch' requested
x'1E':	Make sense only	x'45':	'Destaging' requested
x'1F':	Logical error (make sense only)	x'46':	'High priority destaging' requested (30 slot)
x'30':	Selective reset requested	x'47':	'Write through process' requested
		x'4B':	'DSB = 4E retry' requested (data transfer)
		x'4E':	'DSB = 4E retry' requested (DSB = 4E retry)
		x'4F':	STG requested (Read or STG in progress)

*2: Slot failure

x'80':	Sector write error	x'08':	No decision
x'40':	Sector read error		
x'20':	ECC/LRC error		
x'10':	Incomplete data		

*3: Obtained information flag

bit 0-1: Block type	bit 2-3: Side	bit 4: Segment	bit5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

SSBLOG05-3630

Byte36 = 6F (Storage system Information) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#/ PORT#							
15	RDEV#/ PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Command overrun threshold exceeded							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'1')			
27	Data overrun threshold exceeded							
28	Seek count							
29								
2A	Drive serial number		(x'0C')					
2B			(x'24')					
2C			(DKU sequence number)					
2D								
2E								
2F	DKC serial number		(x'0C')					
30			(x'24')					
31			(x'00')					
32			(DKC sequence number : high)					
33			(DKC sequence number : low)					
34	SSID of self storage system							
35								
36	Symptom code (x'6FYX' Y: 0: Statistics/RRBL command, 2: Channel data overrun							
37	X: For serial CHL : Port#, For parallel CHL : CHL#)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Read/search byte count							
3D								
3E								
3F								

SSBLOG05-3640

Byte36 = 6F (Storage system Information) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3650

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1
(SPC Detected Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('111': SPC Detected Failure)			0	0	0	0	0
28	Internal Failure Part							
29								
2A	SCSI command (See SSBLOG05-3670)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'D0ZZ') (ZZ: RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						

SSBLOG05-3660

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 7 & Byte40: bit7 = 1
(SPC Detected Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Time now							
41								
42								
43								
44	Free area Format Type							
45	Reserved							
46								
47								
48	SAS Port #							
49	EXP #							
4A	CDEV #							
4B	RDEV #							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	Reserved							
58	SCSI command							
59	SCSI command LBA							
5A								
5B								
5C								
5D								
5E	SCSI command Length							
5F								
60	Error Code							
61								
62								
63								
64	Error Code (EVENT)							
65								
66								
67								
68	Error Code (COMPLITION)							
69								
6A								
6B								
6C	Reserved							
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

***1: SCSI command code**

Code	Command Name	Code	Command Name
00	TEST UNIT READY	2E	WRITE AND VERIFY (10)
04	FORMAT UNIT	3B	WRITE BUFFER
07	REASSIGN BLOCKS	55	MODE SELECT
12	INQUIRY	5A	MODE SENSE
1B	START/STOP UNIT	88	READ (16)
1C	RECEIVE DIAGNOSTIC RESULT	8A	WRITE (16)
1D	SEND DIAGNOSTIC	8E	WRITE AND VERIFY (16)
25	READ CAPACITY (10)	9E	READ CAPACITY (16)
28	READ (10)	FF	Command is non-decision
2A	WRITE (10)		

SSBLOG05-3680

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'0')			
27	Type code ('000': Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'D0YY') (YY: RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code	Bit3=1: Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit)	Bit3=1: Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit)	Bit3=1: Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0: Head address	Bit3=1: Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3690

Byte36: Bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit0-2 = 0 (Drive Detected Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Time now							
41								
42								
43								
44	Free area Format Type							
45	Reserved							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	SCSI Status							
58	Sense Data (8byte)							
59								
5F								
60	SCSI RSP acquisition result							
61	Sense Key							
62	Additional Sense Code							
63	Additional Sense Code Qualifier							
64	Non-transfer number of bytes							
65								
66								
67								
68	Obstacle LBA of Sense Data							
69								
6A								
6B								
6C								
6D								
6E								
6F								
70	CDB							
7F								

SSBLOG05-3700

Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'00')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'0')			
27	Type code ('000': Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command							
2B	Not used							
2C	Module ID (≠ x'5X/6X')							
2D	Routine ID							
2E	CDEV number							
2F	Drive serial number (x'0C24' + (DKU sequence number)							
30	(This information is not fixed until DKC reports SSB to HOST.)							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'E0YY') (YY: RDEV number)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.						
3D	Case of Byte3B	Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.						
3E	Case of Byte3B	Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.						
3F	Case of Byte3B	Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address						

SSBLOG05-3710

Byte36: Bit0-3 = E & Byte2C: bit0-7 ≠ 5X/6X (Drive Detected Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Time now							
41								
42								
43								
44	Free area Format Type							
45	Reserved							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	SCSI Status							
58	Sense Data (8byte)							
59								
5F								
60	SCSI RSP acquisition result							
61	Sense Key							
62	Additional Sense Code							
63	Additional Sense Code Qualifier							
64	Non-transfer number of bytes							
65								
66								
67								
68	Obstacle LBA of Sense Data							
69								
6A								
6B								
6C								
6D								
6E								
6F								
70	CDB							
7F								

SSBLOG05-3720

Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
(1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'0')			
27	Type code (b'000')			Not used (0)	Not used ('0')			
28	READY	ENABLE	SSB PEND	Not used (00000)				
29	Not used							
2A	M.M. RSV	PIN VOL	WR INH	Not used (00000)				
2B	Not used							
2C	Module ID (*1)							
2D	Routine ID (*1)							
2E	Not used							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'E000')							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3730

Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
(2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Module ID•Routine ID

x'5011/501C/5202'	: LDEV blockade
x'501A/501B'	: Pin VOL detected
x'6605/6697/6705/6784/6805/6853/6909/6953'	: Write inhibited

SSBLOG05-3740

Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'1F')							
23	Count							
24	Device address							
25	Device type (x'3C')							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'0')			
27	Not used							
28								
29								
2A								
2B								
2C								
2D								
2E								
2F	Drive serial number (x'0CE5' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'EF07')							
37								
38	Log and message control (x'1A')							
39	Program action code (x'01')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3750

Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3760

Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'2')			
27	Type code (b'000')			Not used (0)	Not used			
28	READY	ENABLE	SSB PEND	0	0	0	0	0
29	x'00'							
2A	M.M. RSV	PIN VOL	0	0	0	0	0	0
2B	HOST TYPE	DKC TYPE	0	0	DKU TYPE			
2C	Module ID • Routine ID (x'501C/5202': LDEV NOT READY)							
2D								
2E	Command code							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	DKU sequence number							
35								
36	Exception code (x'E210')							
37								
38	Log and message control (x'01')							
39	Program action code (x'02')							
3A	Configuration information							
3B								
3C	Case of Byte3B	Bit3=0:	Message code (x'01')					
		Bit3=1:	Cylinder address Bit 12-19.					
3D	Case of Byte3B	Bit3=0:	Cylinder address (High Bit)					
		Bit3=1:	Cylinder address Bit 20-27.					
3E	Case of Byte3B	Bit3=0:	Cylinder address (Low Bit)					
		Bit3=1:	Cylinder address Bit 0-7.					
3F	Case of Byte3B	Bit3=0:	Head address					
		Bit3=1:	Bit 0-3 Cylinder address Bit 8-11.					
			Bit 4-7 Head address					

SSBLOG05-3770

Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

SSBLOG05-3780

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Basic SSB (x'009010')							
21								
22								
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Range affected by repair (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)	'0000000'						
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C2200' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*15)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'F1')							
3D	Affected SSID							
3E								
3F	Nou used							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (2/3)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- bit0 : Serial number valid (0:Invalid, 1:Valid)
- bit1 : Device address valid (0:Invalid, 1:Valid)
- bit2 : Track address valid (0:Invalid, 1:Valid)
- bit3 : Not used (0)

*2: Range subject to the error

- x'0': Not used
- x'1': Error range not known
- x'2': SP error
- x'3': CHL-I error
- x'4': Not affect performance
- x'5 ~ F': Not used

*3: Range affected by repair

- x'0': Not used
- x'1': Repair range not known
- x'2': Repair does not affect performance
- x'3': SP disabled during repair
- x'4': SSID disabled during repair
- x'5 ~ F': Not used

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Failure SPs

- 0: 1SP
- 1: 2SP

*6: SPs in SSID

- 0: 2SP
- 1: 4SP

*7: SPs in SSID

- 0: 1SSID
- 1: 2SSID

*8: Affected SSIDs

- 0: 1SSID
- 1: 2SSID

*9: Status

- 0: Reduction
- 1: Stop

*10: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

Byte26: b4-7 = F & Byte3C = F1 (DKC SIM) (3/3)

*11: Hardware level

Bit0: Serial channel hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs PER CLUSTER

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 4

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 4

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 6

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache / NVS hardware level ('000')

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00': Byte 36 and 37 are a symptom code.

Byte 2D = x'00': Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*13: SSB ID

0: SVP

1: SP

*14: SVP ID

0: SVP1

1: SVP2

*15: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00: Don't log

01: Unconditional log

10: Log only once

11: Log only if persistent

Bit 6-7 : Operator message

00: No message

01: Unconditional message

10: Send only once

11: Send only if persistent

SSBLOG05-3810

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'009010')							
21								
22								
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27								
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Failure leve; (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)	'0000000'						
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C2200' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*15)							
39	Program action code (x'10')							
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'F2')							
3D	Affected SSID							
3E								
3F	Not used							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (2/3)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

***1: Content**

- Bit 0: Serial number valid (0:Invalid, 1:Valid)
- Bit 1: Device address valid (0:Invalid, 1:Valid)
- Bit 2: Track address valid (0:Invalid, 1:Valid)
- Bit 3: Not used (0)

***2: Range subject to the error**

- x'0': Not used
- x'1': Error range not known
- x'2': SP error
- x'3': CHL-I error
- x'4': Not affect performance
- x'5 ~ F': Not used

***3: Range affected by repair**

- x'0': Not used
- x'1': Repair range not known
- x'2': Repair does not affect performance
- x'3': SP disabled during repair
- x'4': SSID disabled during repair
- x'5 ~ F': Not used

***4: Failure level**

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

***5: Failure SPs**

- 0: 1SP
- 1: 2SP

***6: SPs in SSID**

- 0: 2SP
- 1: 4SP

***7: Failure SSIDs**

- 0: 1SSID
- 1: 2SSID

***8: Affected SSIDs**

- 0: 1SSID
- 1: 2SSID

***9: Status**

- 0: Reduction
- 1: Stop

***10: Resent SIM**

- 0: Unresent SIM
- 1: Resent SIM

Byte26: b4-7 = F & Byte3C = F2 (CACHE SIM) (3/3)

*11: Hardware level

Bit0: Serial channel hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs PER CLUSTER

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 2

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 2

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 8

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache / NVS hardware level ('000')

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00': Byte 36 and 37 are a symptom code.

Byte 2D = x'00': Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*13: SSB ID

0: SVP

1: SP

*14: SVP ID

0: SVP1

1: SVP2

*15: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00: Don't log

01: Unconditional log

10: Log only once

11: Log only if persistent

Bit 6-7 : Operator message

00: No message

01: Unconditional message

10: Send only once

11: Send only if persistent

SSBLOG05-3840

Byte26: b4-7 = F & Byte3C = FE (Device SIM) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'0000')							
21								
22	Storage control type (x'06')							
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number					SVP ID (*3)	
28	Device exception (x'1')				Service message (x'1': Action, x'C': No action)			
29	Failure level (*4)		'000000'					
2A	Resent SIM (*5)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (x'05')							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*6)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FE')							
3D	Not used							
3E								
3F								

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = FE (Device SIM) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- bit0 : Serial number valid (0:Invalid, 1:Valid)
- bit1 : Device address valid (0:Invalid, 1:Valid)
- bit2 : Track address valid (0:Invalid, 1:Valid)
- bit3 : Not used (0)

*2: SSB ID

- 0: SVP
- 1: SP

*3: SVP ID

- 0: SVP1
- 1: SVP2

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

*6: Log and message control

- Bit 0-2 : Not used
- Bit 3 : Log mode(0)
- Bit 4-5 : Logging action
 - 00: Don't log
 - 01: Unconditional log
 - 10: Log only once
 - 11: Log only if persistent
- Bit 6-7 : Operator message
 - 00: No message
 - 01: Unconditional message
 - 10: Send only once
 - 11: Send only if persistent

SSBLOG05-3860

Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB (x'0000')							
21								
22	Storage control type (x'06')							
23	Count (x'00') *							
24	Device address							
25	Device type							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number				SVP ID (*3)		
28	Device exception (x'1')				Service message (x'1')			
29	Failure level (*4)		Media Maintenance Procedure Number (Correctable : x'07', Uncorrectable : x'05')					
2A	Resent SIM (*5)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (Correctable : x'87', Uncorrectable : x'85')							
2F	Drive serial number (x'0C24' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See "Reference code" table)							
37								
38	Log and message control (*6)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB(0)	0	0	0	0	Extended path	Storage path number	
3C	SIM message code (x'FF')							
3D	Cylinder address (Invalid)							
3E								
3F	Head address (Invalid)							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26: b4-7 = F & Byte3C = FF (MEDIA SIM) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Content

- Bit 0: Serial number valid (0:Invalid, 1:Valid)
- Bit 1: Device address valid (0:Invalid, 1:Valid)
- Bit 2: Track address valid (0:Invalid, 1:Valid)
- Bit 3: Not used (0)

*2: SSB ID

- 0: SVP
- 1: SP

*3: SVP ID

- 0: SVP1
- 1: SVP2

*4: Failure level

- 00: SERVICE ALERT
- 01: MODERATE ALERT
- 10: SERIOUS ALERT
- 11: ACUTE ALERT

*5: Resent SIM

- 0: Unresent SIM
- 1: Resent SIM

*6: Log and message control

- Bit 0-2 : Not used
- Bit 3 : Log mode (0)
- Bit 4-5 : Logging action
 - 00: Don't log
 - 01: Unconditional log
 - 10: Log only once
 - 11: Log only if persistent
- Bit 6-7 : Operator message
 - 00: No message
 - 01: Unconditional message
 - 10: Send only once
 - 11: Send only if persistent