

SSBLOG SECTION

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1. Introduction

The internal SSB information is classified as 24-byte compatibility SSB format and 32-byte SSB format. The information to set in internal SSB is classified in the following 3 kinds:

- (1) Internal SSB common information
This 32 byte information is used for SSB management, and same for all formats.
- (2) Format-dependent information
This is 32 bytes information defined by format of 128 byte internal SSB information.
- (3) Internal SSB individual information
This is 64 byte information, and package format is free. This information is supplementary set, When error information of format-dependent information is not enough.

NOTE: The byte offset in SSB logs is described in hexadecimal number.

Figure 1-1 Outline of 24-byte Format Internal SSB

	SSB Pointer (4 bytes)		Out of description in the manual
	Reserved (28 bytes)		
00	Time stamp		Internal SSB common information
05			
06	Format Message		
07	MP#		
08	System error code		
09			
0A	JCB#		
0B			
0C	REQ ID		
0D	SSB type		
0E	VALID flag		
0F	SSB type		
10	JCB ID		
11	LDEV# high (Logical DKC#)		
12	LDEV# low (LDEV# in Logical DKC)		
13			
14	CDEV# / PORT#		
15	RDEV# / PORT#		
16	Internal SSB log number (SSB#)		
17			
18	Related failure log number (LOG#)		
19			
1A	Related SIM log number (SIM#)		
1B			
1C	Related SSB log number (SSB#)		
1D			
1E	Reserved area		
1F			
20	Byte 0 to 6 of 24 byte formatted SSB		Format-dependent information
26			
27	Format	Message	
28	Byte 8 to 23 of 24 byte formatted SSB		
37			
38	Byte 24 to 31 of 24 byte formatted SSB		
3F			
40	Free area		Information of internal SSB
7F			

Figure 1-2 Outline of 32-byte Format Internal SSB

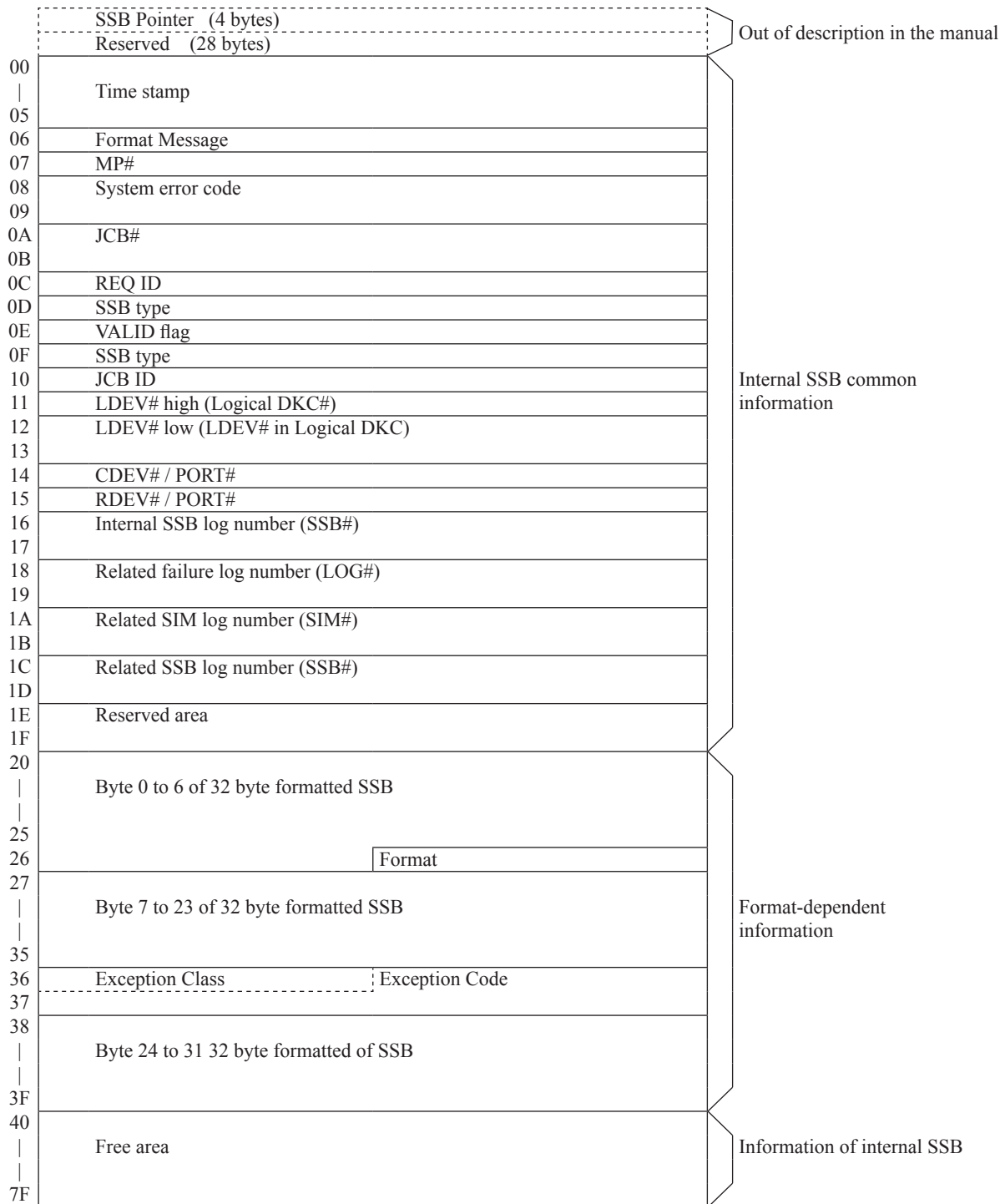
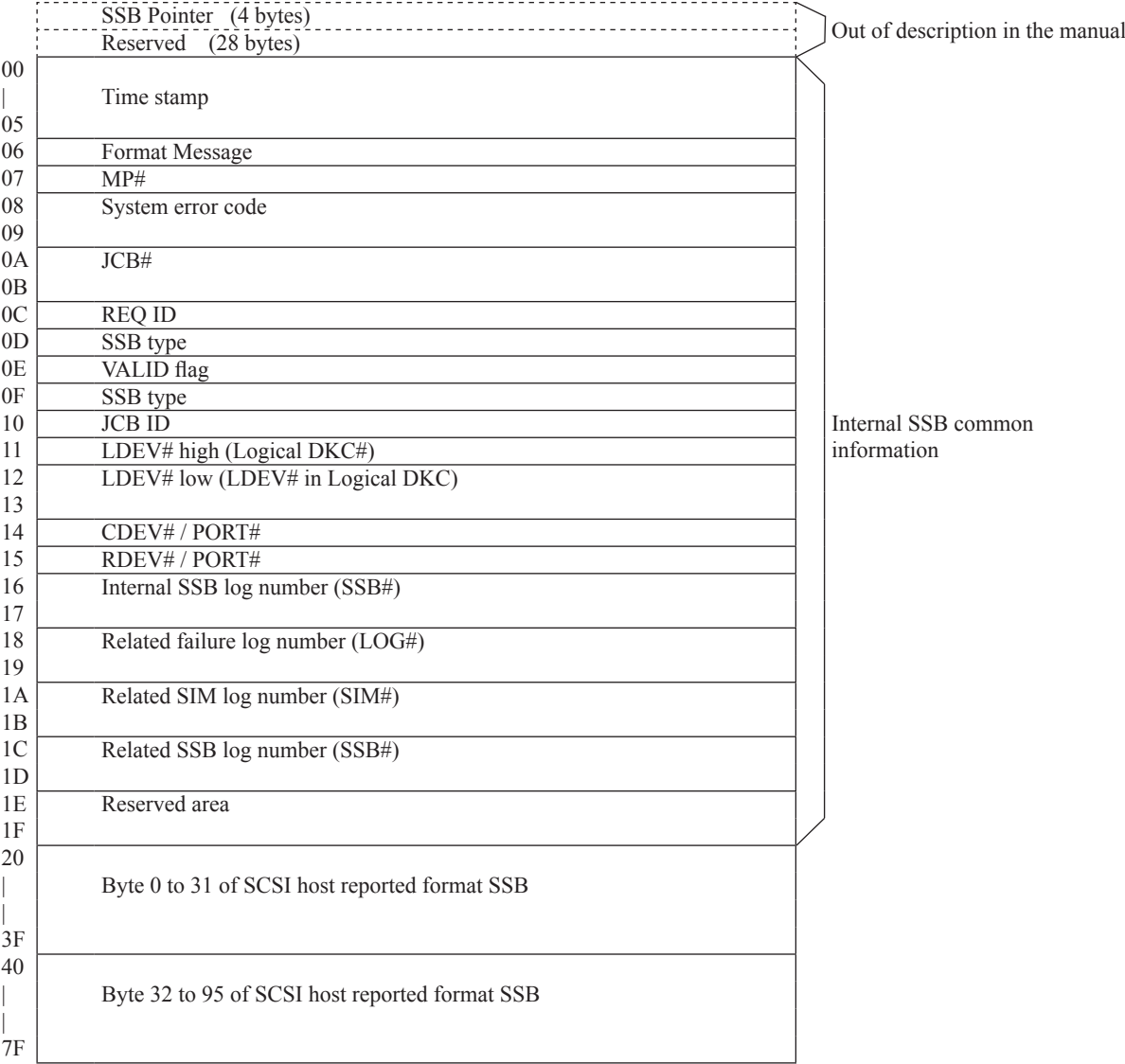


Figure 1-3 Outline of SCSI Host Report Format Internal SSB



2. Classification of Internal SSB

The internal SSB is classified crudely as the 24 byte format and the 32 byte format. The further classification (The search method for SSB table) is shown [Figure 2-2](#).

Figure 2-1 The outline

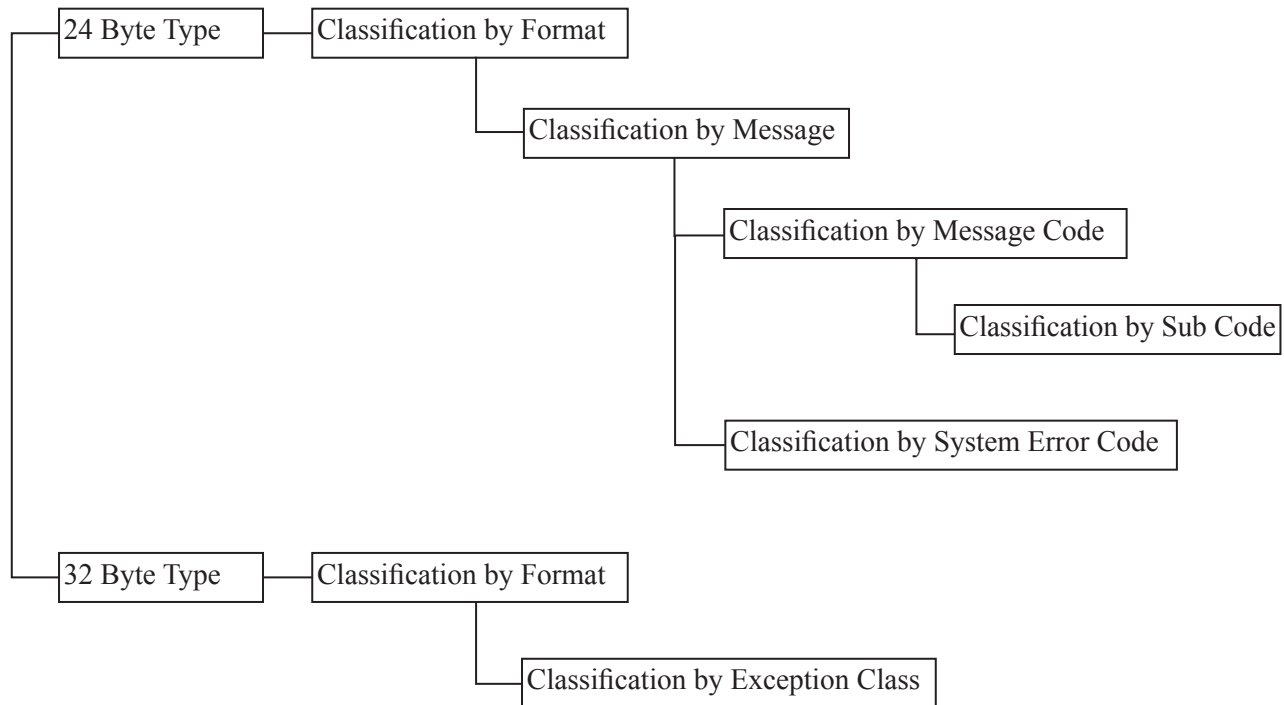
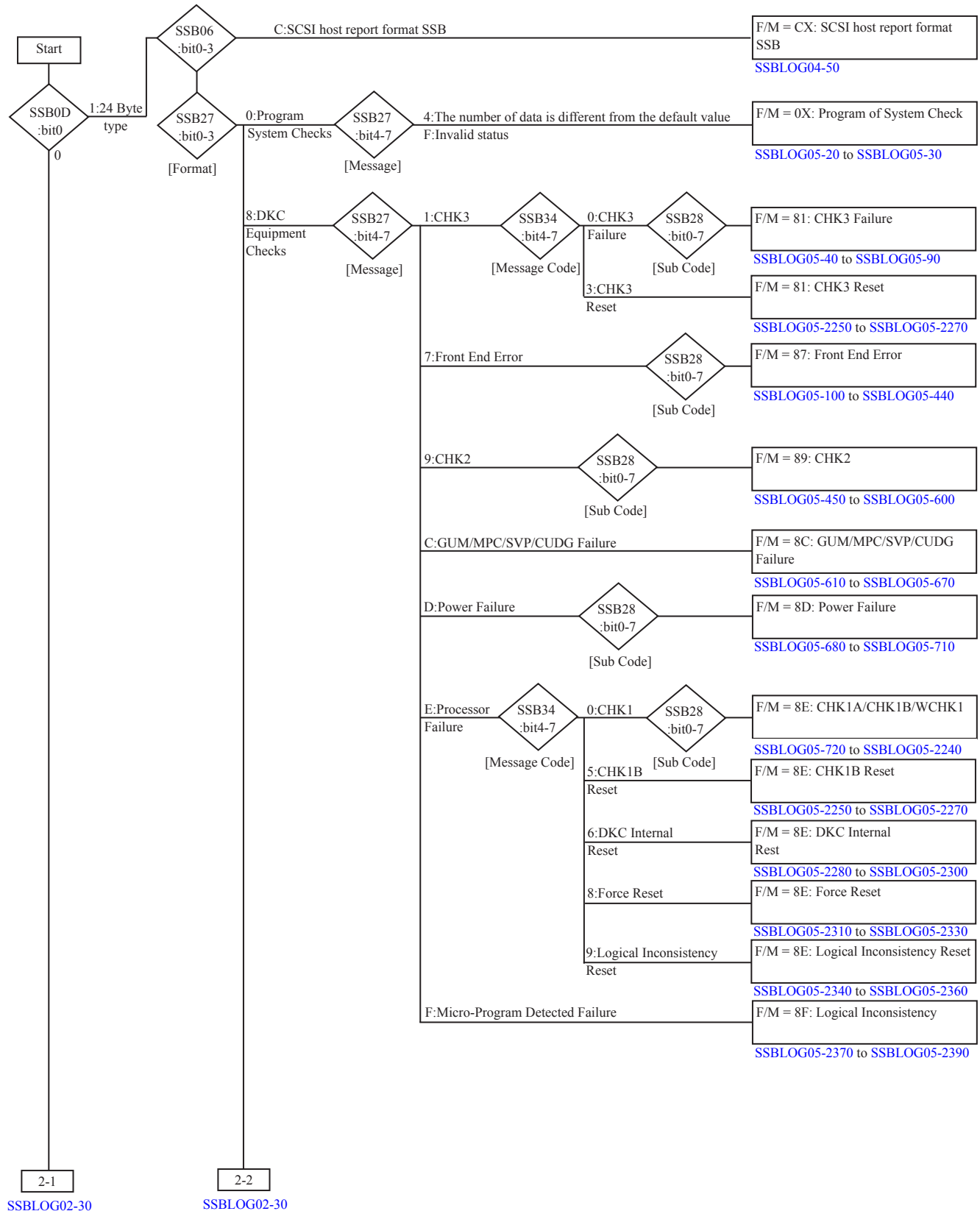
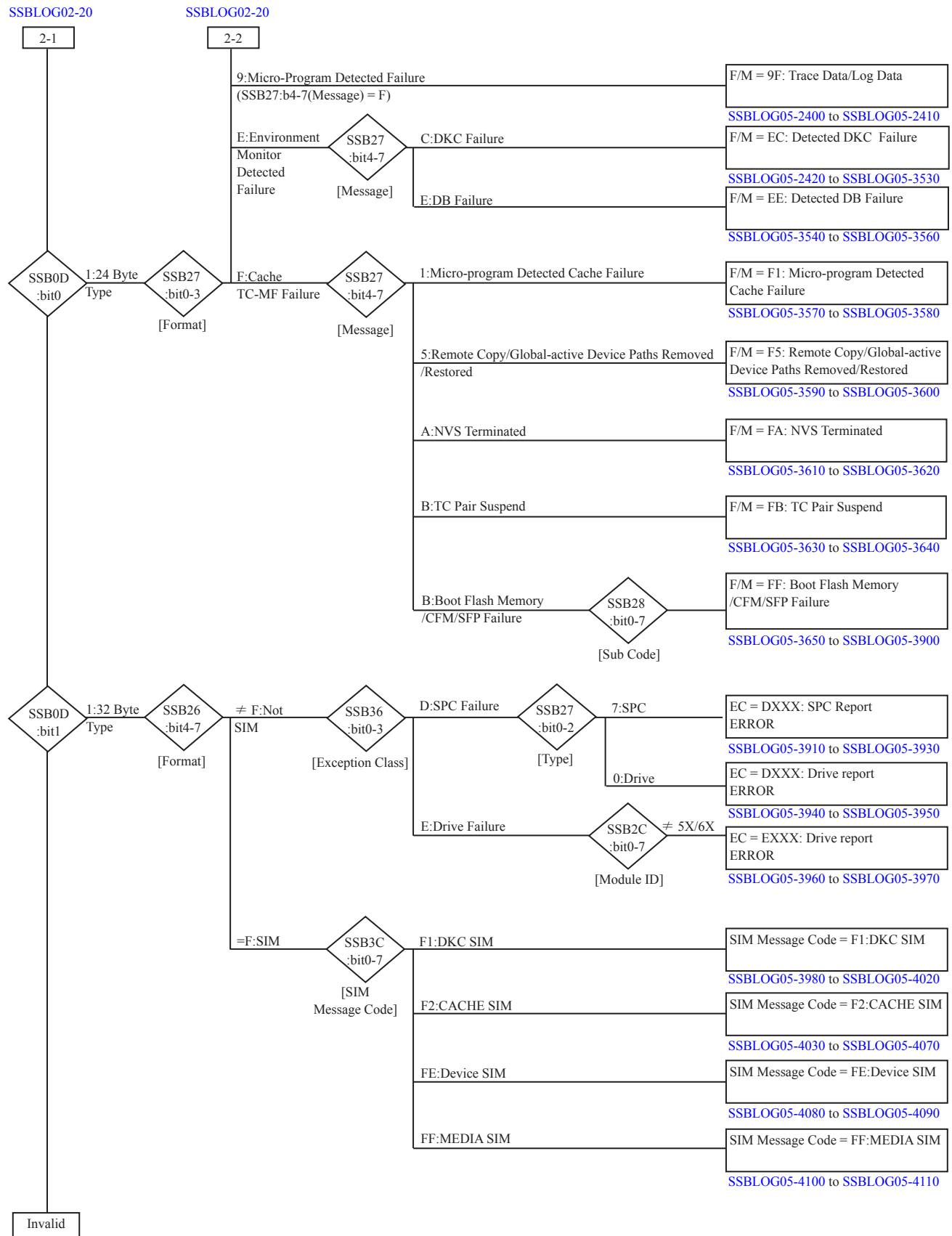


Figure 2-2 Search method for SSB tables





3. Common Information Description

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	No Display log
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Format-Dependent Information							
3F								
40	Internal SSB Information							
7F								

Table 3-1 Common Information

Byte	Bit	Meaning	Remarks
00 to 05		Time stamp The time error occurred (The time maked internal SSB)	
06		Format Message	
07		Processor number (Internal ID) x'00' to x'0F'	
08 to 09		System error code	
	0 to 7	Module ID	
	8 to 15	Routine ID	
0A to 0B		JCB#	
0C		REQ ID	
0D		SSB type	
	0	24 byte type flag 0: Invalid 1: 24 byte type SSB	
	1	32 byte type flag 0: Invalid 1: 32 byte type SSB	
	2	Flag for SCSI host processor detected the error 0: Invalid 1: SSB detected by SCSI host processor	
	3	Remote copy flag 0: Invalid 1: Remote copy valid	
	4	Separately stored log flag 0: General log 1: Separately stored log	
	5	Host report flag 0: Pending 1: Reported	
	6	SVP report flag 0: Pending 1: Reported	
	7	Forcible log flag 0: Normal 1: Forcibly ejected log (store in Controller Chassis beyond a definite period of time)	
0E		Valid flag	
	0	LDEV number valid or invalid 0: Invalid 1: Valid	
	1	PDEV number valid or invalid 0: Invalid 1: Valid	
	2	SSB number valid or invalid 0: Invalid 1: Valid	
	3	Related failure log number valid or invalid 0: Invalid 1: Valid	

(To be continued)

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Byte	Bit	Meaning	Remarks
0E		Valid flag	
	4	Related SIM log number valid or invalid 0: Invalid 1: Valid	
	5	Related SSB log number valid or invalid 0: Invalid 1: Valid	
	6	PK information valid or invalid 0: Invalid 1: Valid	
	7	No display log valid or invalid 0: Display log 1: No display log	
0F		SSB type	
	0	Reserved	
	1	RCU SIM flag 0: Invalid 1: RCU SIM flag	
	2	PORT number valid or invalid 0: Valid 1: Invalid	
	3 to 7	Reserved	
10		JCB ID	
11		LDEV# high (Logical DKC#)	
12 to 13		LDEV# low (LDEV# in Logical DKC)	
14		CDEV# / PORT#	
15		RDEV# / PORT#	
16 to 17		Internal SSB log number	
18 to 19		Related Failure log number	
1A to 1B		Related SIM log number	
1C to 1D		Related SSB log number	
1E to 1F		Reserved area	

4. SSB Format-Depended Information Description

4.1 24-Byte Type SSB

	0	1	2	3	4	5	6	7
00	Internal SSB common information							
1F								
20								
26	System reserved area							
27								
28	Format (Details will be published separately)				Message (Details will be published separately)			
37	Format-depended information (For details, see the following pages)							
38								
3F	System reserved area							
40								
7F								
	Internal SSB specific information (For details, see the following pages)							

Table 4-1 Format and Message

Format	Message	Meaning
0 (Program/ System check)	0 to 3	Not used
	4	The number of data is different from the default value
	5 to E	Not used
	F	Invalid status
1 to 7	0 to F	Not used
8	0	Not used
	1	CHK3
	2 to 6	Not used
	7	Front End Error
	8	Not used
	9	CHK2
	A	Not used
	C	GUM/MPC/MApp/CUDG detected Failure
	D	Power failure
	E	Processor failure
	F	Micro-program detected error (Logical Inconsistency)
9	0 to E	Not used
	F	Micro-program detected error (Trace data/Log data)
E	0 to B	Not used
	C	Environmental monitor detected Controller Chassis failure
	D	Not used
	E	Environmental monitor detected Drive Box failure
	F	Not used
F	0	Not used
	1	Micro-program detected cache failure
	2 to 4	Not used
	5	Remote Copy/Global-active Device Paths Removed/Restored
	6 to 9	Not used
	A	NVS terminated
	B	TrueCopy for Mainframe Pair suspend
	C to E	Not used
	F	Boot Flash Memory/CFM/SFP Failure

4.2 32-Byte Type SSB

	0	1	2	3	4	5	6	7				
00	Internal SSB common information											
1F												
20	System reserved area											
26												
27	Format (Details will be published separately)											
35												
36	Format-depended information (For details, see following pages)											
37												
38	Exception class (Details will be published separately)											
3F												
40	System reserved area											
7F												
	Internal SSB specific information (For datails, see following pages)											

Table 4-2 Exception Class (Byte36: bit0-3) and Format (Byte26: bit4-7)

Exception class	Format	Meaning
0	0 to F	Not used
2	0 to E	Not used
	F	DKC SIM
3	0 to E	Not used
	F	DKC SIM
4	0 to E	Not used
	F	Media SIM
5	0 to E	Not used
	F	DEVICE SIM/Media SIM (ORM detected)
6	2 to F	Not used
C	0 to E	Not used
	F	DKC SIM
D	0	Drive report error
	1	SPC report error / Drive report error
	0 to E	Not used
	F	DKC SIM
E	0	Drive report error
	1 to E	Not used
	F	DEVICE SIM
F	0 to E	Not used
	F	DKC SIM/CACHE SIM

4.3 SCSI Host Report Format SSB

	0	1	2	3	4	5	6	7
00	Internal SSB common information (*1)							
1F								
20	Information byte valid	Error code (0x7)						
21	Segment No (0)							
22	File mark (0)	ECM (0)	ILI (0)	Not used	Sense key			
23	Information byte							
26								
27	Additional sense data length (0x58)							
28	Command specific information							
2B	Sense code							
2C								
2D	Field Replaceable Unit (0)							
2E								
2F	Sense key specific information	Sense key specific information						
30	Additional sense data							
31								
32	Additional sense data valid							
7F								

*1: 0xCX (X=sense key) is set to byte 7 (log type low-order byte). SSB on SVP is indicated as F/M=CX.

Table 4-3 Details of SCSI Host Report Format SSB Information

Byte	Bit	Description	Setting	Remarks
20	0	Information byte valid/invalid 0: Invalid 1: Valid	OPEN command Control	
	1 to 7	Error code (0x7) Indicates an I/O process error.		
21		Segment No. (0)		
22	0	File Mark (0)		
	1	ECM (0)		
	2	ILI (0)		
	3	No used		
	4 to 7	Sense key: For more details, see Table 4-4 .		
23 to 26		Information bytes: LBA related to sense key if information byte is valid.		
27		Additional sense data length (0x58) Indicates sense data length after byte 8 or later.		
28 to 2B		Command specific information		
2C to 2D		Sense codes		
2E		Field Replaceable Unit (0)		
2F to 31	0	Sense key specific information valid/invalid 0: Invalid 1: Valid		
	1 to 17	Sense key specific information		
32 to 7F	0	Additional sense data valid/invalid 0: Invalid 1: Valid	Error detection source	
	1 to END	Additional sense data		

Table 4-4 Sense Keys List

Sense key	Description
0	NO SENSE
1	RECOVERED ERROR
2	NOT READY
3	MEDIUM ERROR
4	HARDWARE ERROR
5	ILLIGAL REQUEST
6	UNIT ATTENTION
7	DATA PROTECT
8	BLANK CHECK (Not used)
9	VENDER UNIQUE
A	COPY ABORTED
B	ABORTED COMMAND
C	EQUAL (No use)
D	VOLUME OVERFLOW
E	MISCOMPARE
F	(RESERVED)

5. Format-Depended Information Description

5.1 Byte27 = 04 (The Number of Data is Different from the Default Value) / 0F (Invalid Status)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PK type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'0')				Message (x'X' : X ≠ 8/A)			
28	Command code (for F/M ≠ 0F) / Reason code (for F/M=0F)							
29	Detail failure information 1							
2A								
2B								
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	Package number							
33	Detail failure information 2							
34	SSID of self storage system							
35								
36	Symptom code (x'0F0X') (X=Message code)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

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	0	1	2	3	4	5	6	7
40	Not used							
55								
56	Module ID (detected the error)							
57	Routine ID (detected the error)							
58	Not used							
7F								

5.2 Byte27 = 81 (CHK3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'1')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF81')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.2.1 Byte27 = 81(CHK3) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15 or 16 or 17

	0	1	2	3	4	5	6	7
28	Subcode : x'11 : CHK3A (Parameter error) x'12 : CHK3B (Queue error) x'13 : CHK3C (Timeout) x'14 : CHK3D (Status) x'15 : CHK3E (Program) x'16 : CHK3F (Timeout) x'17 : CHK3G (Invalid)							
29	MP#							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	GCTL_MATED_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2D	GCTL_MATED_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2E	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
2F	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
31	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

5.2.2 Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15 or 16 or 17 : CHK3

	0	1	2	3	4	5	6	7
40	0x01 (FreeArea No.)							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	CPU1_GBL_GNERRST[23:16]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	CPU1_GBL_GNERRST[15:8]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	CPU1_GBL_GNERRST[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	CPU0 NTB0_XPUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0 NTB0_XPUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0 NTB0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
57	CPU0 NTB0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
58	CPU0 NTB0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU0 NTB0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5A	CPU0 NTB0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5B	CPU0 NTB0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
5C	CPU0 NTB0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5D	CPU0 NTB0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5E	CPU0 NTB0_LNKSTS2[15:8]							
5F	CPU0 NTB0_LNKSTS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_NTB1_XPUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	Reserved
61	CPU1_NTB1_XPUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	Reserved
62	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
63	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
64	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU1_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
66	CPU1_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
67	CPU1_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
68	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
69	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
6A	CPU1_NTB1_LNKSTS2[15:8]							
6B	CPU1_NTB1_LNKSTS2[7:0]							
6C	Driver data[31:24]							
6D	Driver data[23:16]							
6E	Driver data[15:8]							
6F	Driver data[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	SYSTEMTIML[31:24]							
71	SYSTEMTIML [23:16]							
72	SYSTEMTIML [15:8]							
73	SYSTEMTIML [7:0]							
74	ACSID [31:24]							
75	ACSID [23:16]							
76	ACSID [15:8]							
77	ACSID [7:0]							
78	ACS_ADDRESS[63:56]							
79	ACS_ADDRESS[55:48]							
7A	ACS_ADDRESS[47:40]							
7B	ACS_ADDRESS[39:32]							
7C	ACS_ADDRESS [31:24]							
7D	ACS_ADDRESS [23:16]							
7E	ACS_ADDRESS [15:8]							
7F	ACS_ADDRESS [7:0]							

5.3 Byte27 = 87 (Front End Error)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'7')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF87')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.3.1 Byte27 = 87 (Front End Error) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15

	0	1	2	3	4	5	6	7
28	Subcode : x'11 : FEIF-PK FC16G/FC32G Chip REPLACE x'12 : FEIF-PK FC16G/FC32G System Error x'13 : FEIF-PK FC16G/FC32G RISC pause x'14 : FEIF-PK FC16G/FC32G Queue Error(REQ,RSP,ATQ x'15 : FEIF-PK FC16G/FC32G TEMP Alarm							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	RISC to Host Status							
2D								
2E								
2F								
30	Not used							
31	Not used							

(2) Byte28 = 21

	0	1	2	3	4	5	6	7
28	Subcode : x'21: FEIF-PK iSCSI 10G/1G Temperture Warning							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Outgoing MB Register 3[31:24]							
2D	Outgoing MB Register 3[23:16]							
2E	Outgoing MB Register 3[15:8]							
2F	Outgoing MB Register 3[7:0]							
30	Not used							
31	Not used							

(3) Byte28 = 22

	0	1	2	3	4	5	6	7
28	Subcode :x'22 : FEIF-PK iSCSI 10G/1G HeartBeat Error Unrecoverable							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(4) Byte28 = 23

	0	1	2	3	4	5	6	7
28	Subcode : x'23 : FEIF-PK iSCSI 10G/1G HeartBeat Error Recoverable							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(5) Byte28 = 24

	0	1	2	3	4	5	6	7
28	Subcode : x'24 : FEIF-PK iSCSI 10G/1G HeartBeat Error Uninfo							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(6) Byte28 = 25 or 26

	0	1	2	3	4	5	6	7
28	Subcode : x'25 : FEIF-PK iSCSI 10G/1G FE System Error							
	x'26 : FEIF-PK iSCSI 10G/1G PCIe Correctable Error							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.3.2 Byte27 = 87 (Front End Error) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15

	0	1	2	3	4	5	6	7
40	RISC to Host Status[31:24]							
41	RISC to Host Status[23:16]							
42	RISC to Host Status[15:8]							
43	RISC to Host Status[7:20]							
44	Host Command and Control[31:24]							
45	Host Command and Control[23:16]							
46	Host Command and Control[15:8]							
47	Host Command and Control[7:20]							
48	GPIO Data[31:24]							
49	GPIO Data[23:16]							
4A	GPIO Data[15:8]							
4B	GPIO Data[7:20]							
4C	Outgoing MB Register 0[31:24]							
4D	Outgoing MB Register 0[23:16]							
4E	Outgoing MB Register 0[15:8]							
4F	Outgoing MB Register 0[7:20]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Outgoing MB Register 1[31:24]							
51	Outgoing MB Register 1[23:16]							
52	Outgoing MB Register 1[15:8]							
53	Outgoing MB Register 1[7:20]							
54	Outgoing MB Register 2[31:24]							
55	Outgoing MB Register 2[23:16]							
56	Outgoing MB Register 2[15:8]							
57	Outgoing MB Register 2[7:20]							
58	Outgoing MB Register 3[31:24]							
59	Outgoing MB Register 3[23:16]							
5A	Outgoing MB Register 3[15:8]							
5B	Outgoing MB Register 3[7:20]							
5C	Outgoing MB Register 4[31:24]							
5D	Outgoing MB Register 4[23:16]							
5E	Outgoing MB Register 4[15:8]							
5F	Outgoing MB Register 4[7:20]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 5[31:24]							
61	Outgoing MB Register 5[23:16]							
62	Outgoing MB Register 5[15:8]							
63	Outgoing MB Register 5[7:20]							
64	Outgoing MB Register 6[31:24]							
65	Outgoing MB Register 6[23:16]							
66	Outgoing MB Register 6[15:8]							
67	Outgoing MB Register 6[7:20]							
68	Outgoing MB Register 7[31:24]							
69	Outgoing MB Register 7[23:16]							
6A	Outgoing MB Register 7[15:8]							
6B	Outgoing MB Register 7[7:20]							
6C	PCI Config Register(Address : 0x07)							
6D	PCI Config Register(Address : 0x06)							
6E	PCI Config Register(Address : 0x05)							
6F	PCI Config Register(Address : 0x04)							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x5F)							
75	PCI Config Register(Address : 0x5E)							
76	PCI Config Register(Address : 0x5D)							
77	PCI Config Register(Address : 0x5C)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(2) Byte28 = 21

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Outgoing MB Register 0[31:24]							
55	Outgoing MB Register 0[23:16]							
56	Outgoing MB Register 0[15:8]							
57	Outgoing MB Register 0[7:0]							
58	Outgoing MB Register 1[31:24]							
59	Outgoing MB Register 1[23:16]							
5A	Outgoing MB Register 1[15:8]							
5B	Outgoing MB Register 1[7:0]							
5C	Outgoing MB Register 2[31:24]							
5D	Outgoing MB Register 2[23:16]							
5E	Outgoing MB Register 2[15:8]							
5F	Outgoing MB Register 2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 3[31:24]							
61	Outgoing MB Register 3[23:16]							
62	Outgoing MB Register 3[15:8]							
63	Outgoing MB Register 3[7:0]							
64	Outgoing MB Register 4[31:24]							
65	Outgoing MB Register 4[23:16]							
66	Outgoing MB Register 4[15:8]							
67	Outgoing MB Register 4[7:0]							
68	Outgoing MB Register 5[31:24]							
69	Outgoing MB Register 5[23:16]							
6A	Outgoing MB Register 5[15:8]							
6B	Outgoing MB Register 5[7:0]							
6C	Outgoing MB Register 6[31:24]							
6D	Outgoing MB Register 6[23:16]							
6E	Outgoing MB Register 6[15:8]							
6F	Outgoing MB Register 6[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Outgoing MB Register 7[31:24]							
71	Outgoing MB Register 7[23:16]							
72	Outgoing MB Register 7[15:8]							
73	Outgoing MB Register 7[7:0]							
74	PCI Config Register(Address : 0x57)							
75	PCI Config Register(Address : 0x56)							
76	PCI Config Register(Address : 0x55)							
77	PCI Config Register(Address : 0x54)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(3) Byte28 = 22

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = 23

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = 24

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(6) Byte28 = 25

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Outgoing MB Register 0[31:24]							
55	Outgoing MB Register 0[23:16]							
56	Outgoing MB Register 0[15:8]							
57	Outgoing MB Register 0[7:0]							
58	Outgoing MB Register 1[31:24]							
59	Outgoing MB Register 1[23:16]							
5A	Outgoing MB Register 1[15:8]							
5B	Outgoing MB Register 1[7:0]							
5C	Outgoing MB Register 2[31:24]							
5D	Outgoing MB Register 2[23:16]							
5E	Outgoing MB Register 2[15:8]							
5F	Outgoing MB Register 2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 3[31:24]							
61	Outgoing MB Register 3[23:16]							
62	Outgoing MB Register 3[15:8]							
63	Outgoing MB Register 3[7:0]							
64	Outgoing MB Register 4[31:24]							
65	Outgoing MB Register 4[23:16]							
66	Outgoing MB Register 4[15:8]							
67	Outgoing MB Register 4[7:0]							
68	Outgoing MB Register 5[31:24]							
69	Outgoing MB Register 5[23:16]							
6A	Outgoing MB Register 5[15:8]							
6B	Outgoing MB Register 5[7:0]							
6C	Outgoing MB Register 6[31:24]							
6D	Outgoing MB Register 6[23:16]							
6E	Outgoing MB Register 6[15:8]							
6F	Outgoing MB Register 6[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Outgoing MB Register 7[31:24]							
71	Outgoing MB Register 7[23:16]							
72	Outgoing MB Register 7[15:8]							
73	Outgoing MB Register 7[7:0]							
74	PCI Config Register(Address : 0x57)							
75	PCI Config Register(Address : 0x56)							
76	PCI Config Register(Address : 0x55)							
77	PCI Config Register(Address : 0x54)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(7) Byte28 = 26

	0	1	2	3	4	5	6	7
40	PCI Config Register(Address : 0x57)							
41	PCI Config Register(Address : 0x56)							
42	PCI Config Register(Address : 0x55)							
43	PCI Config Register(Address : 0x54)							
44	PCI Config Register(Address : 0x107)							
45	PCI Config Register(Address : 0x106)							
46	PCI Config Register(Address : 0x105)							
47	PCI Config Register(Address : 0x104)							
48	PCI Config Register(Address : 0x113)							
49	PCI Config Register(Address : 0x112)							
4A	PCI Config Register(Address : 0x111)							
4B	PCI Config Register(Address : 0x110)							
4C	Not used							
4D	Not used							
4E	Not used							
4F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

5.4 Byte27 = 89(CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'9')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF89')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.4.1 Byte27 = 89(CHK2) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 20 or 21

	0	1	2	3	4	5	6	7
28	Subcode : x'11 : TIMEOUT (DMA) x'12 : TIMEOUT (DMA) x'13 : STATUS error x'14 : Data protection code error (CPU internal) x'20 : Data protection code error (CPU) x'21 : Data protection code error (Controller path)							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
31	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(2) Byte28 = 22

	0	1	2	3	4	5	6	7
28	Subcode : x'22 : Data protection code error (FE)							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 23

	0	1	2	3	4	5	6	7
28	Subcode : x'23 : Data protection code error(BE)							
29	Detect CPU#							
2A	Slave CPU#							
2B	Transfer Direction(DRIVE_READ : 0x01,DRIVE_WRITE : 0x02							
2C	SAS CTL#							
2D	Port#							
2E	Transfer Mode(notDirect : 0x01,Direct : 0x02)							
2F	Not used							
30	Not used							
31	Not used							

5.4.2 Byte27 = 89 (CHK2) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 20 or 21

	0	1	2	3	4	5	6	7
40	GCTL_I_STATE0							
	CPU	CPU/DIMM	CPU/DIMM	CPU error	CPU error	CPU error	GCTL error	Not used
	Thermal alert	Fatal error	Fatal error					
41	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
42	GCTL_MATED_I_STATE0							
	CPU	CPU/DIMM	CPU/DIMM	CPU error	CPU error	CPU error	GCTL error	Not used
	Thermal alert	Fatal error	Fatal error					
43	GCTL_MATED_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
45	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
47	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	GCTL_MATED_CTLREQ_STS							
	Not used	Not used	Not used	Not used	Power alarm		Power ready	
49	GCTL_IOPKRST_MASK0							
4A	GCTL_IOPKRST_MASK1							
4B	Not used							
4C	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU0_CB00_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_CB00_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_CB00_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
57	CPU0_CB00_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
58	CPU1_CB10_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_CB10_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	CPU1_CB10_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
5B	CPU1_CB10_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
5C	CPU0 NTB0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0 NTB0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0 NTB0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0 NTB0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0 NTB0 LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
61	CPU0 NTB0 LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
62	CPU0 NTB0 LNKSTS2[15:8]							
63	CPU0 NTB0 LNKSTS2[7:0]							
64	CPU0 NTB0 XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
65	CPU0 NTB0 XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
66	CPU0 NTB0 DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
67	Not used							
68	CPU0 NTB0 UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0 NTB0 UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
6A	CPU0 NTB0 UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
6B	CPU0 NTB0 UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
6C	CPU0 NTB0 LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
6D	CPU0 NTB0 LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
6E	CPU0 NTB0 LNKSTS2[15:8]							
6F	CPU0 NTB0 LNKSTS2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
71	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
72	CPU1_NTB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
73	Not used							
74	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	CPU1_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
76	CPU1_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
77	CPU1_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
78	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
79	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
7A	CPU1_NTB1_LNKSTS2[15:8]							
7B	CPU1_NTB1_LNKSTS2[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(2) Byte28 = 22

	0	1	2	3	4	5	6	7
40	ENTRY TYPE[31:24]							
41	ENTRY TYPE[23:16]							
42	ENTRY TYPE[15:8]							
43	ENTRY TYPE[7:0]							
44	HANDLE[31:24]							
45	HANDLE[23:16]							
46	HANDLE[15:8]							
47	HANDLE[7:0]							
48	STATUS[31:24]							
49	STATUS[23:16]							
4A	STATUS[15:8]							
4B	STATUS[7:0]							
4C	TOTAL DATA SEGMENT COUNT[31:24]							
4D	TOTAL DATA SEGMENT COUNT[23:16]							
4E	TOTAL DATA SEGMENT COUNT[15:8]							
4F	TOTAL DATA SEGMENT COUNT[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	STATUS FLAGS[31:24]							
51	STATUS FLAGS[23:16]							
52	STATUS FLAGS[15:8]							
53	STATUS FLAGS[7:0]							
54	RECEIVE EXCHANGE ADDRESS[31:24]							
55	RECEIVE EXCHANGE ADDRESS[23:16]							
56	RECEIVE EXCHANGE ADDRESS[15:8]							
57	RECEIVE EXCHANGE ADDRESS[7:0]							
58	FLAGS[31:24]							
59	FLAGS[23:16]							
5A	FLAGS[15:8]							
5B	FLAGS[7:0]							
5C	RESIDUAL TRANSFER LENGTH[31:24]							
5D	RESIDUAL TRANSFER LENGTH[23:16]							
5E	RESIDUAL TRANSFER LENGTH[15:8]							
5F	RESIDUAL TRANSFER LENGTH[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	OX_ID[31:24]							
61	OX_ID[23:16]							
62	OX_ID[15:8]							
63	OX_ID[7:0]							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	REPORTED RUNT BLOCK GUARD[31:24]							
6D	REPORTED RUNT BLOCK GUARD[23:16]							
6E	REPORTED RUNT BLOCK GUARD[15:8]							
6F	REPORTED RUNT BLOCK GUARD[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	ACTUAL DATA PROTECTION CODE[31:24]							
71	ACTUAL DATA PROTECTION CODE[23:16]							
72	ACTUAL DATA PROTECTION CODE[15:8]							
73	ACTUAL DATA PROTECTION CODE[7:0]							
74	ACTUAL DATA PROTECTION CODE2[31:24]							
75	ACTUAL DATA PROTECTION CODE2[23:16]							
76	ACTUAL DATA PROTECTION CODE2[15:8]							
77	ACTUAL DATA PROTECTION CODE2[7:0]							
78	EXPECTED DATA PROTECTION CODE[31:24]							
79	EXPECTED DATA PROTECTION CODE[23:16]							
7A	EXPECTED DATA PROTECTION CODE[15:8]							
7B	EXPECTED DATA PROTECTION CODE[7:0]							
7C	EXPECTED DATA PROTECTION CODE2[31:24]							
7D	EXPECTED DATA PROTECTION CODE2[23:16]							
7E	EXPECTED DATA PROTECTION CODE2[15:8]							
7F	EXPECTED DATA PROTECTION CODE2[7:0]							

(3) Byte28 = 23

	0	1	2	3	4	5	6	7
40	Outbound Messages DW0[31:24]							
41	Outbound Messages DW0[23:16]							
42	Outbound Messages DW0[15:8]							
43	Outbound Messages DW0[7:0]							
44	Outbound Messages DW1[31:24]							
45	Outbound Messages DW1[23:16]							
46	Outbound Messages DW1[15:8]							
47	Outbound Messages DW1[7:0]							
48	Outbound Messages DW2[31:24]							
49	Outbound Messages DW2[23:16]							
4A	Outbound Messages DW2[15:8]							
4B	Outbound Messages DW2[7:0]							
4C	Outbound Messages DW3[31:24]							
4D	Outbound Messages DW3[23:16]							
4E	Outbound Messages DW3[15:8]							
4F	Outbound Messages DW3[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Outbound Messages DW4[31:24]							
51	Outbound Messages DW4[23:16]							
52	Outbound Messages DW4[15:8]							
53	Outbound Messages DW4[7:0]							
54	Outbound Messages DW5[31:24]							
55	Outbound Messages DW5[23:16]							
56	Outbound Messages DW5[15:8]							
57	Outbound Messages DW5[7:0]							
58	Outbound Messages DW6[31:24]							
59	Outbound Messages DW6[23:16]							
5A	Outbound Messages DW6[15:8]							
5B	Outbound Messages DW6[7:0]							
5C	Outbound Messages DW7[31:24]							
5D	Outbound Messages DW7[23:16]							
5E	Outbound Messages DW7[15:8]							
5F	Outbound Messages DW7[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Outbound Messages DW8[31:24]							
61	Outbound Messages DW8[23:16]							
62	Outbound Messages DW8[15:8]							
63	Outbound Messages DW8[7:0]							
64	Outbound Messages DW9[31:24]							
65	Outbound Messages DW9[23:16]							
66	Outbound Messages DW9[15:8]							
67	Outbound Messages DW9[7:0]							
68	Outbound Messages DW10[31:24]							
69	Outbound Messages DW10[23:16]							
6A	Outbound Messages DW10[15:8]							
6B	Outbound Messages DW10[7:0]							
6C	Outbound Messages DW11[31:24]							
6D	Outbound Messages DW11[23:16]							
6E	Outbound Messages DW11[15:8]							
6F	Outbound Messages DW11[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Outbound Messages DW12[31:24]							
71	Outbound Messages DW12[23:16]							
72	Outbound Messages DW12[15:8]							
73	Outbound Messages DW12[7:0]							
74	Outbound Messages DW13[31:24]							
75	Outbound Messages DW13[23:16]							
76	Outbound Messages DW13[15:8]							
77	Outbound Messages DW13[7:0]							
78	Outbound Messages DW14[31:24]							
79	Outbound Messages DW14[23:16]							
7A	Outbound Messages DW14[15:8]							
7B	Outbound Messages DW14[7:0]							
7C	Outbound Messages DW15[31:24]							
7D	Outbound Messages DW15[23:16]							
7E	Outbound Messages DW15[15:8]							
7F	Outbound Messages DW15[7:0]							

5.5 Byte27 = 8C (GUM/MPC/SVP/CUDG Detected Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'C')			
28	Error information (Details will be published separately)							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Failure type code (*1)							
31								
32	Error information (Details will be published separately)							
33								
34								
35								
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (x'00')							
3B								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
3C	Reserved							
3F								
40	Error information (Details will be published separately)							
7F								

*1: Failure type code

- x'0000' : Logical Inconsist
- x'0001' : Windows memory allocate failure
- x'0002' : File
- x'0003' : LAN failure
- x'0005' : Windows failure
- x'0009' : BOOT detected failure
- x'000B' : Router failure
- x'000D' : Stop SSB overwrite
- x'000E' : Stop SSBS overwrite
- x'000F' : Flash failure
- x'0015' : Battery failure
- x'0030' : Delay of time
- x'0031' : Warning of a stack
- x'0032' : Single task error
- x'0033' : MPC launcher error
- x'0039' : IP address setting error
- x'003A' : Subnet mask setting error
- x'003B' : Virus detection error
- x'003C' : Virus detection error
- x'003D' : Virus detection error
- x'003F' : Situation observation error
- x'0040' : Dummy PS/OFF starting (normal)

5.5.1 Byte27 = 8C (GUM/MPC/SVP/CUDG) Error Information Details

- (1) Logical inconsistency / HEAP / File / LAN / Windows failure / Router failure / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / MPC Launcher error / IP address setting error / Subnet mask setting error / Virus detection error / Situation observation error / Dummy PS/OFF starting (normal)

	0	1	2	3	4	5	6	7
28	Failed AP code (*1)							
29								
2A	Reserved							
2F								
30	Error type code (*2)							
31								
32	Error code							
35								
40	Failed Window class (ASCII code: 8characters)							
47								
48	Failed function name (ASCII code: 24characters)							
5F								
60	Failed line number							
61								
62	Error code							
65								
66	Reserved							
7F								

*1: Failed AP Code

x'0000' : Dump task
x'0001' : HDD inline
x'0002' : Update Firmware (Online)
x'0003' : Information
x'0004' : LOGOUT
x'0005' : PATH inline
x'0006' : Back Ground
x'0007' : DIAG
x'0008' : Install
x'0009' : Online
x'000A' : Update Firmware (Offline)
x'000B' : Special package change
x'000C' : Common function for communication
x'00FF' : Others

*2: Error type code

x'0000' : Logical inconsistency
x'0001' : Windows memory allocate failure
x'0002' : File
x'0003' : LAN failure
x'0005' : Windows failure
x'000B' : Router failure
x'000D' : Stop SSB overwrite
x'000E' : Stop SSBS overwrite
x'000F' : Flash failure
x'0015' : Battery failure
x'0030' : Delay of time
x'0031' : Warning of a stack
x'0032' : Single task error
x'0033' : MPC launcher error
x'0039' : IP address setting error
x'003A' : Subnet mask setting error
x'003B' : Virus detection error
x'003C' : Virus detection error
x'003D' : Virus detection error
x'003F' : Situation observation error
x'0040' : Dummy PS/OFF starting (normal)

(2) CUDG detected error

	0	1	2	3	4	5	6	7
28	'CUDG ERR' (x'43 55 44 47 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E	Error type code (x'0006' : CUDG detection error)							
2F								
30	Error code (x'00000000')							
31								
32								
33								
34								
35								
36								
40	Error occurred time (year)							
41	Error occurred time (month)							
42	Error occurred time (day)							
43	Error occurred time (hour)							
44	Error occurred time (minute)							
45	Error occurred time (second)							
46	Report MPID							
47	Test object							
48	Test type (x'01' : CUDG3)							
49	Not used							
4A	Test contents							
4B	Test contents							
4C	Error code							
4D	Error code							
4E	Not used							
4F	Not used							
50	Cache Module Group No. (slave 1)							
51	Cache Module Group No. (slave 1)							
52	Cache Module Group No. (slave 1)							
53	Cache Module Group No. (slave 1)							
54	Cache Module Group No. (slave 2)							
55	Cache Module Group No. (slave 2)							
56	Cache Module Group No. (slave 2)							
57	Cache Module Group No. (slave 2)							
58	PCB Location (Main Platter)							
59	PCB Location (Main Platter)							
5A	PCB Location (Main Platter)							
5B	PCB Location (Cache Platter 1)							
5C	PCB Location (Cache Platter 1)							
5D	PCB Location (Cache Platter 2)							
5E	PCB Location (Cache Platter 2)							
5F	RUN Option							
60	Additional information (32Byte)							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

(3) BOOT detected error

	0	1	2	3	4	5	6	7
28	'BOOT ERR' (x'42 4F 4F 54 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0009' : BOOT detected error)							
31								
32	Error code (x'00000000' X:Processor number)							
33								
34								
35								
40	Error detection point							
41								
42								
4F								
50	Reserved							
51								
52								
53								
54								
55	Error code for loading error							
56								
57								
58	Error code of initial setting individual function							
59								
5A								
5B								
5C	Error code of initialization function (*1)							
5D								
5E								
5F								
60	Reserved							
61								
7F								

*1: Error code of initialization function.

x'31000041':Forcible start with CE mode switches.

x'48000041':This MP has the firmware which type is different from the PCB name.(In the MP-Install procedure, the package may have been set which type is different from one that is specified in the define configuration.)

x'56000041':Sum check error of the load module.(MPC Failure occurred)

others>Loading for the load module impossible.(The load module was incorrect)

5.6 Byte27 = 8D (Power Failure) Common Information

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'C')			
28	Error information (Details will be published separately)							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Failure type code (*1)							
31								
32	Error information (Details will be published separately)							
33								
34								
35								
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information(x'00')							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Micro-program control information							
7F								

5.6.1 Byte27 = 8D (Power Failure) Byte28-31 Detail

(1) Byte28 = 80

	0	1	2	3	4	5	6	7
28	Subcode (x'80' : DB Detection of Power Failure)							
29	Detected location of Power Failure							
	DB#							
2A	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2B	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2D	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2E	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2F	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
30	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
31	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used

(2) Byte27 = 90

	0	1	2	3	4	5	6	7
28	Subcode (x'90' : DB Recovery of Power Failure)							
29	Detected location of Power Failure							
	DB#							
2A	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2B	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2D	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2E	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2F	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
30	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
31	Reserved							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used

5.7 Byte27 = 8E (CHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK informaiton VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'E')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.7.1 Byte27 = 8E (CHK1) Byte28-31 Detail

(1) Byte28 = 20 : DIMM uncorrectable error

	0	1	2	3	4	5	6	7
28	Subcode : x'20' :DIMM uncorrectable error							
29	MPID (Report SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

- (2) Byte28 = 21 or 30 or 41 or 80: CPU error, CPU uncorrectable/correctable error, CPU core uncorrectable error

	0	1	2	3	4	5	6	7
28	Subcode: x'21 : CPU error x'30 : CPU uncorrectable/correctable error x'41 : CPU correctable error x'80 : CPU core uncorrectable error							
29	MPID (Reported SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	Not used							
2D	MC bunk of error							
2E	IA32_MCi_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
2F	IA32_MCi_STATUS[55:48]							
30	IA32_MCi_STATUS[15:08]							
31	IA32_MCi_STATUS[7:0]							

(3) Byte28 = 22 or 23 or 24 or 31 or 32: CPU error

	0	1	2	3	4	5	6	7
28	Subcode : x'22' : CPU0 uncorrectable error : x'23' : CPU1 uncorrectable error : x'24' : CPU0/CPU1 uncorrectable/correctable error : x'31' : CPU1 uncorrectable/correctable error : x'32' : CPU0 uncorrectable/correctable error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(4) Byte28 = 40: DIMM correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'40': DIMM correctable error							
29	MPID (Reported SSB)							
2A	IA32_MC7_STATUS[63:56]							
	Valid	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	IA32_MC7_STATUS[55:48]							
2C	IA32_MC7_STATUS[47:40]							
2D	IA32_MC7_STATUS[39:32]							
2E	IA32_MC7_STATUS[31:24]							
2F	IA32_MC7_STATUS[23:16]							
30	IA32_MC7_STATUS[15:8]							
31	IA32_MC7_STATUS[7:0]							
	Reserved	Reserved	Reserved	Reserved	indicate the channel number when the Valid bit (IA32_MC7_STATUS[63]) is asserted for VSP G700, G900 and VSP F700, F900 0000b = DIMM00/DIMM02 0001b = DIMM01/DIMM03 0002b = DIMM10/DIMM12 0003b = DIMM11/DIMM13 for VSP G350, G370 and VSP F350, F370 0000b = DIMM00 0001b = DIMM01 for VSP G130 0000b = DIMM00			

(5) Byte28 = 42: DIMM correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'42': DIMM correctable error							
29	MPID (Reported SSB)							
2A	DIMM error information							
2B								
2C								
2D								
2E	DIMM error information (Another CPU's DIMM)							
2F								
30								
31								

- (6) Byte28 = 43 or 44 or 45 or 46 or 4A or 4F or 50 or 51 or 53 or 54 or 55 or 56 or 57 or 58 or 59 or 5A or 60 or 61 or 62 or 63 or 64 or 65 or 66 or 67 or 70 or 71 or 72 or 73 or 74 or 75:

Controller path error, CPU internal data path error, SPI Flash Data Access Error, IO board path error, IO board path error in CHBB, Board management device error in CHBB

	0	1	2	3	4	5	6	7
28	Subcode: x'43' or x'44' : Controller path error x'45' or x'46' or x'4A' or x'50' or x'51' : CPU internal data path error x'4F' : SPI Flash Data Access Error x'53' or x'60' : IO board path error (Slot F(CBL) or Slot B(CBS)) x'54' or x'61' : IO board path error (Slot B(CBL) or SlotA(CBS)) x'55' or x'62' : IO board path error (Slot A) x'56' or x'63' : IO board path error (Slot E) x'57' or x'64' : IO board path error (Slot H) x'58' or x'65' : IO board path error (Slot D) x'59' or x'66' : IO board path error (Slot C) x'5A' or x'67' : IO board path error (Slot G) x'70' : IO board path error (Slot J) in CHBB x'71' : IO board path error (Slot K) in CHBB x'72' : IO board path error (Slot L) in CHBB x'73' : IO board path error (Slot M) in CHBB x'74' or x'75' : Board management device error in CHBB							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU1_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(7) Byte28 = 47 or 48 or 49 or 4D or 52: LAN device or Board management device error

	0	1	2	3	4	5	6	7
28	Subcode: x'47' : Management LAN device error x'48' or x'49' or x'4D' or x'52' : Board management device error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(8) Byte28 = 4B or 4C: CPU internal error

	0	1	2	3	4	5	6	7
28	Subcode : x'4B' or x'4C': CPU internal error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	LOCAL_APIC_ESR							
	Addr_Error	Received Illegal vector	Send Illegal vector	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Not used							
30	Not used							
31	Not used							

(9) Byte28 = 4E: Controller Interconnect Path Failure Related Error

	0	1	2	3	4	5	6	7
28	Subcode : x'4E': Controller Interconnect Path Failure Related Error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(10) Byte28 = 68 or 78 or 79: IO board (Slot A-H) power failure or initialize error, PCP power failure or initialize error, SWPK internal error or power failure or initialize error

	0	1	2	3	4	5	6	7
28	Subcode: x'68' : IO board power failure or initialize error (Slot A-H) x'78' : SWPK internal error or power failure or initialize error x'79' : PCP power failure or initialize error							
29	MPID (Reported SSB)							
2A	GCTL_R_IOPK_ALM_STS1							
	IO_8_ALM	IO_7_ALM	IO_6_ALM	IO_5_ALM	IO_4_ALM	IO_3_ALM	IO_2_ALM	IO_1_ALM
2B	GCTL_R_IOPK_ALM_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MNG_ALM
2C	R_IOPK_PRT_STS1							
	IO_8_IN	IO_7_IN	IO_6_IN	IO_5_IN	IO_4_IN	IO_3_IN	IO_2_IN	IO_1_IN
2D	R_IOPK_PRT_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MNG_IN
2E	GCTL_R_CURRENT_DIAGMODE							
2F	GCTL_R_CURRENT_DCMODE							
30	CPUx_y_LNKSTS[15:8] (x: 0 or 1, y: PA1 or PA2 or PA3 or PA5 or PB1 or PB2 or PB3 or PB5)							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
31	CPUx_y_LNKSTS[7:0] (x: 0 or 1, y: PA1 or PA2 or PA3 or PA5 or PB1 or PB2 or PB3 or PB5)							
	Link width[7:0]				Current link speed			

(11) Byte28 = A0 or A1: Another CTL error or Another CTL's DIMM error

	0	1	2	3	4	5	6	7
28	Subcode: x'A0' : CPU0/CPU1/DIMM uncorrectable error (Another cluster) x'A1' : DIMM uncorrectable error (Another cluster)							
29	MPID (Reported SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
2D	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2E	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
2F	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	GCTL_LOGFLAG							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	Not used							

(12) Byte28 = 76: IO board power failure or initialize error Slot J/K in CHBB

	0	1	2	3	4	5	6	7
28	Subcode: x'76' : IO board power failure or initialize error Slot J/K in CHBB							
29	MPID (Reported SSB)							
2A	eGCTL0_R_IOPK_ALM_STS1							
	IO_8_ALM	IO_7_ALM	IO_6_ALM	IO_5_ALM	IO_4_ALM	IO_3_ALM	IO_2_ALM	IO_1_ALM
2B	eGCTL0_R_IOPK_ALM_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	eGCTL0_R_IOPK_PRT_STS1							
	IO_8_IN	IO_7_IN	IO_6_IN	IO_5_IN	IO_4_IN	IO_3_IN	IO_2_IN	IO_1_IN
2D	eGCTL0_R_IOPK_PRT_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2E	eGCTL0_R_CURRENT_DIAGMODE							
2F	eGCTL0_R_CURRENT_DCMODE							
30	PEX_LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
31	PEX_LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			

(13) Byte28 = 77 : IO board power failure or initialize error Slot L/M in CHBB

	0	1	2	3	4	5	6	7
28	Subcode: x'77' : IO board power failure or initialize error Slot L/M in CHBB							
29	MPID (Reported SSB)							
2A	eGCTL1_R_IOPK_ALM_STS1							
	IO_8_ALM	IO_7_ALM	IO_6_ALM	IO_5_ALM	IO_4_ALM	IO_3_ALM	IO_2_ALM	IO_1_ALM
2B	eGCTL1_R_IOPK_ALM_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2C	eGCTL1_R_IOPK_PRT_STS1							
	IO_8_IN	IO_7_IN	IO_6_IN	IO_5_IN	IO_4_IN	IO_3_IN	IO_2_IN	IO_1_IN
2D	eGCTL1_R_IOPK_PRT_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used
2E	eGCTL1_R_CURRENT_DIAGMODE							
2F	eGCTL1_R_CURRENT_DCMODE							
30	PEX_LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
31	PEX_LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			

5.7.2 Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20: DIMM uncorrectable error

	0	1	2	3	4	5	6	7
40	Free area format number = 01h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
56	CPU1_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
57	CPU1_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
58	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
5A	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
5B	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
5C	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC3_STATUS[55:32]							
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	IA32_MC3_STATUS[31:0]							
61								
62								
63								
64	IA32_MC3_ADDR[63:0]							
65								
66								
67								
68								
69								
6A								
6B								
6C	IA32_MC3_MISC[31:0]							
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	IA32_MC7_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
71	DIMM error information							
72								
73								
74								
75								
76								
77								
78	DIMM error address (When “Error”=1” and “Addr valid” =1)							
79								
7A								
7B								
7C								
7D								
7E								
7F								

(2) Byte28 = 21 or 80: CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 02h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	CPU1_GBL_GNERRST[23:16]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	CPU1_GBL_GNERRST[15:8]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	CPU1_GBL_GNERRST[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
57	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
58	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
59	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	IA32_MCi_STATUS[63:0]							
69	(Error description)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	IA32_MCi_ADDR[63:0]							
71	(Error address)							
72								
73								
74								
75								
76								
77								
78	IA32_MCi_MISC[63:0]							
79	(Error description2)							
7A								
7B								
7C								
7D								
7E								
7F								

(3) Byte28 = 30: CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 04h							
41	Not used							
42	Not used							
43	Not used							
44	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
45	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
46	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
47	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
48	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
49	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4E	Not used							
4F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	IA32_MCi_STATUS[63:0]							
59	(Error description)							
5A								
5B								
5C								
5D								
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	IA32_MCi_ADDR[63:0]							
61	(Error address)							
62								
63								
64								
65								
66								
67								
68	IA32_MCi_MISC[63:0]							
69	(Error description2)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = 22 or 24 or 31 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 05h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_VTUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_VTUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	CPU0_VTUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
53	CPU0_VTUNCERRSTS[7:0]							
	Reserved	Error	Error	Error	Error	Error	Error	Error
54	CPU0_MIERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_MIERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_MIERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	CPU0_MIERRSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Error
58	CPU0_MIFFERRHDR0[31:24]							
59	CPU0_MIFFERRHDR0[23:16]							
5A	CPU0_MIFFERRHDR0[15:8]							
5B	CPU0_MIFFERRHDR0[7:0]							
5C	CPU0_MIFFERRHDR1[31:24]							
5D	CPU0_MIFFERRHDR1[23:16]							
5E	CPU0_MIFFERRHDR1[15:8]							
5F	CPU0_MIFFERRHDR1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_MIFFERRHDR2[31:24]							
61	CPU0_MIFFERRHDR2[23:16]							
62	CPU0_MIFFERRHDR2[15:8]							
63	CPU0_MIFFERRHDR2[7:0]							
64	CPU0_MIFFERRHDR3[31:24]							
65	CPU0_MIFFERRHDR3[23:16]							
66	CPU0_MIFFERRHDR3[15:8]							
67	CPU0_MIFFERRHDR3[7:0]							
68	CPU0_IIOERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_IIOERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU0_IIOERRST[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	CPU0_IIOERRST[7:0]							
	Reserved	Error	Error	Error	Reserved	Reserved	Reserved	Reserved
6C	CPU0_IIOFFERRHD0[31:24]							
6D	CPU0_IIOFFERRHD0[23:16]							
6E	CPU0_IIOFFERRHD0[15:8]							
6F	CPU0_IIOFFERRHD0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_IIOFFERRHD1[31:24]							
71	CPU0_IIOFFERRHD1[23:16]							
72	CPU0_IIOFFERRHD1[15:8]							
73	CPU0_IIOFFERRHD1[7:0]							
74	CPU0_IIOFFERRHD2[31:24]							
75	CPU0_IIOFFERRHD2[23:16]							
76	CPU0_IIOFFERRHD2[15:8]							
77	CPU0_IIOFFERRHD2[7:0]							
78	CPU0_IIOFFERRHD3[31:24]							
79	CPU0_IIOFFERRHD3[23:16]							
7A	CPU0_IIOFFERRHD3[15:8]							
7B	CPU0_IIOFFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = 23 or 32 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 06h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
47	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_VTUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_VTUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	CPU1_VTUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
53	CPU1_VTUNCERRSTS[7:0]							
	Reserved	Error	Error	Error	Error	Error	Error	Error
54	CPU1_MIERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_MIERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU1_MIERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	CPU1_MIERRSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Error
58	CPU1_MIFFERRHDR0[31:24]							
59	CPU1_MIFFERRHDR0[23:16]							
5A	CPU1_MIFFERRHDR0[15:8]							
5B	CPU1_MIFFERRHDR0[7:0]							
5C	CPU1_MIFFERRHDR1[31:24]							
5D	CPU1_MIFFERRHDR1[23:16]							
5E	CPU1_MIFFERRHDR1[15:8]							
5F	CPU1_MIFFERRHDR1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_MIFFERRHDR2[31:24]							
61	CPU1_MIFFERRHDR2[23:16]							
62	CPU1_MIFFERRHDR2[15:8]							
63	CPU1_MIFFERRHDR2[7:0]							
64	CPU1_MIFFERRHDR3[31:24]							
65	CPU1_MIFFERRHDR3[23:16]							
66	CPU1_MIFFERRHDR3[15:8]							
67	CPU1_MIFFERRHDR3[7:0]							
68	CPU1_IIOERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU1_IIOERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU1_IIOERRST[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	CPU1_IIOERRST[7:0]							
	Reserved	Error	Error	Error	Reserved	Reserved	Reserved	Reserved
6C	CPU1_IIOFFERRHD0[31:24]							
6D	CPU1_IIOFFERRHD0[23:16]							
6E	CPU1_IIOFFERRHD0[15:8]							
6F	CPU1_IIOFFERRHD0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_IIOFFERRHD1[31:24]							
71	CPU1_IIOFFERRHD1[23:16]							
72	CPU1_IIOFFERRHD1[15:8]							
73	CPU1_IIOFFERRHD1[7:0]							
74	CPU1_IIOFFERRHD2[31:24]							
75	CPU1_IIOFFERRHD2[23:16]							
76	CPU1_IIOFFERRHD2[15:8]							
77	CPU1_IIOFFERRHD2[7:0]							
78	CPU1_IIOFFERRHD3[31:24]							
79	CPU1_IIOFFERRHD3[23:16]							
7A	CPU1_IIOFFERRHD3[15:8]							
7B	CPU1_IIOFFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(6) Byte28 = 40 : DIMM correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 07h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	DIMM error address							
55								
56								
57								
58								
59								
5A	DIMM error description2[63:16]							
5B								
5C								
5D								
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	DIMM error description2[15:0]							
61								
62	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	DIMM error description (MC9) [55:0]							
65								
65								
66								
67								
68								
69								
6A	DIMM error address (MC9)							
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	DIMM error description2 (MC9)							
71								
72								
73								
74								
75								
76								
77								
78	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
79	DIMM error description (MC10) [55:0]							
7A								
7B								
7C								
7D								
7E								
7F								

(7) Byte28 = 41: CPU correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 08h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
57	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
58	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
59	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	IA32_MCi_STATUS[63:0]							
69	(Error description)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	IA32_MCi_ADDR[63:0]							
71	(Error address)							
72								
73								
74								
75								
76								
77								
78	IA32_MCi_MISC[63:0]							
79	(Error description2)							
7A								
7B								
7C								
7D								
7E								
7F								

(8) Byte28 = 42: DIMM correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 09h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	IA32_MC0_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
45	IA32_MC1_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
46	IA32_MC2_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
47	IA32_MC3_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
48	IA32_MC4_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
49	IA32_MC5_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4A	IA32_MC7_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4B	Not used							
4C	IA32_MC9_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4D	IA32_MC10_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4E	IA32_MC11_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4F	IA32_MC12_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	IA32_MC17_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	IA32_MC18_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	IA32_MC19_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC20_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	DIMM error information[63:32] (CPU0's DIMM)							
5D								
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	DIMM error information[31:0] (CPU0's DIMM)							
61								
62								
63								
64	DIMM error address (CPU0's DIMM)							
65								
66								
67								
68								
69								
6A	DIMM error information[63:16] (CPU1's DIMM)							
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	DIMM error information[15:0] (CPU1's DIMM)							
71								
72	DIMM error address (CPU1's DIMM)							
73								
74								
75								
76								
77								
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(9) Byte28 = 43: Controller path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Ah							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_NTB0_PCISTS[15:8]							
45	CPU0_NTB0_PCISTS[7:0]							
46	Not used							
47	Not used							
48	CPU0_NTB0_ROOTSTS[31:24]							
49	CPU0_NTB0_ROOTSTS[23:16]							
4A	CPU0_NTB0_ROOTSTS[15:8]							
4B	CPU0_NTB0_ROOTSTS[7:0]							
4C	CPU0_NTB0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_NTB0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_NTB0_XPUNCERRPTR[15:8]							
4F	CPU0_NTB0_XPUNCERRPTR[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0 NTB0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
51	CPU0 NTB0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0 NTB0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0 NTB0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0 NTB0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0 NTB0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0 NTB0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0 NTB0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0 NTB0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0 NTB0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0 NTB0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0 NTB0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0 NTB0							
5D	CPU0 NTB0_LNKSTS2[7:0]							
5E	CPU0 NTB0_LNERRST[15:8]							
5F	CPU0 NTB0_LNERRST[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_NTB0_ERRSID[31:24]							
61	CPU0_NTB0_ERRSID[23:16]							
62	CPU0_NTB0_ERRSID[15:8]							
63	CPU0_NTB0_ERRSID[7:0]							
64	CPU0_NTB0_HDRLOG0[31:24]							
65	CPU0_NTB0_HDRLOG0[23:16]							
66	CPU0_NTB0_HDRLOG0[15:8]							
67	CPU0_NTB0_HDRLOG0[7:0]							
68	CPU0_NTB0_HDRLOG1[31:24]							
69	CPU0_NTB0_HDRLOG1[23:16]							
6A	CPU0_NTB0_HDRLOG1[15:8]							
6B	CPU0_NTB0_HDRLOG1[7:0]							
6C	CPU0_NTB0_HDRLOG2[31:24]							
6D	CPU0_NTB0_HDRLOG2[23:16]							
6E	CPU0_NTB0_HDRLOG2[15:8]							
6F	CPU0_NTB0_HDRLOG2[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_NTB0_HDRLOG3[31:24]							
71	CPU0_NTB0_HDRLOG3[23:16]							
72	CPU0_NTB0_HDRLOG3[15:8]							
73	CPU0_NTB0_HDRLOG3[7:0]							
74	GCTL_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	GCTL_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	GCTL_CTLSTS2							
	FWSTS	FWSTS	Reserved	Reserved	Reserved	Reserved	Shutdown	Reserved
77	GCTL_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
79	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
7B	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
7D	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
7E	Not used							
7F	Not used							

(10) Byte28 = 44: Controller path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Bh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_NTB1_PCISTS[15:8]							
45	CPU1_NTB1_PCISTS[7:0]							
46	Not used							
47	Not used							
48	CPU1_NTB1_ROOTSTS[31:24]							
49	CPU1_NTB1_ROOTSTS[23:16]							
4A	CPU1_NTB1_ROOTSTS[15:8]							
4B	CPU1_NTB1_ROOTSTS[7:0]							
4C	CPU1_NTB1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU1_NTB1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU1_NTB1_XPUNCERRPTR[15:8]							
4F	CPU1_NTB1_XPUNCERRPTR[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
51	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU1_NTB1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU1_NTB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU1_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU1_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU1_NTB1_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU1_NTB1_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU1_NTB1_LNKSTS2[15:8]							
5D	CPU1_NTB1_LNKSTS2[7:0]							
5E	CPU1_NTB1_LNERRST[15:8]							
5F	CPU1_NTB1_LNERRST[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_NTB1_ERRSID[31:24]							
61	CPU1_NTB1_ERRSID[23:16]							
62	CPU1_NTB1_ERRSID[15:8]							
63	CPU1_NTB1_ERRSID[7:0]							
64	CPU1_NTB1_HDRLOG0[31:24]							
65	CPU1_NTB1_HDRLOG0[23:16]							
66	CPU1_NTB1_HDRLOG0[15:8]							
67	CPU1_NTB1_HDRLOG0[7:0]							
68	CPU1_NTB1_HDRLOG1[31:24]							
69	CPU1_NTB1_HDRLOG1[23:16]							
6A	CPU1_NTB1_HDRLOG1[15:8]							
6B	CPU1_NTB1_HDRLOG1[7:0]							
6C	CPU1_NTB1_HDRLOG2[31:24]							
6D	CPU1_NTB1_HDRLOG2[23:16]							
6E	CPU1_NTB1_HDRLOG2[15:8]							
6F	CPU1_NTB1_HDRLOG2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_NTB1_HDRLOG3[31:24]							
71	CPU1_NTB1_HDRLOG3[23:16]							
72	CPU1_NTB1_HDRLOG3[15:8]							
73	CPU1_NTB1_HDRLOG3[7:0]							
74	GCTL_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	GCTL_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
76	GCTL_CTLSTS2							
	FWSTS	FWSTS	Reserved	Reserved	Reserved	Reserved	Shutdown	Reserved
77	GCTL_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
78	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
79	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
7B	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7C	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
7D	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
7E	Not used							
7F	Not used							

(11) Byte28 = 45 or 50: CPU internal data path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Ch							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_CB00_DMAUNCERRPTR[7:0]							
45	Not used							
46	Not used							
47	Not used							
48	CPU0_CB00_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_CB00_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	CPU0_CB00_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
4B	CPU0_CB00_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
4C	CPU0_CB00_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_CB00_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
4E	CPU0_CB00_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
4F	CPU0_CB00_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_CB01_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_CB01_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
52	CPU0_CB01_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
53	CPU0_CB01_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
54	CPU0_CB02_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_CB02_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
56	CPU0_CB02_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
57	CPU0_CB02_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
58	CPU0_CB03_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU0_CB03_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5A	CPU0_CB03_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5B	CPU0_CB03_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
5C	CPU0_CB04_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_CB04_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5E	CPU0_CB04_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5F	CPU0_CB04_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_CB05_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	CPU0_CB05_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
62	CPU0_CB05_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
63	CPU0_CB05_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
64	CPU0_CB06_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU0_CB06_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
66	CPU0_CB06_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
67	CPU0_CB06_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
68	CPU0_CB07_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_CB07_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
6A	CPU0_CB07_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
6B	CPU0_CB07_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
6C	CPU0_CB00_CHANSTS_0[31:24]							
6D	CPU0_CB00_CHANSTS_0[23:16]							
6E	CPU0_CB00_CHANSTS_0[15:8]							
6F	CPU0_CB00_CHANSTS_0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_CB00_CHANSTS_1[31:24]							
71	CPU0_CB00_CHANSTS_1[23:16]							
72	CPU0_CB00_CHANSTS_1[15:8]							
73	CPU0_CB00_CHANSTS_1[7:0]							
74	CPU0_CB01_CHANSTS_0[31:24]							
75	CPU0_CB01_CHANSTS_0[23:16]							
76	CPU0_CB01_CHANSTS_0[15:8]							
77	CPU0_CB01_CHANSTS_0[7:0]							
78	CPU0_CB01_CHANSTS_1[31:24]							
79	CPU0_CB01_CHANSTS_1[23:16]							
7A	CPU0_CB01_CHANSTS_1[15:8]							
7B	CPU0_CB01_CHANSTS_1[7:0]							
7C	CPU0_CB02_CHANSTS_0[31:24]							
7D	CPU0_CB02_CHANSTS_0[23:16]							
7E	CPU0_CB02_CHANSTS_0[15:8]							
7F	CPU0_CB02_CHANSTS_0[7:0]							

(12) Byte28 = 46 or 51: CPU internal data path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Dh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_CB10_DMAUNCERRPTR[7:0]							
45	Not used							
46	Not used							
47	Not used							
48	CPU1_CB10_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_CB10_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	CPU1_CB10_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
4B	CPU1_CB10_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
4C	CPU1_CB10_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU1_CB10_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
4E	CPU1_CB10_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
4F	CPU1_CB10_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_CB11_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_CB11_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
52	CPU1_CB11_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
53	CPU1_CB11_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
54	CPU1_CB12_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_CB12_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
56	CPU1_CB12_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
57	CPU1_CB12_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
58	CPU1_CB13_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_CB13_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5A	CPU1_CB13_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5B	CPU1_CB13_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
5C	CPU1_CB14_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_CB14_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5E	CPU1_CB14_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5F	CPU1_CB14_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_CB15_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	CPU1_CB15_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
62	CPU1_CB15_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
63	CPU1_CB15_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
64	CPU1_CB16_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU1_CB16_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
66	CPU1_CB16_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
67	CPU1_CB16_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
68	CPU1_CB17_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU1_CB17_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
6A	CPU1_CB17_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
6B	CPU1_CB17_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
6C	CPU1_CB10_CHANSTS_0[31:24]							
6D	CPU1_CB10_CHANSTS_0[23:16]							
6E	CPU1_CB10_CHANSTS_0[15:8]							
6F	CPU1_CB10_CHANSTS_0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_CB10_CHANSTS_1[31:24]							
71	CPU1_CB10_CHANSTS_1[23:16]							
72	CPU1_CB10_CHANSTS_1[15:8]							
73	CPU1_CB10_CHANSTS_1[7:0]							
74	CPU1_CB11_CHANSTS_0[31:24]							
75	CPU1_CB11_CHANSTS_0[23:16]							
76	CPU1_CB11_CHANSTS_0[15:8]							
77	CPU1_CB11_CHANSTS_0[7:0]							
78	CPU1_CB11_CHANSTS_1[31:24]							
79	CPU1_CB11_CHANSTS_1[23:16]							
7A	CPU1_CB11_CHANSTS_1[15:8]							
7B	CPU1_CB11_CHANSTS_1[7:0]							
7C	CPU1_CB12_CHANSTS_0[31:24]							
7D	CPU1_CB12_CHANSTS_0[23:16]							
7E	CPU1_CB12_CHANSTS_0[15:8]							
7F	CPU1_CB12_CHANSTS_0[7:0]							

(13) Byte28 = 47: LAN device error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Eh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	Not used							
47	Not used							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_IOPK_MNG_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
75	GCTL_IOPK_MNG_CTL							
76	GCTL_R_IO_MNG_TYPE							
77	GCTL_MATED_CTLREQ_STS							
	Not used	Not used	Not used	Not used	Power alarm		Power ready	
78	GCTL_IOPKRST_MASK0							
79	GCTL_IOPKRST_MASK1							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(14) Byte28 = 48: Board management device error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Fh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_GCTL_DIAG_ENABLE							
75	GCTL_GCTLERR							
76	GCTL_FBRIDGEERR							
77	GCTL_PCI_ERRSTS							
78	GCTL_XR_ERRSTS							
79	GCTL_RAM_ERRSTS							
7A	GCTL_FBRG_IF_ERRSTS							
7B	GCTL_MATED_GCTL_IF_ERRSTS							
7C	GCTL_GCTL_RAM_ECC_ERRSTS							
7D	GCTL_PCI_RAM_ERRADR_LT0							
7E	GCTL_PCI_RAM_ERRADR_LT1							
7F	GCTL_PCI_CORRECT_CNT							

(15) Byte28 = 49 or 4D : Board management device error

	0	1	2	3	4	5	6	7
40	Free area format number = 10h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_GCTL_DIAG_ENABLE							
75	GCTL_GCTLERR							
76	GCTL_FBRIDGEERR							
77	GCTL_PCI_ERRSTS							
78	GCTL_XR_ERRSTS							
79	GCTL_RAM_ERRSTS							
7A	GCTL_FBRG_IF_ERRSTS							
7B	GCTL_MATED_GCTL_IF_ERRSTS							
7C	GCTL_GCTL_RAM_ECC_ERRSTS							
7D	GCTL_PCI_RAM_ERRADR_LT0							
7E	GCTL_PCI_RAM_ERRADR_LT1							
7F	GCTL_PCI_CORRECT_CNT							

(16) Byte28 = 4A: CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 11h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU0_IRPP0ERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_IRPP0ERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_IRPP0ERRST[15:8]							
	Reserved	Error	Error	Reserved	Reserved	Error	Reserved	Reserved
57	CPU0_IRPP0ERRST[7:0]							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Reserved
58	CPU0_IRPP0FFERRHD0[31:24]							
59	CPU0_IRPP0FFERRHD0[23:16]							
5A	CPU0_IRPP0FFERRHD0[15:8]							
5B	CPU0_IRPP0FFERRHD0[7:0]							
5C	CPU0_IRPP0FFERRHD1[31:24]							
5D	CPU0_IRPP0FFERRHD1[23:16]							
5E	CPU0_IRPP0FFERRHD1[15:8]							
5F	CPU0_IRPP0FFERRHD1[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_IRPP0FFERRHD2[31:24]							
61	CPU0_IRPP0FFERRHD2[23:16]							
62	CPU0_IRPP0FFERRHD2[15:8]							
63	CPU0_IRPP0FFERRHD2[7:0]							
64	CPU0_IRPP0FFERRHD3[31:24]							
65	CPU0_IRPP0FFERRHD3[23:16]							
66	CPU0_IRPP0FFERRHD3[15:8]							
67	CPU0_IRPP0FFERRHD3[7:0]							
68	CPU0_IRPP1ERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_IRPP1ERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU0_IRPP1ERRST[15:8]							
	Reserved	Error	Error	Reserved	Reserved	Error	Reserved	Reserved
6B	CPU0_IRPP1ERRST[7:0]							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Reserved
6C	CPU0_IRPP1FFERRHD0[31:24]							
6D	CPU0_IRPP1FFERRHD0[23:16]							
6E	CPU0_IRPP1FFERRHD0[15:8]							
6F	CPU0_IRPP1FFERRHD0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_IRPP1FFERRHD1[31:24]							
71	CPU0_IRPP1FFERRHD1[23:16]							
72	CPU0_IRPP1FFERRHD1[15:8]							
73	CPU0_IRPP1FFERRHD1[7:0]							
74	CPU0_IRPP1FFERRHD2[31:24]							
75	CPU0_IRPP1FFERRHD2[23:16]							
76	CPU0_IRPP1FFERRHD2[15:8]							
77	CPU0_IRPP1FFERRHD2[7:0]							
78	CPU0_IRPP1FFERRHD3[31:24]							
79	CPU0_IRPP1FFERRHD3[23:16]							
7A	CPU0_IRPP1FFERRHD3[15:8]							
7B	CPU0_IRPP1FFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(17) Byte28 = 4B or 4C : CPU internal error

	0	1	2	3	4	5	6	7
40	Free area format number = 12h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
51	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
56	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
57	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
58	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
5A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
5B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
5C	IA32_MC0_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC1_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	IA32_MC2_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5F	IA32_MC3_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	IA32_MC4_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC5_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC9_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC10_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	IA32_MC11_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
65	IA32_MC12_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
66	Not used							
67	Not used							
68	IA32_MC17_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
69	IA32_MC18_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6A	IA32_MC19_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6B	IA32_MC20_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(18) Byte28 = 4E: Controller Interconnect Path Failure Related Error

	0	1	2	3	4	5	6	7
40	Free area format number = 40h							
41	Not used							
42	Not used							
43	Not used							
44	Controller Interconnect Path Error Count detected by CPU (Own CTL)							
45								
46								
47								
48	Controller Interconnect Path Error Count detected by CPU (Another CTL)							
49								
4A								
4B								
4C	Controller Interconnect Path Error Count detected by LSI (Own CTL)							
4D								
4E								
4F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Controller Interconnect Path Error Count detected by LSI (Another CTL)							
51								
52								
53								
54	Accumulated Controller Interconnect Path Error Count detected by CPU (Own CTL)							
55								
56								
57								
58	Accumulated Controller Interconnect Path Error Count detected by CPU (Another CTL)							
59								
5A								
5B								
5C	Accumulated Controller Interconnect Path Error Count detected by LSI (Own CTL)							
5D								
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Accumulated Controller Interconnect Path Error Count detected by LSI (Another CTL)							
61								
62								
63								
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(19) Byte28 = 4F : SPI Flash Data Access Error

	0	1	2	3	4	5	6	7
40	Free area format number = 41h							
41	0x00							
42	Not used							
43	Not used							
44	Hardware Sequencing Flash Status[15:08]							
45	Hardware Sequencing Flash Status[07:06]							
46	Hardware Sequencing Flash Control[15:08]							
47	Hardware Sequencing Flash Control[07:06]							
48	Flash Address[31:24]							
49	Flash Address[23:16]							
4A	Flash Address[15:08]							
4B	Flash Address[07:00]							
4C	Software Sequencing Flash Status & Control[31:24]							
4D	Software Sequencing Flash Status & Control[23:16]							
4E	Software Sequencing Flash Status & Control[15:08]							
4F	Software Sequencing Flash Status & Control[07:06]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	FDATA[00] [31:24]							
51	FDATA[00] [23:16]							
52	FDATA[00] [15:08]							
53	FDATA[00] [07:00]							
54	FDATA[01] [31:24]							
55	FDATA[01] [23:16]							
56	FDATA[01] [15:08]							
57	FDATA[01] [07:00]							
58	FDATA[02] [31:24]							
59	FDATA[02] [23:16]							
5A	FDATA[02] [15:08]							
5B	FDATA[02] [07:00]							
5C	FDATA[03] [31:24]							
5D	FDATA[03] [23:16]							
5E	FDATA[03] [15:08]							
5F	FDATA[03] [07:00]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	FDATA[04] [31:24]							
61	FDATA[04] [23:16]							
62	FDATA[04] [15:08]							
63	FDATA[04] [07:00]							
64	FDATA[05] [31:24]							
65	FDATA[05] [23:16]							
66	FDATA[05] [15:08]							
67	FDATA[05] [07:00]							
68	FDATA[06] [31:24]							
69	FDATA[06] [23:16]							
6A	FDATA[06] [15:08]							
6B	FDATA[06] [07:00]							
6C	FDATA[07] [31:24]							
6D	FDATA[07] [23:16]							
6E	FDATA[07] [15:08]							
6F	FDATA[07] [07:00]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	FDATA[08] [31:24]							
71	FDATA[08] [23:16]							
72	FDATA[08] [15:08]							
73	FDATA[08] [07:00]							
74	FDATA[09] [31:24]							
75	FDATA[09] [23:16]							
76	FDATA[09] [15:08]							
77	FDATA[09] [07:00]							
78	FDATA[10] [31:24]							
79	FDATA[10] [23:16]							
7A	FDATA[10] [15:08]							
7B	FDATA[10] [07:00]							
7C	FDATA[11] [31:24]							
7D	FDATA[11] [23:16]							
7E	FDATA[11] [15:08]							
7F	FDATA[11] [07:00]							

(20) Byte28 = 53 or 60: IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 27h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA1_PCISTS[15:8]							
4D	CPU0_PA1_PCISTS[7:0]							
4E	CPU0_PA1_SECSTS[15:8]							
4F	CPU0_PA1_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA1_ROOTSTS[31:24]							
51	CPU0_PA1_ROOTSTS[23:16]							
52	CPU0_PA1_ROOTSTS[15:8]							
53	CPU0_PA1_ROOTSTS[7:0]							
54	CPU0_PA1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA1_XPUNCERRPTR[15:8]							
57	CPU0_PA1_XPUNCERRPTR[7:0]							
58	CPU0_PA1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA1_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA1_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA1_LNKSTS2[15:8]							
65	CPU0_PA1_LNKSTS2[7:0]							
66	CPU0_PA1_LNERRST[15:8]							
67	CPU0_PA1_LNERRST[7:0]							
68	CPU0_PA1_ERRSID[31:24]							
69	CPU0_PA1_ERRSID[23:16]							
6A	CPU0_PA1_ERRSID[15:8]							
6B	CPU0_PA1_ERRSID[7:0]							
6C	CPU0_PA1_HDRLOG0[31:24]							
6D	CPU0_PA1_HDRLOG0[23:16]							
6E	CPU0_PA1_HDRLOG0[15:8]							
6F	CPU0_PA1_HDRLOG0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA1_HDRLOG1[31:24]							
71	CPU0_PA1_HDRLOG1[23:16]							
72	CPU0_PA1_HDRLOG1[15:8]							
73	CPU0_PA1_HDRLOG1[7:0]							
74	CPU0_PA1_HDRLOG2[31:24]							
75	CPU0_PA1_HDRLOG2[23:16]							
76	CPU0_PA1_HDRLOG2[15:8]							
77	CPU0_PA1_HDRLOG2[7:0]							
78	CPU0_PA1_HDRLOG3[31:24]							
79	CPU0_PA1_HDRLOG3[23:16]							
7A	CPU0_PA1_HDRLOG3[15:8]							
7B	CPU0_PA1_HDRLOG3[7:0]							
7C	GCTL_IOPK_2_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_2_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_2_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(21) Byte28 = 54 or 61 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 28h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA2_PCISTS[15:8]							
4D	CPU0_PA2_PCISTS[7:0]							
4E	CPU0_PA2_SECSTS[15:8]							
4F	CPU0_PA2_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA2_ROOTSTS[31:24]							
51	CPU0_PA2_ROOTSTS[23:16]							
52	CPU0_PA2_ROOTSTS[15:8]							
53	CPU0_PA2_ROOTSTS[7:0]							
54	CPU0_PA2_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA2_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA2_XPUNCERRPTR[15:8]							
57	CPU0_PA2_XPUNCERRPTR[7:0]							
58	CPU0_PA2_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA2_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA2_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA2_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA2_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA2_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA2_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA2_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA2_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA2_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA2_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA2_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA2_LNKSTS2[15:8]							
65	CPU0_PA2_LNKSTS2[7:0]							
66	CPU0_PA2_LNERRST[15:8]							
67	CPU0_PA2_LNERRST[7:0]							
68	CPU0_PA2_ERRSID[31:24]							
69	CPU0_PA2_ERRSID[23:16]							
6A	CPU0_PA2_ERRSID[15:8]							
6B	CPU0_PA2_ERRSID[7:0]							
6C	CPU0_PA2_HDRLOG0[31:24]							
6D	CPU0_PA2_HDRLOG0[23:16]							
6E	CPU0_PA2_HDRLOG0[15:8]							
6F	CPU0_PA2_HDRLOG0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA2_HDRLOG1[31:24]							
71	CPU0_PA2_HDRLOG1[23:16]							
72	CPU0_PA2_HDRLOG1[15:8]							
73	CPU0_PA2_HDRLOG1[7:0]							
74	CPU0_PA2_HDRLOG2[31:24]							
75	CPU0_PA2_HDRLOG2[23:16]							
76	CPU0_PA2_HDRLOG2[15:8]							
77	CPU0_PA2_HDRLOG2[7:0]							
78	CPU0_PA2_HDRLOG3[31:24]							
79	CPU0_PA2_HDRLOG3[23:16]							
7A	CPU0_PA2_HDRLOG3[15:8]							
7B	CPU0_PA2_HDRLOG3[7:0]							
7C	GCTL_IOPK_1_STS or GCTL_IOPK_6_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_1_CTL or GCTL_IOPK_6_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_1_TYPE or GCTL_R_IO_6_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(22) Byte28 = 55 or 62: IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 29h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA3_PCISTS[15:8]							
4D	CPU0_PA3_PCISTS[7:0]							
4E	CPU0_PA3_SECSTS[15:8]							
4F	CPU0_PA3_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA3_ROOTSTS[31:24]							
51	CPU0_PA3_ROOTSTS[23:16]							
52	CPU0_PA3_ROOTSTS[15:8]							
53	CPU0_PA3_ROOTSTS[7:0]							
54	CPU0_PA3_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA3_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA3_XPUNCERRPTR[15:8]							
57	CPU0_PA3_XPUNCERRPTR[7:0]							
58	CPU0_PA3_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA3_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA3_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA3_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA3_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA3_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA3_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA3_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA3_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA3_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA3_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA3_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA3_LNKSTS2[15:8]							
65	CPU0_PA3_LNKSTS2[7:0]							
66	CPU0_PA3_LNERRST[15:8]							
67	CPU0_PA3_LNERRST[7:0]							
68	CPU0_PA3_ERRSID[31:24]							
69	CPU0_PA3_ERRSID[23:16]							
6A	CPU0_PA3_ERRSID[15:8]							
6B	CPU0_PA3_ERRSID[7:0]							
6C	CPU0_PA3_HDRLOG0[31:24]							
6D	CPU0_PA3_HDRLOG0[23:16]							
6E	CPU0_PA3_HDRLOG0[15:8]							
6F	CPU0_PA3_HDRLOG0[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA3_HDRLOG1[31:24]							
71	CPU0_PA3_HDRLOG1[23:16]							
72	CPU0_PA3_HDRLOG1[15:8]							
73	CPU0_PA3_HDRLOG1[7:0]							
74	CPU0_PA3_HDRLOG2[31:24]							
75	CPU0_PA3_HDRLOG2[23:16]							
76	CPU0_PA3_HDRLOG2[15:8]							
77	CPU0_PA3_HDRLOG2[7:0]							
78	CPU0_PA3_HDRLOG3[31:24]							
79	CPU0_PA3_HDRLOG3[23:16]							
7A	CPU0_PA3_HDRLOG3[15:8]							
7B	CPU0_PA3_HDRLOG3[7:0]							
7C	GCTL_IOPK_3_STS or GCTL_IOPK_5_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_3_CTL or GCTL_IOPK_5_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_3_TYPE or GCTL_R_IO_5_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(23) Byte28 = 56 or 63 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Ah							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA5_PCISTS[15:8]							
4D	CPU0_PA5_PCISTS[7:0]							
4E	CPU0_PA5_SECSTS[15:8]							
4F	CPU0_PA5_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA5_ROOTSTS[31:24]							
51	CPU0_PA5_ROOTSTS[23:16]							
52	CPU0_PA5_ROOTSTS[15:8]							
53	CPU0_PA5_ROOTSTS[7:0]							
54	CPU0_PA5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA5_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA5_XPUNCERRPTR[15:8]							
57	CPU0_PA5_XPUNCERRPTR[7:0]							
58	CPU0_PA5_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA5_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA5_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA5_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA5_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA5_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA5_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA5_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA5_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA5_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA5_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA5_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA5_LNKSTS2[15:8]							
65	CPU0_PA5_LNKSTS2[7:0]							
66	CPU0_PA5_LNERRST[15:8]							
67	CPU0_PA5_LNERRST[7:0]							
68	CPU0_PA5_ERRSID[31:24]							
69	CPU0_PA5_ERRSID[23:16]							
6A	CPU0_PA5_ERRSID[15:8]							
6B	CPU0_PA5_ERRSID[7:0]							
6C	CPU0_PA5_HDRLOG0[31:24]							
6D	CPU0_PA5_HDRLOG0[23:16]							
6E	CPU0_PA5_HDRLOG0[15:8]							
6F	CPU0_PA5_HDRLOG0[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU0_PA5_HDRLOG1[31:24]							
71	CPU0_PA5_HDRLOG1[23:16]							
72	CPU0_PA5_HDRLOG1[15:8]							
73	CPU0_PA5_HDRLOG1[7:0]							
74	CPU0_PA5_HDRLOG2[31:24]							
75	CPU0_PA5_HDRLOG2[23:16]							
76	CPU0_PA5_HDRLOG2[15:8]							
77	CPU0_PA5_HDRLOG2[7:0]							
78	CPU0_PA5_HDRLOG3[31:24]							
79	CPU0_PA5_HDRLOG3[23:16]							
7A	CPU0_PA5_HDRLOG3[15:8]							
7B	CPU0_PA5_HDRLOG3[7:0]							
7C	GCTL_IOPK_1_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_1_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_1_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(24) Byte28 = 57 or 64: IO board path erro

	0	1	2	3	4	5	6	7
40	Free area format number = 2Bh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB1_PCISTS[15:8]							
4D	CPU1_PB1_PCISTS[7:0]							
4E	CPU1_PB1_SECSTS[15:8]							
4F	CPU1_PB1_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_PB1_ROOTSTS[31:24]							
51	CPU1_PB1_ROOTSTS[23:16]							
52	CPU1_PB1_ROOTSTS[15:8]							
53	CPU1_PB1_ROOTSTS[7:0]							
54	CPU1_PB1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB1_XPUNCERRPTR[15:8]							
57	CPU1_PB1_XPUNCERRPTR[7:0]							
58	CPU1_PB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_PB1_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB1_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB1_LNKSTS2[15:8]							
65	CPU1_PB1_LNKSTS2[7:0]							
66	CPU1_PB1_LNERRST[15:8]							
67	CPU1_PB1_LNERRST[7:0]							
68	CPU1_PB1_ERRSID[31:24]							
69	CPU1_PB1_ERRSID[23:16]							
6A	CPU1_PB1_ERRSID[15:8]							
6B	CPU1_PB1_ERRSID[7:0]							
6C	CPU1_PB1_HDRLOG0[31:24]							
6D	CPU1_PB1_HDRLOG0[23:16]							
6E	CPU1_PB1_HDRLOG0[15:8]							
6F	CPU1_PB1_HDRLOG0[7:0]							

(To be continued)

SSBLOG05-1810

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_PB1_HDRLOG1[31:24]							
71	CPU1_PB1_HDRLOG1[23:16]							
72	CPU1_PB1_HDRLOG1[15:8]							
73	CPU1_PB1_HDRLOG1[7:0]							
74	CPU1_PB1_HDRLOG2[31:24]							
75	CPU1_PB1_HDRLOG2[23:16]							
76	CPU1_PB1_HDRLOG2[15:8]							
77	CPU1_PB1_HDRLOG2[7:0]							
78	CPU1_PB1_HDRLOG3[31:24]							
79	CPU1_PB1_HDRLOG3[23:16]							
7A	CPU1_PB1_HDRLOG3[15:8]							
7B	CPU1_PB1_HDRLOG3[7:0]							
7C	GCTL_IOPK_4_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_4_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_4_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(25) Byte28 = 58 or 65: IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Ch							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB2_PCISTS[15:8]							
4D	CPU1_PB2_PCISTS[7:0]							
4E	CPU1_PB2_SECSTS[15:8]							
4F	CPU1_PB2_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_PB2_ROOTSTS[31:24]							
51	CPU1_PB2_ROOTSTS[23:16]							
52	CPU1_PB2_ROOTSTS[15:8]							
53	CPU1_PB2_ROOTSTS[7:0]							
54	CPU1_PB2_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB2_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB2_XPUNCERRPTR[15:8]							
57	CPU1_PB2_XPUNCERRPTR[7:0]							
58	CPU1_PB2_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB2_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB2_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB2_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB2_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB2_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB2_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB2_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_PB2_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB2_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB2_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB2_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB2_LNKSTS2[15:8]							
65	CPU1_PB2_LNKSTS2[7:0]							
66	CPU1_PB2_LNERRST[15:8]							
67	CPU1_PB2_LNERRST[7:0]							
68	CPU1_PB2_ERRSID[31:24]							
69	CPU1_PB2_ERRSID[23:16]							
6A	CPU1_PB2_ERRSID[15:8]							
6B	CPU1_PB2_ERRSID[7:0]							
6C	CPU1_PB2_HDRLOG0[31:24]							
6D	CPU1_PB2_HDRLOG0[23:16]							
6E	CPU1_PB2_HDRLOG0[15:8]							
6F	CPU1_PB2_HDRLOG0[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_PB2_HDRLOG1[31:24]							
71	CPU1_PB2_HDRLOG1[23:16]							
72	CPU1_PB2_HDRLOG1[15:8]							
73	CPU1_PB2_HDRLOG1[7:0]							
74	CPU1_PB2_HDRLOG2[31:24]							
75	CPU1_PB2_HDRLOG2[23:16]							
76	CPU1_PB2_HDRLOG2[15:8]							
77	CPU1_PB2_HDRLOG2[7:0]							
78	CPU1_PB2_HDRLOG3[31:24]							
79	CPU1_PB2_HDRLOG3[23:16]							
7A	CPU1_PB2_HDRLOG3[15:8]							
7B	CPU1_PB2_HDRLOG3[7:0]							
7C	GCTL_IOPK_8_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_8_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_8_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(26) Byte28 = 59 or 66 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Dh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB3_PCISTS[15:8]							
4D	CPU1_PB3_PCISTS[7:0]							
4E	CPU1_PB3_SECSTS[15:8]							
4F	CPU1_PB3_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_PB3_ROOTSTS[31:24]							
51	CPU1_PB3_ROOTSTS[23:16]							
52	CPU1_PB3_ROOTSTS[15:8]							
53	CPU1_PB3_ROOTSTS[7:0]							
54	CPU1_PB3_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB3_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB3_XPUNCERRPTR[15:8]							
57	CPU1_PB3_XPUNCERRPTR[7:0]							
58	CPU1_PB3_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB3_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB3_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB3_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB3_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB3_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB3_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB3_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_PB3_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB3_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB3_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB3_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB3_LNKSTS2[15:8]							
65	CPU1_PB3_LNKSTS2[7:0]							
66	CPU1_PB3_LNERRST[15:8]							
67	CPU1_PB3_LNERRST[7:0]							
68	CPU1_PB3_ERRSID[31:24]							
69	CPU1_PB3_ERRSID[23:16]							
6A	CPU1_PB3_ERRSID[15:8]							
6B	CPU1_PB3_ERRSID[7:0]							
6C	CPU1_PB3_HDRLOG0[31:24]							
6D	CPU1_PB3_HDRLOG0[23:16]							
6E	CPU1_PB3_HDRLOG0[15:8]							
6F	CPU1_PB3_HDRLOG0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_PB3_HDRLOG1[31:24]							
71	CPU1_PB3_HDRLOG1[23:16]							
72	CPU1_PB3_HDRLOG1[15:8]							
73	CPU1_PB3_HDRLOG1[7:0]							
74	CPU1_PB3_HDRLOG2[31:24]							
75	CPU1_PB3_HDRLOG2[23:16]							
76	CPU1_PB3_HDRLOG2[15:8]							
77	CPU1_PB3_HDRLOG2[7:0]							
78	CPU1_PB3_HDRLOG3[31:24]							
79	CPU1_PB3_HDRLOG3[23:16]							
7A	CPU1_PB3_HDRLOG3[15:8]							
7B	CPU1_PB3_HDRLOG3[7:0]							
7C	GCTL_IOPK_7_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_7_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_7_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(27) Byte28 = 5A or 67: IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Eh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB5_PCISTS[15:8]							
4D	CPU1_PB5_PCISTS[7:0]							
4E	CPU1_PB5_SECSTS[15:8]							
4F	CPU1_PB5_SECSTS[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU1_PB5_ROOTSTS[31:24]							
51	CPU1_PB5_ROOTSTS[23:16]							
52	CPU1_PB5_ROOTSTS[15:8]							
53	CPU1_PB5_ROOTSTS[7:0]							
54	CPU1_PB5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB5_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB5_XPUNCERRPTR[15:8]							
57	CPU1_PB5_XPUNCERRPTR[7:0]							
58	CPU1_PB5_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB5_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB5_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB5_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB5_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB5_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB5_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB5_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU1_PB5_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB5_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB5_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB5_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB5_LNKSTS2[15:8]							
65	CPU1_PB5_LNKSTS2[7:0]							
66	CPU1_PB5_LNERRST[15:8]							
67	CPU1_PB5_LNERRST[7:0]							
68	CPU1_PB5_ERRSID[31:24]							
69	CPU1_PB5_ERRSID[23:16]							
6A	CPU1_PB5_ERRSID[15:8]							
6B	CPU1_PB5_ERRSID[7:0]							
6C	CPU1_PB5_HDRLOG0[31:24]							
6D	CPU1_PB5_HDRLOG0[23:16]							
6E	CPU1_PB5_HDRLOG0[15:8]							
6F	CPU1_PB5_HDRLOG0[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_PB5_HDRLOG1[31:24]							
71	CPU1_PB5_HDRLOG1[23:16]							
72	CPU1_PB5_HDRLOG1[15:8]							
73	CPU1_PB5_HDRLOG1[7:0]							
74	CPU1_PB5_HDRLOG2[31:24]							
75	CPU1_PB5_HDRLOG2[23:16]							
76	CPU1_PB5_HDRLOG2[15:8]							
77	CPU1_PB5_HDRLOG2[7:0]							
78	CPU1_PB5_HDRLOG3[31:24]							
79	CPU1_PB5_HDRLOG3[23:16]							
7A	CPU1_PB5_HDRLOG3[15:8]							
7B	CPU1_PB5_HDRLOG3[7:0]							
7C	GCTL_IOPK_3_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_3_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_3_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(28) Byte28 = 68: IO board power failure or initialize error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Fh							
41	Not used							
42	GCTL_R_IO_x_TYPE							
43	GCTL_IOPK_x_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	Not used	Presence
44	GCTL_R_IO_x_ERRLOG_0[31:24]							
45	GCTL_R_IO_x_ERRLOG_0[23:16]							
46	GCTL_R_IO_x_ERRLOG_0[15:8]							
47	GCTL_R_IO_x_ERRLOG_0[7:0]							
48	GCTL_R_IO_x_ERRLOG_4[31:24]							
49	GCTL_R_IO_x_ERRLOG_4[23:16]							
4A	GCTL_R_IO_x_ERRLOG_4[15:8]							
4B	GCTL_R_IO_x_ERRLOG_4[7:0]							
4C	GCTL_R_IO_x_ERRLOG_8[31:24]							
4D	GCTL_R_IO_x_ERRLOG_8[23:16]							
4E	GCTL_R_IO_x_ERRLOG_8[15:8]							
4F	GCTL_R_IO_x_ERRLOG_8[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	GCTL_R_IO_x_ERRLOG_10[31:24]							
51	GCTL_R_IO_x_ERRLOG_10[23:16]							
52	GCTL_R_IO_x_ERRLOG_10[15:8]							
53	GCTL_R_IO_x_ERRLOG_10[7:0]							
54	GCTL_R_IO_x_ERRLOG_14[31:24]							
55	GCTL_R_IO_x_ERRLOG_14[23:16]							
56	GCTL_R_IO_x_ERRLOG_14[15:8]							
57	GCTL_R_IO_x_ERRLOG_14[7:0]							
58	GCTL_R_IO_x_ERRLOG_18[31:24]							
59	GCTL_R_IO_x_ERRLOG_18[23:16]							
5A	GCTL_R_IO_x_ERRLOG_18[15:8]							
5B	GCTL_R_IO_x_ERRLOG_18[7:0]							
5C	GCTL_R_IO_x_ERRLOG_1C[31:24]							
5D	GCTL_R_IO_x_ERRLOG_1C[23:16]							
5E	GCTL_R_IO_x_ERRLOG_1C[15:8]							
5F	GCTL_R_IO_x_ERRLOG_1C[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	GCTL_R_IO_x_ERRLOG_20[31:24]							
61	GCTL_R_IO_x_ERRLOG_20[23:16]							
62	GCTL_R_IO_x_ERRLOG_20[15:8]							
63	GCTL_R_IO_x_ERRLOG_20[7:0]							
64	GCTL_R_IO_x_ERRLOG_24[31:24]							
65	GCTL_R_IO_x_ERRLOG_24[23:16]							
66	GCTL_R_IO_x_ERRLOG_24[15:8]							
67	GCTL_R_IO_x_ERRLOG_24[7:0]							
68	GCTL_R_IO_x_ERRLOG_28[31:24]							
69	GCTL_R_IO_x_ERRLOG_28[23:16]							
6A	GCTL_R_IO_x_ERRLOG_28[15:8]							
6B	GCTL_R_IO_x_ERRLOG_28[7:0]							
6C	GCTL_R_IO_x_ERRLOG_2C[31:24]							
6D	GCTL_R_IO_x_ERRLOG_2C[23:16]							
6E	GCTL_R_IO_x_ERRLOG_2C[15:8]							
6F	GCTL_R_IO_x_ERRLOG_2C[7:0]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	GCTL_R_IO_x_ERRLOG_30[31:24]							
71	GCTL_R_IO_x_ERRLOG_30[23:16]							
72	GCTL_R_IO_x_ERRLOG_30[15:8]							
73	GCTL_R_IO_x_ERRLOG_30[7:0]							
74	GCTL_R_IO_x_ERRLOG_34[31:24]							
75	GCTL_R_IO_x_ERRLOG_34[23:16]							
76	GCTL_R_IO_x_ERRLOG_34[15:8]							
77	GCTL_R_IO_x_ERRLOG_34[7:0]							
78	GCTL_R_IO_x_ERRLOG_38[31:24]							
79	GCTL_R_IO_x_ERRLOG_38[23:16]							
7A	GCTL_R_IO_x_ERRLOG_38[15:8]							
7B	GCTL_R_IO_x_ERRLOG_38[7:0]							
7C	GCTL_R_IO_x_ERRLOG_3C[31:24]							
7D	GCTL_R_IO_x_ERRLOG_3C[23:16]							
7E	GCTL_R_IO_x_ERRLOG_3C[15:8]							
7F	GCTL_R_IO_x_ERRLOG_3C[7:0]							

(29) Byte28 = A0: Another CTL error

	0	1	2	3	4	5	6	7
40	Free area format number = 30h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_NTB0_PCISTS[15:8]							
45	CPU0_NTB0_PCISTS[7:0]							
46	Not used							
47	Not used							
48	CPU0_NTB0_ROOTSTS[31:24]							
49	CPU0_NTB0_ROOTSTS[23:16]							
4A	CPU0_NTB0_ROOTSTS[15:8]							
4B	CPU0_NTB0_ROOTSTS[7:0]							
4C	CPU0_NTB0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_NTB0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_NTB0_XPUNCERRPTR[15:8]							
4F	CPU0_NTB0_XPUNCERRPTR[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0 NTB0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
51	CPU0 NTB0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0 NTB0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0 NTB0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0 NTB0_UNCERRSTS [31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0 NTB0_UNCERRSTS [23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0 NTB0_UNCERRSTS [15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0 NTB0_UNCERRSTS [7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0 NTB0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width [9:8]	
59	CPU0 NTB0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5A	CPU0 NTB0_LNKSTS2[15:8]							
5B	CPU0 NTB0_LNKSTS2[7:0]							
5C	CPU0 NTB0_LNERRST[15:8]							
5D	CPU0 NTB0_LNERRST[7:0]							
5E	CPU0 NTB0_ERRSID[31:24]							
5F	CPU0 NTB0_ERRSID[23:16]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_NTB0_ERRSID[15:8]							
61	CPU0_NTB0_ERRSID[7:0]							
62	CPU1_NTB1_PCISTS[15:8]							
63	CPU1_NTB1_PCISTS[7:0]							
64	Not used							
65	Not used							
66	CPU1_NTB1_ROOTSTS[31:24]							
67	CPU1_NTB1_ROOTSTS[23:16]							
68	CPU1_NTB1_ROOTSTS[15:8]							
69	CPU1_NTB1_ROOTSTS[7:0]							
6A	CPU1_NTB1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
6B	CPU1_NTB1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
6C	CPU1_NTB1_XPUNCERRPTR[15:8]							
6D	CPU1_NTB1_XPUNCERRPTR[7:0]							
6E	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
6F	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	CPU1_NTB1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
71	CPU1_NTB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
72	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	CPU0_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
74	CPU0_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
75	CPU0_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
76	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width [9:8]	
77	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
78	CPU1_NTB1_LNKSTS2[15:8]							
79	CPU1_NTB1_LNKSTS2[7:0]							
7A	CPU1_NTB1_LNERRST[31:24]							
7B	CPU1_NTB1_LNERRST[23:16]							
7C	CPU1_NTB1_ERRSID[31:24]							
7D	CPU1_NTB1_ERRSID[23:16]							
7E	CPU1_NTB1_ERRSID[15:8]							
7F	CPU1_NTB1_ERRSID[7:0]							

(30) Byte28 = A1: Another CTL's DIMM error

	0	1	2	3	4	5	6	7
40	Free area format number = 31h							
41	Not used							
42	Not used							
43	Not used							
44	GCTL_R_MATED_DIMM_ERRLOG0[31:0]							
45	(Another CTL's CPU0 DIMM error information MC7[31:0])							
46								
47								
48	GCTL_R_MATED_DIMM_ERRLOG1[31:0]							
49	(Another CTL's CPU0 DIMM error information MC7[63:32])							
4A								
4B								
4C	GCTL_R_MATED_DIMM_ERRLOG2[31:0]							
4D	(Another CTL's CPU0 DIMM error address[31:0])							
4E								
4F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	GCTL_R_MATED_DIMM_ERRLOG3[31:0]							
51	(Another CTL's CPU0 DIMM error address[63:32])							
52								
53								
54	GCTL_R_MATED_DIMM_ERRLOG4[31:0]							
55	(Another CTL's CPU1 DIMM error information MC7[31:0])							
56								
57								
58	GCTL_R_MATED_DIMM_ERRLOG5[31:0]							
59	(Another CTL's CPU1 DIMM error information MC7[63:32])							
5A								
5B								
5C	GCTL_R_MATED_DIMM_ERRLOG6[31:0]							
5D	(Another CTL's CPU1 DIMM error address[31:0])							
5E								
5F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	GCTL_R_MATED_DIMM_ERRLOG7[31:0]							
61	(Another CTL's CPU1 DIMM error address[63:32])							
62								
63								
64	GCTL_R_MATED_DIMM_ERRLOG8[31:0]							
65	(Another CTL's CPU2 DIMM error information MC7[31:0])							
66								
67								
68	GCTL_R_MATED_DIMM_ERRLOG9[31:0]							
69	(Another CTL's CPU2 DIMM error information MC7[63:32])							
6A								
6B								
6C	GCTL_R_MATED_DIMM_ERRLOGA[31:0]							
6D	(Another CTL's CPU2 DIMM error address[31:0])							
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	GCTL_R_MATED_DIMM_ERRLOGB[31:0]							
71	(Another CTL's CPU2 DIMM error address[63:32])							
72								
73								
74	GCTL_R_MATED_DIMM_ERRLOGC[31:0]							
75	(Another CTL's CPU3 DIMM error information MC7[31:0])							
76								
77								
78	GCTL_R_MATED_DIMM_ERRLOGD[31:0]							
79	(Another CTL's CPU3 DIMM error information MC7[63:32])							
7A								
7B								
7C	GCTL_R_MATED_DIMM_ERRLOGE[31:0]							
7D	(Another CTL's CPU3 DIMM error address[31:0])							
7E								
7F								

(31) Byte28 = 70/71/72/73/74/75: IO board path error (Slot J/K/L/M), Board management device error in CHBB

	0	1	2	3	4	5	6	7
40	Free area format number = 32h/33h/34h/35h/36h/37h							
41	Not used							
42	PEX BUSNUM							
43	PEX SECBUS							
44	PEX PCISTS[15:8]							
	Detected Parity Error	Signaled System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
45	PEX PCISTS[7:0]							
	Reserved	Reserved	66MHz Capable	Capability List	Interrupt Status	Reserved	Reserved	Reserved
46	PEX SECSTS[15:8]							
	Detected Parity Error	Received System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
47	PEX SECSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	PEX DEVSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	PEX DEVSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Unsupported Request Detected	Fatal Error Detected	Non-Fatal Error Detected	Correctable Error Detected
4A	PEX LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
4B	PEX LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			
4C	PEX SLOTSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Data Link Layer State Changed
4D	PEX SLOTSTS[7:0]							
	Electromechanical Interlock Status	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Command Completed	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Power Fault Detected	Attention Button Pressed
4E	PEX LNKSTS2[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4F	PEX LNKSTS2[7:0]							
	Reserved	Reserved	Link Equalization Request	Equalization Phase 3 Successful	Equalization Phase 2 Successful	Equalization Phase 1 Successful	Equalization Complete	Current De-Emphasis Level

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	PEX LANE DETECT STATUS[15:8]							
	Receiver Detected on Lane							
51	PEX LANE DETECT STATUS[7:0]							
	Receiver Detected on Lane							
52	PEX GEN3 FRAME ERROR STATUS[15:8]							
	Frame Error 15 Status	Frame Error 14 Status	Last TLP or DLLP Has No LIDL or End of Data Stream	Not All Subsequent Lanes Have LIDL or End of Data Stream when LIDL Status	Ordered-Set Not the Same on All Lanes Status	Invalid Skip Ordered-Set Status	FTS Not Followed by Data Block Status	Ordered-Set Block Not Followed by Data Block Status
53	PEX GEN3 FRAME ERROR STATUS[7:0]							
	End of Data Stream Not in Last DWord of Data Block Status	End of Data Stream Not Followed by Ordered-Set Block Status	Length CRC/ Parity Error Status	EDB Not following TLP Status	Second Symbol of DLLP Not Ach Status	Bad Framing Symbol in Data Block Status	Sync Header Not Same on All Lanes Status	Invalid Sync Header Status
54	PEX DEVSPCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Egress Parity Error Detected	Ingress Parity Error Detected	Ingress Link List RAM 2-Bit Error Detected	Destination Queue Data RAM 2-Bit Error Detected
55	PEX DEVSPCERRSTS [7:0]							
	Retry Buffer 2-Bit Error Detected	Destination Queue Link List RAM 2-Bit Error Detected	Reserved	Reserved	Station 3 Source Queue Link List RAM 2-Bit Error Detected	Station 2 Source Queue Link List RAM 2-Bit Error Detected	Station 1 Source Queue Link List RAM 2-Bit Error Detected	Station 0 Source Queue Link List RAM 2-Bit Error Detected
56	PEX CORRECTABLE ERROR STATUS[15:8]							
	Header Log Overflow Status	Corrected Internal Error Status	Advisory Non-Fatal Error Status	Replay Timer Timeout Status	Reserved	Reserved	Reserved	REPLAY NUM Rollover Status
57	PEX CORRECTABLE ERROR STATUS[7:0]							
	Bad DLLP Status	Bad TLP Status	Reserved	Reserved	Reserved	Reserved	Reserved	Receiver Error Status
58	PEX SEEPROM STATUS[31:24]							
59	PEX SEEPROM STATUS [23:16]							
5A	PEX SEEPROM STATUS [15:8]							
5B	PEX SEEPROM STATUS [7:0]							
5C	PEX PORT RECEIVER ERROR COUNTER [31:24]							
5D	PEX PORT RECEIVER ERROR COUNTER [23:16]							
5E	PEX PORT RECEIVER ERROR COUNTER [15:8]							
5F	PEX PORT RECEIVER ERROR COUNTER [7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEX BAD TLP COUNTER [31:24]							
61	PEX BAD TLP COUNTER [23:16]							
62	PEX BAD TLP COUNTER [15:8]							
63	PEX BAD TLP COUNTER [7:0]							
64	PEX BAD DLLP COUNTER [31:24]							
65	PEX BAD DLLP COUNTER [23:16]							
66	PEX BAD DLLP COUNTER [15:8]							
67	PEX BAD DLLP COUNTER [7:0]							
68	PEX UNCORRECTABLE ERROR STATUS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Atomic Operation Egress Blocked Status
69	PEX UNCORRECTABLE ERROR STATUS[23:16]							
	MC Blocked TLP Status	Uncorrectable Internal Error Status	ACS Violation Error Status	Unsupported Request Error Status	ECRC Error Status	Malformed TLP Status	Receiver Overflow Status	Unexpected Completion Status
6A	PEX UNCORRECTABLE ERROR STATUS[15:8]							
	Completer Abort Status	Reserved	Reserved	Poisoned TLP Status	Reserved	Reserved	Reserved	Reserved
6B	PEX UNCORRECTABLE ERROR STATUS[7:0]							
	Reserved	Reserved	Surprise Down Error Status	Data Link Protocol Error Status	Reserved	Reserved	Reserved	Reserved
6C	PEX Port Number							
6D	PTM Slot Number							
6E	Correctable Error Count [15:8]							
6F	Correctable Error Count [7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Reserved							
71	Reserved							
72	Reserved							
73	Reserved							
74	Reserved							
75	Reserved							
76	Reserved							
77	Reserved							
78	Reserved							
79	Reserved							
7A	Reserved							
7B	Reserved							
7C	Reserved							
7D	Reserved							
7E	Reserved							
7F	Reserved							

(32) Byte28 = 76: IO board power failure or initialize error Slot J/K in CHBB

	0	1	2	3	4	5	6	7
40	Free area format number = 38h							
41	Not use							
42	EGCTL0_R_IO_x_TYPE							
43	Not use							
44	EGCTL0_R_IO_x_ERRLOG_0[31:24]							
45	EGCTL0_R_IO_x_ERRLOG_0[23:16]							
46	EGCTL0_R_IO_x_ERRLOG_0[15:8]							
47	EGCTL0_R_IO_x_ERRLOG_0[7:0]							
48	EGCTL0_R_IO_x_ERRLOG_4[31:24]							
49	EGCTL0_R_IO_x_ERRLOG_4[23:16]							
4A	EGCTL0_R_IO_x_ERRLOG_4[15:8]							
4B	EGCTL0_R_IO_x_ERRLOG_4[7:0]							
4C	EGCTL0_R_IO_x_ERRLOG_8[31:24]							
4D	EGCTL0_R_IO_x_ERRLOG_8[23:16]							
4E	EGCTL0_R_IO_x_ERRLOG_8[15:8]							
4F	EGCTL0_R_IO_x_ERRLOG_8[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	EGCTL0_R_IO_x_ERRLOG_10[31:24]							
51	EGCTL0_R_IO_x_ERRLOG_10[23:16]							
52	EGCTL0_R_IO_x_ERRLOG_10[15:8]							
53	EGCTL0_R_IO_x_ERRLOG_10[7:0]							
54	PEX BUSNUM							
55	PEX SECBUS							
56	PEX PCISTS[15:8]							
	Detected Parity Error	Signaled System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
57	PEX PCISTS[7:0]							
	Reserved	Reserved	66MHz Capable	Capability List	Interrupt Status	Reserved	Reserved	Reserved
58	PEX SECSTS[15:8]							
	Detected Parity Error	Received System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
59	PEX SECSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	PEX DEVSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	PEX DEVSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Unsupported Request Detected	Fatal Error Detected	Non-Fatal Error Detected	Correctable Error Detected
5C	PEX LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
5D	PEX LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			
5E	PEX SLOTSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Data Link Layer State Changed
5F	PEX SLOTSTS[7:0]							
	Electro-mechanical Interlock Status	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Command Completed	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Power Fault Detected	Attention Button Pressed

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEX LNKSTS2[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	PEX LNKSTS2[7:0]							
	Reserved	Reserved	Link Equalization Request	Equalization Phase 3 Successful	Equalization Phase 2 Successful	Equalization Phase 1 Successful	Equalization Complete	Current De- Emphasis Level
62	PEX LANE DETECT STATUS[15:8]							
	Receiver Detected on Lane							
63	PEX LANE DETECT STATUS[7:0]							
	Receiver Detected on Lane							
64	PEX GEN3 FRAME ERROR STATUS[15:8]							
	Frame Error 15 Status	Frame Error 14 Status	Last TLP or DLLP Has No LIDL or End of Data Stream	Not All Subsequent Lanes Have LIDL or End of Data Stream when LIDL Status	Ordered-Set Not the Same on All Lanes Status	Invalid Skip Ordered-Set Status	FTS Not Followed by Data Block Status	Ordered-Set Block Not Followed by Data Block Status
65	PEX GEN3 FRAME ERROR STATUS[7:0]							
	End of Data Stream Not in Last DWord of Data Block Status	End of Data Stream Not Followed by Ordered-Set Block Status	Length CRC/Parity Error Status	EDB Not following TLP Status	Second Symbol of DLLP Not Ach Status	Bad Framing Symbol in Data Block Status	Sync Header Not Same on All Lanes Status	Invalid Sync Header Status
66	PEX DEVSPCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Egress Parity Error Detected	Ingress Parity Error Detected	Ingress Link List RAM 2-Bit Error Detected	Destination Queue Data RAM 2-Bit Error Detected
67	PEX DEVSPCERRSTS [7:0]							
	Retry Buffer 2-Bit Error Detected	Destination Queue Link List RAM 2-Bit Error Detected	Reserved	Reserved	Station 3 Source Queue Link List RAM 2-Bit Error Detected	Station 2 Source Queue Link List RAM 2-Bit Error Detected	Station 1 Source Queue Link List RAM 2-Bit Error Detected	Station 0 Source Queue Link List RAM 2-Bit Error Detected
68	PEX CORRECTABLE ERROR STATUS[15:8]							
	Header Log Overflow Status	Corrected Internal Error Status	Advisory Non- Fatal Error Status	Replay Timer Timeout Status	Reserved	Reserved	Reserved	REPLAY NUM Rollover Status
69	PEX CORRECTABLE ERROR STATUS[7:0]							
	Bad DLLP Status	Bad TLP Status	Reserved	Reserved	Reserved	Reserved	Reserved	Receiver Error Status
6A	PEX SEEPROM STATUS[31:24]							
6B	PEX SEEPROM STATUS [23:16]							
6C	PEX SEEPROM STATUS [15:8]							
6D	PEX SEEPROM STATUS [7:0]							
6E	PEX PORT RECEIVER ERROR COUNTER [31:24]							
6F	PEX PORT RECEIVER ERROR COUNTER [23:16]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PEX PORT RECEIVER ERROR COUNTER [15:8]							
71	PEX PORT RECEIVER ERROR COUNTER [7:0]							
72	PEX BAD TLP COUNTER [31:24]							
73	PEX BAD TLP COUNTER [23:16]							
74	PEX BAD TLP COUNTER [15:8]							
75	PEX BAD TLP COUNTER [7:0]							
76	PEX BAD DLLP COUNTER [31:24]							
77	PEX BAD DLLP COUNTER [23:16]							
78	PEX BAD DLLP COUNTER [15:8]							
79	PEX BAD DLLP COUNTER [7:0]							
7A	PEX UNCORRECTABLE ERROR STATUS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Atomic Operation Egress Status
7B	PEX UNCORRECTABLE ERROR STATUS[23:16]							
	MC Blocked TLP Status	Uncorrectable Internal Error Status	ACS Violation Error Status	Unsupported Request Error Status	ECRC Error Status	Malformed TLP Status	Receiver Overflow Status	Unexpected Completion Status
7C	PEX UNCORRECTABLE ERROR STATUS[15:8]							
	Completer Abort Status	Reserved	Reserved	Poisoned TLP Status	Reserved	Reserved	Reserved	Reserved
7D	PEX UNCORRECTABLE ERROR STATUS[7:0]							
	Reserved	Reserved	Surprise Down Error Status	Data Link Protocol Error Status	Reserved	Reserved	Reserved	Reserved
7E	PEX Port Number							
7F	PTM Slot Number							

(33) Byte28 = 77: IO board power failure or initialize error Slot L/M in CHBB

	0	1	2	3	4	5	6	7
40	Free area format number = 39h							
41	Not use							
42	EGCTL0_R_IO_x_TYPE							
43	Not use							
44	EGCTL1_R_IO_x_ERRLOG_0[31:24]							
45	EGCTL1_R_IO_x_ERRLOG_0[23:16]							
46	EGCTL1_R_IO_x_ERRLOG_0[15:8]							
47	EGCTL1_R_IO_x_ERRLOG_0[7:0]							
48	EGCTL1_R_IO_x_ERRLOG_4[31:24]							
49	EGCTL1_R_IO_x_ERRLOG_4[23:16]							
4A	EGCTL1_R_IO_x_ERRLOG_4[15:8]							
4B	EGCTL1_R_IO_x_ERRLOG_4[7:0]							
4C	EGCTL1_R_IO_x_ERRLOG_8[31:24]							
4D	EGCTL1_R_IO_x_ERRLOG_8[23:16]							
4E	EGCTL1_R_IO_x_ERRLOG_8[15:8]							
4F	EGCTL1_R_IO_x_ERRLOG_8[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	EGCTL1_R_IO_x_ERRLOG_10[31:24]							
51	EGCTL1_R_IO_x_ERRLOG_10[23:16]							
52	EGCTL1_R_IO_x_ERRLOG_10[15:8]							
53	EGCTL1_R_IO_x_ERRLOG_10[7:0]							
54	PEX BUSNUM							
55	PEX SECBUS							
56	PEX PCISTS[15:8]							
	Detected Parity Error	Signaled System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
57	PEX PCISTS[7:0]							
	Reserved	Reserved	66MHz Capable	Capability List	Interrupt Status	Reserved	Reserved	Reserved
58	PEX SECSTS[15:8]							
	Detected Parity Error	Received System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
59	PEX SECSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	PEX DEVSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5B	PEX DEVSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Unsupported Request Detected	Fatal Error Detected	Non-Fatal Error Detected	Correctable Error Detected
5C	PEX LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
5D	PEX LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			
5E	PEX SLOTSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Data Link Layer State Changed
5F	PEX SLOTSTS[7:0]							
	Electro-mechanical Interlock Status	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Command Completed	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Power Fault Detected	Attention Button Pressed

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEX LNKSTS2[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	PEX LNKSTS2[7:0]							
	Reserved	Reserved	Link Equalization Request	Equalization Phase 3 Successful	Equalization Phase 2 Successful	Equalization Phase 1 Successful	Equalization Complete	Current De- Emphasis Level
62	PEX LANE DETECT STATUS[15:8]							
	Receiver Detected on Lane							
63	PEX LANE DETECT STATUS[7:0]							
	Receiver Detected on Lane							
64	PEX GEN3 FRAME ERROR STATUS[15:8]							
	Frame Error 15 Status	Frame Error 14 Status	Last TLP or DLLP Has No LIDL or End of Data Stream	Not All Subsequent Lanes Have LIDL or End of Data Stream when LIDL Status	Ordered-Set Not the Same on All Lanes Status	Invalid Skip Ordered-Set Status	FTS Not Followed by Data Block Status	Ordered-Set Block Not Followed by Data Block Status
65	PEX GEN3 FRAME ERROR STATUS[7:0]							
	End of Data Stream Not in Last DWord of Data Block Status	End of Data Stream Not Followed by Ordered-Set Block Status	Length CRC/Parity Error Status	EDB Not following TLP Status	Second Symbol of DLLP Not Ach Status	Bad Framing Symbol in Data Block Status	Sync Header Not Same on All Lanes Status	Invalid Sync Header Status
66	PEX DEVSPCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Egress Parity Error Detected	Ingress Parity Error Detected	Ingress Link List RAM 2-Bit Error Detected	Destination Queue Data RAM 2-Bit Error Detected
67	PEX DEVSPCERRSTS [7:0]							
	Retry Buffer 2-Bit Error Detected	Destination Queue Link List RAM 2-Bit Error Detected	Reserved	Reserved	Station 3 Source Queue Link List RAM 2-Bit Error Detected	Station 2 Source Queue Link List RAM 2-Bit Error Detected	Station 1 Source Queue Link List RAM 2-Bit Error Detected	Station 0 Source Queue Link List RAM 2-Bit Error Detected
68	PEX CORRECTABLE ERROR STATUS[15:8]							
	Header Log Overflow Status	Corrected Internal Error Status	Advisory Non- Fatal Error Status	Replay Timer Timeout Status	Reserved	Reserved	Reserved	REPLAY NUM Rollover Status
69	PEX CORRECTABLE ERROR STATUS[7:0]							
	Bad DLLP Status	Bad TLP Status	Reserved	Reserved	Reserved	Reserved	Reserved	Receiver Error Status
6A	PEX SEEPROM STATUS[31:24]							
6B	PEX SEEPROM STATUS [23:16]							
6C	PEX SEEPROM STATUS [15:8]							
6D	PEX SEEPROM STATUS [7:0]							
6E	PEX PORT RECEIVER ERROR COUNTER [31:24]							
6F	PEX PORT RECEIVER ERROR COUNTER [23:16]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PEX PORT RECEIVER ERROR COUNTER [15:8]							
71	PEX PORT RECEIVER ERROR COUNTER [7:0]							
72	PEX BAD TLP COUNTER [31:24]							
73	PEX BAD TLP COUNTER [23:16]							
74	PEX BAD TLP COUNTER [15:8]							
75	PEX BAD TLP COUNTER [7:0]							
76	PEX BAD DLLP COUNTER [31:24]							
77	PEX BAD DLLP COUNTER [23:16]							
78	PEX BAD DLLP COUNTER [15:8]							
79	PEX BAD DLLP COUNTER [7:0]							
7A	PEX UNCORRECTABLE ERROR STATUS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Atomic Operation Egress Status
7B	PEX UNCORRECTABLE ERROR STATUS[23:16]							
	MC Blocked TLP Status	Uncorrectable Internal Error Status	ACS Violation Error Status	Unsupported Request Error Status	ECRC Error Status	Malformed TLP Status	Receiver Overflow Status	Unexpected Completion Status
7C	PEX UNCORRECTABLE ERROR STATUS[15:8]							
	Completer Abort Status	Reserved	Reserved	Poisoned TLP Status	Reserved	Reserved	Reserved	Reserved
7D	PEX UNCORRECTABLE ERROR STATUS[7:0]							
	Reserved	Reserved	Surprise Down Error Status	Data Link Protocol Error Status	Reserved	Reserved	Reserved	Reserved
7E	PEX Port Number							
7F	PTM Slot Number							

(34) Byte28 = 78 or 79: PCP power failure or initialize error, SWPK internal error or power failure or initialize error

	0	1	2	3	4	5	6	7
40	Free area format number = 3Ah							
41	Not use							
42	SWPK PMIC PK TYPE							
43	SWPK PMIC IF TYPE							
44	SWPK PMIC MIC REV							
45	Reserved							
46	SWPK PMIC DIAG STS							
47	SWPK PMIC ERR FACT							
48	SWPK PMIC I2CTO STS							
49	SWPK PMIC I2C TEST							
4A	SWPK PMIC SELF STS							
4B	SWPK PMIC PCTL STS							
4C	SWPK PMIC MIC STS0							
4D	SWPK PMIC MIC STS1							
4E	SWPK PMIC MIC STS2							
4F	SWPK PMIC MIC STS3							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	PCP PMIC PK TYPE							
51	PCP PMIC IF TYPE							
52	PCP PMIC MIC REV							
53	Reserved							
54	PCP PMIC DIAG STS							
55	PCP PMIC ERR FACT							
56	PCP PMIC I2CTO STS							
57	PCP PMIC I2C TEST							
58	PCP PMIC SELF STS							
59	PCP PMIC PCTL STS							
5A	PCP PMIC MIC STS0							
5B	PCP PMIC MIC STS1							
5C	PCP PMIC MIC STS2							
5D	PCP PMIC MIC STS3							
5E	Reserved							
5F	Reserved							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	PEX PORT1_PCISTS[15:8]							
	Detected Parity Error	Signaled System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
61	PEX PORT1_PCISTS[7:0]							
	Reserved	Reserved	66MHz Capable	Capability List	Interrupt Status	Reserved	Reserved	Reserved
62	PEX PORT1_SECSTS[15:8]							
	Detected Parity Error	Received System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
63	PEX PORT1_SECSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
64	PEX PORT1_DEVSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	PEX PORT1_DEVSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Unsupported Request Detected	Fatal Error Detected	Non-Fatal Error Detected	Correctable Error Detected
66	PEX PORT1_LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configuration	Link Training	Reserved	Reserved	Reserved
67	PEX PORT1_LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			
68	PEX PORT1_SLOTSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Data Link Layer State Changed
69	PEX PORT1_SLOTSTS[7:0]							
	Electro-mechanical Interlock Status	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Command Completed	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Power Fault Detected	Attention Button Pressed

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
6A	PEX PORT1_GEN3 FRAME ERROR STATUS[15:8]							
	Frame Error 15 Status	Frame Error 14 Status	Last TLP or DLLP Has No LIDL or End of Data Stream	Not All Subsequent Lanes Have LIDL or End of Data Stream when LIDL Status	Ordered-Set Not the Same on All Lanes Status	Invalid Skip Ordered-Set Status	FTS Not Followed by Data Block Status	Ordered-Set Block Not Followed by Data Block Status
6B	PEX PORT1_GEN3 FRAME ERROR STATUS[7:0]							
	End of Data Stream Not in Last DWord of Data Block Status	End of Data Stream Not Followed by Ordered-Set Block Status	Length CRC/Parity Error Status	EDB Not following TLP Status	Second Symbol of DLLP Not Ach Status	Bad Framing Symbol in Data Block Status	Sync Header Not Same on All Lanes Status	Invalid Sync Header Status
6C	PEX PORT1_DEVSPCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Egress Parity Error Detected	Ingress Parity Error Detected	Ingress Link List RAM 2-Bit Error Detected	Destination Queue Data RAM 2-Bit Error Detected
6D	PEX PORT1_DEVSPCERRSTS [7:0]							
	Retry Buffer 2-Bit Error Detected	Destination Queue Link List RAM 2-Bit Error Detected	Reserved	Reserved	Station 3 Source Queue Link List RAM 2-Bit Error Detected	Station 2 Source Queue Link List RAM 2-Bit Error Detected	Station 1 Source Queue Link List RAM 2-Bit Error Detected	Station 0 Source Queue Link List RAM 2-Bit Error Detected
6E	PEX PORT1_CORRECTABLE ERROR STATUS[15:8]							
	Header Log Overflow Status	Corrected Internal Error Status	Advisory Non-Fatal Error Status	Replay Timer Timeout Status	Reserved	Reserved	Reserved	REPLAY NUM Rollover Status
6F	PEX PORT1_CORRECTABLE ERROR STATUS[7:0]							
	Bad DLLP Status	Bad TLP Status	Reserved	Reserved	Reserved	Reserved	Reserved	Receiver Error Status

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PEX PORT5_PCISTS[15:8]							
	Detected Parity Error	Signaled System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
71	PEX PORT5_PCISTS[7:0]							
	Reserved	Reserved	66MHz Capable	Capability List	Interrupt Status	Reserved	Reserved	Reserved
72	PEX PORT5_SECSTS[15:8]							
	Detected Parity Error	Received System Error	Reserved	Reserved	Signaled Target Abort	Reserved	Reserved	Master Data Parity Detected
73	PEX PORT5_SECSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
74	PEX PORT5_DEVSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	PEX PORT5_DEVSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Unsupported Request Detected	Fatal Error Detected	Non-Fatal Error Detected	Correctable Error Detected
76	PEX PORT5_LNKSTS[15:8]							
	Link Autonomous Bandwidth Status	Link Bandwidth Management Status	Data Link Layer Link Active	Slot Clock Configurationg	Link Training	Reserved	Reserved	Reserved
77	PEX PORT5_LNKSTS[7:0]							
	Negotiated Link Width				Current Link Speed			
78	PEX PORT5_SLOTSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Data Link Layer State Changed
79	PEX PORT5_SLOTSTS[7:0]							
	Electro-mechanical Interlock Status	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Command Completed	Base and Virtual Switch Mode	Base and Virtual Switch Mode	Power Fault Detected	Attention Button Pressed

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
7A	PEX PORT5_GEN3 FRAME ERROR STATUS[15:8]							
	Frame Error 15 Status	Frame Error 14 Status	Last TLP or DLLP Has No LIDL or End of Data Stream	Not All Subsequent Lanes Have LIDL or End of Data Stream when LIDL Status	Ordered-Set Not the Same on All Lanes Status	Invalid Skip Ordered-Set Status	FTS Not Followed by Data Block Status	Ordered-Set Block Not Followed by Data Block Status
7B	PEX PORT5_GEN3 FRAME ERROR STATUS[7:0]							
	End of Data Stream Not in Last DWord of Data Block Status	End of Data Stream Not Followed by Ordered-Set Block Status	Length CRC/Parity Error Status	EDB Not following TLP Status	Second Symbol of DLLP Not Ach Status	Bad Framing Symbol in Data Block Status	Sync Header Not Same on All Lanes Status	Invalid Sync Header Status
7C	PEX PORT5_DEVSPCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Egress Parity Error Detected	Ingress Parity Error Detected	Ingress Link List RAM 2-Bit Error Detected	Destination Queue Data RAM 2-Bit Error Detected
7D	PEX PORT5_DEVSPCERRSTS [7:0]							
	Retry Buffer 2-Bit Error Detected	Destination Queue Link List RAM 2-Bit Error Detected	Reserved	Reserved	Station 3 Source Queue Link List RAM 2-Bit Error Detected	Station 2 Source Queue Link List RAM 2-Bit Error Detected	Station 1 Source Queue Link List RAM 2-Bit Error Detected	Station 0 Source Queue Link List RAM 2-Bit Error Detected
7E	PEX PORT5_CORRECTABLE ERROR STATUS[15:8]							
	Header Log Overflow Status	Corrected Internal Error Status	Advisory Non-Fatal Error Status	Replay Timer Timeout Status	Reserved	Reserved	Reserved	REPLAY NUM Rollover Status
7F	PEX PORT5_CORRECTABLE ERROR STATUS[7:0]							
	Bad DLLP Status	Bad TLP Status	Reserved	Reserved	Reserved	Reserved	Reserved	Receiver Error Status

5.8 Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
31								
32	Module ID							
33	Routine ID							
34	Package number				Message code (x'0')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00') (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Not used							
7F								

*1: Failed processor number for WCHK1

5.9 Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset)**5.10 Byte27 = 81 & Byte34:bit4-7=3 (CHK3 Reset)**

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'E' or x'1' or x'4')			
28	Related failure internal SSB log number							
29								
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'5' or x'3')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C								
3D								
3E	Reserved							
3F								
40								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C								
4D	Interrupted address							
4E								
4F								
50								
51	Interrupted operation code							
52								
53								
54								
55	Current LDEV number (from LCT)							
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F	wchklmpbitmap (from LCT)							
60								
61								
62								
63	jcb id							
64								
65	req id							
66								
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
71								
72								
73	DCN (wldcn21)							
74								
75								
76	Controller Chassis task head address							
77								
78	SLV-DCN (slvdcnr)							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

5.11 Byte27 = 8E & Byte34:bit4-7=6 (Controller Chassis Internal Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'6')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37	(This information is not fixed until Controller Chassis reports SSB to HOST.)							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C								
3D								
3E	Reserved							
3F								
40								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C								
4D								
4E	Interrupted address							
4F								
50								
51								
52	Interrupted operation code							
53								
54								
55								
56	Current LDEV number (from LCT)							
57	Current LPN number							
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F	wchklmpbitmap (from LCT)							
60								
61								
62								
63	jcb id							
64								
65	req id							
66								
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
71								
72	DCN (wldcn21)							
73								
74	Controller Chassis task head address							
75								
76								
77								
78	SLV-DCN (slvdcnr)							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

5.12 Byte27 = 8E & Byte34:bit4-7 = 8 (Force Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format(x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'8')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C								
4D								
4E	Interrupted address							
4F								
50								
51								
52	Interrupted operation code							
53								
54								
55								
56	Current LDEV number (from LCT)							
57	Current LPN number							
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F	wchklmpbitmap (from LCT)							
60								
61								
62								
63	jcb id							
64								
65	req id							
66	Job type code							
67	job status							
68	Executing processor number							
69	Initiation cause code							
6A	Initiation detail code							
6B	Command code							
6C	Channel sequence							
6D	Reserved (x'00')							
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.13 Byte27 = 8E & Byte34:bit4-7 = 9 (Logical Inconsistency Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29								
2A								
2B								
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'9')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchklac)							
4A	Multi reset type code (rstmultics)							
4B								
4C								
4D								
4E	Interrupted address							
4F								
50								
51								
52	Interrupted operation code							
53								
54								
55								
56	Current LDEV number (from LCT)							
57	Current LPN number							
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F	wchklmpbitmap (from LCT)							
60								
61								
62								
63	jcb id							
64								
65	req id							
66	Job type code							
67	job status							
68	Executing processor number							
69	Initiation cause code							
6A	Initiation detail code							
6B	Command code							
6C	Channel sequence							
6D	Reserved (x'00')							
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.14 Byte27 = 8F (Logical Inconsistency)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'8')				Message (x'F')			
28	Module ID							
29	Routine ID							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	Package number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40								
41		Error information						
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Error information							
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

5.15 Byte27 = 9F (Trace Data/Log Data)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'9')				Message (x'F')			
28	Module ID (*1)							
29	Routine ID (*1)							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	Package number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF9F')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

*1: In case of Mode 449 ON at the primary storage system of UR, if the secondary storage system is powered off or the path between primary and secondary storage system is blocked, error code x'DB00' might be displayed on primary storage system.
These error codes, which are generated when the communication between primary and secondary storage system fails, doesn't indicate any failures.Please ignore them.

5.16 Byte27 = EC (Factory Mode/ Forced Volatile Reboot Mode /Abnormal Mode/Battery/ BKM/PSU/FAN/AC Loss/UPS/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'E')				Message (x'C')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.16.1 Byte27 = EC (Factory Mode/ Forced Volatile Reboot Mode /Abnormal Mode/Battery/ BKM/PSU/FAN/AC Loss/UPS/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor) Byte28-31

(1) Byte28 = 40 or 41 or 42 : Factory mode

	0	1	2	3	4	5	6	7
28	Subcode : x'40'or x'41'or x'42': Factory mode							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(2) Byte28 = 43 : Forced volatile reboot mode warning

	0	1	2	3	4	5	6	7
28	Subcode : x'43': Forced volatile reboot mode warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 45 : Battery warning

	0	1	2	3	4	5	6	7
28	Subcode : x'45': Battery warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKM location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(4) Byte28 = 46 : BKM/BKMF warning

	0	1	2	3	4	5	6	7
28	Subcode : x'46': BKM/BKMF warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKM location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(5) Byte28 = 47 : BKM/BKMF FWwarning

	0	1	2	3	4	5	6	7
28	Subcode : x'47': BKM/BKMF FWwarning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKM location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(6) Byte28 = 48 : CPU thermal warning

	0	1	2	3	4	5	6	7
28	Subcode : x'48': CPU thermal warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(7) Byte28 = 49 : Outside air temperature warning

	0	1	2	3	4	5	6	7
28	Subcode : x'49': Outside air temperature warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(8) Byte28 = 50 : Fan warning

	0	1	2	3	4	5	6	7
28	Subcode : x'50': Fan warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	FAN(BKMF) location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(9) Byte28 = 51 : Power supply unit warning

	0	1	2	3	4	5	6	7
28	Subcode : x'51': Power supply unit warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	PSU location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(10) Byte28 = 52 : AC loss warning

	0	1	2	3	4	5	6	7
28	Subcode : x'52': AC loss warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	PSU location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(11) Byte28 = 53 : Panel device warning

	0	1	2	3	4	5	6	7
28	Subcode : x'53': Panel device warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(12) Byte28 = 54 : UPS warning

	0	1	2	3	4	5	6	7
28	Subcode : x'54': UPS warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	UPS location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(13) Byte28 = 56 : Environment monitor warning

	0	1	2	3	4	5	6	7
28	Subcode : x'56': Environment monitor warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(14) Byte28 = 57 : Abnormal mode warning

	0	1	2	3	4	5	6	7
28	Subcode : x'57': Abnormal mode warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(15) Byte28 = 59 : Environment monitor FW warning

	0	1	2	3	4	5	6	7
28	Subcode : x'59': Environment monitor FW warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(16) Byte28 = 60 : Outside air temperature alarm

	0	1	2	3	4	5	6	7
28	Subcode : x'60': Outside air temperature alarm							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(17) Byte28 = 61 : Illegal factory mode

	0	1	2	3	4	5	6	7
28	Subcode : x'61': Illegal factory mode							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(18) Byte28 = 62 : Battery life time previous notice

	0	1	2	3	4	5	6	7
28	Subcode : x'62': Battery live time previous notice							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKM location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(19) Byte28 = 63 : Battery life time notice

	0	1	2	3	4	5	6	7
28	Subcode : x'63': Battery life time notice							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKM location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(20) Byte28 = 65 : Panel Switch warning

	0	1	2	3	4	5	6	7
28	Subcode : x'65': Panel Switch warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							

(21) Byte28 = 80 or 81 or 82 or 83 or 84 or 85 or 86 : SWPK Thermal warning/eFAN warning/ePS warning/CHBB AC warinig/eEMIC warinig/CHBB Abnormal mode/eEMIC FW Update warning

	0	1	2	3	4	5	6	7
28	Subcode : x'80' or '81' or '82' or '83' or '84' or '85' or '86': BKM/BKMF FWwarning							
29	MPID (Reported SSB)							
2A	SWPK location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(22) Byte28 = A0: GUM ready TOV or A1: GUM Heart beat TOV or A2: GUM linkup failurre

	0	1	2	3	4	5	6	7
28	Subcode : x'A0': GUM ready TOV or x'A1': GUM Heart beat TOV or x'A2': GUM linkup failure							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.16.2 Byte27 = EC (Factory Mode/ Forced Volatile Reboot Mode /Abnormal Mode/Battery/ BKM/PSU/FAN/AC Loss/UPS/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor) Byte40-7F Detail

(1) Byte28 = 40 or 41 or 42 : Factory mode

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	—	—	—	—	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(2) Byte28 = 43 : Forced volatile reboot mode warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	Reserved	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(3) Byte28 = 45 : Battery warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0/BKM_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0/BKM_ALM

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0/BKM Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0/BKM Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0/BKM_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0/BKM Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0/BKM Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0/BKM_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0/BKM_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(4) Byte28 = 46 : BKM/BKMF warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0/BKM_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0/BKM_ALM

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0/BKM Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0/BKM Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0/BKM_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0/BKM Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0/BKM Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0/BKM_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0/BKM_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(5) Byte28 = 47 : BKM/BKMF FWwarning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0/BKM_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0/BKM_ALM

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0/BKM Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0/BKM Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0/BKM_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0/BKM Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0/BKM Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0/BKM_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0/BKM_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(6) Byte28 = 48 : CPU thermal warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(7) Byte28 = 49 : Outside air temperature warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(8) Byte28 = 50 : Fan warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_FAN_STS1 (not used)							
4F	R_FAN_STS2							
	Abnormal FAN location of own cluster							
	CBL - Bit[7:4] : FAN1 , Bit[3:0] : FAN0							
	CBS – bit[3]:FAN3, bit[2]:FAN2, bit[1]:FAN1, bit[0]:FAN0							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_FAN_MNG							
51	R_FAN_CTL							
52	R_FAN_PWM0L							
53	R_FAN_PWM0H							
54	R_FAN_PWM1L							
55	R_FAN_PWM1H							
56	R_FAN_TACH0 : FAN0 speed							
57	R_FAN_TACH1 : FAN1 speed							
58	R_FAN_TACH2 : FAN2 speed							
59	R_FAN_TACH3 : FAN3 speed							
5A	R_FAN_TACH4 : FAN4 speed							
5B	R_FAN_TACH5 : FAN5 speed							
5C	R_FAN_TACH6 : FAN6 speed							
5D	R_FAN_TACH7 : FAN7 speed							
5E	R_MATED_FAN_STS1 (not used)							
5F	R_MATED_FAN_STS2 R_FAN_STS2 Abnormal FAN location of another cluster CBL - Bit[7:4] : FAN1 , Bit[3:0] : FAN0 CBS – bit[3]:FAN3, bit[2]:FAN2, bit[1]:FAN1, bit[0]:FAN0							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(9) Byte28 = 51 : Power supply unit warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PS_STS							
	PSU2_AC_	PSU1_AC_	PSU2_ALM	PSU1_ALM	—	—	PSU2_IN	PSU1_IN
	ALM	ALM						
4F	R_PS_CTL							
	—	—	—	—	—	—	PSU2_LED	PSU1_LED

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFREQ
51	R_PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	OFFOK			
52	R_PWR_MNG							
53	R_LG_CTL							
54	R_UPDOWN							
55	R_PKSTATUS0							
	OFFOK_ACK	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
56	R_PKSTATUS1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(10) Byte28 = 52 : AC loss warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PS_STS							
	PSU2_AC_	PSU1_AC_	PSU2_ALM	PSU1_ALM	—	—	PSU2_IN	PSU1_IN
	ALM	ALM						
4F	R_PS_CTL							
	—	—	—	—	—	—	PSU2_LED	PSU1_LED

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFREQ
51	R_PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	OFFOK			
52	R_PWR_MNG							
53	R_LG_CTL							
54	R_UPDOWN							
55	R_PKSTATUS0							
	OFFOK_ACK	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
56	R_PKSTATUS1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(11) Byte28 = 53 : Panel device warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PANEL_STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Panel alarm
4F	R_PANELLED							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_PANELLEDPULSE							
51	R_PANELSW							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(12) Byte28 = 54 : UPS warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_UPS_STS							
	BKMTYPE	HOSTAC_ON	UPS OFFREQ from another	UPS OFFREQ	Reserved	Reserved	UPS of another cluster alarm	UPS alarm
4F	R_UPS_CTL							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFOK to UPS of another cluster	OFFOK to UPS

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_MATED_UPS_CTL							
	BKMTYPE	HOSTAC_ON	UPS OFFREQ	UPS OFFREQ from another cluster	Reserved	Reserved	UPS alarm	UPS of another cluster alarm
51	R_MATED_UPS_STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFOK to UPS	OFFOK to UPS of another cluster
52	R_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
53	MATED_ECMDSTS							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R	EFW	ERRLOG[16]					
71	R	EFW	ERRLOG[17]					
72	R	EFW	ERRLOG[18]					
73	R	EFW	ERRLOG[19]					
74	R	EFW	ERRLOG[20]					
75	R	EFW	ERRLOG[21]					
76	R	EFW	ERRLOG[22]					
77	R	EFW	ERRLOG[23]					
78	R	EFW	ERRLOG[24]					
79	R	EFW	ERRLOG[25]					
7A	R	EFW	ERRLOG[26]					
7B	R	EFW	ERRLOG[27]					
7C	R	EFW	ERRLOG[28]					
7D	R	EFW	ERRLOG[29]					
7E	R	EFW	ERRLOG[30]					
7F	R	EFW	ERRLOG[31]					

(13) Byte28 = 56 : Environment monitor warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	Not used							
4F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(14) Byte28 = 57 : Abnormal mode warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(15) Byte28 = 59 : Environment monitor FW warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	MN_Micro_Change_CTL_Enable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Enable
4F	MN_Micro_Change_Mode							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Mode Enable

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	MN_Micro_Change_Reset							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Reset
51	MN_Micro_Block_TRNS_REQ							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	TransferREQ
52	MN_Micro_Block_TRNS_ACK							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	TransferACK
53	MN_Micro_Change_COMP							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Complete
54	MN_Micro_Change_Error							
	Error Information							Error
55	MN_Micro_Block_Length							
56	MN_Micro_Block_TRNS_SEQ_NO.							
	Number of complete block							
57	Not used							
58	BFR[0]							
59	BFR[1]							
5A	BFR[2]							
5B	BFR[3]							
5C	BFR[4]							
5D	BFR[5]							
5E	BFR[6]							
5F	BFR[7]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(16) Byte28 = 60 : Outside air temperature alarm

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(17) Byte28 = 61 : Illegal factory mode

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(18) Byte28 = 62 : Battery life time previous notice

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0/BKM_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0/BKM_ALM

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0/BKM Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0/BKM Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0/BKM_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0/BKM Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0/BKM Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0/BKM_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0/BKM_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(19) Byte28 = 63 : Battery life time notice

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0/BKM_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0/BKM_ALM

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0/BKM Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0/BKM Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0/BKM_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0/BKM Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0/BKM Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0/BKM_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0/BKM_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(20) Byte28 = 65 : Panel Switch warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PANEL_STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Panel alarm
4F	R_PANELLED							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	R_PANELLEDPULSE							
51	R_PANELSW							
52	CUDG_ERRCODE0							
53	CUDG_ERRCODE1							
54	MATED_CUDG_ERRCODE0							
55	MATED_CUDG_ERRCODE1							
56	PANELLED							
	Reserved	Reserved	PowerLED (LG)	PowerLED (BS)	Reserved	Reserved	Reserved	Reserved
57	PANELSW							
58	OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PSOFFREQ
59	I_STATE1							
	Emergency	MATED_CHK1	PSOFFREQA SET	LANRSTREQ IN	Reserved	Reserved	Reserved	Reserved
5A	MATED_I_STATE1							
5B	PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	Admit Setting PSOFF			
5C	PKINFO2							
	CTLModel				Reserved	CTLLocation		
5D	CTLSTS1							
	Reserved	Reserved	Reserved	MicroReset SW	Reserved	Reserved	LAN Reset SW	Latch LAN Reset SW
5E	MATED_CTLSTS1							
5F	R_PWR_MNG							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	R EFW ERRLOG[0]							
61	R EFW ERRLOG[1]							
62	R EFW ERRLOG[2]							
63	R EFW ERRLOG[3]							
64	R EFW ERRLOG[4]							
65	R EFW ERRLOG[5]							
66	R EFW ERRLOG[6]							
67	R EFW ERRLOG[7]							
68	R EFW ERRLOG[8]							
69	R EFW ERRLOG[9]							
6A	R EFW ERRLOG[10]							
6B	R EFW ERRLOG[11]							
6C	R EFW ERRLOG[12]							
6D	R EFW ERRLOG[13]							
6E	R EFW ERRLOG[14]							
6F	R EFW ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	R	EFW	ERRLOG[16]					
71	R	EFW	ERRLOG[17]					
72	R	EFW	ERRLOG[18]					
73	R	EFW	ERRLOG[19]					
74	R	EFW	ERRLOG[20]					
75	R	EFW	ERRLOG[21]					
76	R	EFW	ERRLOG[22]					
77	R	EFW	ERRLOG[23]					
78	R	EFW	ERRLOG[24]					
79	R	EFW	ERRLOG[25]					
7A	R	EFW	ERRLOG[26]					
7B	R	EFW	ERRLOG[27]					
7C	R	EFW	ERRLOG[28]					
7D	R	EFW	ERRLOG[29]					
7E	R	EFW	ERRLOG[30]					
7F	R	EFW	ERRLOG[31]					

(21) Byte28 = 80 or 81 or 82 or 83 or 84 or 85 or 86 : SWPK Thermal warning/eFAN warning/ePS warning/CHBB AC warinig/eEMIC warinig/CHBB Abnormal mode/eEMIC FW Update warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	ER FAN STS1							
	Reserved	Reserved	Reserved	eFAN4 IN	eFAN3 IN	eFAN2 IN	eFAN1 IN	eFAN0 IN
49	ER FAN STS2							
	Reserved	Reserved	Reserved	eFAN4 Status	eFAN3 Status	eFAN2 Status	eFAN1 Status	eFAN0 Status
4A	ER IOPK PRT STS1							
	eIOM3 IN	eIOM2 IN	eIOM1 IN	eIOM0 IN	Reserved	Reserved	PCP IN	PCP IN
4B	ER IOPK PRT STS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	SWPK IN
4C	ER IOPK ALM STS1							
	eIOM3 Status	eIOM2 Status	eIOM1 Status	eIOM0 Status	Reserved	Reserved	PCP Status	PCP Status
4D	ER IOPK ALM STS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	SWPK Status
4E	ER THERMAL STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	SWPK Thermal Alarm	SWPK Thermal Warning	Reserved
4F	ER DCMODESET ERR							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	ER THERMAL							
51	ER PS STS							
	ePS2 AC Status	ePS1 AC Status	ePS2 Status	ePS1 Status	Reserved	Reserved	ePS2 IN	ePS1 IN
52	Reserved							
53	ER THERMAL 1L							
54	ER THERMAL 1H							
55	ER PMIC SWPK THERMAL							
56	ER PMIC PCP THERMAL1							
57	Reserved							
58	ER PCP 1 TYPE							
59	ER PCP 2 TYPE							
5A	ER IO 1 TYPE							
5B	ER IO 2 TYPE							
5C	ER IO 3 TYPE							
5D	ER IO 4 TYPE							
5E	ER SWPK TYPE							
5F	Reserved							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	ER_EFW_ERRLOG[0]							
61	ER_EFW_ERRLOG[1]							
62	ER_EFW_ERRLOG[2]							
63	ER_EFW_ERRLOG[3]							
64	ER_EFW_ERRLOG[4]							
65	ER_EFW_ERRLOG[5]							
66	ER_EFW_ERRLOG[6]							
67	ER_EFW_ERRLOG[7]							
68	ER_EFW_ERRLOG[8]							
69	ER_EFW_ERRLOG[9]							
6A	ER_EFW_ERRLOG[10]							
6B	ER_EFW_ERRLOG[11]							
6C	ER_EFW_ERRLOG[12]							
6D	ER_EFW_ERRLOG[13]							
6E	ER_EFW_ERRLOG[14]							
6F	ER_EFW_ERRLOG[15]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	ER_EFW_ERRLOG[16]							
71	ER_EFW_ERRLOG[17]							
72	ER_EFW_ERRLOG[18]							
73	ER_EFW_ERRLOG[19]							
74	ER_EFW_ERRLOG[20]							
75	ER_EFW_ERRLOG[21]							
76	ER_EFW_ERRLOG[22]							
77	ER_EFW_ERRLOG[23]							
78	ER_EFW_ERRLOG[24]							
79	ER_EFW_ERRLOG[25]							
7A	ER_EFW_ERRLOG[26]							
7B	ER_EFW_ERRLOG[27]							
7C	ER_EFW_ERRLOG[28]							
7D	ER_EFW_ERRLOG[29]							
7E	ER_EFW_ERRLOG[30]							
7F	ER_EFW_ERRLOG[31]							

(22) Byte28 = A0: GUM ready TOV or A1: GUM Heart beat TOV or A2: GUM linkup failure

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	Reserved	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	BMC_CTL							
	Reset	Reset mask from Another	Not used	Not used	Not used	Not used	Not used	Not used
61	BMC_CTL2							
	Reset another	Not used	Not used	Not used	Not used	Not used	Not used	Not used
62	BMC_STS							
	Reset from another	Not used	Status1	Status0	WOLREQ	Not used	Status3	VOJP
63	CTLSTS_MASK							
	Not used	Not used	Not used	Reset mask	Not used	Not used	Not used	LAN reset mask
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	h2bq1							
69	h2bq2							
6A	b2hq1							
6B	b2hq2							
6C	bmc2host_q_status							
6D	host2bmc_q_status							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Number of failures (continuous)							
71	Number of failures (within 24h)							
72	BootErrCode_L							
73	BootErrCode_H							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	PCH_PP6_LSTS							
	Reserved	Reserved	Linkup	Reserved	Reserved	Reserved	Reserved	Reserved
79	PCH_PP6_LSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Shared memory status_L							
7B	Shared memory status_H							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

5.17 Byte27 = EE (Environment Monitor Detected Drive Box Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'E')				Message (x'E')			
28	Error Detection code (*1)							
29	Error Component code (*2)							
2A	Error Reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
2F								
30								
03								
31								
32								
33								
34	Package number (invalid)				Message code (invalid)			
35	SSID (low) of self storage system (invalid)							
36	Symptom code (x'FFEE')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (invalid)							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason code (*3)							
43	Not used							
—								
7F								

*1: Error Detection Code

No	Code	Detection	Remarks
1	0x20	ENCxx-1	CTL1
2	0x21	ENCxx-2	CTL2

*2: Error Component Code

No	Code	Detection	No	Code	Detection
1	0x00	DB-00	25	0x18	DB-24
2	0x01	DB-01	26	0x19	DB-25
3	0x02	DB-02	27	0x1A	DB-26
4	0x03	DB-03	28	0x1B	DB-27
5	0x04	DB-04	29	0x1C	DB-28
6	0x05	DB-05	30	0x1D	DB-29
7	0x06	DB-06	31	0x1E	DB-30
8	0x07	DB-07	32	0x1F	DB-31
9	0x08	DB-08	33	0x20	DB-32
10	0x09	DB-09	34	0x21	DB-33
11	0x0A	DB-10	35	0x22	DB-34
12	0x0B	DB-11	36	0x23	DB-35
13	0x0C	DB-12	37	0x24	DB-36
14	0x0D	DB-13	38	0x25	DB-37
15	0x0E	DB-14	39	0x26	DB-38
16	0x0F	DB-15	40	0x27	DB-39
17	0x10	DB-16	41	0x28	DB-40
18	0x11	DB-17	42	0x29	DB-41
19	0x12	DB-18	43	0x2A	DB-42
20	0x13	DB-19	44	0x2B	DB-43
21	0x14	DB-20	45	0x2C	DB-44
22	0x15	DB-21	46	0x2D	DB-45
23	0x16	DB-22	47	0x2E	DB-46
24	0x17	DB-23	48	0x2F	DB-47

*3: Error Reason Code

No	Code	Failure Cause	Failure Name	Remarks
1	0x50	DBPS Failure	PSALM0	
2	0x51		PSALM1	
3	0x60	DBPS input Voltage adnormality	PSFLT0	
4	0x61		PSFLT1	
5	0x70	ENC Failure	I2C#0_ACS	
6	0x71		I2C#1_ACS	
7	0x72		I2C#2_ACS	
8	0x73		I2C#3_ACS	
9	0x74		I2C#4_ACS	
10	0x75		I2C#5_ACS	
11	0x76		I2C#6_ACS	
12	0x77		I2C#7_ACS	
13	0x80		Expander adnormality	
14	0xA0		CPLD_ACC_RTY	
15	0xA1		I2C_ACC_RTY	
16	0xB0		SXP adnormality	
17	0xC0		CABLE_FAIL0	
18	0xC1		CABLE_FAIL1	
19	0xE0		I2C#0_HIT	
20	0xE1		I2C#1_HIT	
21	0xE2		I2C#2_HIT	
22	0xE3		I2C#3_HIT	
23	0xE4		I2C#4_HIT	
24	0xE5		I2C#5_HIT	
25	0xE6		I2C#6_HIT	
26	0xE7		I2C#7_HIT	

5.18 Byte27 = F1 (Micro-program Detected Cache Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	Separate	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'F')				Message (x'1')			
28	Queue transition in progress (Side A)	Queue transition in progress (Side B)	Free SGCB queue operation in progress	Free SLCB queue operation in progress	Free GRPT queue operation in progress	'000'		
29	Not used							(*1)
	VDEV number (high)							(*1)
2A	Queue type							(*1)
	VDEV number (low)		Slot number (high)					(*1)
2B	Queue number (high)							(*1)
	Slot number (middle)							(*1)
2C	Queue number (low)							(*1)
	Slow number (low)							(*1)
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used							
31	Module ID							
32	Routine ID							
33	Package number				Error code (invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF1')							
37								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C								
	Reserved							
3F								
40								
	Error information							
7F								

*1: When byte 28, bit2-4 = 000, upper value is valid.
When byte 28, bit2-4 ≠ 000, lower value is valid.

5.19 Byte27 = F5 (Remote Copy/Global-active Device Paths Removed/Restored)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'F')				Message (x'5')			
28	Reason code (*1)							
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C	RCU Sequence No.							
2D								
2E								
2F	MCU Manufacture code/Factory code							
30								
31	MCU Sequence No.							
32								
33								
34	SSID of self storage system							
35								
36	Symptom code (x'38')							
37	Symptom code (x'87')							
38	Log and message control							
39	Duplex pair failure	Program action code						
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

- *1: Reason code
 - x'01' : One or more Remote Copy/global-active device paths removed unexpectedly
 - x'02' : One or more Remote Copy/global-active device paths restored
 - x'03' : All Remote Copy/global-active device paths removed unexpectedly

5.20 Byte27 = FA (NVS Terminated)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'F')				Message (x'A')			
28	Reason code (x'02': NVS failure)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	Package number				Error code (invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFFA')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Key code (for reconnection)							
41	Format/Message (for reconnection)							
42	Module ID (for reconnection)							
43	Routine ID (for reconnection)							
44	Not used							
7F								

5.21 Byte27 = FB (TrueCopy for Pair Suspend)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'F')				Message (x'B')			
28	R-VOL Suspend	R-VOL failure	Reason code (*1)					
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C								
2D	RCU Sequence No.							
2E								
2F	MCU Manufacture code/Factory code							
30								
31								
32	MCU Sequence No.							
33								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
34	SSID of self storage system							
35								
36	Symptom code (x'FE')							
37	Symptom code (same byte #28)							
38	Log and message control							
39	Duplex pair failure	Program action code						
3A	Configuration information							
3B								
3C	Reserved							
3F								
40								
	Error information							
7F								

*1: Reason code

x'04'-x'2F': Reserved

x'30' : Pair suspended. MCU device write error.

x'31' : Pair suspended. RCU storage system error/or MCU storage system error.

x'32' : Pair suspended. RCU device communication error.

x'33' : Pair suspended (Critical device status). All write command is rejected until the pair is re-established.

x'34' : Pair suspended. RCU device is not ready (intervention required).

5.22 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPAPATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26								
27	Format (x'F')				Message (x'F')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FFFF')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.22.1 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support) Byte28-31
Detail

(1) Byte28 = 70 : Boot flash memory error

	0	1	2	3	4	5	6	7
28	Subcode : x'70' : Boot flash memory error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(2) Byte28 = 71 : Cache flash memory error

	0	1	2	3	4	5	6	7
28	Subcode : x'71' : Cache flash memory error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 72 : Cache flash memory type error

	0	1	2	3	4	5	6	7
28	Subcode : x'72' : Cache flash memory type error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(4) Byte28 = C0 : SFP_TxFault(FC)

	0	1	2	3	4	5	6	7
28	Subcode : x'C0' : SFP_TxFault_FC)							
29	MPID (Reported SSB)							
2A	Clustor number for TxFault							
2B	Slot number for TxFault							
2C	Port number for TxFault							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(5) Byte28 = C1 : SFP_TxFault(iSCSI)

	0	1	2	3	4	5	6	7
28	Subcode : x'C1' : SFP_TxFault(iSCSI)							
29	MPID (Reported SSB)							
2A	Clustor number for TxFault							
2B	Slot number for TxFault							
2C	Port number for TxFault							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.22.2 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support) Byte40-7F Detail

(1) Byte28 = 70 : Boot flash memory error

	0	1	2	3	4	5	6	7
40	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
42	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
43	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
49	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4A	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4B	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
4C	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
4D	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
4E	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
4F	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
52	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
53	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
54	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
56	Not used							
57	Not used							
58	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	B0
59	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5A	CPU0_PA0_LNKSTS2[15:8]							
5B	CPU0_PA0_LNKSTS2[7:0]							
5C	CPU0_PA0_ERRSID[31:24]							
5D	CPU0_PA0_ERRSID[23:16]							
5E	CPU0_PA0_ERRSID[15:8]							
5F	CPU0_PA0_ERRSID[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_HDRLOG0[31:24]							
61	CPU0_PA0_HDRLOG0[23:16]							
62	CPU0_PA0_HDRLOG0[15:8]							
63	CPU0_PA0_HDRLOG0[7:0]							
64	CPU0_PA0_HDRLOG1[31:24]							
65	CPU0_PA0_HDRLOG1[23:16]							
66	CPU0_PA0_HDRLOG1[15:8]							
67	CPU0_PA0_HDRLOG1[7:0]							
68	CPU0_PA0_HDRLOG2[31:24]							
69	CPU0_PA0_HDRLOG2[23:16]							
6A	CPU0_PA0_HDRLOG2[15:8]							
6B	CPU0_PA0_HDRLOG2[7:0]							
6C	CPU0_PA0_HDRLOG3[31:24]							
6D	CPU0_PA0_HDRLOG3[23:16]							
6E	CPU0_PA0_HDRLOG3[15:8]							
6F	CPU0_PA0_HDRLOG3[7:0]							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	PCH_SPI_HSFSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	PCH_SPI_HSFSTS[7:0]							
	Reserved	Reserved	In progress	Reserved	Reserved	Reserved	Error	Done
72	PCH_SPI_HSFC[15:8]							
	Reserved	Reserved	Byte count					
73	PCH_SPI_HSFC[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	State		Go
74	Error address of Boot flash memory address [31:24]							
75	Error address of Boot flash memory address [23:16]							
76	Error address of Boot flash memory address [15:8]							
77	Error address of Boot flash memory address [7:0]							
78	PCH_SPI_SSFS[31:24]							
79	PCH_SPI_SSFS[23:16]							
7A	PCH_SPI_SSFS[15:8]							
7B	PCH_SPI_SSFS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Done	Reserved	In Progress
7C	PCH_LPC_BIOS_CNTL							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Write enable
7D	Not used							
7E	Not used							
7F	Not used							

(2) Byte28 = 71 : Cache flash memory error

	0	1	2	3	4	5	6	7
40	SBDERRLOG[0]							
41	SBDERRLOG[1]							
42	SBDERRLOG[2]							
43	SBDERRLOG[3]							
44	SBDERRLOG[4]							
45	SBDERRLOG[5]							
46	SBDERRLOG[6]							
47	SBDERRLOG[7]							
48	SBDERRLOG[8]							
49	SBDERRLOG[9]							
4A	SBDERRLOG[10]							
4B	SBDERRLOG[11]							
4C	SBDERRLOG[12]							
4D	SBDERRLOG[13]							
4E	SBDERRLOG[14]							
4F	SBDERRLOG[15]							

(To be continued)

SSBLOG05-3760

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	SBDERRLOG[16]							
51	SBDERRLOG[17]							
52	SBDERRLOG[18]							
53	SBDERRLOG[19]							
54	SBDERRLOG[20]							
55	SBDERRLOG[21]							
56	SBDERRLOG[22]							
57	SBDERRLOG[23]							
58	SBDERRLOG[24]							
59	SBDERRLOG[25]							
5A	SBDERRLOG[26]							
5B	SBDERRLOG[27]							
5C	SBDERRLOG[28]							
5D	SBDERRLOG[29]							
5E	SBDERRLOG[30]							
5F	SBDERRLOG[31]							

(To be continued)

SSBLOG05-3770

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	SBDERRLOG[32]							
61	SBDERRLOG[33]							
62	SBDERRLOG[34]							
63	SBDERRLOG[35]							
64	SBDERRLOG[36]							
65	SBDERRLOG[37]							
66	SBDERRLOG[38]							
67	SBDERRLOG[39]							
68	SBDERRLOG[40]							
69	SBDERRLOG[41]							
6A	SBDERRLOG[42]							
6B	SBDERRLOG[43]							
6C	SBDERRLOG[44]							
6D	SBDERRLOG[45]							
6E	SBDERRLOG[46]							
6F	SBDERRLOG[47]							

(To be continued)

SSBLOG05-3780

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	SBDERRLOG[48]							
71	SBDERRLOG[49]							
72	SBDERRLOG[50]							
73	SBDERRLOG[51]							
74	SBDERRLOG[52]							
75	SBDERRLOG[53]							
76	SBDERRLOG[54]							
77	SBDERRLOG[55]							
78	SBDERRLOG[56]							
79	SBDERRLOG[57]							
7A	SBDERRLOG[58]							
7B	SBDERRLOG[59]							
7C	SBDERRLOG[60]							
7D	SBDERRLOG[61]							
7E	SBDERRLOG[62]							
7F	SBDERRLOG[63]							

(3) Byte28 = 72 : Cache flash memory type error

	0	1	2	3	4	5	6	7
40	CFM0 Capacity							
41	CFM1 Capacity							
42	Not used							
43	Not used							
44	Not used							
45	Not used							
46	Not used							
47	Not used							
48	CFM_Type							
49								
4A								
4B								
4C	CFM_SPEC_CODE1							
4D								
4E								
4F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	CFM_SPEC2							
51								
52								
53								
54	CFM_vender_device_type							
55								
56								
57								
58	CFM_SPEC_CODE							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = C0 : SFP_TxFault(FC)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Gigabit Ether Spec.							
45	Fibre Channel Speed							
46	Parts Name 1							
47	Parts Name 2							
48	Parts Name 3							
49	Parts Name 4							
4A	Parts Name 5							
4B	Parts Name 6							
4C	Parts Name 7							
4D	Parts Name 8							
4E	Parts Name 9							
4F	Parts Name 10							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Parts Name 11							
51	Parts Name 12							
52	Parts Name 13							
53	Parts Name 14							
54	Parts Name 15							
55	Parts Name 16							
56	Wave Length 1							
57	Wave Length 2							
58	Serial Number 1							
59	Serial Number 2							
5A	Serial Number 3							
5B	Serial Number 4							
5C	Serial Number 5							
5D	Serial Number 6							
5E	Serial Number 7							
5F	Serial Number 8							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Serial Number 9							
61	Serial Number 10							
62	Serial Number 11							
63	Serial Number 12							
64	Serial Number 13							
65	Serial Number 14							
66	Serial Number 15							
67	Serial Number 16							
68	Custom Code 1							
69	Custom Code 2							
6A	Alarm/Warning 1							
6B	Alarm/Warning 2							
6C	Alarm/Warning 3							
6D	Alarm/Warning 4							
6E	Alarm/Warning 5							
6F	Alarm/Warning 6							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Alarm/Warning 7							
71	Alarm/Warning 8							
72	Custom Code 1							
73	Custom Code 2							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = C1 : SFP_TxFault(iSCSI)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Gigabit Ether Spec.							
45	Ether Speed							
46	Parts Name 1							
47	Parts Name 2							
48	Parts Name 3							
49	Parts Name 4							
4A	Parts Name 5							
4B	Parts Name 6							
4C	Parts Name 7							
4D	Parts Name 8							
4E	Parts Name 9							
4F	Parts Name 10							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
50	Parts Name 11							
51	Parts Name 12							
52	Parts Name 13							
53	Parts Name 14							
54	Parts Name 15							
55	Parts Name 16							
56	Wave Length 1							
57	Wave Length 2							
58	Serial Number 1							
59	Serial Number 2							
5A	Serial Number 3							
5B	Serial Number 4							
5C	Serial Number 5							
5D	Serial Number 6							
5E	Serial Number 7							
5F	Serial Number 8							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
60	Serial Number 9							
61	Serial Number 10							
62	Serial Number 11							
63	Serial Number 12							
64	Serial Number 13							
65	Serial Number 14							
66	Serial Number 15							
67	Serial Number 16							
68	Custom Code 1							
69	Custom Code 2							
6A	Alarm/Warning 1							
6B	Alarm/Warning 2							
6C	Alarm/Warning 3							
6D	Alarm/Warning 4							
6E	Alarm/Warning 5							
6F	Alarm/Warning 6							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
70	Alarm/Warning 7							
71	Alarm/Warning 8							
72	Custom Code 1							
73	Custom Code 2							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

5.23 Byte36: bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit-2 = 7 (SPC Report Error)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	Processor ID							
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26					Format (x'1')			
27	Type Code('111': Drive Report Error)			0	0	0	0	0
28	Error Component							
29								
2A	SCSI command ((see *1 of SSBLOG05-3930)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV#							
2F	Reserved							
33								
34	SSID of self storage system							
35								
36	Exception Class				Reserved			
37	RDEV#							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Time of occurrence							
41								
42								
43								
44	Free area Type Code							
45	Not used							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	Not used							
58	SCSI Command							
59	SCSI Command LBA							
5A								
5B								
5C								
5D								
5E	SCSI Command Length							
5F								
60	Error Code							
61								
62								
63								
64	Error Code (EVENT)							
65								
66								
67								
68	Error Code (COMPLITION)							
69								
6A								
6B								
6C	Not used							
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

*1: SCSI command code

Code	Command Name	Code	Command Name
00	TEST UNIT READY	2E	WRITE AND VERIFY(10)
04	FORMAT UNIT	3B	WRITE BUFFER
07	REASSIGN BLOCKS	55	MODE SELECT
12	INQUIRY	5A	MODE SENSE
1B	START/STOP UNIT	88	READ(16)
1C	RECEIVE DIAGNOSTIC RESULT	8A	WRITE(16)
1D	SEND DIAGNOSTIC	8E	WRITE AND VERIFY(16)
25	READ CAPACITY(10)	9E	READ CAPACITY(16)
28	READ(10)	FF	COMMAND Unfixed
2A	WRITE(10)		

5.24 Byte36: bit0-3 = D & Byte26: bit4-7 = 0 & Byte27 : bit-2 = 0 (Drive Detected Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	Processor ID							
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26					Format (x'0')			
27	Type Code('000': Drive Report Error)			0	Sense Kye			
28	Reserved							
29								
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV#							
2F	Reserved							
33								
34	SSID of self storage system							
35								
36	Exception Class				Reserved			
37	RDEV#							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Time of occurrence							
41								
42								
43								
44	Free area Type Code							
45	Not used							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	SCSI Status							
58	Sense Data (8byte)							
59								
5F								
60	SCSI RSP acquisition result							
61	Sense Key							
62	Additional Sense Code							
63	Additional Sense Code Qualifier							
64	Non-transfer number of bytes							
65								
66								
67								
68	Obstacle LBA of Sense Data							
69								
6A								
6B								
6C								
6D								
6E								
6F								
70	CDB							
71								
7F								

5.25 Byte36: bit0-3 = E & Byte26: bit4-7 = 0 (Drive Detected Failure)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format ID							
07	Processor ID							
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SRAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
 26					Format (x'0')			
27	Type Code('000': Drive Report Error)			0	Sense Key			
28	Reserved							
29								
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV#							
2F 33	Reserved							
34	SSID of self storage system							
35								
36	Exception Class				Reserved			
37	RDEV#							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C 3F	Reserved							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
40	Time of occurrence							
41								
42								
43								
44	Free area Type Code							
45	Not used							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	SCSI Status							
58	Sense Data (8byte)							
5F								
60	SCSI RSP acquisition result							
61	Sense Key							
62	Additional Sense Code							
63	Additional Sense Code Qualifier							
64	Non-transfer number of bytes							
65								
66								
67								
68	Obstacle LBA of Sense Data							
69								
6A								
6B								
6C								
6D								
6E								
6F								
70	CDB							
7F								

5.26 Byte26: bit4-7 = F & Byte3C = F1 (DKC SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Reserved							
26					Format (x'F')			
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*1)				Range affected by repair (*2)			
29	Range affected by repair (*3)		Failure SPs (*4)	SPs in SSID (*5)	Failure SSIDs (*6)	Affected SSIDs (*7)	Status (*8)	0
2A	Resent SIM (*9)	'0000000'						
2B	Hardware level (*10)							
2C								
2D	Isolation code (*11)							
2E	SSB ID (*12)	SIM sequence number						SVP ID (*13)
2F	Controller Chassis serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*14)							
39	Program action code (x'10')							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'F1')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: Range subject to the error

x'0' : Not used

x'1' : Error range not known

x'2' : SP error

x'3' : CHL-I error

x'4' : Not affect performance

x'5-F' : Not used

*2: Range affected by repair

x'0' : Not used

x'1' : Repair range not known

x'2' : Repair does not affect performance

x'3' : SP disabled during repair

x'4' : SSID disabled during repair

x'5-F' : Not used

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Failure SPs

0 : 1SP

1 : 2SP

*5: SPs in SSID

0 : 2SP

1 : 4SP

*6: Failure SSIDs

0 : 1SSID

1 : 2SSID

*7: Affected SSIDs

0 : 1SSID

1 : 2SSID

*8: Status

0 : Reduction

1 : Stop

*9: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*10: Hardware level

Bit 0 : Serial channel hardware level

When Bit 0 is 0

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per Cluster

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

When Bit 0 is 1

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs per Cluster

0000 : Parallel =4, serial =0

0001 : Parallel =8, serial =0

0010 : Parallel =4, serial =2

0100 : Parallel =4, serial =4

0110 : Parallel =0, serial =2

1000 : Parallel =0, serial =4

1010 : Parallel =0, serial =8

1100 : Parallel =0, serial =6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non Cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*11: Isolation code

Byte 2D $\neq$ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*12: SSB ID

0 : SVP

1 : SP

*13: SVP ID

0 : SVP1

1 : SVP2

*14: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditionl log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.27 Byte26: bit4-7 = F & Byte3C = F2 (CACHE SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26					Format (x'F')			
27	SIM sence code ID (x'E0')							
28	Range subject to the error (*1)				Range affected by repair (*2)			
29	Failure level (*3)		Failure SPs (*4)	SPs in SSID (*5)	Failure SSIDs (*6)	Affected SSIDs (*7)	Status (*8)	0
2A	Resent SIM (*9)	'0000000'						
2B	Hardware level (*10)							
2C								
2D	Isolation code (*11)							
2E	SSB ID (*12)	SIM sequence number						SVP ID (*13)
2F	Controller Chassis serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*14)							
39	Program action code (x'10')							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'F2')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: Range subject to the error

x'0' : Not used

x'1' : Error range not known

x'2' : SP error

x'3' : CHL-I error

x'4' : Not affect performance

x'5-F' : Not used

*2: Range affected by repair

x'0' : Not used

x'1' : Repair range not known

x'2' : Repair does not affect performance

x'3' : SP disabled during repair

x'4' : SSID disabled during repair

x'5-F' : Not used

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Failure SPs

0 : 1SP

1 : 2SP

*5: SPs in SSID

0 : 2SP

1 : 4SP

*6: Failure SSIDs

0 : 1SSID

1 : 2SSID

*7: Affected SSIDs

0 : 1SSID

1 : 2SSID

*8: Status

0 : Reduction

1 : Stop

*9: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*10: Hardware level

Bit 0 : Serial channel hardware level

When Bit 0 is 0

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per Cluster

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

When Bit 0 is 1

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs per Cluster

0000 : Parallel =4, serial =0

0001 : Parallel =8, serial =0

0010 : Parallel =4, serial =2

0100 : Parallel =4, serial =4

0110 : Parallel =0, serial =2

1000 : Parallel =0, serial =4

1010 : Parallel =0, serial =8

1100 : Parallel =0, serial =6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non Cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*11: Isolation code

Byte 2D $\neq$ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*12: SSB ID

0 : SVP

1 : SP

*13: SVP ID

0 : SVP1

1 : SVP2

*14: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditionl log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.28 Byte26: bit4-7 = F & Byte3C = FE (Device SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
26					Format (x'F')			
27	SSB ID (*1)	SIM sequence number					SVP ID (*2)	
28	Device exception (x'1')				Service message (x'1': Action, x'C': No Action)			
29	Failure level (*3)		'000000'					
2A	Resent SIM (*4)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See SIM RC SECTION (SIMRC00-00))							
2E	Action flag (x'05')							
2F	Drive serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See SIM RC SECTION (SIMRC00-00))							
37								
38	Log and message control (*5)							
39	Program action code (x'10')							

(To be continued)

(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FE')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: SSB ID

0 : SVP

1 : SP

*2: SVP ID

0 : SVP1

1 : SVP2

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*5: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.29 Byte26: bit4-7 = F & Byte3C = FF (MEDIA SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved
							VLD	
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Reserved							
26					Format (x'F')			
27	SSB ID (*1)	SIM sequence number					SVP ID (*2)	
28	Device exception (x'1')				Service message (x'1')			
29	Failure level (*3)		Media Maintenance Procedure Number (Correctable: x'07', Uncorrectable: x'05')					
2A	Resent SIM (*4)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Microcode Corequisite Indicator (x'01')							
2D	Device address (See SIM RC SECTTION (SIMRC00-00))							
2E	Action flag (Correctable : x'87', Uncorrectable : x'85')							
2F	Drive serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*5)							
39	Program action code (x'10')							

(To be continued)

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(Continued from preceding sheet)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FF')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: SSB ID

0 : SVP

1 : SP

*2: SVP ID

0 : SVP1

1 : SVP2

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*5: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent