

SSBLOG SECTION

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5.27 Byte36 = 6F (Storage system Information)	SSBLOG05-4850
5.28 Byte36: bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit-2 = 7 (SPC Report Error)	SSBLOG05-4870
5.29 Byte36: bit0-3 = D & Byte26: bit4-7 = 0 & Byte27 : bit-2 = 0 (Drive Detected Failure)	SSBLOG05-4900
5.30 Byte36: bit0-3 = E & Byte26: bit4-7 = 0 (Drive Detected Failure)	SSBLOG05-4920
5.31 Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited).....	SSBLOG05-4940
5.32 Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity)	SSBLOG05-4960
5.33 Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready)	SSBLOG05-4980
5.34 Byte26: bit4-7 = F & Byte3C = F1 (DKC SIM).....	SSBLOG05-5000
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SSBLOG00-30

5.36 Byte26: bit4-7 = F & Byte3C = FE (Device SIM)..... SSBLOG05-5100

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6. CUDG3 Error Code Table SSBLOG06-10

1. Introduction

The internal SSB (SSB Log) information is presented as either 24-byte compatibility SSB format or ECKD 32-byte SSB format. The 24-byte SSB is used for DKC and the 32-byte SSB is used for DKU.

The internal SSB is divided into the following sections:

- Internal SSB common information
This information is used for SSB management, and same for all formats.
- External SSB information presented to host.
This is 32 bytes sense information presented to host as SSB.
- Format-dependent information
This is auxiliary information for error analysis.

NOTE: The byte offset in SSB logs is described in hexadecimal number.

This section is corresponding to SSB log on SVP.

If you want to know about SSB which was reported to host, you should see SSB SECTION.

Figure 1-1 Outline of 24-byte Format Internal SSB

SSB Pointer (4 bytes)		Out of description in the manual	
Reserved (28 bytes)			
00	Time stamp	Internal SSB common information	
05			
06	Format Message		
07	MP#		
08	System error code		
09			
0A	JCB#		
0B			
0C	REQ ID		
0D	SSB type		
0E	VALID flag		
0F	SSB type		
10	JCB ID		
11	LDEV# high (Logical DKC#)		
12	LDEV# low (LDEV# in Logical DKC)		
13			
14	CDEV# / PORT		
15	RDEV# / PORT		
16	Internal SSB log number		
17			
18	Related failure log number		
19			
1A	Related SIM log number		
1B			
1C	Related SSB log number		
1D			
1E	Reserved area		
1F	SCB (SSB CONTROL BLOCK)		
20	Byte 0 to 6 of 24 byte formatted SSB reported to host	Same as a SSB reported to host	
26			
27	Format		Message
28	Byte 8 to 23 of 24 byte formatted SSB reported to host		
37			
38	Byte 24 to 31 of 24 byte formatted SSB reported to host		
3F			
40	Free area	Format-dependent information of internal SSB	
7F			

Figure 1-2 Outline of 32-byte Format Internal SSB

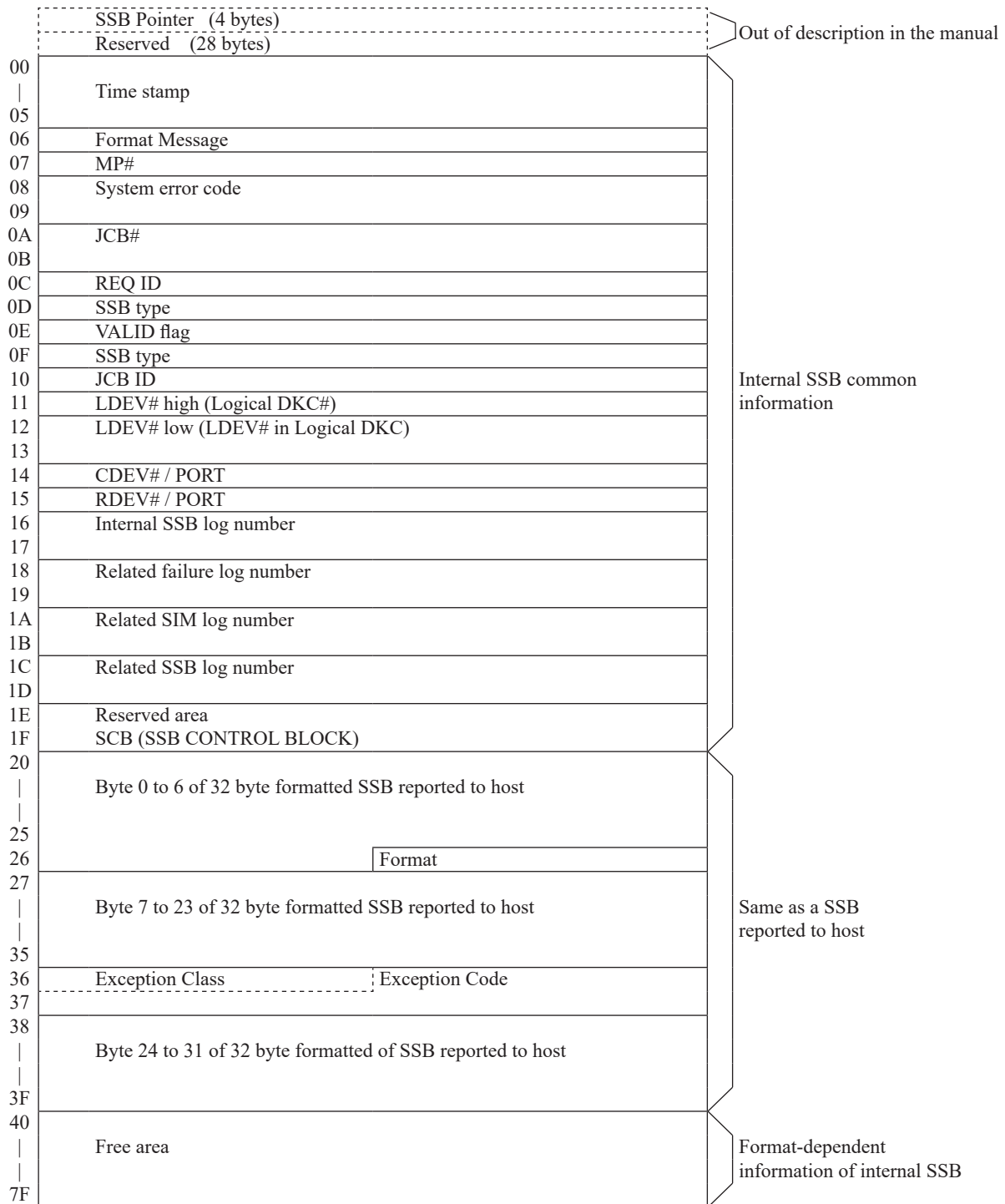
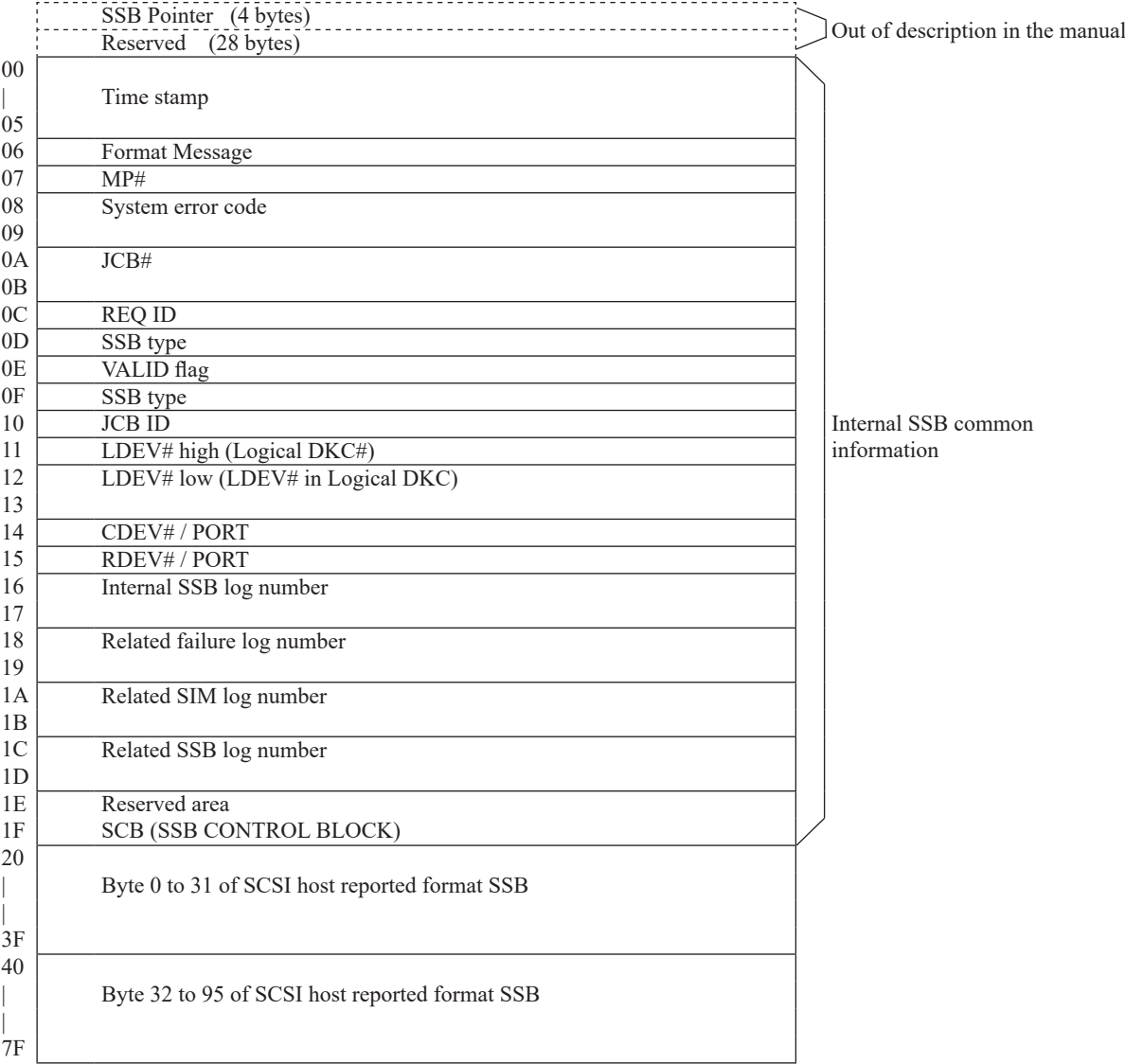


Figure 1-3 Overview of SCSI Host Report Format Internal SSB



2. Classification of Internal SSB

The internal SSB is classified crudely as the 24 byte format and the 32 byte format. The further classification (The search method for SSB table) is shown [Figure 2-2](#).

Figure 2-1 The outline

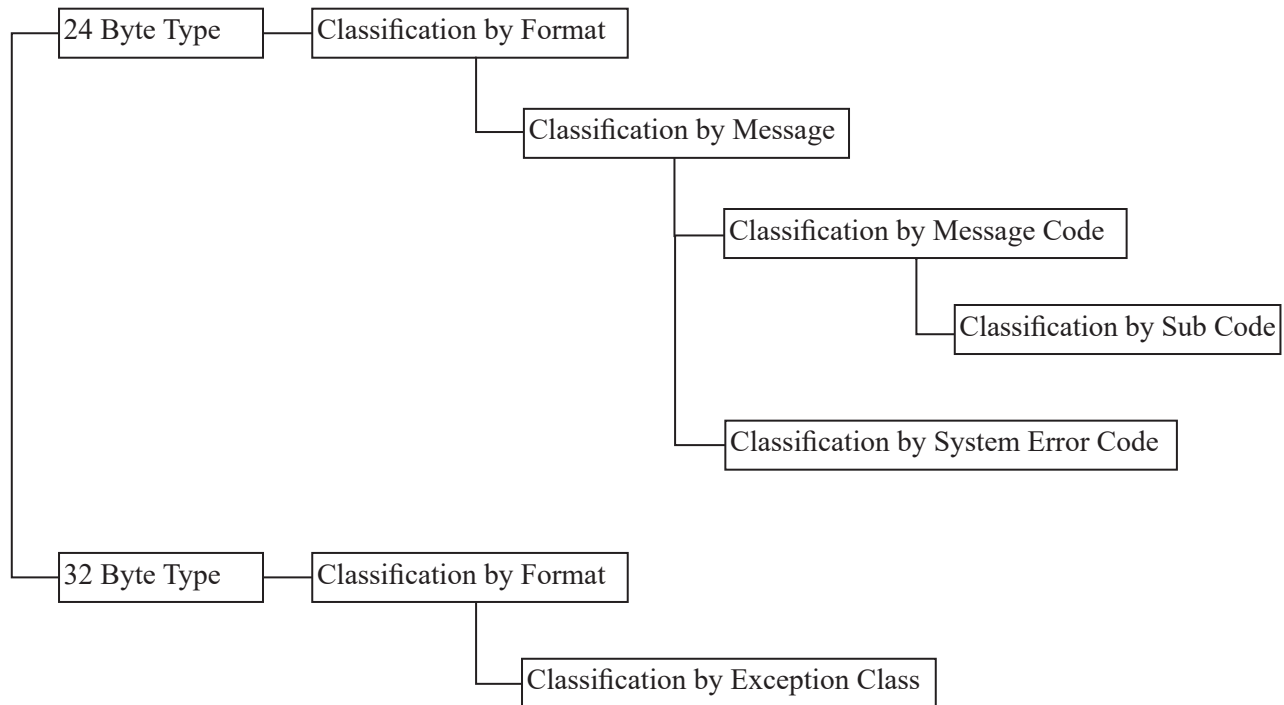
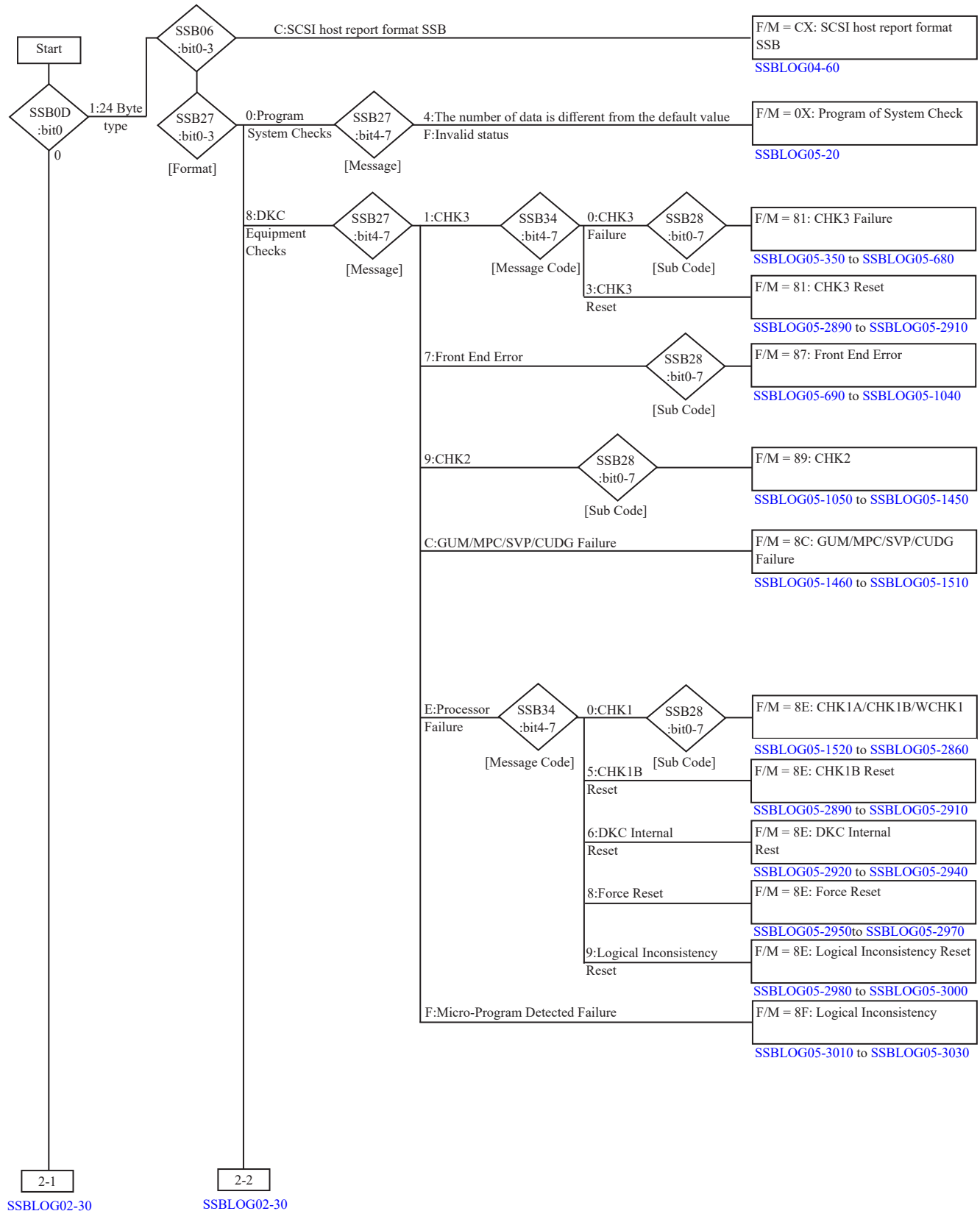
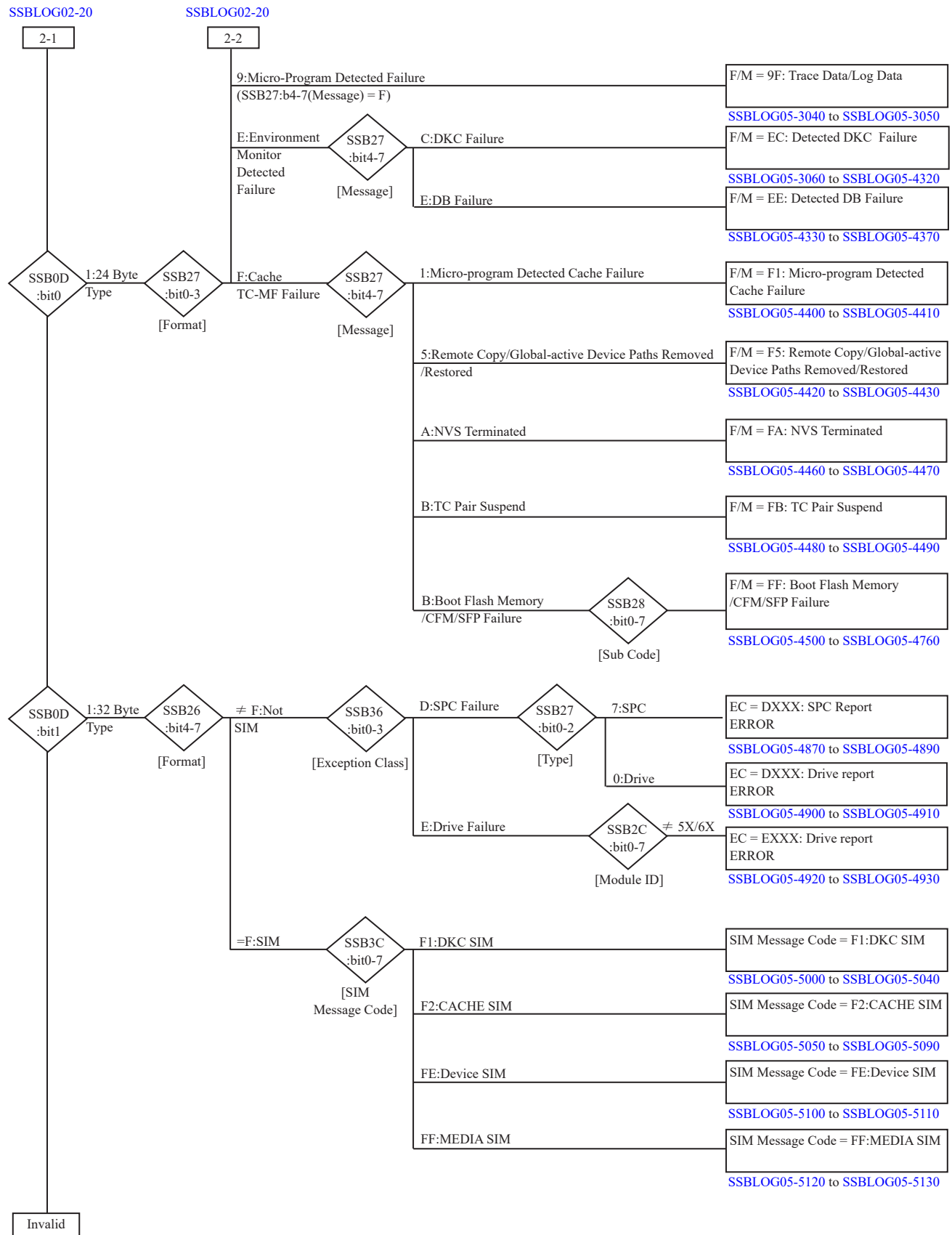


Figure 2-2 Search method for SSB tables





3. Common Information Description

00	0	1	2	3	4	5	6	7
01	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT							
15	RDEV# / PORT							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Host SSB Information							
3F								
40	Internal SSB Format-Dependent Information							
7F								

Table 3-1 Common Information

Byte	Bit	Meaning	Remarks
00 to 05		Time stamp The time error occurred	
06		Format Message	
07		Processor number (Internal ID) x'00' to x'7F': CHB0x00 to 0x7F x'80' to x'BF': DKB0x80 to 0xBF	
08 to 09		System error code	
	0 to 7	Module ID	
	8 to 15	Routine ID	
0A to 0B		JCB#	
0C		REQ ID	
0D		SSB type	
	0	24 byte type flag 0: Invalid 1: 24 byte type SSB	
	1	32 byte type flag 0: Invalid 1: 32 byte type SSB	
	2	Flag for SCSI host processor detected the error 0: SSB detected by CHB and DKB 1: SSB detected by CHS	
	3	Remote copy flag 0: Invalid 1: Remote copy valid	
	4	Separately stored log flag 0: General log 1: Separately stored log	
	5	Host report flag 0: Pending 1: Reported	
	6	SVP report flag 0: Pending 1: Reported	
	7	Forcible log flag 0: Normal 1: Forcibly ejected log	
0E		Valid flag	
	0	LDEV number valid or invalid 0: Invalid 1: Valid	
	1	PDEV number valid or invalid 0: Invalid 1: Valid	
	2	SSB number valid or invalid 0: Invalid 1: Valid	
	3	Related failure log number valid or invalid 0: Invalid 1: Valid	

(To be continued)

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Byte	Bit	Meaning	Remarks
0E		Valid flag	
	4	Related SIM log number valid or invalid 0: Invalid 1: Valid	
	5	Related SSB log number valid or invalid 0: Invalid 1: Valid	
	6	PK information valid or invalid 0: Invalid 1: Valid	
	7	Reserve	
0F		SSB type	
	0	TC-MF type SSB flag 0: Sync TC-MF SSB 1: Semi Sync TC-MF SSB	
	1	RCU SIM flag 0: Invalid 1: RCU SIM flag	
	2 to 7	Reserved	
10		JCB ID	
11		LDEV# high (Logical DKC#)	
12 to 13		LDEV# low (LDEV# in Logical DKC)	
14		CDEV# / PORT	
15		RDEV# / PORT	
16 to 17		Internal SSB log number	
18 to 19		Related Failure log number	
1A to 1B		Related SIM log number	
1C to 1D		Related SSB log number	
1E		Reserved area	
1F		SSB control block Use when assemble BASIC SSB of Host SSB. See Table 3-2 "SSB Key Code".	

Table 3-2 SSB Key Code

Code	Basic SSB	Meaning	Remarks
00	000000	Dummy SSB	
01	800000	Command reject (A program error or invalid link between BAD and ALT)	
02	400000	Intervention required (An addressed device is offline or not connect)	
03	200000	Bus out check (Channel bus out parity error)	
04	100000	Equipment check (Equipment check in storage system)	
05	080000	Data check (Uncorrectable: retry failed or retry inhibited)	
06	040000	Overrun (Command retry failed when service overrun occurred)	
07	004000	Invalid track format (Track capacity over or track format error)	
08	002000	End of cylinder (A multi track operation outside LOCATE domain)	
09	000800	No record found	
0A	000400	File protected (A seek or multi track operation violates the file mask or extent)	
0B	100200	Equipment check and write inhibited (A write command on the write inhibited path by a DIAG CTL command)	
0C	080040	Data check and correctable (The retry succeeded for data check in PCI fetch mode)	
0D	000020	First log mode error	
0E	800400	Command rejected and file protected (A multi track operation outside primary track)	
0F	800200	Command reject and write inhibited	

(To be continued)

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Code	Basic SSB	Meaning	Remarks
10	108000	Equipment check and permanent error (A retry failed for the equipment check)	
11	088000	Data check and permanent error (A retry failed for the data check)	
12	009010	SIM	
13	000000	Reserved	
14	000104	Imprecise ending (This error is for a previously completed CCW)	
15	800104	Command reject and imprecise ending	
16	400104	Intervention required and imprecise ending	
17	200104	Bus out check and imprecise ending	
18	100104	Equipment check and imprecise ending	
19	080104	Data check and imprecise ending	
1A	040104	Overflow and imprecise ending	
1B	004104	Invalid track format and imprecise ending	
1C	002104	End of cylinder and imprecise ending	
1D	000904	No record found and imprecise ending	
1E	000504	File protected and imprecise ending	
1F	100304	Equipment check and write inhibited and imprecise ending	

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Code	Basic SSB	Meaning	Remarks
20	080144	Equipment check and correctable and imprecise ending	
21	000124	First log mode error and imprecise ending	
22	800504	Command reject and file protected and imprecise ending	
23	800304	Command reject and write inhibited and imprecise ending	
24	108104	Equipment check and permanent error and imprecise ending	
25	088104	Data check and permanent error and imprecise ending	
26	009114	SIM and imprecise ending	
27	000000	Reserved	
28	808000	Command reject and permanent error	
29	00C000	Permanent error and invalid track format	
2A	008800	Permanent error and request inhibit write	
2B	088200	Data check and permanent error and write protected	
2C	800001	Command reject and Sense information for Remote Copy Volume	
2D	000000	Reserved	
2E	000000	Reserved	
2F	000000	Reserved	

(To be continued)

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Code	Basic SSB	Meaning	Remarks
30	000000	Reserved	
31	000000	Reserved	
32	010000	Incomplete domain (Received commands < LOCATE command parameter)	
33	804000	Command reject and invalid track format (A TLF mismatch in CKD conversion mode)	
34	000000	Reserved	
35	100010	Equipment check and environmental data present (Status change pending)	
36	800010	Command reject and environmental data present	
37	000000	Reserved	
38	000000	Reserved	
39	000000	Reserved	
3A	000000	Reserved	
3B	000000	Reserved	
3C	000000	Reserved	
3D	000000	Reserved	
3E	000000	Reserved	
3F	000000	Reserved	

4. Host SSB information and internal SSB format-dependent information description

4.1 24-byte type SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common Information							
20	Command reject	Intervention REQ	Bus out check	Equipment check	Data check	Overrun	Not used	Incomplete domain
21	Permanent error	Invalid track FMT	End of cylinder	Operator message	No Record found	File protect	Write inhibit	Imprecise ending
22	Write INH request	Correctable	1st log mode ERR	Environmental DAT	Not used	Imprecise ending	Not used	Remote Copy Volume
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (See next page)				Message (See next page)			
28 37	Host SSB format-dependent information							
38	Log and message control							
39	Program action code							
3A	Dual frame	EDCC Mode (1)	Duplex pair (0)	S-volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR
3B	24 Byte SSB (1)	DEV ADR valid	TRK ADR valid	28 Bit Cylinder valid	3380 TRK COMPT (0)	Not used	Storage path number	
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40 7F	Internal SSB format-dependent information							

*1: Configuration information

*2: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB SECTION.

*3: Basic SSB

*4: Basic information

Table 4-1 Format and Message

Format	Message	Meaning	
0 (program /system check)	0	No message	
	1	Invalid command	
	2	Invalid sequence	
	3	Data short	
	4	Invalid data	
	5	Violate file mask	
	6	Channel disconnect retry	
	7	Invalid retry command	
	8	System reset	
	9	Not used	
	A	Overrun	
	B	Invalid link between bad track and alternate track	
	C	Not used	
	D	Not used	
	E	Invalid command to secondary volume	
	F	Invalid status	
1	0	Intervention required	
	1	Not used	
	2	Drive not ready	
	3	Not used	
	4	Drive report error	
	5 to F	Not used	
3	0 to E	Not used	
	F	Reset allegiance	
4	0	Data check in HA field	
	1	Data check in C field	
	2	Data check in K field	
	3	Data check in D field	
	4	Not used	
	5	PA error	
	6 to F	Not used	
6	0 to 7	Statistics	
	8 to F	Not used	
7	0 to 3	Not used	
	4	FPC report error	
	5 to 8	Not used	
	9	SCSI bus parity error	
	A to F	Not used	

(To be continued)

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Format	Message	Meaning
8	0	Not used
	1	Shared memory failure
	2 to 8	Not used
	9	Host adaptor CHK2
	A	Disk adaptor CHK2
	B	DRR failure
	C	SVP failure
	D	Power failure
	E	Processor failure
	F	Micro-program detected error (Logical Inconsistency)
9	0 to E	Not used
	F	Micro-program detected error (Trace data/Log data)
E	0 to B	Not used
	C	Environmental monitor detected DKC failure
	D	Not used
	E	Environmental monitor detected DKU failure
	F	Not used
F	0	Operation Terminated
	1	Microprogram detected cache failure
	2	Cache failure
	3 to 4	Not used
	5	Remote Copy/global-active device Paths Removed/Restored
	6	CFW not authorized
	7 to 9	Not used
	A	NVS terminated
	B	TrueCopy for Mainframe Pair suspend
	C to E	Not used
	F	Cache/Shared memory/M-bus/F-bus/J-bus warning

4.2 32-byte type SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common Information							
20	Command reject	Intervention REQ	Bus out check	Equipment check	Data check	Not used	Not used	Incomplete domain
21	Permanent error	Invalid track FMT	End of cylinder	Operator message	No Record found	File protect	Write inhibit	Imprecise ending
22	EC = 0/1/2/3/F: Environmental data present (x'10') EC = 4/6/D/E: Storage control type (x'06': CACHE DKC)							
23	Count/Command overrun threshold is exceeded.							
24	Device address							
25	Device type (x'24': DKU871-3)							
26	SSB2/SSB5 valid	SSB4 valid	SSB29-31 valid	Not used	Format			
27 37	Host SSB format-dependent information							
38	Log and message control							
39	Program action code							
3A	Dual frame	EDCC Mode (1)	Duplex pair (0)	S-volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR
3B	32 Byte SSB (1)	Not used	Not used	Not used	3380 TRK COMPT (0)	Not used	Storage path number	
3C	Case of Byte3B Bit0=0: Message code Bit0=1: Indicate cylinder address bit 12-19.							
3D	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 20-27.							
3E	Case of Byte3B Bit0=0: Cylinder address Bit0=1: Indicate cylinder address bit 0-7.							
3F	Case of Byte3B Bit0=0: 0x00 Bit0=0: Head address Bit0=1: Indicate cylinder address bit 8-11 Bit0=1: Head address							
40 7F	Internal SSB format-dependent information							

*1: Configuration information

*2: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB SECTION.

*3: Basic SSB

*4: Basic information

Table 4-2 Exception Class (Byte36:bit0-3) and Format (Byte26:bit4-7)

Exception Class	Format	Meaning
0	0 to 2	Not used
	3	Machine condition exception
	4	I/O Program exception (Incomplete Domain)
	5 to F	Not used
2	0 to E	Not used
	F	DKC SIM
3	0 to E	Not used
	F	DKC SIM
4	0	Not used
	1	Data exception (PCI, Permanent) (*1)
	2 to E	Not used
	F	MEDIA SIM
5	0 to E	Not used
	F	DEVICE SIM/Media SIM
6	0	Not used
	1	Statistics (*1)
	2 to F	Not used
C	0 to E	Not used
	F	DKC SIM
D	0	Drive detected failure (*1)
	1	SPC detected failure (*1)
	0 to E	Not used
	F	DKC SIM
E	0	Drive detected failure/LDEV blockade/PIN VOL detected/ LDEV not ready/Write inhibited (*1)/Shortage of Pool capacity
	1 to E	Not used
	F	DEVICE SIM
F	0 to E	Not used
	F	DKC SIM/CACHE SIM

*1: For DKU87I (IBM 3390 emulation mode) only.

4.3 SCSI Host Report Format SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common information (*1)							
20	Information byte valid	Error code (0x7)						
21	Segment No (0)							
22	File mark (0)	ECM (0)	ILI (0)	Not used	Sense key			
23 26	Information byte							
27	Additional sense data length (0x58)							
28 2B	Command specific information							
2C 2D	Sense code							
2E	Field Replaceable Unit (0)							
2F 30 31	Sense key specific information	Sense key specific information						
32 7F	Additional sense data valid	Additional sense data						

*1: 0xCX (X = sense key) is set to byte 7 (log type low-order byte). SSB on SVP is indicated as F/M = CX.

Table 4-3 Details of SCSI Host Report Format SSB Information

Byte	Bit	Description	Setting	Remarks
20	0	Information byte valid/invalid 0: Invalid 1: Valid	OPEN command Control	
	1 to 7	Error code (0x7) Indicates an I/O process error.		
21		Segment No. (0)		
22	0	File Mark (0)		
	1	ECM (0)		
	2	ILI (0)		
	3	No used		
	4 to 7	Sense key: For more details, see Table 4-4 .		
23 to 26		Information bytes: LBA related to sense key if information byte is valid.		
27		Additional sense data length (0x58) Indicates sense data length after byte 8 or later.		
28 to 2B		Command specific information		
2C to 2D		Sense codes		
2E		Field Replaceable Unit (0)		
2F to 31	0	Sense key specific information valid/invalid 0: Invalid 1: Valid		
	1 to 17	Sense key specific information		
32 to 7F	0	Additional sense data valid/invalid 0: Invalid 1: Valid	Error detection source	
	1 to END	Additional sense data		

Table 4-4 Sense Keys List

Sense key	Description
0	NO SENSE
1	RECOVERED ERROR
2	NOT READY
3	MEDIUM ERROR
4	HARDWARE ERROR
5	ILLIGAL REQUEST
6	UNIT ATTENTION
7	DATA PROTECT
8	BLANK CHECK (Not used)
9	VENDER UNIQUE
A	COPY ABORTED
B	ABORTED COMMAND
C	EQUAL (No use)
D	VOLUME OVERFLOW
E	MISCOMPARE
F	(RESERVED)

5. Format-Dependent Information Description

5.1 Byte27 = 0X to 7X

5.1.1 Byte27 = 0X (X ≠ 8/9/A: System/Program Check)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05								
06	PCB type							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'X' : X ≠ 8/A)			
28	Command code (for F/M ≠ F)/Reason code (for F/M = F) (*1)							
29	Detail failure information 1 (*2)							
2A								
2B								
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID (*4)							
31	Routine ID (*4)							
32	PCB number							
33	Detail failure information 2 (*3)							
34	SSID of self storage system							
35								
36	Symptom code (x'0F0X')				(x'2F00' for dummy SENSE)			
37	X=Message code							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
55								
56	Module ID (detected the error)							
57	Module ID (detected the error)							
58								
	Not used							
7F								

*1: F/M = 0F : Byte 28 Reason code

Code	Reason
00	No message
01	Trying to make cache available and cache status is pending.
02	Not used
03	Trying to make cache force unavailable and cache status is not pending with destage complete.
04	Trying to activate CFW and CFW status is pending.
05	Trying to activate caching (device) and cache status is pending.
06	Trying to destage (track) and NVS is failed.
07	Trying to make NVS unavailable and NVS is initializing.
08	Trying to deactivate caching , DFW or DISCARD (device) , and DFW status is failed.
09	Trying to force deactivate DFW (device) and status is not as follows. • EDFW status is pending or failed. • ENVS is failed and the data exists in NVS, but not in cache.
0A	Trying to make NVS available and DFW status (device) is pending or failed.
0B	Trying to make NVS available and NVS status is pending.
0C	When NVS is failed, trying to stop caching or DFW (device) and the data exists in NVS, but not in cache.
0D	Command requires NVS and NVS is unavailable.
0E	Command requires cache and cache is unavailable.
0F	Not used

(To be continued)

(Continued from the preceding page)

Code	Reason
10 to 28	Not used
29	PFS command (Set Special Interrupt Condition) is issued on an interface on which path group is not established.
2A	Trying to make path group include parallel channel and serial channel.
2B	Message buffer is full.
2C to 34	Not used
35	A Set Guaranteed Path subcommand is issued on a fenced path.
36 to 3E	Not used
3F	A Make NVS Avail for Subsystem subcommand is issued, but NVS is disable. (‘Disable NVS Capability’ is set to disable in VPD.)
40	Not used
41	A normal command is issued to a device reserved by maintenance
42 to 56	Not used
57	In the device for which the address is specified, the TrueCopy pair is in the state of Suspend. All the Write commands where Normal Authorization is shown are rejected.
58 to 78	Not used
79	The command operation was not allowed because the status was Soft Fence. The specific reason is indicated in sense byte 9.
7A to 7B	Not used
7C	The Path Group setting is not allowed because the status was SPID Fence.
7D	Inconsistency was detected in the operation of setting and cancelling the SPID Fence status. The specific reason is indicated in sense byte 9.
7E to 7F	Not used
80	A special command is issued on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
81	An attention report on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
82 to FF	Not used

*2:

(1) Issued command information when F/M = 02

B/b	0	1	2	3	4	5	6	7
29	SCH ID	SCH KEY	Locate, LC EXT	SK, SK CYL	RD IPL	RECAL	SUSP MPR	RD C
2A	DIAG CTL	DIAG CTL (LOC DOM)	DEF EXT	SET FM	SET SUB MOD	SP C	WR CKD	WR R0
2B	SET INT FID	DIAG CTL (PRE REM)	0	Write through	RD C chaining	All byte match	LC domain	ERASE execute
2C	Write through	0	0	Allow WR KD	Allow WR D	Allow WR CKD	Allow WR R0	Allow WR HA

(2) Short byte flag when F/M = 03

Byte 29 : x'00'

Byte 2A : x'40'(short byte)

Byte 2B : x'00'

Byte 2C : x'00'

(3) Mask information when F/M = 05

Byte 29 : Mask for write control

x'00' : Allows all write operation except WR HA, WR R0.

x'40' : Inhibits all write operation.

x'80' : Allows update write operation.

x'C0' : Allows all write operation.

Byte 2A : Mask for seek control

x'00' : Allows all seek commands and RECAL.

x'08' : Allows SK CYL and SK HD only.

x'10' : Allows SK HD only.

x'18' : Inhibits all seek command and multi operation.

Byte 2B : Mask for access

x'00' : Allows normal access.

x'02' : Allows device support.

x'04' : Allows diagnostic access.

x'06' : Allows device support inhibit data check correction and retry.

Byte 2C : Not used (x'00')

(4) Record address when other F/M = other than 02, 03, and 05

Byte 29 and 2A : Cylinder address

Byte 2B : Head address

Byte 2C : Record number

(5) Record address when F/M = 0F and Byte 8 = 79 or 7D

Byte 8	Byte 9	
79	01	A device in the Soft Fence status is a channel connection device, a primary device of TrueCopy for Mainframe, or a source device of FlashCopy.
	02	A device in the Soft Fence status is a secondary device of TrueCopy for Mainframe or a target device of FlashCopy.
7D	01	Inconsistent with the already registered host token when the status is set to SPID Fence.
	02	Inconsistent with the host token for the already set device when the status is set to SPID Fence.
	03	Inconsistent with the already registered host token when the SPID Fence status is cancelled.

*3: Bit 4-7 of byte 32 : Type of byte 33

x'08' : Byte 33 is cylinder address (high).

x'09' : Byte 33 is cylinder address (low).

x'0A' : Byte 33 is head address (high).

x'0B' : Byte 33 is head address (low).

x'0C' : Byte 33 is record number.

Byte 33 : One byte of CCHHR.

*4: The details information of every Module ID & Routine ID

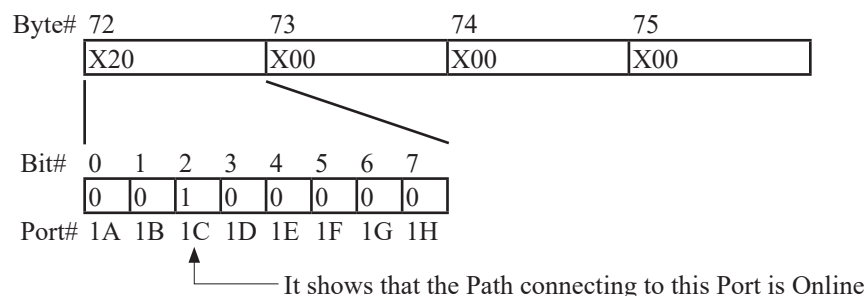
- Module ID & Routine ID = 0xC0CA : The Specified R-VOL of TC-MF is still Online from the HOST.

(Add Pair operation from the Web Console was rejected)

Check the SSB.LOG on SVP of the specified RCU. SSB containing EC = C0CA on the specified R-VOL number should be logged.

(1) Analyze the SSB.LOG

Both SSB are same format, from SSB byte 72 (0x48) though byte 75 (0x4B) show the Bit Map of Port indicating Online from the HOST. Each one bit corresponds to each port. It indicates the Port on which the valid Path Group is established for this device.



(2) Run DSF/ANALYZE-ALLCHPID to trace the Path Group ID of the Online route.

- In case that DKC Emulation Type is 2107.

Run DSF/ANALYZE with ALLCHPID. The output shows the Path Group ID which is Online to the storage system.

LOGICAL PATH		SYSTEM ADAPTER ID	FULL ESCON LINK ADDRESS	SP FENCE 0 1 2 3	HOST PATH GROUP ID		
NUMBER	type				CPU SERIAL#	CPU TYPE	CPU TIME STAMP
1	E	00	0101		XXXXXXXX	YYYY	ZZZZZZZZ
2-32	N/E	00-01					
33	E	02	0200		XXXXXXXX	YYYY	ZZZZZZZZ

“SYSTEM ADAPTER ID” corresponds to the number of the CHL adapter port.

(Ex; Port#1C is “02”).

Trace the Path Group ID indicated the SYSTEM ADAPTER ID corresponding to the value on SSB LOG of EC=C0CA or C0A6.

5.1.2 Byte27 = 08 (Reset Notification)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	EPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'8')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (*1)							
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'0')			
33	Not used (x'00')							
34	SSID of self storage system							
35								
36	Symptom code (x'2FFF')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log & message control (x'00')							
39	Program action code (x'16')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

*1: Hardware Level

Bit0 : Serial Channel or Parallel Channel	
Bit0 = 1 (Serial Channel)	
Bit 1	: Not used
Bit 2 to 3	: Storage Path Number
Bit 4 to 7	: 0xa : Fixed
Bit 8	: Dual Frame
	0 : DUAL FRAME
	1 : MODULAR POWER
Bit 9 to 11	: Cache size
	000 : none Cache
	001 : 256MB
	010 : 512MB
	011 : 768MB
	100 : 1024MB
	101 : 1280MB
	110 : 1536MB
	111 : Over 1536MB
Bit 12 to 13	: Cluster hardware level
Bit 14 to 15	: Cache/NVS hardware level

5.1.3 Byte27 = 09 (Intervention required by the key lock)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	SEPARATE	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'9')			
28	READY	ENABLE	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('00000')					
2B	HOST TYPE	DKC TYPE	Not used (x'00')		DKU TYPE			
2C	Module ID (x'50')							
2D	Routine ID (x'10')							
2E	Command code							
2F	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0000')			
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
7F	Not used							

5.1.4 Byte27 = 0A (Overrun (Byte3A: b4 = 1))

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'A')			
28	Command code							
29	Cylinder address							
2A								
2B	Head address							
2C	Record number							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
30	Module ID							
31	Routine ID							
32	PCB number				BBF OVR	PND OVR	STOP	OVER
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'0F0A')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.1.5 Byte27 = 10 (Intervention required)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	SEPARATE	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'0')			
28	READY	ENABLE	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('00000')					
2B	HOST TYPE	DKC TYPE	Not used (x'00')		DKU TYPE			
2C	Module ID (x'50')							
2D	Routine ID (x'10')							
2E	Command code							
2F	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0000')			
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.1.6 Byte27 = 1X (Drive report error)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'2/4')			
28	Additional Sense Code + Additional Sense Code Qualifier (*2)							
29								
2A	SCSI Command Code (*2)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used (x'0')				DKB#			
2F	CDEV#				RDEV#			
30	Type Code ('000')			0	Sense Key (*1)			
31	Not used (x'00')							
32	Not used (x'00')							
33	Not used ('x00')							
34	SSID of self storage system							
35								
36	Symptom code (x'9F1X' X: Message)							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Status byte							
41	Message							
42								
	CDB							
4B								
4C	SPC status register							
4D	SPC nexus status register							
4E	SPC data block register							
4F								
50	SPC data byte register							
51								
52								
53	SPC SCSI control signal status register							
54								
	SCSI Extended Sense Data							
73								
74								
	Not used							
7F								

*1: Sense key

x'0'	: No sense
x'1'	: Recovered error
x'2'	: Not ready
x'3'	: Medium error
x'4'	: Hardware error
x'5'	: Illegal request
x'6'	: Unit attention
x'7'	: Data protect
x'8'	: Blank check (Not used)
x'9'	: Vender unique (Not used)
x'A'	: Copy aborted (Not used)
x'B'	: Aborted command
x'C'	: Equal (Not used)
x'D'	: Volume overflow (Not used)
x'E'	: Miscompare
x'F'	: (Reserved)

*2: SCSI command

x'00'	: Test unit ready
x'03'	: Request sense
x'04'	: Format unit
x'07'	: Reassign blocks
x'12'	: Inquiry
x'15'	: Mode select
x'1A'	: Mode sense
x'1B'	: Start/stop unit
x'1C'	: Receive diagnostic result
x'1D'	: Send diagnostic
x'28'	: Read (extend)
x'2A'	: Write (extend)
x'2E'	: Write and verify
x'3B'	: Write buffer

5.1.7 Byte27 = 3F (Reset Allegiance)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06	PCB type		Processor ID (NOTE)					
07	System error code							
08								
09	JCB#							
0A								
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'3')				Message (x'F')			
28	Not used							
29	Logical path number							
2A	DCN status (*1)							
2B								
2C	Command code							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'F')			
33	Not used (x'00')							
34	SSID of self storage system							
35								
36	Symptom code (x'3F3F')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

*1:

	0	1	2	3	4	5	6	7
2A	DEVICE BUSY	ECI	COMMAND CHAINING	PATH RESERVE	STACK STATUS	WAIT SENSE	LONG BUSY	PROCESSOR CONNECT
2B	SINGLE PATH MODE	GUARANT PATH MODE	MCB CHAINING	0	BLOCK SWITCH	0	0	0

5.1.8 Byte27 = 4X (Data check, Common information)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0		RCU#		
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'4')				Message (x'X')			
28	Cylinder address (*1)							
29								
2A	Head address (*1)							
2B								
2C	Record number (*1)							
2D	Sector number (*1)							
2E	Controller ID							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'00')			
33	Not used (x'00')							
34	Command code							
35	SSID (low) of self storage system							
36	Symptom code (x'4XY'Y') (X: *2) (YY: *3)							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							

*1: Not determined for occurrence in HA or R0

*2: Contents of message

x'0' : Data check in HA field
 x'1' : Data check in C field
 x'2' : Data check in K field
 x'3' : Data check in D field
 x'4' : Missing SYNC byte in HA field
 x'5' : Missing SYNC byte in C field (PA error)
 x'6' : Missing SYNC byte in K field
 x'7' : Missing SYNC byte in D field
 x'8' : Not used
 x'9' : Missing AM during retry
 x'A' to x'F' : Not used

*3: Contents of correction flag

Bits 0 and 1 : ECC correction bit
 10 : Correctable (Recovered)
 11 : Uncorrectable
 Bit 2 : Offset active
 Bits 3 to 7 : Not used

5.1.9 Byte27 = 4X (Data check, When Byte27 = 43)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56								
	Not used (x'00)							
7F								

*1: EXIT FLAG

x'08': CHL I/F CHK2 time over	x'41': 'Waiting free segment' requested
x'0D': EOF	x'42': 'Time over of waiting for staging' requested
x'0E': DSB = 0E requested	x'43': 'Staging for PA unmatched' requested
x'0F': Logical error	x'45': 'Destaging' requested
x'1E': Make sense only	x'46': 'High priority destaging' requested
x'1F': Logical error (make sense only)	x'47': 'Write through process' requested
x'30': Selective reset requested	x'4B': 'DSB = 4E retry' requested (data transfer)
x'40': 'Staging' requested	x'4E': 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F': STG requested (Read or STG in progress)

*2: Slot failure

x'80': Sector write error	x'08': No decision
x'40': Sector read error	
x'20': ECC/LRC error	
x'10': Incomplete data	

*3: Obtained information flag

bit0-1	: Block type	bit4	: Segment
	00 : Data block		0 : No segment
	01 : HA/RO block		1 : Segment exist
	10 : Bit map block	bit5-7	: Reserved
bit2-3	: Side		
	00 : Cache write side		
	01 : NVS write side		
	10 : Cache read side		

5.1.10 Byte27 = 4X (Data check, When Byte27 = 45)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, SLOT number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flat (*3)							
54	PACC in CBUF							
55								
56	PAHF in CBUF							
57								
58	Current CC in JCB							
59								
5A	Current HH in JCB							
5B								
5C	Reserved (x'00')							
7F								

***1: EXIT FLAG**

x'08': CHL I/F CHK2 time over	x'41': 'Waiting free segment' requested
x'0D': EOF	x'42': 'Time over of waiting for staging' requested
x'0E': DSB = 0E requested	x'43': 'Staging for PA unmatched' requested
x'0F': Logical error	x'45': 'Destaging' requested
x'1E': Make sense only	x'46': 'High priority destaging' requested
x'1F': Logical error (make sense only)	x'47': 'Write through process' requested
x'30': Selective reset requested	x'4B': 'DSB = 4E retry' requested (data transfer)
x'40': 'Staging' requested	x'4E': 'DSB = 4E retry' requested (DSB = 4E retry)
	x'4F': STG requested (Read or STG in progress)

***2: Slot failure**

x'80' : Sector write error

x'10' : Incomplete data

x'40' : Sector read error

x'08' : No decision

x'20' : ECC/LRC error

***3: Obtained information flag**

bit0-1 : Block type

bit4 : Segment

00 : Data block

0 : No segment

01 : HA/RO block

1 : Segment exist

10 : Bit map block

bit5-7 : Reserved

bit2-3 : Side

00 : Cache write side

01 : NVS write side

10 : Cache read side

5.1.11 Byte27 = 6X (Statistics)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06	PCB type		Processor ID (NOTE)					
07	System error code							
08								
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Controller ID							
24	Device address							
25	Command overrun threshold exceeded							
26	Data overrun threshold exceeded							
27	Format (x'6')				Message (x'X')			
28	Read/search byte count							
29								
2A								
2B								
2C	Seek count							
2D								
2E	Not used (x'00')							
2F	Manufacture code ('000000')						Factory code ('00')	
30	Not used (x'00')							
31	DKC serial number							
32								
33								
34	SSID of self storage system							
35								
36	Symptom code (x'6FXY')			X: 0: Statistics/RRBL command, 2: Channel data overrun				
37				Y: CHL# (parallel CHL)/Port# (serial CHL))				

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.1.12 Byte27 = 74 & Byte40: Bit7 = 0 (FPC Detected Failure: DSP)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VLD	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06': Cache DKC)							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0D05') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	SSB edit format form (x'00')							
41	Not used							
42	CDB information							
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD BLK#							
4F	Data transfer end flag							
50	IO status							
51								
52	PDEV status							
53								
54	LOOP status							
55								
56								
57								
58	Frame Manager status							
59								
5A								
5B								
5C	TL status							
5D								
5E								
5F								
60	IO issued status1							
61								
62	IO issued status2							
63								
64	Not used							
65								
66								
67								
68	JOB require info							
69								
6A								
6B								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
6C	Data transfer status							
6D								
6E								
6F								
70	Not used							
7F								

5.1.13 Byte27 = 74 & Byte40: Bit7 = 1 (FPC Detected Failure: FBP)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format ID							
05								
06	PCB type				MP number			
07								
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0D05') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	SSB edit format (x'01')							
41	Not used							
42								
43								
44	ERQ index							
45								
46								
47								
48	IMQ index							
49								
4A								
4B								
4C	IO request information							
4D								
4E								
4F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FPC Config/Control register							
51								
52								
53								
54	FPC Status register							
55								
56								
57								
58	FM Configuration register							
59								
5A								
5B								
5C	FM Status register							
5D								
5E								
5F								
60	FM Link Status1 register							
61								
62								
63								
64	FM Link Status2 register							
65								
66								
67								
68	FCA register 1							
69								
6A								
6B								
6C	FCA register 2							
6D								
6E								
6F								
70	FPC PCI register							
71								
72	AL_PA							
73								
74	FPC Config Command							
75								
76								
77								
78	Fibre status							
79								
7A								
7B								
7C	Fibre error outbreak count							
7D								
7E								
7F								

5.2 Byte27 = 81 (CHK3)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09	REQ ID							
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# (Logical DKC#)							
0F	LDEV# (GDEV#)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B	Reserved							
1C	SCB							
1D	Reserved							
1E								
1F								
20								
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'1')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF81')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3D	Reserved							
3E								
3F								
58	Hard information (See following pages)							
7F								

5.2.1 Byte27 = 81 (CHK3) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15 or 16 or 17

	0	1	2	3	4	5	6	7
28	Subcode : x'11' : CHK3A (Parameter error) x'12' : CHK3B (Queue error) x'13' : CHK3C (Timeout) x'14' : CHK3D (Status) x'15' : CHK3E (Program) x'16' : CHK3F (Timeout) x'17' : CHK3G (Invalid)							
29	MP#							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	GCTL_MATED_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2D	GCTL_MATED_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2E	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
2F	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
31	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

(2) Byte28 = 31 or 32 or 33 or 34 or 35 or 36 or 37 or 38 or 41 or 42 or 46 or 47 or 48 : CHK3

	0	1	2	3	4	5	6	7
28	Subcode : x'31' : C3HI x'32' : C3XI x'33' : C3XL x'34' : C3PR x'35' : C3NC x'36' : C3HO x'37' : C3XO x'38' : C3SW x'41' : C3TB_HI x'42' : C3TB_XI x'46' : C3TB_HO x'47' : C3TB_XO x'48' : C3TBSW							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	HIE_STS0							
2D	HIE_MODE_SET							
2E	INT_BITREG_STS							
2F	HIE_PA_LINKSTS							
30	IED INFO_81_0							
31	IED INFO_81_1							

5.2.2 Byte27 = 81 (CHK3) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15 or 16 or 17 : CHK3

	0	1	2	3	4	5	6	7
40	0x01 (FreeArea No.)							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	CPU1_GBL_GNERRST[23:16]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	CPU1_GBL_GNERRST[15:8]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	CPU1_GBL_GNERRST[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	CPU0 NTB0_XPUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0 NTB0_XPUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0 NTB0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
57	CPU0 NTB0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
58	CPU0 NTB0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU0 NTB0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5A	CPU0 NTB0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5B	CPU0 NTB0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
5C	CPU0 NTB0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5D	CPU0 NTB0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5E	CPU0 NTB0_LNKSTS2[15:8]							
5F	CPU0 NTB0_LNKSTS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_NTB1_XPUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	Reserved
61	CPU1_NTB1_XPUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	Reserved
62	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
63	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
64	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU1_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
66	CPU1_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
67	CPU1_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
68	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
69	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
6A	CPU1_NTB1_LNKSTS2[15:8]							
6B	CPU1_NTB1_LNKSTS2[7:0]							
6C	Driver data[31:24]							
6D	Driver data[23:16]							
6E	Driver data[15:8]							
6F	Driver data[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	SYSTEMTIML[31:24]							
71	SYSTEMTIML [23:16]							
72	SYSTEMTIML [15:8]							
73	SYSTEMTIML [7:0]							
74	ACSID [31:24]							
75	ACSID [23:16]							
76	ACSID [15:8]							
77	ACSID [7:0]							
78	ACS_ADDRESS[63:56]							
79	ACS_ADDRESS[55:48]							
7A	ACS_ADDRESS[47:40]							
7B	ACS_ADDRESS[39:32]							
7C	ACS_ADDRESS [31:24]							
7D	ACS_ADDRESS [23:16]							
7E	ACS_ADDRESS [15:8]							
7F	ACS_ADDRESS [7:0]							

(2) Byte28 = 31 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81310000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(3) Byte28 = 32 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81320000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(4) Byte28 = 33 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81330000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(5) Byte28 = 34 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81340000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	STATUS W0_L							
4D								
4E								
4F								
50	STATUS W0_H							
51								
52								
53								
54	STATUS W1_L							
55								
56								
57								
58	STATUS W1_H							
59								
5A								
5B								
5C	STATUS W2_L							
5D								
5E								
5F								
60	STATUS W2_H							
61								
62								
63								
64	STATUS W3_L							
65								
66								
67								
68	STATUS W3_H							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(6) Byte28 = 35 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81350000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	ISW_PORT_ST_LOG0							
4D								
4E								
4F								
50	ISW_PORT_ST_LOG1							
51								
52								
53								
54	ISW_PORT_ST_LOG2							
55								
56								
57								
58	ISW_FW_STS1							
59								
5A	ISW_FW_STS2							
5B								
5C	ISW_PORT_ST_LOG0							
5D								
5E								
5F								
60	ISW_PORT_ST_LOG1							
61								
62								
63								
64	ISW_PORT_ST_LOG2							
65								
66								
67								
68	ISW_FW_STS1							
69								
6A	ISW_FW_STS2							
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(7) Byte28 = 36 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81360000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(8) Byte28 = 37 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81370000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_DA_ERRCODE0							
5D								
5E								
5F								
60	PARBx_NODE_ERR							
61								
62								
63								
64	NODE_XxERR_STS_QDMA							
65								
66								
67								
68	NODE_XxERR_STS_LRP							
69								
6A								
6B								
6C	MICRO_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_INFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

(9) Byte28 = 38 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81380000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(10) Byte28 = 41 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81410000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(11) Byte28 = 42 : CHK3

	0	1	2	3	4	5	6	7
40								
41	Fixed value (0x81420000)							
42								
43								
44								
45	—							
46								
47								
48								
49	INT_INTSTS0							
4A								
4B								
4C								
4D	INT_STS_REG0							
4E								
4F								
50								
51	INT_STS_REG1							
52								
53								
54								
55	INT_STS_REG2							
56								
57								
58								
59	HIEP_STS							
5A								
5B								
5C								
5D	HIEP_ERR0							
5E								
5F								
60								
61	HIEP_ERR1							
62								
63								
64								
65	HIEP_DA_ERRCODE0							
66								
67								
68								
69	HIEP_DA_ERRCODE1							
6A								
6B								
6C								
6D	MICRO_FREE_INFO_0							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(12) Byte28 = 46 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81460000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_INFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

(13) Byte28 = 47 : CHK3

	0	1	2	3	4	5	6	7
40								
41	Fixed value (0x81470000)							
42								
43								
44								
45	—							
46								
47								
48								
49	INT_INTSTS0							
4A								
4B								
4C								
4D	INT_STS_REG0							
4E								
4F								
50								
51	INT_STS_REG1							
52								
53								
54								
55	INT_STS_REG2							
56								
57								
58								
59	HIEP_STS							
5A								
5B								
5C								
5D	HIEP_DA_ERRCODE0							
5E								
5F								
60								
61	PARBx_NODE_ERR							
62								
63								
64								
65	NODE_XxERR_STS_QDMA							
66								
67								
68								
69	NODE_XxERR_STS_LRP							
6A								
6B								
6C								
6D	MICRO_INFO_0							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_INFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

(14) Byte28 = 48 : CHK3

	0	1	2	3	4	5	6	7
40	Fixed value (0x81480000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

5.3 Byte27 = 87 (Front End Error)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09	REQ ID							
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# (Logical DKC#)							
0F	LDEV# (GDEV#)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B	Reserved							
1C	SCB							
1D	Reserved							
1E								
1F								
20								
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'7')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF87')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.3.1 Byte27 = 87 (Front End Error) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15

	0	1	2	3	4	5	6	7
28	Subcode : x'11' : FEIF-PK FC16G/FC32G Chip REPLACE x'12' : FEIF-PK FC16G/FC32G System Error x'13' : FEIF-PK FC16G/FC32G RISC pause x'14' : FEIF-PK FC16G/FC32G Queue Error(REQ,RSP,ATQ x'15' : FEIF-PK FC16G/FC32G TEMP Alarm							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	RISC to Host Status							
2D								
2E								
2F								
30	Not used							
31	Not used							

(2) Byte28 = 21

	0	1	2	3	4	5	6	7
28	Subcode : x'21' : FEIF-PK iSCSI 10G/1G Temperture Warning							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Outgoing MB Register 3[31:24]							
2D	Outgoing MB Register 3[23:16]							
2E	Outgoing MB Register 3[15:8]							
2F	Outgoing MB Register 3[7:0]							
30	Not used							
31	Not used							

(3) Byte28 = 22

	0	1	2	3	4	5	6	7
28	Subcode :x'22': FEIF-PK iSCSI 10G/1G HeartBeat Error Unrecoverable							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(4) Byte28 = 23

	0	1	2	3	4	5	6	7
28	Subcode : x'23' : FEIF-PK iSCSI 10G/1G HeartBeat Error Recoverable							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(5) Byte28 = 24

	0	1	2	3	4	5	6	7
28	Subcode : x'24' : FEIF-PK iSCSI 10G/1G HeartBeat Error Uninfo							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	PEG_HALT_STATUS1[31:24]							
2D	PEG_HALT_STATUS1[23:16]							
2E	PEG_HALT_STATUS1[15:8]							
2F	PEG_HALT_STATUS1[7:0]							
30	Not used							
31	Not used							

(6) Byte28 = 25 or 26

	0	1	2	3	4	5	6	7
28	Subcode : x'25' : FEIF-PK iSCSI 10G/1G FE System Error							
	x'26' : FEIF-PK iSCSI 10G/1G PCIe Correctable Error							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.3.2 Byte27 = 87 (Front End Error) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 15

	0	1	2	3	4	5	6	7
40	RISC to Host Status[31:24]							
41	RISC to Host Status[23:16]							
42	RISC to Host Status[15:8]							
43	RISC to Host Status[7:20]							
44	Host Command and Control[31:24]							
45	Host Command and Control[23:16]							
46	Host Command and Control[15:8]							
47	Host Command and Control[7:20]							
48	GPIO Data[31:24]							
49	GPIO Data[23:16]							
4A	GPIO Data[15:8]							
4B	GPIO Data[7:20]							
4C	Outgoing MB Register 0[31:24]							
4D	Outgoing MB Register 0[23:16]							
4E	Outgoing MB Register 0[15:8]							
4F	Outgoing MB Register 0[7:20]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Outgoing MB Register 1[31:24]							
51	Outgoing MB Register 1[23:16]							
52	Outgoing MB Register 1[15:8]							
53	Outgoing MB Register 1[7:20]							
54	Outgoing MB Register 2[31:24]							
55	Outgoing MB Register 2[23:16]							
56	Outgoing MB Register 2[15:8]							
57	Outgoing MB Register 2[7:20]							
58	Outgoing MB Register 3[31:24]							
59	Outgoing MB Register 3[23:16]							
5A	Outgoing MB Register 3[15:8]							
5B	Outgoing MB Register 3[7:20]							
5C	Outgoing MB Register 4[31:24]							
5D	Outgoing MB Register 4[23:16]							
5E	Outgoing MB Register 4[15:8]							
5F	Outgoing MB Register 4[7:20]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 5[31:24]							
61	Outgoing MB Register 5[23:16]							
62	Outgoing MB Register 5[15:8]							
63	Outgoing MB Register 5[7:20]							
64	Outgoing MB Register 6[31:24]							
65	Outgoing MB Register 6[23:16]							
66	Outgoing MB Register 6[15:8]							
67	Outgoing MB Register 6[7:20]							
68	Outgoing MB Register 7[31:24]							
69	Outgoing MB Register 7[23:16]							
6A	Outgoing MB Register 7[15:8]							
6B	Outgoing MB Register 7[7:20]							
6C	PCI Config Register(Address : 0x07)							
6D	PCI Config Register(Address : 0x06)							
6E	PCI Config Register(Address : 0x05)							
6F	PCI Config Register(Address : 0x04)							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x5F)							
75	PCI Config Register(Address : 0x5E)							
76	PCI Config Register(Address : 0x5D)							
77	PCI Config Register(Address : 0x5C)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(2) Byte28 = 21

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Outgoing MB Register 0[31:24]							
55	Outgoing MB Register 0[23:16]							
56	Outgoing MB Register 0[15:8]							
57	Outgoing MB Register 0[7:0]							
58	Outgoing MB Register 1[31:24]							
59	Outgoing MB Register 1[23:16]							
5A	Outgoing MB Register 1[15:8]							
5B	Outgoing MB Register 1[7:0]							
5C	Outgoing MB Register 2[31:24]							
5D	Outgoing MB Register 2[23:16]							
5E	Outgoing MB Register 2[15:8]							
5F	Outgoing MB Register 2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 3[31:24]							
61	Outgoing MB Register 3[23:16]							
62	Outgoing MB Register 3[15:8]							
63	Outgoing MB Register 3[7:0]							
64	Outgoing MB Register 4[31:24]							
65	Outgoing MB Register 4[23:16]							
66	Outgoing MB Register 4[15:8]							
67	Outgoing MB Register 4[7:0]							
68	Outgoing MB Register 5[31:24]							
69	Outgoing MB Register 5[23:16]							
6A	Outgoing MB Register 5[15:8]							
6B	Outgoing MB Register 5[7:0]							
6C	Outgoing MB Register 6[31:24]							
6D	Outgoing MB Register 6[23:16]							
6E	Outgoing MB Register 6[15:8]							
6F	Outgoing MB Register 6[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Outgoing MB Register 7[31:24]							
71	Outgoing MB Register 7[23:16]							
72	Outgoing MB Register 7[15:8]							
73	Outgoing MB Register 7[7:0]							
74	PCI Config Register(Address : 0x57)							
75	PCI Config Register(Address : 0x56)							
76	PCI Config Register(Address : 0x55)							
77	PCI Config Register(Address : 0x54)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(3) Byte28 = 22

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = 23

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = 24

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Response Queue In-Pointer[31:24]							
55	Response Queue In-Pointer[23:16]							
56	Response Queue In-Pointer[15:8]							
57	Response Queue In-Pointer[7:0]							
58	Response Queue Out-Pointer[31:24]							
59	Response Queue Out-Pointer[23:16]							
5A	Response Queue Out-Pointer[15:8]							
5B	Response Queue Out-Pointer[7:0]							
5C	PEG_HALT_STATUS1[31:24]							
5D	PEG_HALT_STATUS1[23:16]							
5E	PEG_HALT_STATUS1[15:8]							
5F	PEG_HALT_STATUS1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	PEG_HALT_STATUS2[31:24]							
61	PEG_HALT_STATUS2[23:16]							
62	PEG_HALT_STATUS2[15:8]							
63	PEG_HALT_STATUS2[7:0]							
64	FIRMWARE_HEARTBEAT[31:24]							
65	FIRMWARE_HEARTBEAT[23:16]							
66	FIRMWARE_HEARTBEAT[15:8]							
67	FIRMWARE_HEARTBEAT[7:0]							
68	Response Queue In-Pointer[31:24]							
69	Response Queue In-Pointer[23:16]							
6A	Response Queue In-Pointer[15:8]							
6B	Response Queue In-Pointer[7:0]							
6C	Response Queue Out-Pointer[31:24]							
6D	Response Queue Out-Pointer[23:16]							
6E	Response Queue Out-Pointer[15:8]							
6F	Response Queue Out-Pointer[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PCI Config Register(Address : 0x57)							
71	PCI Config Register(Address : 0x56)							
72	PCI Config Register(Address : 0x55)							
73	PCI Config Register(Address : 0x54)							
74	PCI Config Register(Address : 0x107)							
75	PCI Config Register(Address : 0x106)							
76	PCI Config Register(Address : 0x105)							
77	PCI Config Register(Address : 0x104)							
78	PCI Config Register(Address : 0x113)							
79	PCI Config Register(Address : 0x112)							
7A	PCI Config Register(Address : 0x111)							
7B	PCI Config Register(Address : 0x110)							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(6) Byte28 = 25

	0	1	2	3	4	5	6	7
40	CMDPEG_STATE[31:24]							
41	CMDPEG_STATE[23:16]							
42	CMDPEG_STATE[15:8]							
43	CMDPEG_STATE[7:0]							
44	ASIC_TEMPERATURE[31:24]							
45	ASIC_TEMPERATURE[23:16]							
46	ASIC_TEMPERATURE[15:8]							
47	ASIC_TEMPERATURE[7:0]							
48	PEG_HALT_STATUS1[31:24]							
49	PEG_HALT_STATUS1[23:16]							
4A	PEG_HALT_STATUS1[15:8]							
4B	PEG_HALT_STATUS1[7:0]							
4C	PEG_HALT_STATUS2[31:24]							
4D	PEG_HALT_STATUS2[23:16]							
4E	PEG_HALT_STATUS2[15:8]							
4F	PEG_HALT_STATUS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FIRMWARE_HEARTBEAT[31:24]							
51	FIRMWARE_HEARTBEAT[23:16]							
52	FIRMWARE_HEARTBEAT[15:8]							
53	FIRMWARE_HEARTBEAT[7:0]							
54	Outgoing MB Register 0[31:24]							
55	Outgoing MB Register 0[23:16]							
56	Outgoing MB Register 0[15:8]							
57	Outgoing MB Register 0[7:0]							
58	Outgoing MB Register 1[31:24]							
59	Outgoing MB Register 1[23:16]							
5A	Outgoing MB Register 1[15:8]							
5B	Outgoing MB Register 1[7:0]							
5C	Outgoing MB Register 2[31:24]							
5D	Outgoing MB Register 2[23:16]							
5E	Outgoing MB Register 2[15:8]							
5F	Outgoing MB Register 2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Outgoing MB Register 3[31:24]							
61	Outgoing MB Register 3[23:16]							
62	Outgoing MB Register 3[15:8]							
63	Outgoing MB Register 3[7:0]							
64	Outgoing MB Register 4[31:24]							
65	Outgoing MB Register 4[23:16]							
66	Outgoing MB Register 4[15:8]							
67	Outgoing MB Register 4[7:0]							
68	Outgoing MB Register 5[31:24]							
69	Outgoing MB Register 5[23:16]							
6A	Outgoing MB Register 5[15:8]							
6B	Outgoing MB Register 5[7:0]							
6C	Outgoing MB Register 6[31:24]							
6D	Outgoing MB Register 6[23:16]							
6E	Outgoing MB Register 6[15:8]							
6F	Outgoing MB Register 6[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Outgoing MB Register 7[31:24]							
71	Outgoing MB Register 7[23:16]							
72	Outgoing MB Register 7[15:8]							
73	Outgoing MB Register 7[7:0]							
74	PCI Config Register(Address : 0x57)							
75	PCI Config Register(Address : 0x56)							
76	PCI Config Register(Address : 0x55)							
77	PCI Config Register(Address : 0x54)							
78	PCI Config Register(Address : 0x107)							
79	PCI Config Register(Address : 0x106)							
7A	PCI Config Register(Address : 0x105)							
7B	PCI Config Register(Address : 0x104)							
7C	PCI Config Register(Address : 0x113)							
7D	PCI Config Register(Address : 0x112)							
7E	PCI Config Register(Address : 0x111)							
7F	PCI Config Register(Address : 0x110)							

(7) Byte28 = 26

	0	1	2	3	4	5	6	7
40	PCI Config Register(Address : 0x57)							
41	PCI Config Register(Address : 0x56)							
42	PCI Config Register(Address : 0x55)							
43	PCI Config Register(Address : 0x54)							
44	PCI Config Register(Address : 0x107)							
45	PCI Config Register(Address : 0x106)							
46	PCI Config Register(Address : 0x105)							
47	PCI Config Register(Address : 0x104)							
48	PCI Config Register(Address : 0x113)							
49	PCI Config Register(Address : 0x112)							
4A	PCI Config Register(Address : 0x111)							
4B	PCI Config Register(Address : 0x110)							
4C	Not used							
4D	Not used							
4E	Not used							
4F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

5.4 Byte27 = 89 (CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'9')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF89')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.4.1 Byte27 = 89(CHK2) Byte28-31 Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 20 or 21

	0	1	2	3	4	5	6	7
28	Subcode : x'11' : TIMEOUT (DMA) x'12' : TIMEOUT (DMA) x'13' : STATUS error x'14' : Data protection code error (CPU internal) x'20' : Data protection code error (CPU) x'21' : Data protection code error (Controller path)							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
31	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(2) Byte28 = 22

	0	1	2	3	4	5	6	7
28	Subcode : x'22' : Data protection code error (FE)							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 23

	0	1	2	3	4	5	6	7
28	Subcode : x'23' : Data protection code error (BE)							
29	Detect CPU#							
2A	Slave CPU#							
2B	Transfer Direction (DRIVE_READ : 0x01,DRIVE_WRITE : 0x02							
2C	SAS CTL#							
2D	Port#							
2E	Transfer Mode (notDirect : 0x01,Direct : 0x02)							
2F	Not used							
30	Not used							
31	Not used							

(4) Byte28 = 24

	0	1	2	3	4	5	6	7
28	Subcode : x'24' : Data protection code error (BE)							
29	Detect CPU#							
2A	Slave CPU#							
2B	Transfer Direction (DRIVE_READ : 0x01,DRIVE_WRITE : 0x02							
2C	SAS CTL#							
2D	Port#							
2E	Transfer Mode (notDirect : 0x01,Direct : 0x02)							
2F	Not used							
30	Not used							
31	Not used							

(5) Byte28 = 25

	0	1	2	3	4	5	6	7
28	Subcode : x'25' : HIE access DIF CRC failure IED							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	HIE_STS0							
2D	HIE_MODE_SET							
2E	INT_BITREG_STS							
2F	HIE_PA_LINKSTS							
30	IED INFO_81_0							
31	IED INFO_81_1							

(6) Byte28 = 26

	0	1	2	3	4	5	6	7
28	Subcode : x'26' : HIE access DIF CRC failure MF							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	HIE_STS0							
2D	Not used							
2E	Not used							
2F	Not used							
30	PSIF_REG INITVAL							
31	Not used							

(7) Byte28 = 31 or 32 or 33 or 34 or 35 or 36 or 37 or 38

	0	1	2	3	4	5	6	7
28	Subcode : x'31' : CHK2[C2HI] (HIE access failure of HIE in CTL concerned) x'32' : CHK2[C2XI] (HIE access failure of X-path in CTL concerned) x'33' : CHK2[C2XL] (HIE access X-path read failure) x'34' : CHK2[C2PR] (HIE access parameter invalid) x'35' : CHK2[C2NC] (HIE access failure without cause) x'36' : CHK2[C2HO] (HIE access failure of HIE in the other CTL) x'37' : CHK2[C2XO] (HIE access failure of X-path in the other CTL) x'38' : CHK2[C2SW] (HIE access failure of ISW)							
29	CTL#							
2A	SLOT#							
2B	PORT#							
2C	HIE_STS0							
2D	HIE_MODE_SET							
2E	INT_BITREG_STS							
2F	HIE_PA_LINKSTS							
30	IED INFO_81_0							
31	IED INFO_81_1							

5.4.2 Byte27 = 89 (CHK2) Byte40-7F Detail

(1) Byte28 = 11 or 12 or 13 or 14 or 20 or 21

	0	1	2	3	4	5	6	7
40	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
41	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
42	GCTL_MATED_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_MATED_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
45	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
46	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
47	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
48	GCTL_MATED_CTLREQ_STS							
	Not used	Not used	Not used	Not used	Power alarm		Power ready	
49	GCTL_IOPKRST_MASK0							
4A	GCTL_IOPKRST_MASK1							
4B	Not used							
4C	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU0_CB00_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_CB00_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_CB00_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
57	CPU0_CB00_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
58	CPU1_CB10_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_CB10_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5A	CPU1_CB10_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
5B	CPU1_CB10_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
5C	CPU0 NTB0 UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0 NTB0 UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0 NTB0 UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0 NTB0 UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0 NTB0 LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
61	CPU0 NTB0 LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
62	CPU0 NTB0 LNKSTS2[15:8]							
63	CPU0 NTB0 LNKSTS2[7:0]							
64	CPU0 NTB0 XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
65	CPU0 NTB0 XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
66	CPU0 NTB0 DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
67	Not used							
68	CPU0 NTB0 UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0 NTB0 UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
6A	CPU0 NTB0 UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
6B	CPU0 NTB0 UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
6C	CPU0 NTB0 LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
6D	CPU0 NTB0 LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
6E	CPU0 NTB0 LNKSTS2[15:8]							
6F	CPU0 NTB0 LNKSTS2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
71	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
72	CPU1_NTB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
73	Not used							
74	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
75	CPU1_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
76	CPU1_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
77	CPU1_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
78	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
79	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
7A	CPU1_NTB1_LNKSTS2[15:8]							
7B	CPU1_NTB1_LNKSTS2[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(2) Byte28 = 22

	0	1	2	3	4	5	6	7
40	ENTRY TYPE[31:24]							
41	ENTRY TYPE[23:16]							
42	ENTRY TYPE[15:8]							
43	ENTRY TYPE[7:0]							
44	HANDLE[31:24]							
45	HANDLE[23:16]							
46	HANDLE[15:8]							
47	HANDLE[7:0]							
48	STATUS[31:24]							
49	STATUS[23:16]							
4A	STATUS[15:8]							
4B	STATUS[7:0]							
4C	TOTAL DATA SEGMENT COUNT[31:24]							
4D	TOTAL DATA SEGMENT COUNT[23:16]							
4E	TOTAL DATA SEGMENT COUNT[15:8]							
4F	TOTAL DATA SEGMENT COUNT[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	STATUS FLAGS[31:24]							
51	STATUS FLAGS[23:16]							
52	STATUS FLAGS[15:8]							
53	STATUS FLAGS[7:0]							
54	RECEIVE EXCHANGE ADDRESS[31:24]							
55	RECEIVE EXCHANGE ADDRESS[23:16]							
56	RECEIVE EXCHANGE ADDRESS[15:8]							
57	RECEIVE EXCHANGE ADDRESS[7:0]							
58	FLAGS[31:24]							
59	FLAGS[23:16]							
5A	FLAGS[15:8]							
5B	FLAGS[7:0]							
5C	RESIDUAL TRANSFER LENGTH[31:24]							
5D	RESIDUAL TRANSFER LENGTH[23:16]							
5E	RESIDUAL TRANSFER LENGTH[15:8]							
5F	RESIDUAL TRANSFER LENGTH[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	OX_ID[31:24]							
61	OX_ID[23:16]							
62	OX_ID[15:8]							
63	OX_ID[7:0]							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	REPORTED RUNT BLOCK GUARD[31:24]							
6D	REPORTED RUNT BLOCK GUARD[23:16]							
6E	REPORTED RUNT BLOCK GUARD[15:8]							
6F	REPORTED RUNT BLOCK GUARD[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ACTUAL DATA PROTECTION CODE[31:24]							
71	ACTUAL DATA PROTECTION CODE[23:16]							
72	ACTUAL DATA PROTECTION CODE[15:8]							
73	ACTUAL DATA PROTECTION CODE[7:0]							
74	ACTUAL DATA PROTECTION CODE2[31:24]							
75	ACTUAL DATA PROTECTION CODE2[23:16]							
76	ACTUAL DATA PROTECTION CODE2[15:8]							
77	ACTUAL DATA PROTECTION CODE2[7:0]							
78	EXPECTED DATA PROTECTION CODE[31:24]							
79	EXPECTED DATA PROTECTION CODE[23:16]							
7A	EXPECTED DATA PROTECTION CODE[15:8]							
7B	EXPECTED DATA PROTECTION CODE[7:0]							
7C	EXPECTED DATA PROTECTION CODE2[31:24]							
7D	EXPECTED DATA PROTECTION CODE2[23:16]							
7E	EXPECTED DATA PROTECTION CODE2[15:8]							
7F	EXPECTED DATA PROTECTION CODE2[7:0]							

(3) Byte28 = 23

	0	1	2	3	4	5	6	7
40	Outbound Messages DW0[31:24]							
41	Outbound Messages DW0[23:16]							
42	Outbound Messages DW0[15:8]							
43	Outbound Messages DW0[7:0]							
44	Outbound Messages DW1[31:24]							
45	Outbound Messages DW1[23:16]							
46	Outbound Messages DW1[15:8]							
47	Outbound Messages DW1[7:0]							
48	Outbound Messages DW2[31:24]							
49	Outbound Messages DW2[23:16]							
4A	Outbound Messages DW2[15:8]							
4B	Outbound Messages DW2[7:0]							
4C	Outbound Messages DW3[31:24]							
4D	Outbound Messages DW3[23:16]							
4E	Outbound Messages DW3[15:8]							
4F	Outbound Messages DW3[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Outbound Messages DW4[31:24]							
51	Outbound Messages DW4[23:16]							
52	Outbound Messages DW4[15:8]							
53	Outbound Messages DW4[7:0]							
54	Outbound Messages DW5[31:24]							
55	Outbound Messages DW5[23:16]							
56	Outbound Messages DW5[15:8]							
57	Outbound Messages DW5[7:0]							
58	Outbound Messages DW6[31:24]							
59	Outbound Messages DW6[23:16]							
5A	Outbound Messages DW6[15:8]							
5B	Outbound Messages DW6[7:0]							
5C	Outbound Messages DW7[31:24]							
5D	Outbound Messages DW7[23:16]							
5E	Outbound Messages DW7[15:8]							
5F	Outbound Messages DW7[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Outbound Messages DW8[31:24]							
61	Outbound Messages DW8[23:16]							
62	Outbound Messages DW8[15:8]							
63	Outbound Messages DW8[7:0]							
64	Outbound Messages DW9[31:24]							
65	Outbound Messages DW9[23:16]							
66	Outbound Messages DW9[15:8]							
67	Outbound Messages DW9[7:0]							
68	Outbound Messages DW10[31:24]							
69	Outbound Messages DW10[23:16]							
6A	Outbound Messages DW10[15:8]							
6B	Outbound Messages DW10[7:0]							
6C	Outbound Messages DW11[31:24]							
6D	Outbound Messages DW11[23:16]							
6E	Outbound Messages DW11[15:8]							
6F	Outbound Messages DW11[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Outbound Messages DW12[31:24]							
71	Outbound Messages DW12[23:16]							
72	Outbound Messages DW12[15:8]							
73	Outbound Messages DW12[7:0]							
74	Outbound Messages DW13[31:24]							
75	Outbound Messages DW13[23:16]							
76	Outbound Messages DW13[15:8]							
77	Outbound Messages DW13[7:0]							
78	Outbound Messages DW14[31:24]							
79	Outbound Messages DW14[23:16]							
7A	Outbound Messages DW14[15:8]							
7B	Outbound Messages DW14[7:0]							
7C	Outbound Messages DW15[31:24]							
7D	Outbound Messages DW15[23:16]							
7E	Outbound Messages DW15[15:8]							
7F	Outbound Messages DW15[7:0]							

(4) Byte28 = 25 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89250000)							
41								
42								
43								
44	XDMA_STATUS_W14							
45								
46								
47								
48	XDMA_STATUS_W0							
49								
4A								
4B								
4C	XDMA_STATUS_W1							
4D								
4E								
4F								
50	XDMA_STATUS_W2							
51								
52								
53								
54	XDMA_STATUS_W3							
55								
56								
57								
58	XDMA_STATUS_W4							
59								
5A								
5B								
5C	XDMA_STATUS_W5							
5D								
5E								
5F								
60	XDMA_STATUS_W6							
61								
62								
63								
64	XDMA_STATUS_W7							
65								
66								
67								
68	XDMA_STATUS_W8							
69								
6A								
6B								
6C	XDMA_STATUS_W9							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	XDMA_STATUS_W10							
71								
72								
73								
74	XDMA_STATUS_W11							
75								
76								
77								
78	XDMA_STATUS_W12							
79								
7A								
7B								
7C	XDMA_STATUS_W13							
7D								
7E								
7F								

(5) Byte28 = 26 : CHK2

	0	1	2	3	4	5	6	7
40								
41	MFDMA CCW ERR STATUS1 W0							
42								
43								
44								
45	MFDMA CCW ERR STATUS1 W1							
46								
47								
48								
49	MFDMA CCW ERR STATUS1 W2							
4A								
4B								
4C								
4D	MFDMA CCW ERR STATUS1 W3							
4E								
4F								
50								
51	MFDMA CCW ERR STATUS1 W6							
52								
53								
54								
55	MFDMA CCW ERR STATUS1 W8							
56								
57								
58								
59	MFDMA CCW ERR STATUS1 W22							
5A								
5B								
5C								
5D	MFDMA CCW ERR STATUS1 W23							
5E								
5F								
60								
61	MFDMA CCW ERR STATUS1 W24							
62								
63								
64								
65	MFDMA CCW ERR STATUS1 W25							
66								
67								
68								
69	MFDMA CCW ERR STATUS1 W26							
6A								
6B								
6C								
6D	MFDMA CCW ERR STATUS1 W27							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MFDMA CCW ERR STATUS1 W28							
71								
72								
73								
74	MFDMA CCW ERR STATUS1 W29							
75								
76								
77								
78	MFDMA CCW ERR STATUS1 W30							
79								
7A								
7B								
7C	MFDMA CCW ERR STATUS1 W31							
7D								
7E								
7F								

(6) Byte28 = 31 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89310000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(7) Byte28 = 32 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89320000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(8) Byte28 = 33 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89330000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(9) Byte28 = 34 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89340000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	IED_FREE_INFO_89_34_0							
4D								
4E								
4F								
50	IED_FREE_INFO_89_34_1							
51								
52								
53								
54	IED_FREE_INFO_89_34_2							
55								
56								
57								
58	IED_FREE_INFO_89_34_3							
59								
5A								
5B								
5C	IED_FREE_INFO_89_34_4							
5D								
5E								
5F								
60	IED_FREE_INFO_89_34_5							
61								
62								
63								
64	IED_FREE_INFO_89_34_6							
65								
66								
67								
68	IED_INFO_FREE_89_34_7							
69								
6A								
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(10) Byte28 = 35 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89350000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	ISW_PORT_ST_LOG0							
4D								
4E								
4F								
50	ISW_PORT_ST_LOG1							
51								
52								
53								
54	ISW_PORT_ST_LOG2							
55								
56								
57								
58	ISW_FW_STS1							
59								
5A	ISW_FW_STS2							
5B								
5C	ISW_PORT_ST_LOG0							
5D								
5E								
5F								
60	ISW_PORT_ST_LOG1							
61								
62								
63								
64	ISW_PORT_ST_LOG2							
65								
66								
67								
68	ISW_FW_STS1							
69								
6A	ISW_FW_STS2							
6B								
6C	MICRO_FREE_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREE_INFO_1							
71								
72								
73								
74	MICRO_FREE_INFO_2							
75								
76								
77								
78	MICRO_FREE_INFO_3							
79								
7A								
7B								
7C	MICRO_FREE_INFO_4							
7D								
7E								
7F								

(11) Byte28 = 36 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89360000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_FREEINFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

(12) Byte28 = 37 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89370000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_DA_ERRCODE0							
5D								
5E								
5F								
60	PARBx_NODE_ERR							
61								
62								
63								
64	NODE_XxERR_STS_QDMA							
65								
66								
67								
68	NODE_XxERR_STS_LRP							
69								
6A								
6B								
6C	MICRO_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_INFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

(13) Byte28 = 38 : CHK2

	0	1	2	3	4	5	6	7
40	Fixed value (0x89380000)							
41								
42								
43								
44	—							
45								
46								
47								
48	INT_INTSTS0							
49								
4A								
4B								
4C	INT_STS_REG0							
4D								
4E								
4F								
50	INT_STS_REG1							
51								
52								
53								
54	INT_STS_REG2							
55								
56								
57								
58	HIEP_STS							
59								
5A								
5B								
5C	HIEP_ERR0							
5D								
5E								
5F								
60	HIEP_ERR1							
61								
62								
63								
64	HIEP_DA_ERRCODE0							
65								
66								
67								
68	HIEP_DA_ERRCODE1							
69								
6A								
6B								
6C	MICRO_INFO_0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	MICRO_INFO_1							
71								
72								
73								
74	MICRO_INFO_2							
75								
76								
77								
78	MICRO_INFO_3							
79								
7A								
7B								
7C	MICRO_INFO_4							
7D								
7E								
7F								

5.5 Byte27 = 8C (GUM/MPC/SVP/CUDG Detected Failure)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	Processor ID							
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20 26	Reserved							
27	Format (x'8')				Message (x'C')			
28 29 2A 2B 2C 2D 2E 2F	Error information (Details will be published separately)							
30	Failure type code (*1)							
31								
32 33 34 35	Error information (Details will be published separately)							
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
39	Program action code (x'00')							
3A	Configuration information (x'00')							
3B								
3C	Reserved							
3F								
40	Error information (Details will be published separately)							
7F								

*1: Failure type code

x'0000' : Logical Inconsist
 x'0001' : Windows memory allocate failure
 x'0002' : File
 x'0003' : LAN failure
 x'0005' : Windows failure
 x'0009' : BOOT detected failure
 x'000B' : Router failure
 x'000D' : Stop SSB overwrite
 x'000E' : Stop SSBS overwrite
 x'000F' : Flash failure
 x'0015' : Battery failure
 x'0030' : Delay of time
 x'0031' : Warning of a stack
 x'0032' : Single task error
 x'0033' : MPC launcher error
 x'0039' : IP address setting error
 x'003A' : Subnet mask setting error
 x'003B' : Virus detection error
 x'003C' : Virus detection error
 x'003D' : Virus detection error
 x'003F' : Situation observation error
 x'0040' : Dummy PS/OFF starting (normal)

5.5.1 Byte27 = 8C (GUM/MPC/SVP/CUDG) Error Information Details

- (1) Logical inconsistency / HEAP / File / LAN / Windows failure / Router failure / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / MPC Launcher error / IP address setting error / Subnet mask setting error / Virus detection error / Situation observation error / Dummy PS/OFF starting (normal)

	0	1	2	3	4	5	6	7
28	Failed AP code (*1)							
29								
2A	Reserved							
2F	Error type code (*2)							
30								
31	Error code							
32								
35								
40	Failed Window class (ASCII code: 8characters)							
47	Failed function name (ASCII code: 24characters)							
48								
5F	Failed line number							
60								
61	Error code							
62								
65	Reserved							
66								
7F								

*1: Failed AP Code

x'0000' : Dump task
x'0002' : Micro-program Exchange (Online)
x'0003' : Information
x'0004' : LOGOUT
x'0006' : Back Ground
x'0007' : DIAG
x'0008' : Install
x'0009' : Online
x'000A' : Micro-program Exchange (Offline)
x'000B' : Special package change
x'000C' : Common function for communication
x'00FF' : Others

*2: Error type code

x'0000' : Logical inconsistency
x'0001' : Windows memory allocate failure
x'0002' : File
x'0003' : LAN failure
x'0005' : Windows failure
x'000B' : Router failure
x'000D' : Stop SSB overwrite
x'000E' : Stop SSBS overwrite
x'000F' : Flash failure
x'0015' : Battery failure
x'0030' : Delay of time
x'0031' : Warning of a stack
x'0032' : Single task error
x'0033' : MPC launcher error
x'0039' : IP address setting error
x'003A' : Subnet mask setting error
x'003B' : Virus detection error
x'003C' : Virus detection error
x'003D' : Virus detection error
x'003F' : Situation observation error
x'0040' : Dummy PS/OFF starting (normal)

(2) CUDG detected error

	0	1	2	3	4	5	6	7
28	'CUDG ERR' (x'43 55 44 47 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0006' : CUDG detection error)							
31								
32								
33	Error code (x'00000000')							
34								
35								
40	Error occurred time (year)							
41	Error occurred time (month)							
42	Error occurred time (day)							
43	Error occurred time (hour)							
44	Error occurred time (minute)							
45	Error occurred time (second)							
46	Report MPID							
47	Test object							
48	Test type (x'01' : CUDG3, x'10' : FCDG3)							
49	Not used							
4A	Test ID (*1)							
4B								
4C	Error code (*1)							
4D								
4E	Error type							
4F	Error factor							
50	PCB location (ISW)							
51	PCB location (own CTL)							
52	PCB location (DKB in own CTL)							
53	PCB location (CHB in own CTL)							
54	PCB location (DIMM in own CTL)							
55	Reserved							
56	PCB location (CFM/LANB/all DIMMs in own CTL)							
57	PCB location (HIE/X path in own CTL)							
58	Reserved							
59	Reserved							
5A	Reserved							
5B	Reserved							
5C	PCB location (another CTL)							
5D	PCB location (HIE/X path in another CTL)							
5E	Reserved							
5F	Reserved							
60	Additional information (32Byte)							
7F								

*1: For the list of the test IDs and error codes, see "6. CUDG3 Error Code Table".

(3) BOOT detected error

	0	1	2	3	4	5	6	7
28	'BOOT ERR' (x'42 4F 4F 54 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0009' : BOOT detected error)							
31								
32	Error code (x'00000000' X:Processor number)							
33								
34								
35								
40	Error detection point							
4F	Reserved							
50								
51								
52								
53	Error code for loading error							
54								
55								
56								
57	Error code of initial setting individual function							
58								
59								
5A								
5B	Error code of initialization function (*1)							
5C								
5D								
5E								
5F	Reserved							
60								
7F								

*1: Error code of initialization function.

x'31000041' : Forcible start with CE mode switches.

x'48000041' : This MP has the micro-program which type is different from the PCB name.(In the MP-Install procedure, the package may have been set which type is different from one that is specified in the define configuration.)

x'56000041' : Sum check error of the load module.(MPC Failure occurred)

others : Loading for the load module impossible.(The load module was incorrect)

5.6 Byte27 = 8E (CHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force Log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK informaiton VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'E')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.6.1 Byte27 = 8E (CHK1) Byte28-31 Detail

(1) Byte28 = 20 : DIMM uncorrectable error

	0	1	2	3	4	5	6	7
28	Subcode : x'20' :DIMM uncorrectable error							
29	MPID (Report SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(2) Byte28 = 21 or 30 or 41 or 80: CPU error, CPU uncorrectable/correctable error, CPU core uncorrectable error

	0	1	2	3	4	5	6	7
28	Subcode: x'21' : CPU error x'30' : CPU uncorrectable/correctable error x'41' : CPU correctable error x'80' : CPU core uncorrectable error							
29	MPID (Reported SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	Not used							
2D	MC bunk of error							
2E	IA32_MCi_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
2F	IA32_MCi_STATUS[55:48]							
30	IA32_MCi_STATUS[15:08]							
31	IA32_MCi_STATUS[7:0]							

(3) Byte28 = 22 or 23 or 24 or 31 or 32: CPU error

	0	1	2	3	4	5	6	7
28	Subcode: x'22': CPU0 uncorrectable error x'23': CPU1 uncorrectable error x'24': CPU0/CPU1 uncorrectable/correctable error x'31': CPU1 uncorrectable/correctable error x'32': CPU0 uncorrectable/correctable error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(4) Byte28 = 40 : DIMM correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'40': DIMM correctable error							
29	MPID (Reported SSB)							
2A	IA32_MC7_STATUS[63:56]							
	Valid	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	IA32_MC7_STATUS[55:48]							
2C	IA32_MC7_STATUS[47:40]							
2D	IA32_MC7_STATUS[39:32]							
2E	IA32_MC7_STATUS[31:24]							
2F	IA32_MC7_STATUS[23:16]							
30	IA32_MC7_STATUS[15:8]							
31	IA32_MC7_STATUS[7:0]							
	Reserved	Reserved	Reserved	Reserved	indicate the channel number when the Valid bit (IA32_MC7_STATUS[63]) is asserted for VSP 5100, 5500 0000b = DIMM00/DIMM02 0001b = DIMM01/DIMM03 0002b = DIMM10/DIMM12 0003b = DIMM11/DIMM13			

(5) Byte28 = 42 : DIMM correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'42': DIMM correctable error							
29	MPID (Reported SSB)							
2A	DIMM error information							
2B								
2C								
2D								
2E	DIMM error information (Another CPU's DIMM)							
2F								
30								
31								

- (6) Byte28 = 43 or 44 or 45 or 46 or 4A or 4F or 50 or 51 or 53 or 54 or 55 or 56 or 57 or 58 or 59 or 5A or 60 or 61 or 62 or 63 or 64 or 65 or 66 or 67 or 70 or 71 or 72 or 73 or 74 or 75 or 76 or 77 :
Controller path error, CPU internal data path error, SPI Flash Data Access Error, IO board path error

	0	1	2	3	4	5	6	7
28	Subcode: x'43' or x'44' : Controller path error x'45' or x'46' or x'4A' or x'50' or x'51' : CPU internal data path error x'4F' : SPI Flash Data Access Error x'53' or x'60' : IO board path error (Slot F(CBL) or Slot B(CBS)) x'54' or x'61' : IO board path error (Slot B(CBL) or SlotA(CBS)) x'55' or x'62' : IO board path error (Slot A) x'56' or x'63' : IO board path error (Slot E) x'57' or x'64' : IO board path error (Slot H) x'58' or x'65' : IO board path error (Slot D) x'59' or x'66' : IO board path error (Slot C) x'5A' or x'67' : IO board path error (Slot G) x'70' : CHK1B HIE(SlotC)HIE HIEP failure x'71' : CHK1B HIE(SlotC) SWIF failure x'72' : CHK1B HIE(SlotC) XDMA failure x'73' : CHK1B HIE(SlotC) MFDMA failure x'74' : CHK1B HIE(SlotG) HIEP failure x'75' : CHK1B HIE(SlotG) SWIF failure x'76' : CHK1B HIE(SlotG) XDMA failure x'77' : CHK1B HIE(SlotG) MFDMA failure							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU1_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU1_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(7) Byte28 = 47 or 48 or 49 or 4D or 52 : LAN device or Board management device error

	0	1	2	3	4	5	6	7
28	Subcode: x'47' : Management LAN device error x'48' or x'49' or x'4D' or x'52' : Board management device error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2F	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
30	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
31	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(8) Byte28 = 4B or 4C : CPU internal error

	0	1	2	3	4	5	6	7
28	Subcode : x'4B' or x'4C': CPU internal error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	LOCAL_APIC_ESR							
	Addr_Error	Received Illegal vector	Send Illegal vector	Reserved	Reserved	Reserved	Reserved	Reserved
2F	Not used							
30	Not used							
31	Not used							

(9) Byte28 = 4E : Controller Interconnect Path Failure Related Error

	0	1	2	3	4	5	6	7
28	Subcode : x'4E': Controller Interconnect Path Failure Related Error							
29	MPID (Reported SSB)							
2A	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2B	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
2C	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
2D	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(10) Byte28 = 68 or 78 or 79 : IO board (Slot A-H) power failure or initialize error

	0	1	2	3	4	5	6	7
28	Subcode: x'68': IO board power failure or initialize error (Slot A-H)							
29	MPID (Reported SSB)							
2A	GCTL_R_IOPK_ALM_STS1							
	IO_8_ALM	IO_7_ALM	IO_6_ALM	IO_5_ALM	IO_4_ALM	IO_3_ALM	IO_2_ALM	IO_1_ALM
2B	GCTL_R_IOPK_ALM_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MNG_ALM
2C	R_IOPK_PRT_STS1							
	IO_8_IN	IO_7_IN	IO_6_IN	IO_5_IN	IO_4_IN	IO_3_IN	IO_2_IN	IO_1_IN
2D	R_IOPK_PRT_STS2							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	MNG_IN
2E	GCTL_R_CURRENT_DIAGMODE							
2F	GCTL_R_CURRENT_DCMODE							
30	CPUx_y_LNKSTS[15:8] (x: 0 or 1, y: PA1 or PA2 or PA3 or PA5 or PB1 or PB2 or PB3 or PB5)							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
31	CPUx_y_LNKSTS[7:0] (x: 0 or 1, y: PA1 or PA2 or PA3 or PA5 or PB1 or PB2 or PB3 or PB5)							
	Link width[7:0]				Current link speed			

(11) Byte28 = A0 or A1 : Another CTL error or Another CTL's DIMM error

	0	1	2	3	4	5	6	7
28	Subcode: x'A0' : CPU0/CPU1/DIMM uncorrectable error (Another cluster) x'A1' : DIMM uncorrectable error (Another cluster)							
29	MPID (Reported SSB)							
2A	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
2B	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
2C	GCTL_MATED_CTLSTS0							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL Power	Another CTL Presence
2D	GCTL_MATED_CTLSTS1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
2E	GCTL_MATED_CTLSTS2							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Another CTL shutdown	Reserved
2F	GCTL_MATED_CTLSTS3							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
30	GCTL_LOGFLAG							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
31	Not used							

5.6.2 Byte27 = 8E (CHK1) Byte40-7F Detail

(1) Byte28 = 20 : DIMM uncorrectable error

	0	1	2	3	4	5	6	7
40	Free area format number = 01h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GFERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
56	CPU1_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
57	CPU1_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
58	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
5A	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
5B	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
5C	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC3_STATUS[55:32]							
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	IA32_MC3_STATUS[31:0]							
61								
62								
63								
64	IA32_MC3_ADDR[63:0]							
65								
66								
67								
68								
69								
6A								
6B								
6C	IA32_MC3_MISC[31:0]							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	IA32_MC7_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
71	DIMM error information							
72								
73								
74								
75								
76								
77								
78	DIMM error address (When “Error”=1” and “Addr valid” =1)							
79								
7A								
7B								
7C								
7D								
7E								
7F								

(2) Byte28 = 21 or 80 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 02h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	CPU1_GBL_GNERRST[23:16]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	CPU1_GBL_GNERRST[15:8]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	CPU1_GBL_GNERRST[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
57	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
58	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
59	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	IA32_MCi_STATUS[63:0]							
69	(Error description)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	IA32_MCi_ADDR[63:0]							
71	(Error address)							
72								
73								
74								
75								
76								
77								
78	IA32_MCi_MISC[63:0]							
79	(Error description2)							
7A								
7B								
7C								
7D								
7E								
7F								

(3) Byte28 = 30 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 04h							
41	Not used							
42	Not used							
43	Not used							
44	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
45	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
46	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
47	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
48	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
49	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4E	Not used							
4F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
51	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
52	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	IA32_MCi_STATUS[63:0]							
59	(Error description)							
5A								
5B								
5C								
5D								
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	IA32_MCi_ADDR[63:0]							
61	(Error address)							
62								
63								
64								
65								
66								
67								
68	IA32_MCi_MISC[63:0]							
69	(Error description2)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = 22 or 24 or 31 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 05h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_VTUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_VTUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	CPU0_VTUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
53	CPU0_VTUNCERRSTS[7:0]							
	Reserved	Error	Error	Error	Error	Error	Error	Error
54	CPU0_MIERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_MIERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_MIERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	CPU0_MIERRSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Error
58	CPU0_MIFFERRHDR0[31:24]							
59	CPU0_MIFFERRHDR0[23:16]							
5A	CPU0_MIFFERRHDR0[15:8]							
5B	CPU0_MIFFERRHDR0[7:0]							
5C	CPU0_MIFFERRHDR1[31:24]							
5D	CPU0_MIFFERRHDR1[23:16]							
5E	CPU0_MIFFERRHDR1[15:8]							
5F	CPU0_MIFFERRHDR1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_MIFFERRHDR2[31:24]							
61	CPU0_MIFFERRHDR2[23:16]							
62	CPU0_MIFFERRHDR2[15:8]							
63	CPU0_MIFFERRHDR2[7:0]							
64	CPU0_MIFFERRHDR3[31:24]							
65	CPU0_MIFFERRHDR3[23:16]							
66	CPU0_MIFFERRHDR3[15:8]							
67	CPU0_MIFFERRHDR3[7:0]							
68	CPU0_IIOERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_IIOERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU0_IIOERRST[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	CPU0_IIOERRST[7:0]							
	Reserved	Error	Error	Error	Reserved	Reserved	Reserved	Reserved
6C	CPU0_IIOFFERRHD0[31:24]							
6D	CPU0_IIOFFERRHD0[23:16]							
6E	CPU0_IIOFFERRHD0[15:8]							
6F	CPU0_IIOFFERRHD0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_IIOFFERRHD1[31:24]							
71	CPU0_IIOFFERRHD1[23:16]							
72	CPU0_IIOFFERRHD1[15:8]							
73	CPU0_IIOFFERRHD1[7:0]							
74	CPU0_IIOFFERRHD2[31:24]							
75	CPU0_IIOFFERRHD2[23:16]							
76	CPU0_IIOFFERRHD2[15:8]							
77	CPU0_IIOFFERRHD2[7:0]							
78	CPU0_IIOFFERRHD3[31:24]							
79	CPU0_IIOFFERRHD3[23:16]							
7A	CPU0_IIOFFERRHD3[15:8]							
7B	CPU0_IIOFFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = 23 or 32 : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 06h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
47	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_VTUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_VTUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
52	CPU1_VTUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
53	CPU1_VTUNCERRSTS[7:0]							
	Reserved	Error	Error	Error	Error	Error	Error	Error
54	CPU1_MIERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_MIERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU1_MIERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	CPU1_MIERRSTS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Error
58	CPU1_MIFFERRHDR0[31:24]							
59	CPU1_MIFFERRHDR0[23:16]							
5A	CPU1_MIFFERRHDR0[15:8]							
5B	CPU1_MIFFERRHDR0[7:0]							
5C	CPU1_MIFFERRHDR1[31:24]							
5D	CPU1_MIFFERRHDR1[23:16]							
5E	CPU1_MIFFERRHDR1[15:8]							
5F	CPU1_MIFFERRHDR1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_MIFFERRHDR2[31:24]							
61	CPU1_MIFFERRHDR2[23:16]							
62	CPU1_MIFFERRHDR2[15:8]							
63	CPU1_MIFFERRHDR2[7:0]							
64	CPU1_MIFFERRHDR3[31:24]							
65	CPU1_MIFFERRHDR3[23:16]							
66	CPU1_MIFFERRHDR3[15:8]							
67	CPU1_MIFFERRHDR3[7:0]							
68	CPU1_IIOERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU1_IIOERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU1_IIOERRST[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6B	CPU1_IIOERRST[7:0]							
	Reserved	Error	Error	Error	Reserved	Reserved	Reserved	Reserved
6C	CPU1_IIOFFERRHD0[31:24]							
6D	CPU1_IIOFFERRHD0[23:16]							
6E	CPU1_IIOFFERRHD0[15:8]							
6F	CPU1_IIOFFERRHD0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_IIOFFERRHD1[31:24]							
71	CPU1_IIOFFERRHD1[23:16]							
72	CPU1_IIOFFERRHD1[15:8]							
73	CPU1_IIOFFERRHD1[7:0]							
74	CPU1_IIOFFERRHD2[31:24]							
75	CPU1_IIOFFERRHD2[23:16]							
76	CPU1_IIOFFERRHD2[15:8]							
77	CPU1_IIOFFERRHD2[7:0]							
78	CPU1_IIOFFERRHD3[31:24]							
79	CPU1_IIOFFERRHD3[23:16]							
7A	CPU1_IIOFFERRHD3[15:8]							
7B	CPU1_IIOFFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(6) Byte28 = 40 : DIMM correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 07h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	DIMM error address							
55								
56								
57								
58								
59								
5A	DIMM error description2[63:16]							
5B								
5C								
5D								
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	DIMM error description2[15:0]							
61								
62	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	DIMM error description (MC9) [55:0]							
65								
66								
67								
68								
69								
6A	DIMM error address (MC9)							
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	DIMM error description2 (MC9)							
71								
72								
73								
74								
75								
76								
77								
78	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
79	DIMM error description (MC10) [55:0]							
7A								
7B								
7C								
7D								
7E								
7F								

(7) Byte28 = 41 : CPU correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 08h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	IA32_MC0_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC1_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	IA32_MC2_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
57	IA32_MC3_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
58	IA32_MC4_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
59	IA32_MC5_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5A	IA32_MC9_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5B	IA32_MC10_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5C	IA32_MC11_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC12_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	IA32_MC17_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC18_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC19_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC20_STATUS[63:56]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	IA32_MCi_STATUS[63:0]							
69	(Error description)							
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	IA32_MCi_ADDR[63:0]							
71	(Error address)							
72								
73								
74								
75								
76								
77								
78	IA32_MCi_MISC[63:0]							
79	(Error description2)							
7A								
7B								
7C								
7D								
7E								
7F								

(8) Byte28 = 42 : DIMM correctable error

	0	1	2	3	4	5	6	7
40	Free area format number = 09h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	IA32_MC0_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
45	IA32_MC1_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
46	IA32_MC2_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
47	IA32_MC3_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
48	IA32_MC4_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
49	IA32_MC5_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4A	IA32_MC7_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4B	Not used							
4C	IA32_MC9_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4D	IA32_MC10_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4E	IA32_MC11_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
4F	IA32_MC12_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	IA32_MC17_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
53	IA32_MC18_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
54	IA32_MC19_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
55	IA32_MC20_STATUS[7:0]							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	DIMM error information[63:32] (CPU0's DIMM)							
5D								
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	DIMM error information[31:0] (CPU0's DIMM)							
61								
62								
63								
64	DIMM error address (CPU0's DIMM)							
65								
66								
67								
68								
69								
6A	DIMM error information[63:16] (CPU1's DIMM)							
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	DIMM error information[15:0] (CPU1's DIMM)							
71								
72	DIMM error address (CPU1's DIMM)							
73								
74								
75								
76								
77								
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(9) Byte28 = 45 or 50 : CPU internal data path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Ch							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_CB00_DMAUNCERRPTR[7:0]							
45	Not used							
46	Not used							
47	Not used							
48	CPU0_CB00_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_CB00_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	CPU0_CB00_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
4B	CPU0_CB00_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
4C	CPU0_CB00_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_CB00_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
4E	CPU0_CB00_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
4F	CPU0_CB00_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_CB01_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_CB01_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
52	CPU0_CB01_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
53	CPU0_CB01_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
54	CPU0_CB02_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_CB02_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
56	CPU0_CB02_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
57	CPU0_CB02_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
58	CPU0_CB03_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU0_CB03_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5A	CPU0_CB03_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5B	CPU0_CB03_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
5C	CPU0_CB04_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_CB04_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5E	CPU0_CB04_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5F	CPU0_CB04_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_CB05_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	CPU0_CB05_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
62	CPU0_CB05_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
63	CPU0_CB05_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
64	CPU0_CB06_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU0_CB06_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
66	CPU0_CB06_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
67	CPU0_CB06_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
68	CPU0_CB07_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_CB07_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
6A	CPU0_CB07_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
6B	CPU0_CB07_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
6C	CPU0_CB00_CHANSTS_0[31:24]							
6D	CPU0_CB00_CHANSTS_0[23:16]							
6E	CPU0_CB00_CHANSTS_0[15:8]							
6F	CPU0_CB00_CHANSTS_0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_CB00_CHANSTS_1[31:24]							
71	CPU0_CB00_CHANSTS_1[23:16]							
72	CPU0_CB00_CHANSTS_1[15:8]							
73	CPU0_CB00_CHANSTS_1[7:0]							
74	CPU0_CB01_CHANSTS_0[31:24]							
75	CPU0_CB01_CHANSTS_0[23:16]							
76	CPU0_CB01_CHANSTS_0[15:8]							
77	CPU0_CB01_CHANSTS_0[7:0]							
78	CPU0_CB01_CHANSTS_1[31:24]							
79	CPU0_CB01_CHANSTS_1[23:16]							
7A	CPU0_CB01_CHANSTS_1[15:8]							
7B	CPU0_CB01_CHANSTS_1[7:0]							
7C	CPU0_CB02_CHANSTS_0[31:24]							
7D	CPU0_CB02_CHANSTS_0[23:16]							
7E	CPU0_CB02_CHANSTS_0[15:8]							
7F	CPU0_CB02_CHANSTS_0[7:0]							

(10) Byte28 = 46 or 51 : CPU internal data path error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Dh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_CB10_DMAUNCERRPTR[7:0]							
45	Not used							
46	Not used							
47	Not used							
48	CPU1_CB10_DMAUNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_CB10_DMAUNCERRSTS[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4A	CPU1_CB10_DMAUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Error	Reserved	Error	Reserved	Reserved
4B	CPU1_CB10_DMAUNCERRSTS[7:0]							
	Error	Reserved	Reserved	Error	Error	Error	Reserved	Reserved
4C	CPU1_CB10_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU1_CB10_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
4E	CPU1_CB10_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
4F	CPU1_CB10_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_CB11_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_CB11_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
52	CPU1_CB11_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
53	CPU1_CB11_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
54	CPU1_CB12_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_CB12_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
56	CPU1_CB12_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
57	CPU1_CB12_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
58	CPU1_CB13_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_CB13_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5A	CPU1_CB13_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5B	CPU1_CB13_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
5C	CPU1_CB14_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_CB14_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
5E	CPU1_CB14_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
5F	CPU1_CB14_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_CB15_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
61	CPU1_CB15_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
62	CPU1_CB15_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
63	CPU1_CB15_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
64	CPU1_CB16_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
65	CPU1_CB16_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
66	CPU1_CB16_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
67	CPU1_CB16_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
68	CPU1_CB17_CHANERR_INT[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU1_CB17_CHANERR_INT[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Desc error	Reserved	Reserved
6A	CPU1_CB17_CHANERR_INT[15:8]							
	Reserved	Reserved	Error	Error	Error	Error	Error	Error
6B	CPU1_CB17_CHANERR_INT[7:0]							
	Error	Error	Error	Error	Error	Error	Error	Error
6C	CPU1_CB10_CHANSTS_0[31:24]							
6D	CPU1_CB10_CHANSTS_0[23:16]							
6E	CPU1_CB10_CHANSTS_0[15:8]							
6F	CPU1_CB10_CHANSTS_0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_CB10_CHANSTS_1[31:24]							
71	CPU1_CB10_CHANSTS_1[23:16]							
72	CPU1_CB10_CHANSTS_1[15:8]							
73	CPU1_CB10_CHANSTS_1[7:0]							
74	CPU1_CB11_CHANSTS_0[31:24]							
75	CPU1_CB11_CHANSTS_0[23:16]							
76	CPU1_CB11_CHANSTS_0[15:8]							
77	CPU1_CB11_CHANSTS_0[7:0]							
78	CPU1_CB11_CHANSTS_1[31:24]							
79	CPU1_CB11_CHANSTS_1[23:16]							
7A	CPU1_CB11_CHANSTS_1[15:8]							
7B	CPU1_CB11_CHANSTS_1[7:0]							
7C	CPU1_CB12_CHANSTS_0[31:24]							
7D	CPU1_CB12_CHANSTS_0[23:16]							
7E	CPU1_CB12_CHANSTS_0[15:8]							
7F	CPU1_CB12_CHANSTS_0[7:0]							

(11) Byte28 = 47 : LAN device error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Eh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	Not used							
47	Not used							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_IOPK_MNG_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
75	GCTL_IOPK_MNG_CTL							
76	GCTL_R_IO_MNG_TYPE							
77	GCTL_MATED_CTLREQ_STS							
	Not used	Not used	Not used	Not used	Power alarm		Power ready	
78	GCTL_IOPKRST_MASK0							
79	GCTL_IOPKRST_MASK1							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(12) Byte28 = 48 : Board management device error

	0	1	2	3	4	5	6	7
40	Free area format number = 0Fh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_GCTL_DIAG_ENABLE							
75	GCTL_GCTLERR							
76	GCTL_FBRIDGEERR							
77	GCTL_PCI_ERRSTS							
78	GCTL_XR_ERRSTS							
79	GCTL_RAM_ERRSTS							
7A	GCTL_FBRG_IF_ERRSTS							
7B	GCTL_MATED_GCTL_IF_ERRSTS							
7C	GCTL_GCTL_RAM_ECC_ERRSTS							
7D	GCTL_PCI_RAM_ERRADR_LT0							
7E	GCTL_PCI_RAM_ERRADR_LT1							
7F	GCTL_PCI_CORRECT_CNT							

(13) Byte28 = 49 or 4D : Board management device error

	0	1	2	3	4	5	6	7
40	Free area format number = 10h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4F	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
51	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
59	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
5A	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
5B	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5C	CPU0_PA0_LNKSTS2[15:8]							
5D	CPU0_PA0_LNKSTS2[7:0]							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_ERRSID[31:24]							
61	CPU0_PA0_ERRSID[23:16]							
62	CPU0_PA0_ERRSID[15:8]							
63	CPU0_PA0_ERRSID[7:0]							
64	CPU0_PA0_HDRLOG0[31:24]							
65	CPU0_PA0_HDRLOG0[23:16]							
66	CPU0_PA0_HDRLOG0[15:8]							
67	CPU0_PA0_HDRLOG0[7:0]							
68	CPU0_PA0_HDRLOG1[31:24]							
69	CPU0_PA0_HDRLOG1[23:16]							
6A	CPU0_PA0_HDRLOG1[15:8]							
6B	CPU0_PA0_HDRLOG1[7:0]							
6C	CPU0_PA0_HDRLOG2[31:24]							
6D	CPU0_PA0_HDRLOG2[23:16]							
6E	CPU0_PA0_HDRLOG2[15:8]							
6F	CPU0_PA0_HDRLOG2[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA0_HDRLOG3[31:24]							
71	CPU0_PA0_HDRLOG3[23:16]							
72	CPU0_PA0_HDRLOG3[15:8]							
73	CPU0_PA0_HDRLOG3[7:0]							
74	GCTL_GCTL_DIAG_ENABLE							
75	GCTL_GCTLERR							
76	GCTL_FBRIDGEERR							
77	GCTL_PCI_ERRSTS							
78	GCTL_XR_ERRSTS							
79	GCTL_RAM_ERRSTS							
7A	GCTL_FBRG_IF_ERRSTS							
7B	GCTL_MATED_GCTL_IF_ERRSTS							
7C	GCTL_GCTL_RAM_ECC_ERRSTS							
7D	GCTL_PCI_RAM_ERRADR_LT0							
7E	GCTL_PCI_RAM_ERRADR_LT1							
7F	GCTL_PCI_CORRECT_CNT							

(14) Byte28 = 4A : CPU error

	0	1	2	3	4	5	6	7
40	Free area format number = 11h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
4D	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4F	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU0_IRPP0ERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0_IRPP0ERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	CPU0_IRPP0ERRST[15:8]							
	Reserved	Error	Error	Reserved	Reserved	Error	Reserved	Reserved
57	CPU0_IRPP0ERRST[7:0]							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Reserved
58	CPU0_IRPP0FFERRHD0[31:24]							
59	CPU0_IRPP0FFERRHD0[23:16]							
5A	CPU0_IRPP0FFERRHD0[15:8]							
5B	CPU0_IRPP0FFERRHD0[7:0]							
5C	CPU0_IRPP0FFERRHD1[31:24]							
5D	CPU0_IRPP0FFERRHD1[23:16]							
5E	CPU0_IRPP0FFERRHD1[15:8]							
5F	CPU0_IRPP0FFERRHD1[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_IRPP0FFERRHD2[31:24]							
61	CPU0_IRPP0FFERRHD2[23:16]							
62	CPU0_IRPP0FFERRHD2[15:8]							
63	CPU0_IRPP0FFERRHD2[7:0]							
64	CPU0_IRPP0FFERRHD3[31:24]							
65	CPU0_IRPP0FFERRHD3[23:16]							
66	CPU0_IRPP0FFERRHD3[15:8]							
67	CPU0_IRPP0FFERRHD3[7:0]							
68	CPU0_IRPP1ERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
69	CPU0_IRPP1ERRST[23:16]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
6A	CPU0_IRPP1ERRST[15:8]							
	Reserved	Error	Error	Reserved	Reserved	Error	Reserved	Reserved
6B	CPU0_IRPP1ERRST[7:0]							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Reserved
6C	CPU0_IRPP1FFERRHD0[31:24]							
6D	CPU0_IRPP1FFERRHD0[23:16]							
6E	CPU0_IRPP1FFERRHD0[15:8]							
6F	CPU0_IRPP1FFERRHD0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_IRPP1FFERRHD1[31:24]							
71	CPU0_IRPP1FFERRHD1[23:16]							
72	CPU0_IRPP1FFERRHD1[15:8]							
73	CPU0_IRPP1FFERRHD1[7:0]							
74	CPU0_IRPP1FFERRHD2[31:24]							
75	CPU0_IRPP1FFERRHD2[23:16]							
76	CPU0_IRPP1FFERRHD2[15:8]							
77	CPU0_IRPP1FFERRHD2[7:0]							
78	CPU0_IRPP1FFERRHD3[31:24]							
79	CPU0_IRPP1FFERRHD3[23:16]							
7A	CPU0_IRPP1FFERRHD3[15:8]							
7B	CPU0_IRPP1FFERRHD3[7:0]							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(15) Byte28 = 4B or 4C : CPU internal error

	0	1	2	3	4	5	6	7
40	Free area format number = 12h							
41	Not used							
42	Not used							
43	Not used							
44	CPU0_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
45	CPU0_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GFERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GFERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4B	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4D	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
4E	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
4F	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_GBL_GFERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	VTDERR	MISCERR
51	CPU1_GBL_GFERRST[23:16]							
	Internal error	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
52	CPU1_GBL_GFERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
53	CPU1_GBL_GFERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
54	CPU1_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU1_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
56	CPU1_GBL_GNERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
57	CPU1_GBL_GNERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
58	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
59	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
5A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
5B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
5C	IA32_MC0_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5D	IA32_MC1_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5E	IA32_MC2_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
5F	IA32_MC3_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	IA32_MC4_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
61	IA32_MC5_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
62	IA32_MC9_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
63	IA32_MC10_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
64	IA32_MC11_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
65	IA32_MC12_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
66	Not used							
67	Not used							
68	IA32_MC17_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
69	IA32_MC18_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6A	IA32_MC19_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6B	IA32_MC20_STATUS							
	Error	Overflow	Uncorrectable	Error EN	Misc valid	Addr Valid	PCC	S
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(16) Byte28 = 4F : SPI Flash Data Access Error

	0	1	2	3	4	5	6	7
40	Free area format number = 41h							
41	0x00							
42	Not used							
43	Not used							
44	Hardware Sequencing Flash Status[15:08]							
45	Hardware Sequencing Flash Status[07:06]							
46	Hardware Sequencing Flash Control[15:08]							
47	Hardware Sequencing Flash Control[07:06]							
48	Flash Address[31:24]							
49	Flash Address[23:16]							
4A	Flash Address[15:08]							
4B	Flash Address[07:00]							
4C	Software Sequencing Flash Status & Control[31:24]							
4D	Software Sequencing Flash Status & Control[23:16]							
4E	Software Sequencing Flash Status & Control[15:08]							
4F	Software Sequencing Flash Status & Control[07:06]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	FDATA[00] [31:24]							
51	FDATA[00] [23:16]							
52	FDATA[00] [15:08]							
53	FDATA[00] [07:00]							
54	FDATA[01] [31:24]							
55	FDATA[01] [23:16]							
56	FDATA[01] [15:08]							
57	FDATA[01] [07:00]							
58	FDATA[02] [31:24]							
59	FDATA[02] [23:16]							
5A	FDATA[02] [15:08]							
5B	FDATA[02] [07:00]							
5C	FDATA[03] [31:24]							
5D	FDATA[03] [23:16]							
5E	FDATA[03] [15:08]							
5F	FDATA[03] [07:00]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	FDATA[04] [31:24]							
61	FDATA[04] [23:16]							
62	FDATA[04] [15:08]							
63	FDATA[04] [07:00]							
64	FDATA[05] [31:24]							
65	FDATA[05] [23:16]							
66	FDATA[05] [15:08]							
67	FDATA[05] [07:00]							
68	FDATA[06] [31:24]							
69	FDATA[06] [23:16]							
6A	FDATA[06] [15:08]							
6B	FDATA[06] [07:00]							
6C	FDATA[07] [31:24]							
6D	FDATA[07] [23:16]							
6E	FDATA[07] [15:08]							
6F	FDATA[07] [07:00]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	FDATA[08] [31:24]							
71	FDATA[08] [23:16]							
72	FDATA[08] [15:08]							
73	FDATA[08] [07:00]							
74	FDATA[09] [31:24]							
75	FDATA[09] [23:16]							
76	FDATA[09] [15:08]							
77	FDATA[09] [07:00]							
78	FDATA[10] [31:24]							
79	FDATA[10] [23:16]							
7A	FDATA[10] [15:08]							
7B	FDATA[10] [07:00]							
7C	FDATA[11] [31:24]							
7D	FDATA[11] [23:16]							
7E	FDATA[11] [15:08]							
7F	FDATA[11] [07:00]							

(17) Byte28 = 53 or 60 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 27h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA1_PCISTS[15:8]							
4D	CPU0_PA1_PCISTS[7:0]							
4E	CPU0_PA1_SECSTS[15:8]							
4F	CPU0_PA1_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA1_ROOTSTS[31:24]							
51	CPU0_PA1_ROOTSTS[23:16]							
52	CPU0_PA1_ROOTSTS[15:8]							
53	CPU0_PA1_ROOTSTS[7:0]							
54	CPU0_PA1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA1_XPUNCERRPTR[15:8]							
57	CPU0_PA1_XPUNCERRPTR[7:0]							
58	CPU0_PA1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA1_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA1_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA1_LNKSTS2[15:8]							
65	CPU0_PA1_LNKSTS2[7:0]							
66	CPU0_PA1_LNERRST[15:8]							
67	CPU0_PA1_LNERRST[7:0]							
68	CPU0_PA1_ERRSID[31:24]							
69	CPU0_PA1_ERRSID[23:16]							
6A	CPU0_PA1_ERRSID[15:8]							
6B	CPU0_PA1_ERRSID[7:0]							
6C	CPU0_PA1_HDRLOG0[31:24]							
6D	CPU0_PA1_HDRLOG0[23:16]							
6E	CPU0_PA1_HDRLOG0[15:8]							
6F	CPU0_PA1_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA1_HDRLOG1[31:24]							
71	CPU0_PA1_HDRLOG1[23:16]							
72	CPU0_PA1_HDRLOG1[15:8]							
73	CPU0_PA1_HDRLOG1[7:0]							
74	CPU0_PA1_HDRLOG2[31:24]							
75	CPU0_PA1_HDRLOG2[23:16]							
76	CPU0_PA1_HDRLOG2[15:8]							
77	CPU0_PA1_HDRLOG2[7:0]							
78	CPU0_PA1_HDRLOG3[31:24]							
79	CPU0_PA1_HDRLOG3[23:16]							
7A	CPU0_PA1_HDRLOG3[15:8]							
7B	CPU0_PA1_HDRLOG3[7:0]							
7C	GCTL_IOPK_2_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_2_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_2_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(18) Byte28 = 54 or 61 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 28h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA2_PCISTS[15:8]							
4D	CPU0_PA2_PCISTS[7:0]							
4E	CPU0_PA2_SECSTS[15:8]							
4F	CPU0_PA2_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA2_ROOTSTS[31:24]							
51	CPU0_PA2_ROOTSTS[23:16]							
52	CPU0_PA2_ROOTSTS[15:8]							
53	CPU0_PA2_ROOTSTS[7:0]							
54	CPU0_PA2_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA2_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA2_XPUNCERRPTR[15:8]							
57	CPU0_PA2_XPUNCERRPTR[7:0]							
58	CPU0_PA2_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA2_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA2_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA2_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA2_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA2_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA2_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA2_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA2_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA2_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA2_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA2_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA2_LNKSTS2[15:8]							
65	CPU0_PA2_LNKSTS2[7:0]							
66	CPU0_PA2_LNERRST[15:8]							
67	CPU0_PA2_LNERRST[7:0]							
68	CPU0_PA2_ERRSID[31:24]							
69	CPU0_PA2_ERRSID[23:16]							
6A	CPU0_PA2_ERRSID[15:8]							
6B	CPU0_PA2_ERRSID[7:0]							
6C	CPU0_PA2_HDRLOG0[31:24]							
6D	CPU0_PA2_HDRLOG0[23:16]							
6E	CPU0_PA2_HDRLOG0[15:8]							
6F	CPU0_PA2_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA2_HDRLOG1[31:24]							
71	CPU0_PA2_HDRLOG1[23:16]							
72	CPU0_PA2_HDRLOG1[15:8]							
73	CPU0_PA2_HDRLOG1[7:0]							
74	CPU0_PA2_HDRLOG2[31:24]							
75	CPU0_PA2_HDRLOG2[23:16]							
76	CPU0_PA2_HDRLOG2[15:8]							
77	CPU0_PA2_HDRLOG2[7:0]							
78	CPU0_PA2_HDRLOG3[31:24]							
79	CPU0_PA2_HDRLOG3[23:16]							
7A	CPU0_PA2_HDRLOG3[15:8]							
7B	CPU0_PA2_HDRLOG3[7:0]							
7C	GCTL_IOPK_1_STS or GCTL_IOPK_6_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_1_CTL or GCTL_IOPK_6_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_1_TYPE or GCTL_R_IO_6_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(19) Byte28 = 55 or 62 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 29h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA3_PCISTS[15:8]							
4D	CPU0_PA3_PCISTS[7:0]							
4E	CPU0_PA3_SECSTS[15:8]							
4F	CPU0_PA3_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA3_ROOTSTS[31:24]							
51	CPU0_PA3_ROOTSTS[23:16]							
52	CPU0_PA3_ROOTSTS[15:8]							
53	CPU0_PA3_ROOTSTS[7:0]							
54	CPU0_PA3_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA3_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA3_XPUNCERRPTR[15:8]							
57	CPU0_PA3_XPUNCERRPTR[7:0]							
58	CPU0_PA3_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA3_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA3_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA3_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA3_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA3_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA3_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA3_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA3_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA3_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA3_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA3_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA3_LNKSTS2[15:8]							
65	CPU0_PA3_LNKSTS2[7:0]							
66	CPU0_PA3_LNERRST[15:8]							
67	CPU0_PA3_LNERRST[7:0]							
68	CPU0_PA3_ERRSID[31:24]							
69	CPU0_PA3_ERRSID[23:16]							
6A	CPU0_PA3_ERRSID[15:8]							
6B	CPU0_PA3_ERRSID[7:0]							
6C	CPU0_PA3_HDRLOG0[31:24]							
6D	CPU0_PA3_HDRLOG0[23:16]							
6E	CPU0_PA3_HDRLOG0[15:8]							
6F	CPU0_PA3_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA3_HDRLOG1[31:24]							
71	CPU0_PA3_HDRLOG1[23:16]							
72	CPU0_PA3_HDRLOG1[15:8]							
73	CPU0_PA3_HDRLOG1[7:0]							
74	CPU0_PA3_HDRLOG2[31:24]							
75	CPU0_PA3_HDRLOG2[23:16]							
76	CPU0_PA3_HDRLOG2[15:8]							
77	CPU0_PA3_HDRLOG2[7:0]							
78	CPU0_PA3_HDRLOG3[31:24]							
79	CPU0_PA3_HDRLOG3[23:16]							
7A	CPU0_PA3_HDRLOG3[15:8]							
7B	CPU0_PA3_HDRLOG3[7:0]							
7C	GCTL_IOPK_3_STS or GCTL_IOPK_5_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_3_CTL or GCTL_IOPK_5_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_3_TYPE or GCTL_R_IO_5_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(20) Byte28 = 56 or 63 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Ah							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU0_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU0_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU0_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU0_PA5_PCISTS[15:8]							
4D	CPU0_PA5_PCISTS[7:0]							
4E	CPU0_PA5_SECSTS[15:8]							
4F	CPU0_PA5_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA5_ROOTSTS[31:24]							
51	CPU0_PA5_ROOTSTS[23:16]							
52	CPU0_PA5_ROOTSTS[15:8]							
53	CPU0_PA5_ROOTSTS[7:0]							
54	CPU0_PA5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU0_PA5_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU0_PA5_XPUNCERRPTR[15:8]							
57	CPU0_PA5_XPUNCERRPTR[7:0]							
58	CPU0_PA5_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU0_PA5_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU0_PA5_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU0_PA5_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU0_PA5_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU0_PA5_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU0_PA5_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU0_PA5_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA5_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU0_PA5_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU0_PA5_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU0_PA5_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU0_PA5_LNKSTS2[15:8]							
65	CPU0_PA5_LNKSTS2[7:0]							
66	CPU0_PA5_LNERRST[15:8]							
67	CPU0_PA5_LNERRST[7:0]							
68	CPU0_PA5_ERRSID[31:24]							
69	CPU0_PA5_ERRSID[23:16]							
6A	CPU0_PA5_ERRSID[15:8]							
6B	CPU0_PA5_ERRSID[7:0]							
6C	CPU0_PA5_HDRLOG0[31:24]							
6D	CPU0_PA5_HDRLOG0[23:16]							
6E	CPU0_PA5_HDRLOG0[15:8]							
6F	CPU0_PA5_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU0_PA5_HDRLOG1[31:24]							
71	CPU0_PA5_HDRLOG1[23:16]							
72	CPU0_PA5_HDRLOG1[15:8]							
73	CPU0_PA5_HDRLOG1[7:0]							
74	CPU0_PA5_HDRLOG2[31:24]							
75	CPU0_PA5_HDRLOG2[23:16]							
76	CPU0_PA5_HDRLOG2[15:8]							
77	CPU0_PA5_HDRLOG2[7:0]							
78	CPU0_PA5_HDRLOG3[31:24]							
79	CPU0_PA5_HDRLOG3[23:16]							
7A	CPU0_PA5_HDRLOG3[15:8]							
7B	CPU0_PA5_HDRLOG3[7:0]							
7C	GCTL_IOPK_1_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_1_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_1_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(21) Byte28 = 57 or 64 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Bh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB1_PCISTS[15:8]							
4D	CPU1_PB1_PCISTS[7:0]							
4E	CPU1_PB1_SECSTS[15:8]							
4F	CPU1_PB1_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_PB1_ROOTSTS[31:24]							
51	CPU1_PB1_ROOTSTS[23:16]							
52	CPU1_PB1_ROOTSTS[15:8]							
53	CPU1_PB1_ROOTSTS[7:0]							
54	CPU1_PB1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB1_XPUNCERRPTR[15:8]							
57	CPU1_PB1_XPUNCERRPTR[7:0]							
58	CPU1_PB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_PB1_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB1_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB1_LNKSTS2[15:8]							
65	CPU1_PB1_LNKSTS2[7:0]							
66	CPU1_PB1_LNERRST[15:8]							
67	CPU1_PB1_LNERRST[7:0]							
68	CPU1_PB1_ERRSID[31:24]							
69	CPU1_PB1_ERRSID[23:16]							
6A	CPU1_PB1_ERRSID[15:8]							
6B	CPU1_PB1_ERRSID[7:0]							
6C	CPU1_PB1_HDRLOG0[31:24]							
6D	CPU1_PB1_HDRLOG0[23:16]							
6E	CPU1_PB1_HDRLOG0[15:8]							
6F	CPU1_PB1_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_PB1_HDRLOG1[31:24]							
71	CPU1_PB1_HDRLOG1[23:16]							
72	CPU1_PB1_HDRLOG1[15:8]							
73	CPU1_PB1_HDRLOG1[7:0]							
74	CPU1_PB1_HDRLOG2[31:24]							
75	CPU1_PB1_HDRLOG2[23:16]							
76	CPU1_PB1_HDRLOG2[15:8]							
77	CPU1_PB1_HDRLOG2[7:0]							
78	CPU1_PB1_HDRLOG3[31:24]							
79	CPU1_PB1_HDRLOG3[23:16]							
7A	CPU1_PB1_HDRLOG3[15:8]							
7B	CPU1_PB1_HDRLOG3[7:0]							
7C	GCTL_IOPK_4_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_4_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_4_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(22) Byte28 = 58 or 65 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Ch							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB2_PCISTS[15:8]							
4D	CPU1_PB2_PCISTS[7:0]							
4E	CPU1_PB2_SECSTS[15:8]							
4F	CPU1_PB2_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_PB2_ROOTSTS[31:24]							
51	CPU1_PB2_ROOTSTS[23:16]							
52	CPU1_PB2_ROOTSTS[15:8]							
53	CPU1_PB2_ROOTSTS[7:0]							
54	CPU1_PB2_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB2_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB2_XPUNCERRPTR[15:8]							
57	CPU1_PB2_XPUNCERRPTR[7:0]							
58	CPU1_PB2_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB2_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB2_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB2_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB2_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB2_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB2_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB2_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_PB2_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB2_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB2_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB2_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB2_LNKSTS2[15:8]							
65	CPU1_PB2_LNKSTS2[7:0]							
66	CPU1_PB2_LNERRST[15:8]							
67	CPU1_PB2_LNERRST[7:0]							
68	CPU1_PB2_ERRSID[31:24]							
69	CPU1_PB2_ERRSID[23:16]							
6A	CPU1_PB2_ERRSID[15:8]							
6B	CPU1_PB2_ERRSID[7:0]							
6C	CPU1_PB2_HDRLOG0[31:24]							
6D	CPU1_PB2_HDRLOG0[23:16]							
6E	CPU1_PB2_HDRLOG0[15:8]							
6F	CPU1_PB2_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_PB2_HDRLOG1[31:24]							
71	CPU1_PB2_HDRLOG1[23:16]							
72	CPU1_PB2_HDRLOG1[15:8]							
73	CPU1_PB2_HDRLOG1[7:0]							
74	CPU1_PB2_HDRLOG2[31:24]							
75	CPU1_PB2_HDRLOG2[23:16]							
76	CPU1_PB2_HDRLOG2[15:8]							
77	CPU1_PB2_HDRLOG2[7:0]							
78	CPU1_PB2_HDRLOG3[31:24]							
79	CPU1_PB2_HDRLOG3[23:16]							
7A	CPU1_PB2_HDRLOG3[15:8]							
7B	CPU1_PB2_HDRLOG3[7:0]							
7C	GCTL_IOPK_8_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_8_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_8_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(23) Byte28 = 59 or 66 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Dh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB3_PCISTS[15:8]							
4D	CPU1_PB3_PCISTS[7:0]							
4E	CPU1_PB3_SECSTS[15:8]							
4F	CPU1_PB3_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_PB3_ROOTSTS[31:24]							
51	CPU1_PB3_ROOTSTS[23:16]							
52	CPU1_PB3_ROOTSTS[15:8]							
53	CPU1_PB3_ROOTSTS[7:0]							
54	CPU1_PB3_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB3_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB3_XPUNCERRPTR[15:8]							
57	CPU1_PB3_XPUNCERRPTR[7:0]							
58	CPU1_PB3_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB3_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB3_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB3_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB3_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB3_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB3_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB3_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_PB3_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB3_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB3_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB3_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB3_LNKSTS2[15:8]							
65	CPU1_PB3_LNKSTS2[7:0]							
66	CPU1_PB3_LNERRST[15:8]							
67	CPU1_PB3_LNERRST[7:0]							
68	CPU1_PB3_ERRSID[31:24]							
69	CPU1_PB3_ERRSID[23:16]							
6A	CPU1_PB3_ERRSID[15:8]							
6B	CPU1_PB3_ERRSID[7:0]							
6C	CPU1_PB3_HDRLOG0[31:24]							
6D	CPU1_PB3_HDRLOG0[23:16]							
6E	CPU1_PB3_HDRLOG0[15:8]							
6F	CPU1_PB3_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_PB3_HDRLOG1[31:24]							
71	CPU1_PB3_HDRLOG1[23:16]							
72	CPU1_PB3_HDRLOG1[15:8]							
73	CPU1_PB3_HDRLOG1[7:0]							
74	CPU1_PB3_HDRLOG2[31:24]							
75	CPU1_PB3_HDRLOG2[23:16]							
76	CPU1_PB3_HDRLOG2[15:8]							
77	CPU1_PB3_HDRLOG2[7:0]							
78	CPU1_PB3_HDRLOG3[31:24]							
79	CPU1_PB3_HDRLOG3[23:16]							
7A	CPU1_PB3_HDRLOG3[15:8]							
7B	CPU1_PB3_HDRLOG3[7:0]							
7C	GCTL_IOPK_7_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_7_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_7_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(24) Byte28 = 5A or 67 : IO board path error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Eh							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
45	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
46	CPU1_GBL_GCERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
47	CPU1_GBL_GCERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
48	CPU1_GBL_GCERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
49	CPU1_GBL_GCERRST[23:16]							
	Reserved	Reserved	Reserved	PB0_ERR	Reserved	Reserved	Reserved	Reserved
4A	CPU1_GBL_GCERRST[15:8]							
	PBA_ERR	PB5_ERR	PB9_ERR	PB4_ERR	PB8_ERR	PB3_ERR	PB7_ERR	PB2_ERR
4B	CPU1_GBL_GCERRST[7:0]							
	PB6_ERR	PB1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
4C	CPU1_PB5_PCISTS[15:8]							
4D	CPU1_PB5_PCISTS[7:0]							
4E	CPU1_PB5_SECSTS[15:8]							
4F	CPU1_PB5_SECSTS[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU1_PB5_ROOTSTS[31:24]							
51	CPU1_PB5_ROOTSTS[23:16]							
52	CPU1_PB5_ROOTSTS[15:8]							
53	CPU1_PB5_ROOTSTS[7:0]							
54	CPU1_PB5_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
55	CPU1_PB5_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
56	CPU1_PB5_XPUNCERRPTR[15:8]							
57	CPU1_PB5_XPUNCERRPTR[7:0]							
58	CPU1_PB5_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
59	CPU1_PB5_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
5A	CPU1_PB5_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
5B	CPU1_PB5_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
5C	CPU1_PB5_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
5D	CPU1_PB5_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
5E	CPU1_PB5_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
5F	CPU1_PB5_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU1_PB5_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
61	CPU1_PB5_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
62	CPU1_PB5_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	
63	CPU1_PB5_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
64	CPU1_PB5_LNKSTS2[15:8]							
65	CPU1_PB5_LNKSTS2[7:0]							
66	CPU1_PB5_LNERRST[15:8]							
67	CPU1_PB5_LNERRST[7:0]							
68	CPU1_PB5_ERRSID[31:24]							
69	CPU1_PB5_ERRSID[23:16]							
6A	CPU1_PB5_ERRSID[15:8]							
6B	CPU1_PB5_ERRSID[7:0]							
6C	CPU1_PB5_HDRLOG0[31:24]							
6D	CPU1_PB5_HDRLOG0[23:16]							
6E	CPU1_PB5_HDRLOG0[15:8]							
6F	CPU1_PB5_HDRLOG0[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_PB5_HDRLOG1[31:24]							
71	CPU1_PB5_HDRLOG1[23:16]							
72	CPU1_PB5_HDRLOG1[15:8]							
73	CPU1_PB5_HDRLOG1[7:0]							
74	CPU1_PB5_HDRLOG2[31:24]							
75	CPU1_PB5_HDRLOG2[23:16]							
76	CPU1_PB5_HDRLOG2[15:8]							
77	CPU1_PB5_HDRLOG2[7:0]							
78	CPU1_PB5_HDRLOG3[31:24]							
79	CPU1_PB5_HDRLOG3[23:16]							
7A	CPU1_PB5_HDRLOG3[15:8]							
7B	CPU1_PB5_HDRLOG3[7:0]							
7C	GCTL_IOPK_3_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	IN_Change	IN
7D	GCTL_IOPK_3_CTL							
	SFPOE	ATN_LED	Reset	Power Enable	Not used	Not used	Not used	Not used
7E	GCTL_R_IO_3_TYPE							
7F	GCTL_MATED_CTLREQ_STS							
	Not used	M_LEDOFF	M_ALMLED	Not used	10 : Reset from another CTL		10 : Emergency	

(25) Byte28 = 68 : IO board power failure or initialize error

	0	1	2	3	4	5	6	7
40	Free area format number = 2Fh							
41	Not used							
42	GCTL_R_IO_x_TYPE							
43	GCTL_IOPK_x_STS							
	Not used	Not used	Not used	Not used	Power alarm	Power ready	Not used	Presence
44	GCTL_R_IO_x_ERRLOG_0[31:24]							
45	GCTL_R_IO_x_ERRLOG_0[23:16]							
46	GCTL_R_IO_x_ERRLOG_0[15:8]							
47	GCTL_R_IO_x_ERRLOG_0[7:0]							
48	GCTL_R_IO_x_ERRLOG_4[31:24]							
49	GCTL_R_IO_x_ERRLOG_4[23:16]							
4A	GCTL_R_IO_x_ERRLOG_4[15:8]							
4B	GCTL_R_IO_x_ERRLOG_4[7:0]							
4C	GCTL_R_IO_x_ERRLOG_8[31:24]							
4D	GCTL_R_IO_x_ERRLOG_8[23:16]							
4E	GCTL_R_IO_x_ERRLOG_8[15:8]							
4F	GCTL_R_IO_x_ERRLOG_8[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	GCTL_R_IO_x_ERRLOG_10[31:24]							
51	GCTL_R_IO_x_ERRLOG_10[23:16]							
52	GCTL_R_IO_x_ERRLOG_10[15:8]							
53	GCTL_R_IO_x_ERRLOG_10[7:0]							
54	GCTL_R_IO_x_ERRLOG_14[31:24]							
55	GCTL_R_IO_x_ERRLOG_14[23:16]							
56	GCTL_R_IO_x_ERRLOG_14[15:8]							
57	GCTL_R_IO_x_ERRLOG_14[7:0]							
58	GCTL_R_IO_x_ERRLOG_18[31:24]							
59	GCTL_R_IO_x_ERRLOG_18[23:16]							
5A	GCTL_R_IO_x_ERRLOG_18[15:8]							
5B	GCTL_R_IO_x_ERRLOG_18[7:0]							
5C	GCTL_R_IO_x_ERRLOG_1C[31:24]							
5D	GCTL_R_IO_x_ERRLOG_1C[23:16]							
5E	GCTL_R_IO_x_ERRLOG_1C[15:8]							
5F	GCTL_R_IO_x_ERRLOG_1C[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	GCTL_R_IO_x_ERRLOG_20[31:24]							
61	GCTL_R_IO_x_ERRLOG_20[23:16]							
62	GCTL_R_IO_x_ERRLOG_20[15:8]							
63	GCTL_R_IO_x_ERRLOG_20[7:0]							
64	GCTL_R_IO_x_ERRLOG_24[31:24]							
65	GCTL_R_IO_x_ERRLOG_24[23:16]							
66	GCTL_R_IO_x_ERRLOG_24[15:8]							
67	GCTL_R_IO_x_ERRLOG_24[7:0]							
68	GCTL_R_IO_x_ERRLOG_28[31:24]							
69	GCTL_R_IO_x_ERRLOG_28[23:16]							
6A	GCTL_R_IO_x_ERRLOG_28[15:8]							
6B	GCTL_R_IO_x_ERRLOG_28[7:0]							
6C	GCTL_R_IO_x_ERRLOG_2C[31:24]							
6D	GCTL_R_IO_x_ERRLOG_2C[23:16]							
6E	GCTL_R_IO_x_ERRLOG_2C[15:8]							
6F	GCTL_R_IO_x_ERRLOG_2C[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	GCTL_R_IO_x_ERRLOG_30[31:24]							
71	GCTL_R_IO_x_ERRLOG_30[23:16]							
72	GCTL_R_IO_x_ERRLOG_30[15:8]							
73	GCTL_R_IO_x_ERRLOG_30[7:0]							
74	GCTL_R_IO_x_ERRLOG_34[31:24]							
75	GCTL_R_IO_x_ERRLOG_34[23:16]							
76	GCTL_R_IO_x_ERRLOG_34[15:8]							
77	GCTL_R_IO_x_ERRLOG_34[7:0]							
78	GCTL_R_IO_x_ERRLOG_38[31:24]							
79	GCTL_R_IO_x_ERRLOG_38[23:16]							
7A	GCTL_R_IO_x_ERRLOG_38[15:8]							
7B	GCTL_R_IO_x_ERRLOG_38[7:0]							
7C	GCTL_R_IO_x_ERRLOG_3C[31:24]							
7D	GCTL_R_IO_x_ERRLOG_3C[23:16]							
7E	GCTL_R_IO_x_ERRLOG_3C[15:8]							
7F	GCTL_R_IO_x_ERRLOG_3C[7:0]							

(26) Byte28 = A0 : Another CTL error

	0	1	2	3	4	5	6	7
40	Free area format number = 30h							
41	Not used							
42	GCTL_I_STATE0							
	CPU Thermal alert	CPU/DIMM Fatal error	CPU/DIMM Fatal error	CPU error	CPU error	CPU error	GCTL error	Not used
43	GCTL_I_STATE1							
	Emergency	Mated CTL is CHK1	PSOFFREQ	LANRSTIN	Not used	Not used	Not used	Not used
44	CPU0_NTB0_PCISTS[15:8]							
45	CPU0_NTB0_PCISTS[7:0]							
46	Not used							
47	Not used							
48	CPU0_NTB0_ROOTSTS[31:24]							
49	CPU0_NTB0_ROOTSTS[23:16]							
4A	CPU0_NTB0_ROOTSTS[15:8]							
4B	CPU0_NTB0_ROOTSTS[7:0]							
4C	CPU0_NTB0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
4D	CPU0_NTB0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4E	CPU0_NTB0_XPUNCERRPTR[15:8]							
4F	CPU0_NTB0_XPUNCERRPTR[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0 NTB0 XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
51	CPU0 NTB0 XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
52	CPU0 NTB0 XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
53	CPU0 NTB0 DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
54	CPU0 NTB0 UNCERRSTS [31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	CPU0 NTB0 UNCERRSTS [23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
56	CPU0 NTB0 UNCERRSTS [15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
57	CPU0 NTB0 UNCERRSTS [7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
58	CPU0 NTB0 LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width [9:8]	
59	CPU0 NTB0 LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5A	CPU0 NTB0 LNKSTS2[15:8]							
5B	CPU0 NTB0 LNKSTS2[7:0]							
5C	CPU0 NTB0 LNERRST[15:8]							
5D	CPU0 NTB0 LNERRST[7:0]							
5E	CPU0 NTB0 ERRSID[31:24]							
5F	CPU0 NTB0 ERRSID[23:16]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_NTB0_ERRSID[15:8]							
61	CPU0_NTB0_ERRSID[7:0]							
62	CPU1_NTB1_PCISTS[15:8]							
63	CPU1_NTB1_PCISTS[7:0]							
64	Not used							
65	Not used							
66	CPU1_NTB1_ROOTSTS[31:24]							
67	CPU1_NTB1_ROOTSTS[23:16]							
68	CPU1_NTB1_ROOTSTS[15:8]							
69	CPU1_NTB1_ROOTSTS[7:0]							
6A	CPU1_NTB1_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
6B	CPU1_NTB1_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
6C	CPU1_NTB1_XPUNCERRPTR[15:8]							
6D	CPU1_NTB1_XPUNCERRPTR[7:0]							
6E	CPU1_NTB1_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
6F	CPU1_NTB1_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	CPU1_NTB1_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
71	CPU1_NTB1_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable
72	CPU1_NTB1_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
73	CPU0_NTB1_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
74	CPU0_NTB1_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
75	CPU0_NTB1_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
76	CPU1_NTB1_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width [9:8]	
77	CPU1_NTB1_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
78	CPU1_NTB1_LNKSTS2[15:8]							
79	CPU1_NTB1_LNKSTS2[7:0]							
7A	CPU1_NTB1_LNERRST[31:24]							
7B	CPU1_NTB1_LNERRST[23:16]							
7C	CPU1_NTB1_ERRSID[31:24]							
7D	CPU1_NTB1_ERRSID[23:16]							
7E	CPU1_NTB1_ERRSID[15:8]							
7F	CPU1_NTB1_ERRSID[7:0]							

(27) Byte28 = A1: Another CTL's DIMM error

	0	1	2	3	4	5	6	7
40	Free area format number = 31h							
41	Not used							
42	Not used							
43	Not used							
44	GCTL_R_MATED_DIMM_ERRLOG0[31:0]							
45	(Another CTL's CPU0 DIMM error information MC7[31:0])							
46								
47								
48	GCTL_R_MATED_DIMM_ERRLOG1[31:0]							
49	(Another CTL's CPU0 DIMM error information MC7[63:32])							
4A								
4B								
4C	GCTL_R_MATED_DIMM_ERRLOG2[31:0]							
4D	(Another CTL's CPU0 DIMM error address[31:0])							
4E								
4F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	GCTL_R_MATED_DIMM_ERRLOG3[31:0]							
51	(Another CTL's CPU0 DIMM error address[63:32])							
52								
53								
54	GCTL_R_MATED_DIMM_ERRLOG4[31:0]							
55	(Another CTL's CPU1 DIMM error information MC7[31:0])							
56								
57								
58	GCTL_R_MATED_DIMM_ERRLOG5[31:0]							
59	(Another CTL's CPU1 DIMM error information MC7[63:32])							
5A								
5B								
5C	GCTL_R_MATED_DIMM_ERRLOG6[31:0]							
5D	(Another CTL's CPU1 DIMM error address[31:0])							
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	GCTL_R_MATED_DIMM_ERRLOG7[31:0]							
61	(Another CTL's CPU1 DIMM error address[63:32])							
62								
63								
64	GCTL_R_MATED_DIMM_ERRLOG8[31:0]							
65	(Another CTL's CPU2 DIMM error information MC7[31:0])							
66								
67								
68	GCTL_R_MATED_DIMM_ERRLOG9[31:0]							
69	(Another CTL's CPU2 DIMM error information MC7[63:32])							
6A								
6B								
6C	GCTL_R_MATED_DIMM_ERRLOGA[31:0]							
6D	(Another CTL's CPU2 DIMM error address[31:0])							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	GCTL_R_MATED_DIMM_ERRLOGB[31:0]							
71	(Another CTL's CPU2 DIMM error address[63:32])							
72								
73								
74	GCTL_R_MATED_DIMM_ERRLOGC[31:0]							
75	(Another CTL's CPU3 DIMM error information MC7[31:0])							
76								
77								
78	GCTL_R_MATED_DIMM_ERRLOGD[31:0]							
79	(Another CTL's CPU3 DIMM error information MC7[63:32])							
7A								
7B								
7C	GCTL_R_MATED_DIMM_ERRLOGE[31:0]							
7D	(Another CTL's CPU3 DIMM error address[31:0])							
7E								
7F								

(28) Byte28 = 70 or 74 : CHK1

	0	1	2	3	4	5	6	7
40	0x32 (Free area format number)							
41	0x00							
42	INT_BITREG_STS							
43								
44								
45	INT_INTSTS0							
46								
47								
48								
49	INT_STS_REG0							
4A								
4B								
4C								
4D	INT_STS_REG1							
4E								
4F								
50								
51	INT_STS_REG2							
52								
53								
54								
55	HIEP_DA_ERRCODE0							
56								
57								
58								
59	HIEP_DA_ERRCODE1							
5A								
5B								
5C								
5D	PARBP_NODE_ERR							
5E								
5F								
60								
61	PARBP_CQ_ERROR_STATUS							
62								
63								
64								
65	PARBP_RC_ERROR_STATUS0							
66								
67								
68								
69	PARBP_RC_ERROR_STATUS1							
6A								
6B								
6C								
6D	PARBP_RQ_ERROR_STATUS0							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PARBP_RQ_ERROR_STATUS1							
71								
72								
73								
74	PARBP_RQ_ERROR_STATUS2							
75								
76								
77								
78	PARBP_RQ_ERROR_STATUS3							
79								
7A								
7B								
7C	PARBP_CC_ERROR_STATUS							
7D								
7E								
7F								

(29) Byte28 = 71 or 75 : CHK1

	0	1	2	3	4	5	6	7
40	0x33 (Free area format number)							
41	0x00							
42	INT_BITREG_STS							
43								
44								
45	INT_INTSTS0							
46								
47								
48								
49	INT_STS_REG0							
4A								
4B								
4C								
4D	INT_STS_REG1							
4E								
4F								
50								
51	INT_STS_REG2							
52								
53								
54								
55	HIEP_DA_ERRCODE0							
56								
57								
58								
59	HIEP_DA_ERRCODE1							
5A								
5B								
5C								
5D	SWIF_SB_ERR_STS							
5E								
5F								
60								
61	SWIF_SB_ERR1_STS							
62								
63								
64								
65	SWIF_SA_ERR_STS							
66								
67								
68								
69	SWIF_SA_ERR1_STS							
6A								
6B								
6C								
6D	HARX_ALL_HERR_STS							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	RAIF_ALL_HERR_STS							
71								
72								
73								
74	HATX_ALL_HERR_STS							
75								
76								
77								
78	ALL_ERR_STS							
79								
7A								
7B								
7C	1ST_ERR_STS							
7D								
7E								
7F								

(30) Byte28 = 72 or 76 : CHK1

	0	1	2	3	4	5	6	7
40	0x34 (Free area format number)							
41	0x00							
42	INT_BITREG_STS							
43								
44								
45	INT_INTSTS0							
46								
47								
48								
49	INT_STS_REG0							
4A								
4B								
4C								
4D	INT_STS_REG1							
4E								
4F								
50								
51	INT_STS_REG2							
52								
53								
54								
55	HIEP_DA_ERRCODE0							
56								
57								
58								
59	HIEP_DA_ERRCODE1							
5A								
5B								
5C								
5D	Not used							
5E								
5F								
60								
61	Not used							
62								
63								
64								
65	Not used							
66								
67								
68								
69	Not used							
6A								
6B								
6C								
6D	Not used							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	SMDMA_ERROR00							
71								
72								
73								
74	SMDMA_ERROR02							
75								
76								
77								
78	SMDMA_ERROR04							
79								
7A								
7B								
7C	Not used							
7D								
7E								
7F								

(31) Byte28 = 73 or 77 : CHK1

	0	1	2	3	4	5	6	7
40	0x35 (Free area format number)							
41	0x00							
42	INT_BITREG_STS							
43								
44								
45	INT_INTSTS0							
46								
47								
48								
49	INT_STS_REG0							
4A								
4B								
4C								
4D	INT_STS_REG1							
4E								
4F								
50								
51	INT_STS_REG2							
52								
53								
54								
55	HIEP_DA_ERRCODE0							
56								
57								
58								
59	HIEP_DA_ERRCODE1							
5A								
5B								
5C								
5D	PSIF_REG INITVAL							
5E								
5F								
60								
61	PSIF_REG INITVAL							
62								
63								
64								
65	PSIF_REG INITVAL							
66								
67								
68								
69	PSIF_REG INITVAL							
6A								
6B								
6C								
6D	FCI_REG INITVAL							
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	FCI_REG INITVAL							
71								
72								
73								
74	FCI_REG INITVAL							
75								
76								
77								
78	FCI_REG INITVAL							
79								
7A								
7B								
7C	WR_XFR_REG ERRSTS							
7D								
7E								
7F								

Blank Sheet

Blank Sheet

Blank Sheet

Blank Sheet

Blank Sheet

Blank Sheet

5.7 Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID (*1)							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
30								
31								
32	Module ID							
33	Routine ID							
34	Package number				Message code (x'0')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								

*1: Failed processor number for WCHK1

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3C	Reserved							
3F								
40	Not used							
7F								

5.8 Byte27 = 8E & Byte34:bit4-7 = 5 (CHK1B Reset)**5.9 Byte27 = 81 & Byte34:bit4-7 = 3 (CHK3 Reset)**

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'E' or x'1' or x'4')			
28	Related failure internal SSB log number							
29								
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'5' or x'3')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.10 Byte27 = 8E & Byte34:bit4-7=6 (Controller Chassis Internal Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'6')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37	(This information is not fixed until Controller Chassis reports SSB to HOST.)							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.11 Byte27 = 8E & Byte34:bit4-7 = 8 (Force Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format(x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'8')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcode)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindolc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.12 Byte27 = 8E & Byte34:bit4-7 = 9 (Logical Inconsistency Reset)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29								
2A								
2B								
2C	Old PSW (interrupted address)							
2D								
2E								
2F								
30	Internal SSB log number (reset)							
31								
32	Related reset detailed log number							
33								
34	Package number				Message code (x'9')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT)							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved (x'00')							
6F								
70	DCN (wldcn11)							
73								
74	DCN (wldcn21)							
77								
78	Controller Chassis task head address							
7B								
7C	SLV-DCN (slvdcnr)							
7F								

5.13 Byte27 = 8F (Logical Inconsistency)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09	REQ ID							
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# HIGH (LOGICAL DKC#)							
0F	LDEV# LOW (LDEV# IN LOGICAL DKC)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	Internal SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B								
1C	Reserved							
1D	SCB							
1E	Reserved							
1F								
20								
21								
22								
23								
24								
25								
26								
27	Format (x'8')				Message (x'F')			
28	Module ID							
29	Routine ID							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	Package number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF8E')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Error information							
41								
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Error information							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

5.14 Byte27 = 9F (Trace Data/Log Data)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09	REQ ID							
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# HIGH (LOGICAL DKC#)							
0F	LDEV# LOW (LDEV# IN LOGICAL DKC)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	Internal SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B								
1C	Reserved							
1D	SCB							
1E	Reserved							
1F								
20								
21								
22								
23								
24								
25								
26								
27	Format (x'9')				Message (x'F')			
28	Module ID (*1)							
29	Routine ID (*1)							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	Package number				Message code (x'00')			
35	SSID (low) of self storage system							
36	Symptom code (x'FF9F')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40								
	Error information							
7F								

- *1: In case of Mode 449 ON at the primary storage system of UR, if the secondary storage system is powered off or the path between primary and secondary storage system is blocked, error code x'DB00' might be displayed on primary storage system.

These error codes, which are generated when the communication between primary and secondary storage system fails, doesn't indicate any failures. Please ignore them.

5.15 Byte27 = EC (Factory Mode/Forced Volatile Reboot Mode/Battery/BKMF/PSU/FAN/AC Loss/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	Processor ID							
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# (Logical DKC#)							
12	LDEV# (GDEV#)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20 26	Reserved							
27	Format (x'E')				Message (x'C')			
28	Subcode (See following pages)							
29 31	Hardware information (See following pages)							
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number				Message code (x'0')			
35	SSID of self storage system							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.15.1 Byte27 = EC (Factory Mode/Forced Volatile Reboot Mode/Battery/BKMF/PSU/FAN/ AC Loss/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor)
Byte28-31

(1) Byte28 = 40 or 41 or 42 : Factory mode

	0	1	2	3	4	5	6	7
28	Subcode : x'40' or x'41' or x'42': Factory mode							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(2) Byte28 = 43 : Forced volatile reboot mode warning

	0	1	2	3	4	5	6	7
28	Subcode : x'43': Forced volatile reboot mode warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 45 : Battery warning

	0	1	2	3	4	5	6	7
28	Subcode : x'45': Battery warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKMF location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(4) Byte28 = 46 : BKMF warning

	0	1	2	3	4	5	6	7
28	Subcode : x'46': BKMF warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKMF location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(5) Byte28 = 47 : BKMF FWwarning

	0	1	2	3	4	5	6	7
28	Subcode : x'47': BKMF FWwarning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKMF location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(6) Byte28 = 48 : CPU thermal warning

	0	1	2	3	4	5	6	7
28	Subcode : x'48': CPU thermal warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(7) Byte28 = 49 : Outside air temperature warning

	0	1	2	3	4	5	6	7
28	Subcode : x'49': Outside air temperature warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(8) Byte28 = 50 : Fan warning

	0	1	2	3	4	5	6	7
28	Subcode : x'50': Fan warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	FAN (BKMF) location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(9) Byte28 = 51 : Power supply unit warning

	0	1	2	3	4	5	6	7
28	Subcode : x'51': Power supply unit warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	PSU location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(10) Byte28 = 52 : AC loss warning

	0	1	2	3	4	5	6	7
28	Subcode : x'52': AC loss warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	PSU location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(11) Byte28 = 53 : Panel device warning

	0	1	2	3	4	5	6	7
28	Subcode : x'53': Panel device warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(12) Byte28 = 56 : Environment monitor warning

	0	1	2	3	4	5	6	7
28	Subcode : x'56': Environment monitor warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(13) Byte28 = 59 : Environment monitor FW warning

	0	1	2	3	4	5	6	7
28	Subcode : x'59': Environment monitor FW warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(14) Byte28 = 60 : Outside air temperature alarm

	0	1	2	3	4	5	6	7
28	Subcode : x'60': Outside air temperature alarm							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(15) Byte28 = 61 : Illegal factory mode

	0	1	2	3	4	5	6	7
28	Subcode : x'61': Illegal factory mode							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(16) Byte28 = 62 : Battery life time previous notice

	0	1	2	3	4	5	6	7
28	Subcode : x'62': Battery live time previous notice							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKMF location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(17) Byte28 = 63 : Battery life time notice

	0	1	2	3	4	5	6	7
28	Subcode : x'63': Battery life time notice							
29	MPID (Reported SSB)							
2A	CTL location							
2B	BKMF location							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(18) Byte28 = 65 : Panel Switch warning

	0	1	2	3	4	5	6	7
28	Subcode : x'65': Panel Switch warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							

(19) Byte28 = A0: GUM ready TOV or A1: GUM Heart beat TOV or A2: GUM linkup failurre

	0	1	2	3	4	5	6	7
28	Subcode : x'A0': GUM ready TOV or x'A1': GUM Heart beat TOV or x'A2': GUM linkup failure							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(20) Byte28 = A8 : Internal HIE correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'A8':							
29	HIE# (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(21) Byte28 = A9 : HIE Config RAM failure

	0	1	2	3	4	5	6	7
28	Subcode : x'A9':							
29	HIE# (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(22) Byte28 = AA : X-path correctable

	0	1	2	3	4	5	6	7
28	Subcode : x'AA':							
29	HIE# (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(23) Byte28 = D0 or D1 or D2 or D3 or D4 or D5 or D6 or D7 or D8 or D9 : HSNBX warning

	0	1	2	3	4	5	6	7
28	Subcode: x'D0' : Temperature warning x'D1' : ISW FAN warning x'D2' : ISW PSU warning x'D3' : ISW AC warning x'D4' : ISW firmware warning x'D5' : X-path correctable x'D6' : HSNBX thermal monitor warning x'D7' : HSNBX ID JP setting error warning x'D8' : HSNBX forced volatile/nonvolatile setting report warning x'D9' : PSON/OFF request signal disagreement warning							
29	MPID (Reported SSB)							
2A	CTL location							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.15.2 Byte27 = EC (Factory Mode/Forced Volatile Reboot Mode/Battery/BKMF/PSU/FAN/ AC Loss/CPU Thermal/Outside Air Temperature/GUM/ Board Environment Monitor) Byte40-7F Detail

(1) Byte28 = 40 or 41 or 42 : Factory mode

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	—	—	—	—	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(2) Byte28 = 43 : Forced volatile reboot mode warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	Reserved	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(3) Byte28 = 45 : Battery warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0_ALM

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0 Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0 Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0 Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0 Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(4) Byte28 = 46 : BKMF warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0_ALM

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0 Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0 Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0 Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0 Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(5) Byte28 = 47 : BKMF FWwarning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0_ALM

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0 Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0 Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0 Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0 Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(6) Byte28 = 48 : CPU thermal warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(7) Byte28 = 49 : Outside air temperature warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(8) Byte28 = 50 : Fan warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_FAN_STS1 (not used)							
4F	R_FAN_STS2							
	Abnormal FAN location of own cluster							
	CBL - Bit[7:4] : FAN1 , Bit[3:0] : FAN0							
	CBS - Bit[3] : FAN3, Bit[2] : FAN2, Bit[1] : FAN1, Bit[0] : FAN0							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_FAN_MNG							
51	R_FAN_CTL							
52	R_FAN_PWM0L							
53	R_FAN_PWM0H							
54	R_FAN_PWM1L							
55	R_FAN_PWM1H							
56	R_FAN_TACH0 : FAN0 speed							
57	R_FAN_TACH1 : FAN1 speed							
58	R_FAN_TACH2 : FAN2 speed							
59	R_FAN_TACH3 : FAN3 speed							
5A	R_FAN_TACH4 : FAN4 speed							
5B	R_FAN_TACH5 : FAN5 speed							
5C	R_FAN_TACH6 : FAN6 speed							
5D	R_FAN_TACH7 : FAN7 speed							
5E	R_MATED_FAN_STS1 (not used)							
5F	R_MATED_FAN_STS2 R_FAN_STS2 Abnormal FAN location of another cluster CBL - Bit[7:4] : FAN1 , Bit[3:0] : FAN0 CBS - Bit[3] : FAN3, Bit[2] : FAN2, Bit[1] : FAN1, Bit[0] : FAN0							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(9) Byte28 = 51 : Power supply unit warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PS_STS							
	PSU2_AC_ ALM	PSU1_AC_ ALM	PSU2_ALM	PSU1_ALM	—	—	PSU2_IN	PSU1_IN
4F	R_PS_CTL							
	—	—	—	—	—	—	PSU2_LED	PSU1_LED

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFREQ
51	R_PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	OFFOK			
52	R_PWR_MNG							
53	R_LG_CTL							
54	R_UPDOWN							
55	R_PKSTATUS0							
	OFFOK_ ACK	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
56	R_PKSTATUS1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(10) Byte28 = 52 : AC loss warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PS_STS							
	PSU2_AC_ ALM	PSU1_AC_ ALM	PSU2_ALM	PSU1_ALM	—	—	PSU2_IN	PSU1_IN
4F	R_PS_CTL							
	—	—	—	—	—	—	PSU2_LED	PSU1_LED

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	OFFREQ
51	R_PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	OFFOK			
52	R_PWR_MNG							
53	R_LG_CTL							
54	R_UPDOWN							
55	R_PKSTATUS0							
	OFFOK_ ACK	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
56	R_PKSTATUS1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(11) Byte28 = 53 : Panel device warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PANEL_STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Panel alarm
4F	R_PANELLED							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_PANELLEDPULSE							
51	R_PANELSW							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(12) Byte28 = 56 : Environment monitor warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	Not used							
4F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(13) Byte28 = 59 : Environment monitor FW warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	MN_Micro_Change_CTL_Enable							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Enable
4F	MN_Micro_Change_Mode							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Mode Enable

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	MN_Micro_Change_Reset							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Reset
51	MN_Micro_Block_TRNS_REQ							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	TransferREQ
52	MN_Micro_Block_TRNS_ACK							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	TransferACK
53	MN_Micro_Change_COMP							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Complete
54	MN_Micro_Change_Error							
	Error Information							Error
55	MN_Micro_Block_Length							
56	MN_Micro_Block_TRNS_SEQ_NO.							
	Number of complete block							
57	Not used							
58	BFR[0]							
59	BFR[1]							
5A	BFR[2]							
5B	BFR[3]							
5C	BFR[4]							
5D	BFR[5]							
5E	BFR[6]							
5F	BFR[7]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(14) Byte28 = 60 : Outside air temperature alarm

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_THERMAL_STS							
	—	—	—	—	—	Out THE warn	Out THE alm	MP THE alm
4F	R_MATED_THERMAL_STS							
	—	—	—	—	—	—	—	M_MP THE alm

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_THERMAL							
	Outside air temperature (hex, degree)							
51	R_LMTERMAL							
52	R_RXTHERMAL							
53	R_THERMAL1L							
54	R_THERMAL1H							
55	R_PMIC_THERMAL0							
56	R_PMIC_THERMAL1							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(15) Byte28 = 61 : Illegal factory mode

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(16) Byte28 = 62 : Battery life time previous notice

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0_ALM

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0 Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0 Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0 Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0 Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(17) Byte28 = 63 : Battery life time notice

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_BAT_STS1							
	Reserved	BKMF3_IN	Reserved	BKMF2_IN	Reserved	BKMF1_IN	Reserved	BKMF0_IN
4F	R_BAT_STS2							
	Reserved	Reserved	Reserved	Reserved	BKMF3_ALM	BKMF2_ALM	BKMF1_ALM	BKMF0_ALM

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_BAT_CAPACITY							
	Total battery capacity of own cluster [%]							
51	R_BAT_PACK_STS							
	BKMF3 Option battery IN	BKMF2 Option battery IN	BKMF1 Option battery IN	BKMF0 Option battery IN	BKMF3 Basic battery IN	BKMF2 Basic battery IN	BKMF1 Basic battery IN	BKMF0 Basic battery IN
52	R_BAT_EVENT							
	Not used	Not used	Not used	Not used	BKMF3_ Event	BKMF2_ Event	BKMF1_ Event	BKMF0_ Event
53	R_BAT_CTL2							
54	R_BAT_CAPACITY0							
	Select	BKMF0 Basic battery capacity [%]						
55	R_BAT_CAPACITY1							
	Select	BKMF0 Option battery capacity [%]						
56	R_BAT_CAPACITY2							
	Select	BKMF1 Basic battery capacity [%]						
57	R_BAT_CAPACITY3							
	Select	BKMF1 Option battery capacity [%]						
58	R_BAT_CAPACITY4							
	Select	BKMF2 Basic battery capacity [%]						
59	R_BAT_CAPACITY5							
	Select	BKMF2 Option battery capacity [%]						
5A	R_BAT_CAPACITY6							
	Select	BKMF3 Basic battery capacity [%]						
5B	R_BAT_CAPACITY7							
	Select	BKMF3 Option battery capacity [%]						
5C	R_BAT_LED0							
	BKMF1_LED				BKMF0_LED			
5D	R_BAT_LED1							
	BKMF3_LED				BKMF2_LED			
5E	R_BAT_WARN_PART1							
	BKMF1_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF0_ALM_INFO 1h : BKMF Error 2h : Basic battery error			
5F	R_BAT_WARN_PART2							
	BKMF3_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error				BKMF2_ALM_INFO 1h : BKMF Error 2h : Basic battery error 3h : Option battery error			

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(18) Byte28 = 65 : Panel Switch warning

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_PANEL_STS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Panel alarm
4F	R_PANELLED							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	R_PANELLEDPULSE							
51	R_PANELSW							
52	CUDG_ERRCODE0							
53	CUDG_ERRCODE1							
54	MATED_CUDG_ERRCODE0							
55	MATED_CUDG_ERRCODE1							
56	PANELLED							
	Reserved	Reserved	PowerLED (LG)	PowerLED (BS)	Reserved	Reserved	Reserved	Reserved
57	PANELSW							
58	OFFREQ_SET							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	PSOFFREQ
59	I_STATE1							
	Emergency	MATED CHK1	PSOFFREQA SET	LANRSTREQ IN	Reserved	Reserved	Reserved	Reserved
5A	MATED_I_STATE1							
5B	PSOFFOK0							
	Reserved	Reserved	Reserved	Reserved	Admit Setting PSOFF			
5C	PKINFO2							
	CTLModel				Reserved	CTLLocation		
5D	CTLSTS1							
	Reserved	Reserved	Reserved	MicroReset SW	Reserved	Reserved	LAN Reset SW	Latch LAN Reset SW
5E	MATED_CTLSTS1							
5F	R_PWR_MNG							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	R_EFW_ERRLOG[0]							
61	R_EFW_ERRLOG[1]							
62	R_EFW_ERRLOG[2]							
63	R_EFW_ERRLOG[3]							
64	R_EFW_ERRLOG[4]							
65	R_EFW_ERRLOG[5]							
66	R_EFW_ERRLOG[6]							
67	R_EFW_ERRLOG[7]							
68	R_EFW_ERRLOG[8]							
69	R_EFW_ERRLOG[9]							
6A	R_EFW_ERRLOG[10]							
6B	R_EFW_ERRLOG[11]							
6C	R_EFW_ERRLOG[12]							
6D	R_EFW_ERRLOG[13]							
6E	R_EFW_ERRLOG[14]							
6F	R_EFW_ERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	R_EFW_ERRLOG[16]							
71	R_EFW_ERRLOG[17]							
72	R_EFW_ERRLOG[18]							
73	R_EFW_ERRLOG[19]							
74	R_EFW_ERRLOG[20]							
75	R_EFW_ERRLOG[21]							
76	R_EFW_ERRLOG[22]							
77	R_EFW_ERRLOG[23]							
78	R_EFW_ERRLOG[24]							
79	R_EFW_ERRLOG[25]							
7A	R_EFW_ERRLOG[26]							
7B	R_EFW_ERRLOG[27]							
7C	R_EFW_ERRLOG[28]							
7D	R_EFW_ERRLOG[29]							
7E	R_EFW_ERRLOG[30]							
7F	R_EFW_ERRLOG[31]							

(19) Byte28 = A0: GUM ready TOV or A1: GUM Heart beat TOV or A2: GUM linkup failure

	0	1	2	3	4	5	6	7
40	R_PKINFO2							
41	R_ENVMIC_STS							
42	R_ENVMIC_CTL							
43	R_MIC_REV							
44	Environment monitor revision							
45								
46								
47	R_MIC_FLASHPLANE							
	Active firmware plane							
48	R_CURRENT_ACTMODE							
49	R_NEXT_ACTMODE							
4A	R_CURRENT_AUTOPWRONMODE							
4B	R_CURRENT_SINGLEMODE							
4C	R_CURRENT_DIAGMODE							
4D	R_CURRENT_DCMODE							
4E	R_MODESET_ERR							
	Reserved	Reserved	Reserved	Reserved	Auto PON Error	Single mode Error	Next actmode Error	Actmode Error
4F	R_DCMODESET_ERR							
	DCMODE_DEV_Error				Reserved	Reserved	Reserved	DCMODE Error

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	PLDERR_0							
	Reserved	Reserved	Reserved	Reserved	Error	Error	Error	Error
51	PLDERR_1							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Error
52	GCTL_PCI_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
53	GCTL_XR_ERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Error	Error	Error
54	GCTL_RAM_ERRSTS							
	Error	Error	Error	Error	Error	Error	Error	Error
55	FBRG_IF_ERRSTS							
	Reserved	Reserved	Reserved	Error	Error	Error	Error	Error
56	GCTL_RAM_ECC_ERRSTS							
	Reserved	Reserved	Reserved	Error	Reserved	Reserved	Reserved	Error
57	Not used							
58	JP_STS							
	VOJP	OFFON	Factory	BFM_CS	CEMODE	CEDT2	CEMODE	CEDT0
59	PKSTATUS0							
	Reserved	Reserved	Reserved	Reserved	CEMODE	CEDT2	CEMODE	CEDT0
5A	UPDOWN							
5B	MODE_SET							
5C	Not used							
5D	Not used							
5E	Not used							
5F	FPGAREV							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	BMC_CTL							
	Reset	Reset mask from Another	Not used	Not used	Not used	Not used	Not used	Not used
61	BMC_CTL2							
	Reset another	Not used	Not used	Not used	Not used	Not used	Not used	Not used
62	BMC_STS							
	Reset from another	Not used	Status1	Status0	WOLREQ	Not used	Status3	VOJP
63	CTLSTS_MASK							
	Not used	Not used	Not used	Reset mask	Not used	Not used	Not used	LAN reset mask
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	h2bq1							
69	h2bq2							
6A	b2hq1							
6B	b2hq2							
6C	bmc2host_q_status							
6D	host2bmc_q_status							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Number of failures (continuous)							
71	Number of failures (within 24h)							
72	BootErrCode_L							
73	BootErrCode_H							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	PCH_PP6_LSTS							
	Reserved	Reserved	Linkup	Reserved	Reserved	Reserved	Reserved	Reserved
79	PCH_PP6_LSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
7A	Shared memory status_L							
7B	Shared memory status_H							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(20) Byte28 = A8

	0	1	2	3	4	5	6	7
40	HIE warning per logical block							
41								
42								
43								
44	SWIF detail							
45								
46								
47								
48	SWIF destination invalid							
49								
4A								
4B								
4C	BRAM detail							
4D	SMDMA detail							
4E	XDMA0 detail							
4F	XDMA1 detail							
50	QDMA detail							
51								
52	LRP detail							
53	(Reserved)							
54	MFDMA-WRXFR							
55								
56								
57								
58	MFDMA-RDXFR							
59								
5A								
5B								
5C	MFDMA-RDXFR							
5D								
5E								
5F								
60	MFDMA-RPR							
61								
62	PARB							
63								
64								
65	PARB							
66								
67								
68	PARB(X0)							
69								
6A								
6B	PARB(X1)							
6C								
6D								
6E	PARB(P)							
6F								
70								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
71	PARB							
72								
73								
74	PARB							
75								
76								
77								
78	PARB							
79								
7A								
7B								
7C	PARB							
7D								
7E								
7F								

(21) Byte28 = A9

	0	1	2	3	4	5	6	7
40	HIE warning detail							
41								
42								
43								
44	SEM correct information							
45								
46								
47								
48	SEM BUSY Monitor							
49								
4A								
4B								
4C	SEM Status							
4D								
4E								
4F								
50	SEM Monitor information							
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	SEM Monitor information							
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

(22) Byte28 = AA

	0	1	2	3	4	5	6	7
40	HIE warning per logical unit							
41								
42								
43								
44	LRP FW warning							
45								
46								
47								
48	PARB							
49								
4A								
4B								
4C	PARB							
4D								
4E								
4F								
50	PARB							
51								
52								
53								
54	PARB							
55								
56								
57								
58	PARB							
59								
5A								
5B								
5C	PARB							
5D								
5E								
5F								
60	Not used							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Not used							
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

(23) Byte28 = D0

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PANEL_STS							
59								
5A								
5B								
5C	ISW_FAN_STS							
5D								
5E								
5F								
60	ISW_THERMAL_STS							
61								
62								
63								
64	ISW_PMICON_LOG0							
65								
66								
67								
68	ISW_FAN_SPEED							
69								
6A								
6B								
6C	ISW_PS0_LOG0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG0							
71								
72								
73								
74	ISW_ENV_LOG0							
75								
76								
77								
78	ISW_ENV_LOG1							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

(24) Byte28 = D1

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PANEL_STS							
59								
5A								
5B								
5C	ISW_FAN_STS							
5D								
5E								
5F								
60	ISW_THERMAL_STS							
61								
62								
63								
64	ISW_PMICON_LOG0							
65								
66								
67								
68	ISW_FAN_SPEED							
69								
6A								
6B								
6C	ISW_PS0_LOG0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG0							
71								
72								
73								
74	ISW_ENV_LOG0							
75								
76								
77								
78	ISW_ENV_LOG1							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

(25) Byte28 = D2

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_PS0_LOG0							
51								
52								
53								
54	ISW_PS0_LOG1							
55								
56								
57								
58	ISW_PS0_LOG2							
59								
5A								
5B								
5C	ISW_PS0_LOG3							
5D								
5E								
5F								
60	ISW_PS0_LOG4							
61								
62								
63								
64	ISW_PS0_LOG5							
65								
66								
67								
68	ISW_PS1_LOG0							
69								
6A								
6B								
6C	ISW_PS1_LOG1							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG2							
71								
72								
73								
74	ISW_PS1_LOG3							
75								
76								
77								
78	ISW_PS1_LOG4							
79								
7A								
7B								
7C	ISW_PS1_LOG5							
7D								
7E								
7F								

(26) Byte28 = D3

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_PS0_LOG0							
51								
52								
53								
54	ISW_PS0_LOG1							
55								
56								
57								
58	ISW_PS0_LOG2							
59								
5A								
5B								
5C	ISW_PS0_LOG3							
5D								
5E								
5F								
60	ISW_PS0_LOG4							
61								
62								
63								
64	ISW_PS0_LOG5							
65								
66								
67								
68	ISW_PS1_LOG0							
69								
6A								
6B								
6C	ISW_PS1_LOG1							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG2							
71								
72								
73								
74	ISW_PS1_LOG3							
75								
76								
77								
78	ISW_PS1_LOG4							
79								
7A								
7B								
7C	ISW_PS1_LOG5							
7D								
7E								
7F								

(27) Byte28 = D4

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PORT_ST_LOG0							
59								
5A								
5B								
5C	ISW_PORT_ST_LOG1							
5D								
5E								
5F								
60	ISW_PORT_ST_LOG2							
61								
62								
63								
64	ISW_PORT_ST_LOG3							
65								
66								
67								
68	ISW_FW_ERR0							
69								
6A								
6B								
6C	ISW_FW_ERR1							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_FW_ERR2							
71								
72								
73								
74	ISW_FW_ERR3							
75								
76								
77								
78	ISW_FW_WARN0							
79								
7A								
7B								
7C	ISW_FW_WARN1							
7D								
7E								
7F								

(28) Byte28 = D5

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS1							
49								
4A	ISW_FW_STS2							
4B								
4C	ISW_FW_STS3							
4D								
4E	Counter type							
4F								
50	ISW_PORT0_BADTLP							
51	ISW_PORT0_BADDLLP							
52	ISW_PORT0_RECEIVER_ERR							
53								
54	ISW_PORT1_BADTLP							
55	ISW_PORT1_BADDLLP							
56	ISW_PORT1_RECEIVER_ERR							
57								
58	ISW_PORT2_BADTLP							
59	ISW_PORT2_BADDLLP							
5A	ISW_PORT2_RECEIVER_ERR							
5B								
5C	ISW_PORT3_BADTLP							
5D	ISW_PORT3_BADDLLP							
5E	ISW_PORT3_RECEIVER_ERR							
5F								
60	ISW_PORT4_BADTLP							
61	ISW_PORT4_BADDLLP							
62	ISW_PORT4_RECEIVER_ERR							
63								
64	ISW_PORT5_BADTLP							
65	ISW_PORT5_BADDLLP							
66	ISW_PORT5_RECEIVER_ERR							
67								
68	ISW_PORT6_BADTLP							
69	ISW_PORT6_BADDLLP							
6A	ISW_PORT6_RECEIVER_ERR							
6B								
6C	ISW_PORT7_BADTLP							
6D	ISW_PORT7_BADDLLP							
6E	ISW_PORT7_RECEIVER_ERR							
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PORT8_BADTLP							
71	ISW_PORT8_BADDLLP							
72	ISW_PORT8_RECEIVER_ERR							
73								
74	ISW_PORT9_BADTLP							
75	ISW_PORT9_BADDLLP							
76	ISW_PORT9_RECEIVER_ERR							
77								
78	ISW_PORT10_BADTLP							
79	ISW_PORT10_BADDLLP							
7A	ISW_PORT10_RECEIVER_ERR							
7B								
7C	ISW_PORT11_BADTLP							
7D	ISW_PORT11_BADDLLP							
7E	ISW_PORT11_RECEIVER_ERR							
7F								

(29) Byte28 = D6

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PANEL_STS							
59								
5A								
5B								
5C	ISW_FAN_STS							
5D								
5E								
5F								
60	ISW_THERMAL_STS							
61								
62								
63								
64	ISW_PMICON_LOG0							
65								
66								
67								
68	ISW_FAN_SPEED							
69								
6A								
6B								
6C	ISW_PS0_LOG0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG0							
71								
72								
73								
74	ISW_ENV_LOG0							
75								
76								
77								
78	ISW_ENV_LOG1							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

(30) Byte28 = D7

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PANEL_STS							
59								
5A								
5B								
5C	ISW_FAN_STS							
5D								
5E								
5F								
60	ISW_THERMAL_STS							
61								
62								
63								
64	ISW_PMICON_LOG0							
65								
66								
67								
68	ISW_FAN_SPEED							
69								
6A								
6B								
6C	ISW_PS0_LOG0							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PS1_LOG0							
71								
72								
73								
74	ISW_ENV_LOG0							
75								
76								
77								
78	ISW_ENV_LOG1							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

(31) Byte28 = D8

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PSON							
59								
5A								
5B								
5C	ISW_PSOFFREQ							
5D								
5E								
5F								
60	ISW_PSOFF_MASK							
61								
62								
63								
64	ISW_NV_SHUTDOWN_REQ							
65								
66								
67								
68	ISW_PSOFFOK							
69								
6A								
6B								
6C	ISW_PSOFF_FLG_CLR							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PSOFF_FLG							
71								
72								
73								
74	ISW_FRC_VOLATILE							
75								
76								
77								
78	ISW_AUTO_PSON							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

(32) Byte28 = D9

	0	1	2	3	4	5	6	7
40	ISW_PK_LOC							
41								
42								
43								
44	ISW_FW_VERSION							
45								
46								
47								
48	ISW_FW_STS0							
49								
4A								
4B								
4C	ISW_FW_STS1							
4D								
4E								
4F								
50	ISW_FW_STS2							
51								
52								
53								
54	ISW_FW_STS5							
55								
56								
57								
58	ISW_PSON							
59								
5A								
5B								
5C	ISW_PSOFFREQ							
5D								
5E								
5F								
60	ISW_PSOFF_MASK							
61								
62								
63								
64	ISW_NV_SHUTDOWN_REQ							
65								
66								
67								
68	ISW_PSOFFOK							
69								
6A								
6B								
6C	ISW_PSOFF_FLG_CLR							
6D								
6E								
6F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	ISW_PSOFF_FLG							
71								
72								
73								
74	ISW_FRC_VOLATILE							
75								
76								
77								
78	ISW_AUTO_PSON							
79								
7A								
7B								
7C	ISW_ENV_LOG2							
7D								
7E								
7F								

5.16 Byte27 = EE (Environment Monitor Detected Drive Box Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# HIGH (LOGICAL DKC#)							
12	LDEV# LOW (LDEV# IN LOGICAL DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'E')				Message (x'E')			
28	Error Detection code (*1)							
29	Error Component code (*2)							
2A	Error Reason code (*3)							
2B								
2C	Reserved area							
2D								
2E								
2F								
30								
31								
32								
33								
34	Package number (invalid)				Message code (invalid)			
35	SSID (low) of self storage system (invalid)							
36	Symptom code (x'FFEE')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (invalid)							
3B								
3C	Reserved							
3F								
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason code (*3)							
43								
7F	Not used							

*1: Error Detection Code

No	Code	Detection	Remarks
1	0x20	ENCxx-1	CTL1
2	0x21	ENCxx-2	CTL2

*2: Error Component Code

No	Code	Detection	No	Code	Detection
1	0x00	DB-000	25	0x18	DB-024
2	0x01	DB-002	26	0x19	DB-026
3	0x02	DB-004	27	0x1A	DB-028
4	0x03	DB-006	28	0x1B	DB-030
5	0x04	DB-001	29	0x1C	DB-025
6	0x05	DB-003	30	0x1D	DB-027
7	0x06	DB-005	31	0x1E	DB-029
8	0x07	DB-007	32	0x1F	DB-031
9	0x08	DB-008	33	0x20	DB-032
10	0x09	DB-010	34	0x21	DB-034
11	0x0A	DB-012	35	0x22	DB-036
12	0x0B	DB-014	36	0x23	DB-038
13	0x0C	DB-009	37	0x24	DB-033
14	0x0D	DB-011	38	0x25	DB-035
15	0x0E	DB-013	39	0x26	DB-037
16	0x0F	DB-015	40	0x27	DB-039
17	0x10	DB-016	41	0x28	DB-040
18	0x11	DB-018	42	0x29	DB-042
19	0x12	DB-020	43	0x2A	DB-044
20	0x13	DB-022	44	0x2B	DB-046
21	0x14	DB-017	45	0x2C	DB-041
22	0x15	DB-019	46	0x2D	DB-043
23	0x16	DB-021	47	0x2E	DB-045
24	0x17	DB-023	48	0x2F	DB-047

(To be continued)

(Continued from the preceding page)

No	Code	Detection	No	Code	Detection
49	0x30	DB-048	91	0x5A	DB-092
50	0x31	DB-050	92	0x5B	DB-094
51	0x32	DB-052	93	0x5C	DB-089
52	0x33	DB-054	94	0x5D	DB-091
53	0x34	DB-049	95	0x5E	DB-093
54	0x35	DB-051	96	0x5F	DB-095
55	0x36	DB-053	97	0x60	DB-096
56	0x37	DB-055	98	0x61	DB-098
57	0x38	DB-056	99	0x62	DB-100
58	0x39	DB-058	100	0x63	DB-102
59	0x3A	DB-060	101	0x64	DB-097
60	0x3B	DB-062	102	0x65	DB-099
61	0x3C	DB-057	103	0x66	DB-101
62	0x3D	DB-059	104	0x67	DB-103
63	0x3E	DB-061	105	0x68	DB-104
64	0x3F	DB-063	106	0x69	DB-106
65	0x40	DB-064	107	0x6A	DB-108
66	0x41	DB-066	108	0x6B	DB-110
67	0x42	DB-068	109	0x6C	DB-105
68	0x43	DB-070	110	0x6D	DB-107
69	0x44	DB-065	111	0x6E	DB-109
70	0x45	DB-067	112	0x6F	DB-111
71	0x46	DB-069	113	0x70	DB-112
72	0x47	DB-071	114	0x71	DB-114
73	0x48	DB-072	115	0x72	DB-116
74	0x49	DB-074	116	0x73	DB-118
75	0x4A	DB-076	117	0x74	DB-113
76	0x4B	DB-078	118	0x75	DB-115
77	0x4C	DB-073	119	0x76	DB-117
78	0x4D	DB-075	120	0x77	DB-119
79	0x4E	DB-077	121	0x78	DB-120
80	0x4F	DB-079	122	0x79	DB-122
81	0x50	DB-080	123	0x7A	DB-124
82	0x51	DB-082	124	0x7B	DB-126
83	0x52	DB-084	125	0x7C	DB-121
84	0x53	DB-086	126	0x7D	DB-123
85	0x54	DB-081	127	0x7E	DB-125
86	0x55	DB-083	128	0x7F	DB-127
87	0x56	DB-085	129	0x80	DB-128
88	0x57	DB-087	130	0x81	DB-130
89	0x58	DB-088	131	0x82	DB-132
90	0x59	DB-090	132	0x83	DB-134

(To be continued)

(Continued from the preceding page)

No	Code	Detection	No	Code	Detection
133	0x84	DB-129	163	0xA2	DB-164
134	0x85	DB-131	164	0xA3	DB-166
135	0x86	DB-133	165	0xA4	DB-161
136	0x87	DB-135	166	0xA5	DB-163
137	0x88	DB-136	167	0xA6	DB-165
138	0x89	DB-138	168	0xA7	DB-167
139	0x8A	DB-140	169	0xA8	DB-168
140	0x8B	DB-142	170	0xA9	DB-170
141	0x8C	DB-137	171	0xAA	DB-172
142	0x8D	DB-139	172	0xAB	DB-174
143	0x8E	DB-141	173	0xAC	DB-169
144	0x8F	DB-143	174	0xAD	DB-171
145	0x90	DB-144	175	0xAE	DB-173
146	0x91	DB-146	176	0xAF	DB-175
147	0x92	DB-148	177	0xB0	DB-176
148	0x93	DB-150	178	0xB1	DB-178
149	0x94	DB-145	179	0xB2	DB-180
150	0x95	DB-147	180	0xB3	DB-182
151	0x96	DB-149	181	0xB4	DB-177
152	0x97	DB-151	182	0xB5	DB-179
153	0x98	DB-152	183	0xB6	DB-181
154	0x99	DB-154	184	0xB7	DB-183
155	0x9A	DB-156	185	0xB8	DB-184
156	0x9B	DB-158	186	0xB9	DB-186
157	0x9C	DB-153	187	0xBA	DB-188
158	0x9D	DB-155	188	0xBB	DB-190
159	0x9E	DB-157	189	0xBC	DB-185
160	0x9F	DB-159	190	0xBD	DB-187
161	0xA0	DB-160	191	0xBE	DB-189
162	0xA1	DB-162	192	0xBF	DB-191

*3: Error Reason Code

No	Code	Failure Cause	Failure Name	Remarks
1	0x50	DBPS Failure	PSALM0	
2	0x51		PSALM1	
3	0x60	DBPS input Voltage adnormality	PSFLT0	
4	0x61		PSFLT1	
5	0x70	ENC Failure	I2C#0_ACS	
6	0x71		I2C#1_ACS	
7	0x72		I2C#2_ACS	
8	0x73		I2C#3_ACS	
9	0x74		I2C#4_ACS	
10	0x75		I2C#5_ACS	
11	0x76		I2C#6_ACS	
12	0x77		I2C#7_ACS	
13	0x80		Expander adnormality	
14	0xA0		CPLD_ACC_RTY	
15	0xA1		I2C_ACC_RTY	
16	0xB0		SXP adnormality	
17	0xC0		CABLE_FAIL0	
18	0xC1		CABLE_FAIL1	
19	0xE0		I2C#0_HIT	
20	0xE1		I2C#1_HIT	
21	0xE2		I2C#2_HIT	
22	0xE3		I2C#3_HIT	
23	0xE4		I2C#4_HIT	
24	0xE5		I2C#5_HIT	
25	0xE6		I2C#6_HIT	
26	0xE7		I2C#7_HIT	

5.17 Byte27 = F0 (Operation Terminated)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#							
15	RDEV#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'35')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'0')			
28	Not used							
29	Not used							
2A	Not used							
2B	Hardware level (*1)							
2C								
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (Invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF0')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'1D')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

*1: Hardware level

Bit 0 : Serial channel hardware level	
For bit 0 = 0	For bit 0 = 1
Bit 1 : Not used	Bit 1 : Not used
Bit 2 to 3 : SP number	Bit 2 to 3 : SP number
Bit 4 to 5 : CHLs per Cluster	Bit 4 to 5 : CHLs per Cluster
00 : 4	0000 : Parallel = 4, Serial = 0
01 : 8	0001 : Parallel = 8, Serial = 0
10 : Not used	0010 : Parallel = 4, Serial = 2
11 : Not used	0100 : Parallel = 4, Serial = 4
Bit 6 : NVS	0110 : Parallel = 0, Serial = 2
0 : Without NVS	1000 : Parallel = 0, Serial = 4
1 : With NVS	1010 : Parallel = 0, Serial = 8
Bit 7 : Not used	1100 : Parallel = 0, Serial = 8
Bit 8 to 10 : Cache size	Bit 8 : DUAL FRAME
000 : Non cache	0 : DUAL FRAME
001 : 256MB	1 : MODULAR POWER
010 : 512MB	Bit 9 to 11 : Cache size
011 : 768MB	000 : Non cache
100 : 1024MB	001 : 256MB
101 : 1280MB	010 : 512MB
110 : 1536MB	011 : 768MB
111 : Over 1537MB	100 : 1024MB
Bit 11 to 13 : Cluster hardware level	101 : 1280MB
Bit 14 to 15 : Cache/NVS hardware level	110 : 1536MB
	111 : Over 1536MB
	Bit 12 to 13 : Cluster hardware level
	Bit 14 to 15 : Cache/NVS hardware level

5.18 Byte27 = F1 (Micro-program Detected Cache Failure)

	0	1	2	3	4	5	6	7					
00	Time stamp												
01													
02													
03	Format Message												
04	Processor ID												
05	System error code												
06													
07	JCB#												
08													
09	REQ ID												
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	Separate	Host report	SVP report	Force log					
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved					
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved									
0D	JCB ID												
0E	LDEV# high (Logical DKC#)												
0F	LDEV# low (LDEV# in Logical DKC)												
10													
11	CDEV# / PORT#												
12	RDEV# / PORT#												
13	Internal SSB log number												
14													
15	Related failure log number												
16													
17	Related SIM log number												
18													
19	Related SSB log number												
1A													
1B													
1C	Reserved												
1D	SCB												
1E	Reserved												
1F													
20													
21													
22													
23													
24													
25													
26													
27	Format (x'F')				Message (x'1')								
28	Queue transition in progress (Side A)	Queue transition in progress (Side B)	Free SGCB queue operation in progress	Free SLCB queue operation in progress	Free GRPT queue operation in progress	'000'							
29	Not used												
2A	VDEV number (high)												
2B	Queue type												
2C	VDEV number (low)		Slot number (high)										
2D	Queue number (high)												
2E	Slot number (middle)												
2F	Queue number (low)												
30	Slow number (low)												
31	SSID of mate storage system (x'0000' for EDCC)												
32													
33	Maker code & Manufacture code (x'00')												

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
30	Not used							
31	Module ID							
32	Routine ID							
33	Package number				Error code (invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF1')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Error information							
7F								

*1: When byte 28, bit2-4 = 000, upper value is valid.
When byte 28, bit2-4 ≠ 000, lower value is valid.

5.19 Byte27 = F5 (Remote Copy/Global-active Device Paths Removed/Restored)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0E								
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'F')				Message (x'5')			
28	Reason code (*1)							
29	RCU device address							
2A	RCU Manufacture code / Factory code							
2B								
2C	RCU Sequence No.							
2D								
2E								
2F	MCU Manufacture code / Factory code							
30								
31	MCU Sequence No.							
32								
33								
34	SSID of self storage system							
35								
36	Symptom code (x'38')							
37	Symptom code (x'87')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control							
39	Duplex pair failure	Program action code						
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Error information							
7F								

- *1: Reason code
- x'01' : One or more Remote Copy/global-active device paths removed unexpectedly
 - x'02' : One or more Remote Copy/global-active device paths restored
 - x'03' : All Remote Copy/global-active device paths removed unexpectedly

5.20 Byte27 = F6 (CFW Access not Authorized)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05	PCB type		Processor ID					
06	System error code							
07								
08	JCB#							
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0E	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
0F	JCB ID							
10	LDEV# high (Logical DKC#)							
11	LDEV# low (LDEV# in Logical DKC)							
12								
13								
14	CDEV#							
15	RDEV#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'6')			
28	Reason code (x'01': CFW ID unmatched)							
29	Not used							
2A	Not used							
2B	Not used							
2C	Not used							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used							
31	Module ID							
32	Routine ID							
33	PCB number				Error code			
34	SSID of self storage system							
35								
36	Symptom code (x'FFF6')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Not used Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.21 Byte27 = FA (NVS Terminated)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C								
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26								
27	Format (x'F')				Message (x'A')			
28	Reason code (x'02': NVS failure)							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	SSID of mate storage system (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	Packeage number				Error code (invalid)			
34	SSID of self storage system							
35								
36	Symptom code (x'FFFA')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Key code (for reconnection)							
41	Format/Message (for reconnection)							
42	Module ID (for reconnection)							
43	Routine ID (for reconnection)							
44	Not used							
7F								

5.22 Byte27 = FB (TrueCopy for Pair Suspend)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	Processor ID							
08 09	System error code							
0A 0B	JCB#							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Intetnal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20 26	Reserved							
27	Format (x'F')				Message (x'B')			
28	R-VOL Suspend	R-VOL failure	Reason code (*1)					
29	RCU device address							
2A 2B	RCU Manufacture code/Factory code							
2C								
2D	RCU Sequence No.							
2E								
2F	MCU Manufacture code/Factory code							
30 31								
32	MCU Sequence No.							
33								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
34	SSID of self storage system							
35								
36	Symptom code (x'FE')							
37	Symptom code (same byte #28)							
38	Log and message control							
39	Duplex pair failure	Program action code						
3A	Configuration information							
3B								
3C	Reserved							
3F								
40	Error information							
7F								

*1: Reason code

x'04'-x'2F' : Reserved

x'30' : Pair suspended. MCU device write error.

x'31' : Pair suspended. RCU storage systemerror/or MCU storage system error.

x'32' : Pair suspended. RCU device communication error.

x'33' : Pair suspended (Critical device status). All write command is rejected until the pair is re-established.

x'34' : Pair suspended. RCU device is not ready (intervention required).

5.23 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support)

	0	1	2	3	4	5	6	7					
00	Time stamp												
01													
02													
03	Format Message												
04	Processor ID												
05	System error code												
06													
07	JCB#												
08													
09	REQ ID												
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPAPATE	Host report	SVP report	Force log					
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved					
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved									
0D	JCB ID												
0E	LDEV# (Logical DKC#)												
0F	LDEV# (GDEV#)												
10													
11	CDEV# / PORT#												
12	RDEV# / PORT#												
13	SSB log number												
14													
15	Related failure log number												
16													
17	Related SIM log number												
18													
19	Related SSB log number												
1A													
1B	Reserved												
1C	SCB												
1D	Reserved												
1E													
1F													
20													
21													
22													
23													
24													
25													
26													
27	Format (x'F')				Message (x'F')								
28	Subcode (See following pages)												
29	Hardware information (See following pages)												
30													
31													
32	Not used (x'00')												
33	Not used (x'00')												
34	PCB number				Message code (x'0')								
35	SSID of self storage system												
36	Symptom code (x'FFFF')												
37													
38	Log and message control (x'00')												
39	Program action code (x'00')												
3A	Configuration information												
3B													
3C	Not used (x'00')												

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3D	Reserved							
3E								
3F								
40	Hard information (See following pages)							
7F								

5.23.1 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support) Byte28-31
Detail

(1) Byte28 = 70 : Boot flash memory error

	0	1	2	3	4	5	6	7
28	Subcode : x'70' : Boot flash memory error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(2) Byte28 = 71 : Cache flash memory error

	0	1	2	3	4	5	6	7
28	Subcode : x'71' : Cache flash memory error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(3) Byte28 = 72 : Cache flash memory type error

	0	1	2	3	4	5	6	7
28	Subcode : x'72' : Cache flash memory type error							
29	MPID (Reported SSB)							
2A	Not used							
2B	Not used							
2C	Not used							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(4) Byte28 = C0 : SFP_TxFault (FC)

	0	1	2	3	4	5	6	7
28	Subcode : x'C0' : SFP_TxFault (FC)							
29	MPID (Reported SSB)							
2A	Clustor number for TxFault							
2B	Slot number for TxFault							
2C	Port number for TxFault							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

(5) Byte28 = C1 : SFP_TxFault (iSCSI)

	0	1	2	3	4	5	6	7
28	Subcode : x'C1' : SFP_TxFault (iSCSI)							
29	MPID (Reported SSB)							
2A	Clustor number for TxFault							
2B	Slot number for TxFault							
2C	Port number for TxFault							
2D	Not used							
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

5.23.2 Byte27 = FF (Boot Flash Memory/CFM/SFP : Error Handling Support) Byte40-7F Detail

(1) Byte28 = 70 : Boot flash memory error

	0	1	2	3	4	5	6	7
40	CPU0_GBL_GNERRST[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
41	CPU0_GBL_GNERRST[23:16]							
	Reserved	Reserved	Reserved	PA0_ERR	Reserved	Reserved	Reserved	Reserved
42	CPU0_GBL_GNERRST[15:8]							
	PAA_ERR	PA5_ERR	PA9_ERR	PA4_ERR	PA8_ERR	PA3_ERR	PA7_ERR	PA2_ERR
43	CPU0_GBL_GNERRST[7:0]							
	PA6_ERR	PA1_ERR	Reserved	Reserved	Reserved	Reserved	IRP1_ERR	IRP0_ERR
44	CPU0_PA0_PCISTS[15:8]							
45	CPU0_PA0_PCISTS[7:0]							
46	CPU0_PA0_SECSTS[15:8]							
47	CPU0_PA0_SECSTS[7:0]							
48	CPU0_PA0_XPGLBERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable	NonFatal	Fatal
49	CPU0_PA0_RPERRSTS							
	Reserved	Fatal	NonFatal	FirstFatal	MultiNonFatal	Fatal/NonFatal	MultiCOR	Correctable
4A	CPU0_PA0_XPUNCERRPTR[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Poisoned	INT error
4B	CPU0_PA0_XPUNCERRPTR[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
4C	CPU0_PA0_XPUNCERRSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
4D	CPU0_PA0_XPUNCERRSTS[7:0]							
	Reserved	Received UR	Received CA	Sent UR	Sent CA	Reserved	Internal error	Reserved
4E	CPU0_PA0_XPCORERRSTS							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Link change
4F	CPU0_PA0_DEVSTS							
	Reserved	Reserved	Reserved	Reserved	UR	Fatal	NonFatal	Correctable

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CPU0_PA0_UNCERRSTS[31:24]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
51	CPU0_PA0_UNCERRSTS[23:16]							
	Reserved	Reserved	Error	UR	Reserved	Malformed	Buffer overflow	UC
52	CPU0_PA0_UNCERRSTS[15:8]							
	CA	CT	Internal error	Poisoned	Reserved	Reserved	Reserved	Reserved
53	CPU0_PA0_UNCERRSTS[7:0]							
	Reserved	Reserved	Linkdown	Internal error	Reserved	Reserved	Reserved	Reserved
54	CPU0_PA0_CORERRSTS[15:8]							
	Reserved	Reserved	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved
55	CPU0_PA0_CORERRSTS[7:0]							
	Correctable	Correctable	Reserved	Reserved	Reserved	Reserved	Reserved	Correctable
56	Not used							
57	Not used							
58	CPU0_PA0_LNKSTS[15:8]							
	Reserved	Reserved	Link active	Reserved	Link training	Reserved	Link width[9:8]	B0
59	CPU0_PA0_LNKSTS[7:0]							
	Link width[7:0]				Current link speed			
5A	CPU0_PA0_LNKSTS2[15:8]							
5B	CPU0_PA0_LNKSTS2[7:0]							
5C	CPU0_PA0_ERRSID[31:24]							
5D	CPU0_PA0_ERRSID[23:16]							
5E	CPU0_PA0_ERRSID[15:8]							
5F	CPU0_PA0_ERRSID[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	CPU0_PA0_HDRLOG0[31:24]							
61	CPU0_PA0_HDRLOG0[23:16]							
62	CPU0_PA0_HDRLOG0[15:8]							
63	CPU0_PA0_HDRLOG0[7:0]							
64	CPU0_PA0_HDRLOG1[31:24]							
65	CPU0_PA0_HDRLOG1[23:16]							
66	CPU0_PA0_HDRLOG1[15:8]							
67	CPU0_PA0_HDRLOG1[7:0]							
68	CPU0_PA0_HDRLOG2[31:24]							
69	CPU0_PA0_HDRLOG2[23:16]							
6A	CPU0_PA0_HDRLOG2[15:8]							
6B	CPU0_PA0_HDRLOG2[7:0]							
6C	CPU0_PA0_HDRLOG3[31:24]							
6D	CPU0_PA0_HDRLOG3[23:16]							
6E	CPU0_PA0_HDRLOG3[15:8]							
6F	CPU0_PA0_HDRLOG3[7:0]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	PCH_SPI_HSFSTS[15:8]							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
71	PCH_SPI_HSFSTS[7:0]							
	Reserved	Reserved	In progress	Reserved	Reserved	Reserved	Error	Done
72	PCH_SPI_HSFC[15:8]							
	Reserved	Reserved	Byte count					
73	PCH_SPI_HSFC[7:0]							
	Reserved	Reserved	Reserved	Reserved	Reserved	State		Go
74	Error address of Boot flash memory address [31:24]							
75	Error address of Boot flash memory address [23:16]							
76	Error address of Boot flash memory address [15:8]							
77	Error address of Boot flash memory address [7:0]							
78	PCH_SPI_SSFS[31:24]							
79	PCH_SPI_SSFS[23:16]							
7A	PCH_SPI_SSFS[15:8]							
7B	PCH_SPI_SSFS[7:0]							
	Reserved	Reserved	Reserved	Reserved	Error	Done	Reserved	In Progress
7C	PCH_LPC_BIOS_CNTL							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Write enable
7D	Not used							
7E	Not used							
7F	Not used							

(2) Byte28 = 71 : Cache flash memory error

	0	1	2	3	4	5	6	7
40	SBDERRLOG[0]							
41	SBDERRLOG[1]							
42	SBDERRLOG[2]							
43	SBDERRLOG[3]							
44	SBDERRLOG[4]							
45	SBDERRLOG[5]							
46	SBDERRLOG[6]							
47	SBDERRLOG[7]							
48	SBDERRLOG[8]							
49	SBDERRLOG[9]							
4A	SBDERRLOG[10]							
4B	SBDERRLOG[11]							
4C	SBDERRLOG[12]							
4D	SBDERRLOG[13]							
4E	SBDERRLOG[14]							
4F	SBDERRLOG[15]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	SBDERRLOG[16]							
51	SBDERRLOG[17]							
52	SBDERRLOG[18]							
53	SBDERRLOG[19]							
54	SBDERRLOG[20]							
55	SBDERRLOG[21]							
56	SBDERRLOG[22]							
57	SBDERRLOG[23]							
58	SBDERRLOG[24]							
59	SBDERRLOG[25]							
5A	SBDERRLOG[26]							
5B	SBDERRLOG[27]							
5C	SBDERRLOG[28]							
5D	SBDERRLOG[29]							
5E	SBDERRLOG[30]							
5F	SBDERRLOG[31]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	SBDERRLOG[32]							
61	SBDERRLOG[33]							
62	SBDERRLOG[34]							
63	SBDERRLOG[35]							
64	SBDERRLOG[36]							
65	SBDERRLOG[37]							
66	SBDERRLOG[38]							
67	SBDERRLOG[39]							
68	SBDERRLOG[40]							
69	SBDERRLOG[41]							
6A	SBDERRLOG[42]							
6B	SBDERRLOG[43]							
6C	SBDERRLOG[44]							
6D	SBDERRLOG[45]							
6E	SBDERRLOG[46]							
6F	SBDERRLOG[47]							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	SBDERRLOG[48]							
71	SBDERRLOG[49]							
72	SBDERRLOG[50]							
73	SBDERRLOG[51]							
74	SBDERRLOG[52]							
75	SBDERRLOG[53]							
76	SBDERRLOG[54]							
77	SBDERRLOG[55]							
78	SBDERRLOG[56]							
79	SBDERRLOG[57]							
7A	SBDERRLOG[58]							
7B	SBDERRLOG[59]							
7C	SBDERRLOG[60]							
7D	SBDERRLOG[61]							
7E	SBDERRLOG[62]							
7F	SBDERRLOG[63]							

(3) Byte28 = 72 : Cache flash memory type error

	0	1	2	3	4	5	6	7
40	CFM0 Capacity							
41	CFM1 Capacity							
42	Not used							
43	Not used							
44	Not used							
45	Not used							
46	Not used							
47	Not used							
48	CFM_Type							
49								
4A								
4B								
4C	CFM_SPEC_CODE1							
4D								
4E								
4F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	CFM_SPEC2							
51								
52								
53								
54	CFM_vender_device_type							
55								
56								
57								
58	CFM_SPEC_CODE							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(4) Byte28 = C0 : SFP_TxFault (FC)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Gigabit Ether Spec.							
45	Fibre Channel Speed							
46	Parts Name 1							
47	Parts Name 2							
48	Parts Name 3							
49	Parts Name 4							
4A	Parts Name 5							
4B	Parts Name 6							
4C	Parts Name 7							
4D	Parts Name 8							
4E	Parts Name 9							
4F	Parts Name 10							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Parts Name 11							
51	Parts Name 12							
52	Parts Name 13							
53	Parts Name 14							
54	Parts Name 15							
55	Parts Name 16							
56	Wave Length 1							
57	Wave Length 2							
58	Serial Number 1							
59	Serial Number 2							
5A	Serial Number 3							
5B	Serial Number 4							
5C	Serial Number 5							
5D	Serial Number 6							
5E	Serial Number 7							
5F	Serial Number 8							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Serial Number 9							
61	Serial Number 10							
62	Serial Number 11							
63	Serial Number 12							
64	Serial Number 13							
65	Serial Number 14							
66	Serial Number 15							
67	Serial Number 16							
68	Custom Code 1							
69	Custom Code 2							
6A	Alarm/Warning 1							
6B	Alarm/Warning 2							
6C	Alarm/Warning 3							
6D	Alarm/Warning 4							
6E	Alarm/Warning 5							
6F	Alarm/Warning 6							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Alarm/Warning 7							
71	Alarm/Warning 8							
72	Custom Code 1							
73	Custom Code 2							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

(5) Byte28 = C1 : SFP_TxFault (iSCSI)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Gigabit Ether Spec.							
45	Ether Speed							
46	Parts Name 1							
47	Parts Name 2							
48	Parts Name 3							
49	Parts Name 4							
4A	Parts Name 5							
4B	Parts Name 6							
4C	Parts Name 7							
4D	Parts Name 8							
4E	Parts Name 9							
4F	Parts Name 10							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
50	Parts Name 11							
51	Parts Name 12							
52	Parts Name 13							
53	Parts Name 14							
54	Parts Name 15							
55	Parts Name 16							
56	Wave Length 1							
57	Wave Length 2							
58	Serial Number 1							
59	Serial Number 2							
5A	Serial Number 3							
5B	Serial Number 4							
5C	Serial Number 5							
5D	Serial Number 6							
5E	Serial Number 7							
5F	Serial Number 8							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
60	Serial Number 9							
61	Serial Number 10							
62	Serial Number 11							
63	Serial Number 12							
64	Serial Number 13							
65	Serial Number 14							
66	Serial Number 15							
67	Serial Number 16							
68	Custom Code 1							
69	Custom Code 2							
6A	Alarm/Warning 1							
6B	Alarm/Warning 2							
6C	Alarm/Warning 3							
6D	Alarm/Warning 4							
6E	Alarm/Warning 5							
6F	Alarm/Warning 6							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
70	Alarm/Warning 7							
71	Alarm/Warning 8							
72	Custom Code 1							
73	Custom Code 2							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

5.24 Byte0D: Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05	PCB type		Processor ID					
06	System error code							
07								
08	JCB#							
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0E	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
0F	JCB ID							
10	LDEV# high (Logical DKC#)							
11	LDEV# low (LDEV# in Logical DKC)							
12								
13	CDEV# / PORT#							
14	RDEV# / PORT#							
15	SSB log number							
16								
17	Related failure log number							
18								
19	Related SIM log number							
1A								
1B	Related SSB log number							
1C								
1D	Reserved							
1E	SCB							
1F	Basic SSB							
20								
21	Storage control type							
22	Count							
23	Device address							
24	Device type							
25	Content (x'6')				Format (x'3')			
26	Operation byte of LR/LRE command parameter							
27	Auxiliary byte of LR/LRE command parameter							
28	Byte 2 for LR/LRE command							
29	Reserved							
2A								
2B								
2C	Search parameter of LR command (CCHHR)							
2D	or Record ID of re-execution command (R)							
2E								
2F								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
30	Sector number to re-execute the LR command							
31	TLF for LR/LRE command							
32								
33	Operation byte = 0x3F : LRE External operation byte Others : Reserved							
34	SSID of self storage system							
35								
36	Symptom code (x'08C1')							
37								
38	Log and message control (x'00')							
39	Program action code (x'1D')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.25 Byte0D: Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05	PCB type		Processor ID					
06	System error code							
07								
08	JCB#							
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0E	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
0F	JCB ID							
10	LDEV# high (Logical DKC#)							
11	LDEV# low (LDEV# in Logical DKC)							
12								
13	CDEV# / PORT#							
14	RDEV# / PORT#							
15	SSB log number							
16								
17	Related failure log number							
18								
19	Related SIM log number							
1A								
1B	Related SSB log number							
1C								
1D	Reserved							
1E	SCB							
1F	Basic SSB							
20								
21	Storage control type							
22	Count							
23	Device address							
24	Device type							
25	Content (x'6')				Format (x'4')			
26	Operation byte of LR/LRE command parameter							
27	Extension operation byte of LR/LRE command parameter							
28								
29	Head address of extent (DX command parameter)							
2A								
2B								
2C								
2D								
2E								
2F	Path group ID (byte 1 to 7 of the ID by the last Set Path Group ID command)							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Reference code (x'0212')							
37								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01')							
	Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit)							
	Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit)							
	Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address							
	Bit3=1: Bit 0-3 Cylinder address Bit 8-11.							
	Bit 4-7 Head address							
40								
	Not used							
55								
56	Module ID (detected error)							
57	Routine ID (detected error)							
58								
	Not used							
7F								

5.26 Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): Common information

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05	PCB type		Processor ID					
06	System error code							
07								
08	JCB#							
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0E	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
0F	JCB ID							
10	LDEV# high (Logical DKC#)							
11	LDEV# low (LDEV# in Logical DKC)							
12								
13	CDEV# / PORT#							
14	RDEV# / PORT#							
15	SSB log number							
16								
17	Related failure log number							
18								
19	Related SIM log number							
1A								
1B	Related SSB log number							
1C								
1D	Reserved							
1E	SCB							
1F	Basic SSB							
20								
21	Storage control type							
22	Count							
23	Device address							
24	Device type							
25	Content (x'6')				Format (x'1')			
26	Sector number							
27	Cylinder address							
28								
29	Head address							
2A								
2B	Record number							
2C	Error displacement							
2D								
2E								
2F	Drive serial number (x'0D05' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Reference code (x'4XYY') X: 0: Data check in HA field or R0 field, 1: Data check in C field, 2: Data check in K field, 3: Data check in D field, 5: PA error YY: x'80': Correctable (Recovered), x'C0': Uncorrectable							
37								
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'00') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							

5.26.1 Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception): For Byte36 = 40/41/42/43

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, slot number							
45								
46								
47	Slot status							
48								
49								
4A	Staging bit map/Dirty bit map							
4B								
4C								
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	Head sub block number for HIT/MISS decision							
55	Remain sub block count							
56	Not used (x'00')							
7F								

***1: EXIT FLAG**

x'08':	CHL I/F CHK2 time over	x'40':	'Staging' requested
x'0D':	EOF	x'41':	'Waiting free segment' requested
x'0E':	DSB = 0E requested	x'42':	'Time over of waiting for staging' requested
x'0F':	Logical error	x'43':	'Staging for PA mismatch' requested
x'1E':	Make sense only	x'45':	'Destaging' requested
x'1F':	Logical error (make sense only)	x'46':	'High priority destaging' requested(30 slot)
x'30':	Selective reset requested	x'47':	'Write through process' requested
		x'4B':	'DSB = 4E retry' requested (data transfer)
		x'4E':	'DSB = 4E retry' requested (DSB = 4E retry)
		x'4F':	STG requested (Read or STG in progress)

***2: Slot failure**

x'80':	Sector write error	x'08':	No decision
x'40':	Sector read error		
x'20':	ECC/LRC error		
x'10':	Incomplete data		

***3: Obtained information flag**

bit0-1: Block type	bit2-3: Side	bit4: Segment	bit5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

5.26.2 Byte0D: Bit1 = 1 & Byte36-37 = 4XXX (Data exception):For Byte36 = 45

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, slot number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT FLAG (*1)							
52	Slot failure information (*2)							
53	Obtained information flag (*3)							
54	PACC in CBUF							
55	PAHF in CBUF							
56								
57								
58	Current CC in JCB							
59	Current HH in JCB							
5A								
5B								
5C	Not used (x'00')							
7F								

***1: EXIT FLAG**

x'08':	CHL I/F CHK2 time over	x'40':	'Staging' requested
x'0D':	EOF	x'41':	'Waiting free segment' requested
x'0E':	DSB = 0E requested	x'42':	'Time over of waiting for staging' requested
x'0F':	Logical error	x'43':	'Staging for PA mismatch' requested
x'1E':	Make sense only	x'45':	'Destaging' requested
x'1F':	Logical error (make sense only)	x'46':	'High priority destaging' requested (30 slot)
x'30':	Selective reset requested	x'47':	'Write through process' requested
		x'4B':	'DSB = 4E retry' requested (data transfer)
		x'4E':	'DSB = 4E retry' requested (DSB = 4E retry)
		x'4F':	STG requested (Read or STG in progress)

***2: Slot failure**

x'80':	Sector write error	x'08':	No decision
x'40':	Sector read error		
x'20':	ECC/LRC error		
x'10':	Incomplete data		

***3: Obtained information flag**

bit0-1: Block type	bit2-3: Side	bit4: Segment	bit5-7: Reserved
00: Data block	00: CACHE WR side	0: No segment	
01: HA/RO block	01: NVS WR side	1: Segment exist	
10: Bit map block	10: CACHE RD side		

5.27 Byte36 = 6F (Storage system Information)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04								
05	PCB type		Processor ID					
06								
07	System error code							
08								
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV#/ PORT#							
15	RDEV#/ PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Command overrun threshold exceeded							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'1')			
27	Data overrun threshold exceeded							
28	Seek count							
29								
2A	Drive serial number		(x'0D')					
2B			(x'05')					
2C			(DKU sequence number)					
2D								
2E								
2F	DKC serial number		(x'0D')					
30			(x'05')					
31			(x'00')					
32			(DKC sequence number : high)					
33			(DKC sequence number : low)					
34	SSID of self storage system							
35								
36	Symptom code (x'6FYX' Y: 0: Statistics/RRBL command, 2: Channel data overrun							
37	X: For serial CHL : Port#, For parallel CHL : CHL#)							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C								
3D								
3E	Read/search byte count							
3F								
40								
	Not used							
7F								

5.28 Byte36: bit0-3 = D & Byte26: bit4-7 = 1 & Byte27: bit-2 = 7 (SPC Report Error)

	0	1	2	3	4	5	6	7				
00 05	Time stamp											
06	Format ID											
07	Processor ID											
08 09	System error code (Return Code : Low 2 Byte)											
0A 0B 0C	JCB# REQ ID											
0D	24CMPT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log				
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved				
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved								
10	JCB ID											
11	LDEV# high (Logical DKC#)											
12	LDEV# low (LDEV# in Logical DKC)											
13												
14	CDEV# / PORT#											
15	RDEV# / PORT#											
16	Internal SSB log number											
17												
18	Related failure log number											
19												
1A	Related SIM log number											
1B												
1C	Related SSB log number											
1D												
1E	Reserved											
1F	SCB											
20	Reserved											
 26									Format (x'1')			
27	Type Code('111': Drive Report Error)			0					0	0	0	0
28	Error Component											
29												
2A	SCSI command ((see *1 of SSBLOG05-4890)											
2B	Not used											
2C	Module ID											
2D	Routine ID											
2E	CDEV#											
2F	Reserved											
 33												
34	SSID of self storage system											
35												
36	Exception Class				Reserved							
37	RDEV#											
38	Log and message control (x'00')											
39	Program action code (x'00')											

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3A	Configuration information							
3B								
3C								
3D								
3E	Reserved							
3F								
40								
41								
42	Time of occurrence							
43								
44								
45								
46	Free area Type Code							
47								
48								
49								
4A	Not used							
4B								
4C								
4D								
4E	SAS Port#							
4F								
50								
51								
52	EXP#							
53								
54								
55								
56	CDEV#							
57								
58								
59								
5A	RDEV#							
5B								
5C								
5D								
5E	CMDB#							
5F								
60								
61								
62	JCB ID							
63								
64								
65								
66	Command start time							
67								
68								
69								
6A	Command Type							
6B								
6C								
6D								
6E	Target Type							
6F								
70								
71								
72	Not used							
73								
74								
75								
76	SCSI Command							
77								
78								
79								
7A	SCSI Command LBA							
7B								
7C								
7D								
7E	SCSI Command Length							
7F								
80								
81								
82	Error Code							
83								
84								
85								
86	Error Code (EVENT)							
87								
88								
89								
8A	Error Code (COMPLITION)							
8B								
8C								
8D								
8E	Not used							
8F								
90								
91								

*1: SCSI command code

Code	Command Name	Code	Command Name
00	TEST UNIT READY	2E	WRITE AND VERIFY(10)
04	FORMAT UNIT	3B	WRITE BUFFER
07	REASSIGN BLOCKS	55	MODE SELECT
12	INQUIRY	5A	MODE SENSE
1B	START/STOP UNIT	88	READ (16)
1C	RECEIVE DIAGNOSTIC RESULT	8A	WRITE (16)
1D	SEND DIAGNOSTIC	8E	WRITE AND VERIFY (16)
25	READ CAPACITY (10)	9E	READ CAPACITY (16)
28	READ (10)	FF	COMMAND Unfixed
2A	WRITE (10)		

5.29 Byte36: bit0-3 = D & Byte26: bit4-7 = 0 & Byte27 : bit-2 = 0 (Drive Detected Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format ID							
04	Processer ID							
05	System error code							
06	(Return Code : Low 2 Byte)							
07	JCB#							
08								
09	REQ ID							
0A	24CMPT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# high (Logical DKC#)							
0F	LDEV# low (LDEV# in Logical DKC)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	Internal SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B	Reserved							
1C	SCB							
1D	Reserved							
1E								
1F								
20								
21								
22								
23								
24								
25								
26					Format (x'0')			
27	Type Code('000': Drive Report Error)			0	Sense Kye			
28	Reserved							
29								
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV#							
2F	Reserved							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception Class				Reserved			
37	RDEV#							
38	Log and message control (x'00')							
39	Program action code (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3A	Configuration information							
3B								
3C								
3F								
40	Time of occurrence							
41								
42								
43								
44	Free area Type Code							
45	Not used							
46								
47								
48	SAS Port#							
49	EXP#							
4A	CDEV#							
4B	RDEV#							
4C	CMDB#							
4D								
4E	JCB ID							
4F								
50	Command start time							
51								
52								
53								
54	Command Type							
55								
56	Target Type							
57	SCSI Status							
58	Sense Data (8byte)							
5F								
60								
61								
62	SCSI RSP acquisition result							
63	Sense Key							
64	Additional Sense Code							
65	Additional Sense Code Qualifier							
66	Non-transfer number of bytes							
67								
68								
69								
6A	Obstacle LBA of Sense Data							
6B								
6C								
6D								
6E	CDB							
6F								
70								
7F								

5.30 Byte36: bit0-3 = E & Byte26: bit4-7 = 0 (Drive Detected Failure)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format ID							
04	Processor ID							
05	System error code							
06	(Return Code : Low 2 Byte)							
07	JCB#							
08								
09	REQ ID							
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK Information VLD	Reserved
0C	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
0D	JCB ID							
0E	LDEV# high (Logical DKC#)							
0F	LDEV# low (LDEV# in Logical DKC)							
10								
11	CDEV# / PORT#							
12	RDEV# / PORT#							
13	Internal SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B								
1C	Reserved							
1D	SCB							
1E	Reserved							
1F								
20								
21								
22								
23								
24								
25								
26					Format (x'0')			
27	Type Code('000': Drive Report Error)			0	Sense Key			
28	Reserved							
29								
2A	SCSI command							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	CDEV#							
2F	Reserved							
30								
31								
32								
33								
34	SSID of self storage system							
35								
36	Exception Class				Reserved			
37	RDEV#							
38	Log and message control (x'00')							
39	Program action code (x'00')							

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
3A	Configuration information							
3B								
3C								
3F	Reserved							
40								
41								
42								
43	Time of occurrence							
44								
45								
46								
47	Free area Type Code							
48								
49								
4A								
4B	Not used							
4C								
4D								
4E								
4F	SAS Port#							
50								
51								
52								
53	EXP#							
54								
55								
56								
57	CDEV#							
58								
59								
5A								
5B	RDEV#							
5C								
5D								
5E								
5F	CMDB#							
60								
61								
62								
63	JCB ID							
64								
65								
66								
67	Command start time							
68								
69								
6A								
6B	Command Type							
6C								
6D								
6E								
6F	Target Type							
70								
71								
72								
73	SCSI Status							
74								
75								
76								
77	Sense Data (8byte)							
78								
79								
7A								
7B	SCSI RSP acquisition result							
7C								
7D								
7E								
7F	Sense Key							
80								
81								
82								
83	Additional Sense Code							
84								
85								
86								
87	Additional Sense Code Qualifier							
88								
89								
8A								
8B	Non-transfer number of bytes							
8C								
8D								
8E								
8F	Obstacle LBA of Sense Data							
90								
91								
92								
93	CDB							
94								
95								
96								

5.31 Byte36 = E0 & Byte2C = 5x/6x & Byte20: b3 = 1 (LDEV blockade/Pin VOL detected/ Write inhibited)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used	Format (x'0')			
27	Type code (b'000')			Not used	Not used			
28	READY	ENABLE	SSB PEND	Not used				
29	Not used							
2A	M.M. RSV	PIN VOL	WR INH	Not used				
2B	Not used							
2C	Module ID (*1)							
2D	Routine ID (*1)							
2E	Not used							
2F	Drive serial number (x'0D05' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Exception code (x'E000')							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used							
7F								

*1: Module ID•Routine ID

x'5011/501C/5202' : LDEV blockade
 x'501A/501B' : Pin VOL detected
 x'6605/6697/6705/6784/6805/6853/6909/6953' : Write inhibited

5.32 Byte36 = E0 & Byte2C = 00 & Byte20: b3 = 1 & Byte21: b0, b6 = 1 (Shortage of Pool capacity)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04	Format Message							
05								
06								
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'1F')							
23	Count							
24	Device address							
25	Device type (x'3C')							
26	Unit flag	DEV ADR V	TRK ADR V	Not used (0)	Format (x'0')			
27	Not used							
28								
29								
2A								
2B								
2C								
2D								
2E								
2F	Drive serial number (x'0D05' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Exception code (x'EF07')							
37								
38	Log and message control (x'1A')							
39	Program action code (x'01')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40								
	Not used							
7F								

5.33 Byte36-37 = E210 & Byte2C = 5x/6x & Byte20: b1 = 1 (Intervention requested : LDEV not ready)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB#							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#VLD	0	RCU#			
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used	Format (x'2')			
27	Type code (b'000')			Not used	Not used			
28	READY	ENABLE	SSB PEND	0	0	0	0	0
29	x'00'							
2A	M.M. RSV	PIN VOL	0	0	0	0	0	0
2B	HOST TYPE	DKC TYPE	0	0	DKU TYPE			
2C	Module ID • Routine ID (x'501C/5202': LDEV NOT READY)							
2D								
2E	Command code							
2F	Drive serial number (x'0D05' + DKU sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Exception code (x'E210')							
37								
38	Log and message control (x'01')							
39	Program action code (x'02')							
3A	Configuration information							
3B								
3C	Case of Byte3B Bit3=0: Message code (x'01') Bit3=1: Cylinder address Bit 12-19.							
3D	Case of Byte3B Bit3=0: Cylinder address (High Bit) Bit3=1: Cylinder address Bit 20-27.							
3E	Case of Byte3B Bit3=0: Cylinder address (Low Bit) Bit3=1: Cylinder address Bit 0-7.							
3F	Case of Byte3B Bit3=0: Head address Bit3=1: Bit 0-3 Cylinder address Bit 8-11. Bit 4-7 Head address							
40	Not used							
7F								

5.34 Byte26: bit4-7 = F & Byte3C = F1 (DKC SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0E								
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Reserved							
21								
22								
23								
24								
25								
26						Format (x'F')		
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*1)				Range affected by repair (*2)			
29	Range affected by repair (*3)		Failure SPs (*4)	SPs in SSID (*5)	Failure SSIDs (*6)	Affected SSIDs (*7)	Status (*8)	0
2A	Resent SIM (*9)	'0000000'						
2B	Hardware level (*10)							
2C								
2D	Isolation code (*11)							
2E	SSB ID (*12)	SIM sequence number						SVP ID (*13)
2F	Controller Chassis serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*14)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'F1')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: Range subject to the error

x'0' : Not used

x'1' : Error range not known

x'2' : SP error

x'3' : CHL-I error

x'4' : Not affect performance

x'5-F' : Not used

*2: Range affected by repair

x'0' : Not used

x'1' : Repair range not known

x'2' : Repair does not affect performance

x'3' : SP disabled during repair

x'4' : SSID disabled during repair

x'5-F' : Not used

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Failure SPs

0 : 1SP

1 : 2SP

*5: SPs in SSID

0 : 2SP

1 : 4SP

*6: Failure SSIDs

0 : 1SSID

1 : 2SSID

*7: Affected SSIDs

0 : 1SSID

1 : 2SSID

*8: Status

0 : Reduction

1 : Stop

*9: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*10: Hardware level

Bit 0 : Serial channel hardware level

When Bit 0 is 0

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per Cluster

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

When Bit 0 is 1

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs per Cluster

0000 : Parallel =4, serial =0

0001 : Parallel =8, serial =0

0010 : Parallel =4, serial =2

0100 : Parallel =4, serial =4

0110 : Parallel =0, serial =2

1000 : Parallel =0, serial =4

1010 : Parallel =0, serial =8

1100 : Parallel =0, serial =6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non Cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*11: Isolation code

Byte 2D ≠ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*12: SSB ID

0 : SVP

1 : SP

*13: SVP ID

0 : SVP1

1 : SVP2

*14: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode (0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditionl log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.35 Byte26: bit4-7 = F & Byte3C = F2 (CACHE SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0E								
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26					Format (x'F')			
27	SIM sence code ID (x'E0')							
28	Range subject to the error (*1)				Range affected by repair (*2)			
29	Failure level (*3)		Failure SPs (*4)	SPs in SSID (*5)	Failure SSIDs (*6)	Affected SSIDs (*7)	Status (*8)	0
2A	Resent SIM (*9)	'0000000'						
2B	Hardware level (*10)							
2C								
2D	Isolation code (*11)							
2E	SSB ID (*12)	SIM sequence number						SVP ID (*13)
2F	Controller Chassis serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*14)							
39	Program action code (x'10')							
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'F2')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: Range subject to the error

x'0': Not used

x'1': Error range not known

x'2': SP error

x'3': CHL-I error

x'4': Not affect performance

x'5-F': Not used

*2: Range affected by repair

x'0': Not used

x'1': Repair range not known

x'2': Repair does not affect performance

x'3': SP disabled during repair

x'4': SSID disabled during repair

x'5-F': Not used

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Failure SPs

0 : 1SP

1 : 2SP

*5: SPs in SSID

0 : 2SP

1 : 4SP

*6: Failure SSIDs

0 : 1SSID

1 : 2SSID

*7: Affected SSIDs

0 : 1SSID

1 : 2SSID

*8: Status

0 : Reduction

1 : Stop

*9: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*10: Hardware level

Bit 0 : Serial channel hardware level

When Bit 0 is 0

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per Cluster

00 : 4

01 : 8

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 7 : Not used

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

When Bit 0 is 1

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-7 : CHLs per Cluster

0000 : Parallel =4, serial =0

0001 : Parallel =8, serial =0

0010 : Parallel =4, serial =2

0100 : Parallel =4, serial =4

0110 : Parallel =0, serial =2

1000 : Parallel =0, serial =4

1010 : Parallel =0, serial =8

1100 : Parallel =0, serial =6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non Cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1,024MB

101 : 1,280MB

110 : 1,536MB

111 : Over 1,537MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*11: Isolation code

Byte 2D $\neq$ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code. This byte is byte 2 of the isolation code.

*12: SSB ID

0 : SVP

1 : SP

*13: SVP ID

0 : SVP1

1 : SVP2

*14: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditionl log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.36 Byte26: bit4-7 = F & Byte3C = FE (Device SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0E								
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Reserved							
21								
22								
23								
24								
25								
26						Format (x'F')		
27	SSB ID (*1)	SIM sequence number						SVP ID (*2)
28	Device exception (x'1')				Service message (x'1': Action, x'C': No Action)			
29	Failure level (*3)		'000000'					
2A	Resent SIM (*4)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Micro-program Corequisite Indicator (x'01')							
2D	Device address (See SIM RC SECTION (SIMRC00-00))							
2E	Action flag (x'05')							
2F	Drive serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*5)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FE')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: SSB ID

0 : SVP

1 : SP

*2: SVP ID

0 : SVP1

1 : SVP2

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*5: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

5.37 Byte26: bit4-7 = F & Byte3C = FF (MEDIA SIM)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03	Format Message							
04	Processor ID							
05	System error code							
06								
07	JCB#							
08								
09								
0A	REQ ID							
0B								
0C	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARAT	Host report	SVP report	Force log
0D	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0E								
0F	SEMI-SYNC	RCU SIM	PORT#VLD	Reserved				
10	JCB ID							
11	LDEV# high (Logical DKC#)							
12	LDEV# low (LDEV# in Logical DKC)							
13								
14	CDEV# / PORT#							
15	RDEV# / PORT#							
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x' ')							
20	Reserved							
21								
22								
23								
24								
25								
26						Format (x'F')		
27	SSB ID (*1)	SIM sequence number						SVP ID (*2)
28	Device exception (x'1')				Service message (x'1')			
29	Failure level (*3)		Media Maintenance Procedure Number (Correctable: x'07', Uncorrectable: x'05')					
2A	Resent SIM (*4)	'0000000'						
2B	Exception Class FRU List Code (x'80')							
2C	Hardware and Micro-program Corequisite Indicator (x'01')							
2D	Device address (See SIM RC SECTTION (SIMRC00-00))							
2E	Action flag (Correctable : x'87', Uncorrectable : x'85')							
2F	Drive serial number (x'0CE5' + DKC sequence number)							
30								
31								
32								
33								
34	SSID of self storage system							
35								

(To be continued)

(Continued from the preceding page)

	0	1	2	3	4	5	6	7
36	Reference code (See SIM RC SECTTION (SIMRC00-00))							
37								
38	Log and message control (*5)							
39	Program action code (x'10')							
3A	Dual frame	EDCC Mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR
3B	32 byte SSB (0)	0	0	0	0	Extended path	Storage path number	
3C	Message code (x'FF')							
3D	SSID of mate storage system							
3E								
3F	Not used							
40								
	Not used							
7F								

*1: SSB ID

0 : SVP

1 : SP

*2: SVP ID

0 : SVP1

1 : SVP2

*3: Failure level

00 : SERVICE ALERT

01 : MODERATE ALERT

10 : SERIOUS ALERT

11 : ACUTE ALERT

*4: Resent SIM

0 : Unresent SIM

1 : Resent SIM

*5: Log and message control

Bit 0-2 : Not used

Bit 3 : Log mode(0)

Bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

Bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

6. CUDG3 Error Code Table

LSI type	Test ID	Error Code	Error Contents
—	0006	0000	Config acquisition failure in INLINE CUDG.
	0007	0000	Config CMG number is unjustified of Cache PK.
	000B	0000	INLINE CUDG parameter error.
	000C	0000	CHK1 error occurred in INLINE CUDG.
	000D	0000	Loop count excess in INLINE CUDG.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		SAS Diagnosis Initial processing.
		100F	IFPK# Invalid Parameter(s).
		1104	Beginning CHK4 check error detected.
		1204	Last CHK4 check error detected.
		3901	Code parameter check: IQ PI Read error.
		3909	Code parameter check: Response HTAG error.
		3A01	Code parameter check: OQ CI Read error.
		3A09	Code parameter check: Response OP-Code error.
		3B04	Code parameter check: Last CHK4 check error detected.
		3B09	Code parameter check: Response status error.
		3C01	Code parameter check: Read error of the IQ PI update.
		3C02	Code parameter check: Write error of the IQ PI update.
		3D09	Code parameter check: An IQ_CI value awaiting IQ_CI update exceeds the depth.
		3D0E	Code parameter check: Time-out awaiting IQ_CI update.
		3E09	Code parameter check: An IQ_CI value awaiting IQ_CI update exceeds the depth.
		3E0E	Code parameter check: Time-out awaiting OQ_Pindex update.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		5101	SPCv Firmware Write processing: HDA mode check Read error.
		5109	SPCv Firmware Write processing: It is not an HDA mode.
		5601	SPCv Firmware Write processing: ILA HDAC_DMA Parameter Setting error.
		5604	SPCv Firmware Write processing: Dummy read error after setting ILA HDAC_DMA Parameter.
		5609	SPCv Firmware Write processing: ILA image size 0.
		560C	SPCv Firmware Write processing: Invalid WAIT Retry max value.
		5701	SPCv Firmware Write processing: ILA HDAC_DMA response Code read error.
		5704	SPCv Firmware Write processing: ILA HDAC_DMA polling CHK4 check error.
		5709	SPCv Firmware Write processing: ILA HDAC_DMA expected value error.
		570C	SPCv Firmware Write processing: ILA HDAC_DMA Retry counter value error.
		570E	SPCv Firmware Write processing: ILA HDAC_DMA Polling time-out.
		570F	SPCv Firmware Write processing: ILA HDAC_DMA base value error.
		5801	SPCv Firmware Write processing: ILA HDAC_DMA response Status Read error.
		5809	SPCv Firmware Write processing: ILA HDAC_DMA response Status value error.
		5B01	SPCv Firmware Write processing: ILA HDAC_EXEC Parameter Setting error.
		5B04	SPCv Firmware Write processing: Dummy read error after setting ILA HDAC_EXEC Parameter Read error.
		5B09	SPCv Firmware Write processing: ILA image size 0.
		5C01	SPCv Firmware Write processing: ILA HDAC_EXEC polling response Code read error.
		5C04	SPCv Firmware Write processing: ILA HDAC_EXEC polling CHK4 check error.
		5C09	SPCv Firmware Write processing: ILA HDAC_EXEC polling expected value error.
		5C0C	SPCv Firmware Write processing: ILA HDAC_EXEC polling Retry counter value error.
		5C0E	SPCv Firmware Write processing: ILA HDAC_EXEC Polling time out.
		5C0F	SPCv Firmware Write processing: ILA HDAC_EXEC polling base value error.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		6001	SPCv Firmware Write: Scratchpad 0 Read error of Scratchpad 0 polling.
		6004	SPCv Firmware Write: Polling CHK4 check error of Scratchpad 0 polling.
		600C	SPCv Firmware Write: Polling Retry max value of Scratchpad 0 polling.
		600E	SPCv Firmware Write: Polling Time-out of Scratchpad 0 polling.
		600F	SPCv Firmware Write: Phase number Parameter.
		6101	SPCv Firmware Write processing: Host Scratchpad 4 Write error of RAAE.
		6109	SPCv Firmware Write processing: RAAE image size 0.
		6201	SPCv Firmware Write processing: Host Scratchpad 5 Write error of RAAE.
		6301	SPCv Firmware Write processing: Host Scratchpad 3 Write error of RAAE.
		6401	SPCv Firmware Write: Scratchpad 0 Read error of Scratchpad 0 polling.
		6404	SPCv Firmware Write: Polling CHK4 check error of Scratchpad 0 polling.
		640C	SPCv Firmware Write: Polling Retry max value of Scratchpad 0 polling.
		640E	SPCv Firmware Write: Polling Time-out of Scratchpad 0 polling.
		640F	SPCv Firmware Write: Phase number Parameter error of Scratchpad 0 polling.
		6501	SPCv Firmware Write processing: Host Scratchpad 4 Write error of IOP.
		6509	SPCv Firmware Write processing: IOP image size 0.
		6601	SPCv Firmware Write processing: Host Scratchpad 5 Write error of IOP.
		6701	SPCv Firmware Write processing: Host Scratchpad 3 Write error of IOP.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		6901	SPCv Firmware Write processing: Scratchpad 1 Read error of IOP.
		6904	SPCv Firmware Write processing: CHK4 check error of IOP.
		690C	SPCv Firmware Write processing: Invalid Retry max value of IOP.
		690E	SPCv Firmware Write processing: Polling Time-out of IOP.
		690F	SPCv Firmware Write processing: Phase number Parameter error of IOP.
		6A01	SPCv Firmware Write processing: FW Version acquisition error.
		6B0C	SPCv Firmware Write processing: Phase number processing contradiction.
		6C04	SPCv Firmware Write processing: Last CHK4 check error detected.
		8409	Outbound Queue processing: The Valid value of IOMB of OQ is 0.
		8601	Outbound Queue processing: OQ CI Read error.
		8A01	Outbound Queue processing: Code parameter check: OQ Cindex update error.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		9401	MPI Initialize processing: MPI Main Config access error.
		9409	MPI Initialize processing: MPI Main Config expected value check error.
		9501	MPI Initialize processing: MPI MNIQ/MNOQ/HPIQS Read error.
		9509	MPI Initialize processing: The IQ/OQ maximum number exceeds a limit value.
		9601	MPI Initialize processing: MPI IQCT access error.
		9609	MPI Initialize processing: MPI IQ Config expected value check error.
		9701	Register Test: Register Save error.
		9801	Register Test: Register Write error.
		9901	MPI Initialize processing: MPI OQCT access error.
		9909	MPI Initialize processing: MPI OQ Config expected value check error.
		9A01	Register Test: Register Read error.
		9B0D	Register Test: Register Comparison error.
		9C01	Register Test: Register Recovery error.
		9D01	MPI Initialize Completion check: Register Read error.
		9D04	MPI Initialize Completion check: CHK4 check error.
		9D0C	MPI Initialize Completion check: Processing contradiction.
		9D0E	MPI Initialize Completion check: Polling Time-out error.
		9E01	MPI Initialize Completion check: Register Access error.
		9E04	MPI Initialize Completion check: Polling CHK4 check error.
		9E0C	MPI Initialize Completion check: Polling Retry max value.
		9E0E	MPI Initialize Completion check: Polling Time-out error.
		9F01	MPI Initialize processing: MPI GST access error.
		9F09	MPI Initialize processing: MPI General Status expected value check error.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		A301	MPI Initialize processing: MPI Initialize Request write error.
		A304	MPI Initialize processing: Last CHK4 check error.
		A50D	Read comparison error after SPROM Write.
		A901	MPI Initialize processing: PSPAT offset Read error.
		AA01	SPCve Initialize: Scratchpad 3 read error.
		AB09	SPCve Initialize: Encryption Error (POST code error).
		AC01	SPCve Initialize: SPC Firmware status check: Scratchpad 1 read error.
		AC04	SPCve Initialize: SPC Firmware status check: CHK4 check error detected.
		AC0C	SPCve Initialize: SPC Firmware status check: Invalid Retry max value.
		AC0E	SPCve Initialize: SPC Firmware status check: Polling Time-out.
		AC0F	SPCve Initialize: SPC Firmware status check: Parameter error.
		AD0C	SPCve Initialize: Invalid Phase processing number.

LSI type	Test ID	Error Code	Error Contents
SAS	0F20		
		CB01	SPROM Preparations: HDAC Register access error.
		CB04	SPROM Preparations: HDAC CHK4 check error.
		CB09	SPROM Preparations: HDAC expected value error.
		CB0C	SPROM Preparations: HDAC retry error.
		CB0E	SPROM Preparations: HDAC Polling Time-out.
		CB0F	SPROM Preparations: HDAC Base Value error.
		CC01	SPROM Read processing: HDAC Register access error.
		CC04	SPROM Read processing: HDAC CHK4 check error.
		CC09	SPROM Read processing: HDAC expected value error.
		CC0C	SPROM Read processing: HDAC retry error.
		CC0E	SPROM Read processing: HDAC Polling Time-out.
		CC0F	SPROM Read processing: HDAC Base Value error.
		CD01	SPROM Write processing: HDAC Register access error.
		CD04	SPROM Write processing: HDAC CHK4 check error.
		CD09	SPROM Write processing: HDAC expected value error.
		CD0C	SPROM Write processing: HDAC retry error.
		CD0E	SPROM Write processing: HDAC Polling Time-out.
		CD0F	SPROM Write processing: HDAC Base Value error.
		CE01	SPROM Write processing: LM to SPCv access error.
		CE02	SPROM Write processing: LM to SPCv data transfer error of data write.
		CE09	SPROM Write processing: LM to SPCv confirmation data comparison error of data write.
		CE0E	SPROM Write processing: LM to SPCv transfer Time-out of data write.
		CE0F	SPROM Write processing: LM to SPCv Write size error. (less than 4 bytes)
		CF0C	Invalid Phase processing number.
	0F20		
		D00C	Phase division processing contradiction. (Excessive number of times)
		D101	SPC EPROM CRC check error.
		D30C	SPCv Initialize: Invalid Phase processing number.
		D40F	SPC Initialize: Invalid PK type.
		D509	SPCve Initialize: SPCve is not encryption mode.

LSI type	Test ID	Error Code	Error Contents
SAS	0F21		SAS Diagnosis 1: Non-PORT Test.
		0309	MPIbaseget: PCI_BAR error detected
		0401	MPI Config Table address acquisition: IQCT offset Read error.
		0501	MPI Config Table address acquisition: OQCT offset Read error.
		0601	MPI Config Table address acquisition: MPI IQ Pindex PCI Base type Read error.
		0709	MPI Config Table address acquisition: PCI_BAR type base address acquisition error.
		0801	MPI Config Table address acquisition: MPI IQ Pindex address Offset Read error.
		0901	MPI Config Table address acquisition: MPI OQ Cindex PCI Base type Read error.
		0A09	MPI Config Table address acquisition: PCI_BAR type base address acquisition error.
		0B01	MPI Config Table address acquisition: MPI OQ Cindex address Offset Read error.
		0C01	Max Que (IQ/OQ) information acquisition: MPI MNIQ/MNOQ/HPIQS Read error.
		0D01	MPI Config Table address acquisition: MPI base address (MPI main config) acquisition error.
		0E01	Max Que (IQ/OQ) information acquisition: MPI base address (MPI main config) acquisition error.
		0F09	MPI_BASE address acquisition error

LSI type	Test ID	Error Code	Error Contents
SAS	0F21		
		1504	Beginning CHK4 check error detected.
		150F	IFPK# Invalid Parameter(s).
		1601	Scratchpad 1 Read error.
		1604	Last CHK4 check error detected.
		1609	Scratchpad 1 Status is not Ready.
		170C	Phase division contradiction. (Excessive number of times)
		1801	ECHO command: IQ PI Read error.
		1901	ECHO command: OQ CI Read error.
		1A01	ECHO command: Read error of IQ PI update.
		1A02	ECHO command: Write error of IQ PI update.
		1B09	ECHO command: An IQ_CI value awaiting IQ_CI update exceeds the depth.
		1B0E	ECHO command: Time-out awaiting IQ_CI update.
		1C09	ECHO command: An OQ_PI value awaiting OQ_PI update exceeds the depth.
		1C0E	ECHO command: Time-out awaiting OQ_Pindex update.
		1D09	ECHO command: Response HTAG error
		1E09	ECHO command: Response OP-Code error.
		1F09	ECHO command: Response status error.
	0F21		
		2004	ECHO command: Last CHK4 check error detected.
		2009	PHY Start Linkup: An error detected
		2101	SAS Test Error.
	0F21		
		2009	Fwversion acquisition: MPICongfig error.
		7F01	An error not to adjust Pindex and Cindex of OQ to the same value.
		8109	Inbound Queue processing: Invalid Index
		810e	Inbound Queue processing: Time-out
		8209	Inbound Queue processing: Pindex Invalid Index
		820e	Inbound Queue processing: Pindex Time-out
		8301	Outbound Queue processing: ECHO Response: OQ Cindex update error.
		8409	Outbound Queue processing: Valid value of IOMB of OQ is 0.
		8601	Outbound Queue processing: OQ CI Read error.
		8709	Outbound Queue processing: IOMB of OQ was unused Response.
		8801	Outbound Queue processing: PCIE_DIAG_EXECUTE Response: OQ Cindex update error.

LSI type	Test ID	Error Code	Error Contents
SAS	0F21		
		B001	PCIE_DIAG_EXECUTE command DIF: IQ PI Read error.
		B101	PCIE_DIAG_EXECUTE command DIF: OQ CI Read error.
		B201	PCIE_DIAG_EXECUTE command DIF: Read error of IQ PI update.
		B202	PCIE_DIAG_EXECUTE command DIF: Write error of IQ PI update.
		B309	PCIE_DIAG_EXECUTE command DIF: An IQ_CI value awaiting IQ_CI update exceeds the depth..
		B30E	PCIE_DIAG_EXECUTE command DIF: Time-out awaiting IQ_CI update.
		B409	PCIE_DIAG_EXECUTE command DIF: An OQ_PI value awaiting OQ_PI update exceeds the depth.
		B40E	PCIE_DIAG_EXECUTE command DIF: Time-out awaiting OQ_Pindex update.
		B509	PCIE_DIAG_EXECUTE command DIF: Response HTAG error.
		B609	PCIE_DIAG_EXECUTE command DIF: Response OP-Code error.
		B709	PCIE_DIAG_EXECUTE command DIF: Response status error.
		B801	PCIE_DIAG_EXECUTE command: IQ PI Read error.
		B901	PCIE_DIAG_EXECUTE command: OQ CI Read error.
		BA04	PCIE_DIAG_EXECUTE command DIF: CHK4 check error detected after command response.
		BB01	PCIE_DIAG_EXECUTE command: Read error of IQ PI update.
		BB02	PCIE_DIAG_EXECUTE command: Write error of IQ PI update.
		BC09	PCIE_DIAG_EXECUTE command: An error awaiting IQ_CI update.
		BC0E	PCIE_DIAG_EXECUTE command: Time-out awaiting IQ_CI update.
		BD09	PCIE_DIAG_EXECUTE command: An IQ_CI value awaiting IQ_CI update exceeds the depth.
		BD0E	PCIE_DIAG_EXECUTE command: Time-out awaiting OQ_Pindex update.
		BE09	PCIE_DIAG_EXECUTE command: Response HTAG error.
		BF09	PCIE_DIAG_EXECUTE command: Response OP-Code error.

LSI type	Test ID	Error Code	Error Contents
SAS	0F21		
		C009	PCIE_DIAG_EXECUTE command: Response status error.
		C104	PCIE_DIAG_EXECUTE command: CHK4 check error detected after command response.
		C20C	Loopback Transfer Test: Invalid Parameter Table value.
		C304	Loopback Transfer Test: Last CHK4 check error detected.
		C40D	Loopback Transfer Test: Data comparison error.
		C50D	Loopback Transfer Test2: Data comparison error.
		C604	Loopback Transfer Test2: Last CHK4 check error detected.
		C70D	Loopback Transfer T10-DIF Test: Data comparison error.
		C804	Loopback Transfer T10-DIF Test: Last CHK4 check error detected.
		D20C	Invalid Phase number.
	0F23		SAS Diagnosis End processing.
		1304	Beginning CHK4 check error detected.
		130F	IFPK# Invalid Parameter(s).
		1404	Last CHK4 check error detected.
		CA0C	Phase division internal processing contradiction (excessive number of times)
PSW	0F50		PSW access test.
		0009	PSW access error.
	0F51		PSW FW CRC test.
		0001	PSW FW CRC error.
	0F52		NSW FW CRC test.
		0001	NSW FW CRC error.

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LSI type	Test ID	Error Code	Error Contents
LRP	1301		Main LRP diagnosis control.
		0003	Illegal Parameter Error.

LSI type	Test ID	Error Code	Error Contents
Tachyon/SAS/Hilda (FC16G, iSCSI)/Baker (FC16G, FC32G)	1903		Main IFPK diagnosis control.
		0002	Hard adap mode Acquisition error.
		0003	Illegal Parameter error.
		001F	Invalid Illegal Hard adap mode value.
		0023	CHK1 error detected.
		0043	Failure detected in P-Path Linkup check
		0044	IFPK type mismatch
	1990		IFPK reset/P-Path recovery
		0040	P-Path Blockade/Failure detected in IFPK resetting.
		0041	Failure detected in canceling of IFPK resetting.
		0042	Failure detected in setting of P-Path Linkup.
		0043	Failure detected in checking of P-Path Linkup.
		0023	CHK1 error detected.

LSI type	Test ID	Error Code	Error Contents
Tachyon	1910		Tachyon diagnosis.
		01xC *1	Internal processing contradiction
		01xF *1	Invalid Port# parameter
		11x4 *1	Beginning CHK4 check error detected.
		12x4 *1	Last CHK4 check error detected.
		13xC *1	Phase processing number contradiction.
		15xC *1	Internal processing contradiction Phase division contradiction. (Excessive LOOP count)
		17xF *1	IFPK# Invalid Parameter(s).
		18xF *1	Port# Invalid Parameter(s).
		1CxF *1	Loop Mode Invalid Parameter(s).
		1DxF *1	Transfer speed Invalid Parameter(s).
		22xC *1	Beginning processing: Phase processing number contradiction.
		23x4 *1	Beginning processing: CHK4 check error detected.
		27xC *1	End processing: Phase processing number contradiction.
		28x4 *1	End processing: CHK4 check error detected.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1910		
		40x4 *1	Tachyon_Linkup processing: Last CHK4 check error detected. (at the end)
		42x1 *1	Register Test: Register Save error.
		43x1 *1	Register Test: Register Write error.
		44x1 *1	Register Test: Register Read error.
		45xD *1	Register Test: Register Comparison error.
		46x1 *1	Register Test: Register Recovery error.
		47x1 *1	Register Setting Value Addition: QE8_Config_2 Register Write error.
		48x1 *1	Register Setting Value Addition: QE8_Config_1 Register Read error.
		49x1 *1	Register Setting Value Addition: QE8_Config_1 Register Write error.
		4Bx1 *1	Linkup processing: The acquisition of Frame Manager Link Status 1 Register (BAD CHAR CHK) failed.
		4BxE *1	Linkup processing: BAD CHAR CHK is time-out error.
		4Cx1 *1	Tachyon_Linkup processing: The setting of Frame Manager Primitive Register failed.
		4Dx1 *1	Linkup processing: The setting of Frame Manager Control Register (LIP) failed.
		4Ex1 *1	Linkup processing: Register dummy read failed after setting Frame Manager Control Register (LIP).
		4Fx1 *1	Linkup processing: The setting of Frame Manager Control Register (Offline) failed.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1910		
		50x1 *1	Tachyon_Linkup processing: Dummy read failed after setting Frame Manager Control Register (Offline)
		51x1 *1	Linkup processing: The setting of Frame Manager Control Register (Initialize) failed.
		52x1 *1	Linkup processing: Dummy read failed after setting Frame Manager Control Register (Initialize).
		53xE *1	Linkup processing: IMQ-PIndex doesn't become an expected value, and the Time-out error.
		54x1 *1	Linkup processing: The setting (+1) of Tachyon_IMQ_C-Index failed.
		55x1 *1	Linkup processing: Register dummy read error.
		57x1 *1	Linkup processing: The setting (+1) of Tachyon_IMQ_C-Index failed.
		58x1 *1	Linkup processing: The setting of Tachyon_SFQ_C-Index failed.
		59x1 *1	Linkup processing: The acquisition of Frame Manager Status Register failed.
		5Ax9 *1	Linkup processing: Frame Manager Status Register is outside the expected value.
		5Bx1 *1	Linkup processing: The acquisition of Frame Manager Received AL_PA Register failed.
		5Cx9 *1	Linkup processing: ACQ_ALPA of Frame Manager Received AL_PA Register is outside the expected value.
		5Dx1 *1	Linkup processing: The setting of My ID Register failed.
		5Ex1 *1	Linkup processing: The setting of Frame Manager Status Register (INT STS CLR) failed.
		5Fx1 *1	Linkup processing: The acquisition of Frame Manager Link Status 1 Register (LINK STS CLR) failed.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1910		
		60x9 *1	Tachyon_LINKUP processing: Link Status is not cleared.
		61x1 *1	Tachyon_Linkup processing: The acquisition of Frame Manager Link Status 1 Register (LINK STS CLR CHECK) failed.
		62x9 *1	Linkup processing: Link Status is not cleared.
		68x9 *1	Tachyon_LINKUP processing: Other than Completion_Message was returned.
		69x9 *1	Tachyon_LINKUP processing: Other than Completion_Message was returned.
		73x7 *1	Beginning processing: A protocol initial setting result error.
		74x4 *1	Beginning processing: CHK4 check error detected in protocol initial setting.
		74x7 *1	Beginning processing: INIT_STATUS error.
		79x4 *1	Beginning/Terminate processing: CHK4 check error detected in port initial setting.
		7Ax1 *1	Terminate processing (optical port closed) : The setting of Frame Manager Configuration 5 Register failed.
		7Bx1 *1	Terminate processing (optical port closed) : Dummy read of Frame Manager Configuration 5 Register failed.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1910		
		83x7 *1	Terminate processing: A Protocol Blockade result error.
		84x4 *1	Terminate processing: A Protocol Blockade CHK4 check error detected.
		84x7 *1	Terminate processing: Protocol Blockade result is a STATUS error.
		87x7 *1	Terminate processing: Protocol Installation Information acquisition result error.
		88x4 *1	Terminate processing: Protocol Installation information acquisition CHK4 check error detected.
		88x7 *1	Terminate processing: Protocol initial setting result is an INIT_STATUS error.
		B5x1 *1	Beginning processing: S_ID entry Write error in MP search setting.
		B6x1 *1	Beginning processing: S_ID entry register enable set error in MP search setting.
		B7x2 *1	Beginning processing: MP search table Write error in MP search setting.
		C8x1 *1	Beginning processing: MP search data Read error in register save.
		C9x1 *1	Beginning processing: MP search register Read error in register save.
		CAX1 *1	Terminate processing: MP search data Write error in register recovery.
		CBx1 *1	Terminate processing: MP search register Write error in register recovery in register save.
		CCx1 *1	Beginning processing: S_ID entry register table Read error.
		CDx1 *1	Terminate processing: S_ID entry register table Write error in register recovery.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1911		Tachyon Transfer : LM -> LM.
		02xD *1	Transfer data comparison error.
		13xD *1	Data comparison error
		14x4 *1	Last CHK4 check error detected.
		19xD *1	Data comparison error
		41x1 *1	Dummy read after IMP_C-Index update failed.
		45xD *1	Data comparison error
		63x1 *1	Data The acquisition of Virtual IMQ C-Index failed.
		64x2 *1	Data Transfer to SEST(TRE) failed.
		65x2 *1	Data Transfer to sfs(FCHS) failed.
		66x2 *1	Data Transfer to SEST(IRE) failed.
		67x1 *1	Transfer to ERQ(IRB) failed.
		69xE *1	IMQ_P-Index is not updated and the Time-out error.
		6Ax9 *1	The content of the 1st IMQ failed outside the expected value.
		6Bx1 *1	The setting (+1) of IMQ_C-Index failed.
		6CxE *1	IMQ_P-Index(2) is not updated and the Time-out error.
		6Dx9 *1	The content of the 2nd IMQ failed outside the expected value.
		6Ex1 *1	The setting (+1) of IMQ_C-Index failed.
		70x4 *1	CHK4 check error detected.
		CEx1 *1	Burst Transfer access error from LM to XR.
		CEx2 *1	Burst Transfer error from LM to XR.
		CEx9 *1	Burst Transfer check data error from LM to XR.
		CExE *1	Burst Transfer Time-out error from LM to XR.
		CExF *1	Burst Transfer Parameter error from LM to XR.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1913		Tachyon Transfer : T10-DIF.
		02xD *1	Transfer data comparison error.
		13xD *1	Data comparison error
		19x4 *1	Last CHK4 check error detected.
		19xD *1	Data comparison error
		1Ax1 *1	T10mode2 UDT4-5 Read error.
		1Bx1 *1	T10mode2 UDT0-3 Read error.
		1Ex1 *1	T10mode3 UDT4-5 Read error.
		1Fx1 *1	T10mode3 UDT0-3 Read error.
		41x1 *1	Dummy read after IMQ_C-Index update failed.
		45xD *1	Data comparison error
		63x1 *1	The acquisition of Virtual IMQ C-Index failed.
		64x2 *1	Data Transfer to SEST(TRE) failed.
		65x2 *1	Data Transfer to sfs(FCHS) failed.
		66x2 *1	Data Transfer to SEST(IRE) failed.
		67x1 *1	Data Transfer to ERQ(IRB) failed.
		69xE *1	IMQ_P-Index is not updated and the Time-out error.
		6Ax9 *1	The content of the 1st IMQ failed outside the expected value.
		6Bx1 *1	The setting (+1) of IMQ_C-Index failed.
		6CxE *1	IMQ_P-Index(2) is not updated and the Time-out error.
		6Dx9 *1	The content of the 2nd IMQ failed outside the expected value.
		6Ex1 *1	The setting (+1) of IMQ_C-Index failed.
		70x4 *1	CHK4 check error detected.
		CEx1 *1	Burst Transfer access error from LM to XR.
		CEx2 *1	Burst Transfer error from LM to XR.
		CEx9 *1	Burst Transfer check data error from LM to XR.
		CExE *1	Burst Transfer Time-out error from LM to XR.
		CExF *1	Burst Transfer Parameter error from LM to XR.

*1 : x: Port number

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1760		FC16G diagnosis starts
		010C	Internal processing is inconsistent (1)
		010F	Parameter error
		020C	Internal processing is inconsistent(2)
		1404	CHK4 check detects an error (1)
		1504	CHK4 check detects an error (2)
		2609	PCI configuration value check error
	1761		Register Test
		010C	Internal processing is inconsistent
		1504	CHK4 check detects an error
		4001	Address register dummy read error (at the end)
		4101	Address register '0' Write error (at the head)
		4201	Address register dummy read error (at the head)
		4301	Data register Write error
		4401	Data register Read error
		450D	Comparison error
		4601	Address register '0' Write error (at the end)
	1762		Soft Reset Check
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		1C01	Dummy read error (1)
		1C04	CHK4 check detects an error (2)
		1D01	Soft Reset register Write error
		1E01	GPIO Data Register Write error
		1F01	GPIO Enable Register Write error
		2001	Dummy read error (2)
		2101	Register register Write error
		2201	Register access error
		2204	CHK4 check detects an error (3)
		220E	Polling time-out error
		220F	Parameter error (2)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1763		ROM Firmware Boot Check
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1108	Firmware sum value error on PM
		1504	CHK4 check detects an error (1)
		280F	Parameter error (2)
		2901	Register access error (1)
		2904	CHK4 check detects an error (3)
		290E	Polling time-out error
		290F	Parameter error (3)
		2F01	Register access error(2)
		300C	Table creation error
	1764		Flash contents exchange
		010F	Illegal Parameter Error occurred
		1108	Flash data SUM check Error
		1504	CHK4 Error occurred
		1604	CHK4 Error occurred
		2501	PCI Reset Error end
		2504	CHK4 Error occurred
		2507	LRP Mailbox Error
		250C	Internal processing contradiction
		250E	PCI Reset Time Out Error occurred
		2A01	Register Access Error
		2A04	CHK4 Error occurred
		2A0E	Time Out Error occurred
		2A0F	Illegal Parameter Error occurred
		3701	Register Access Error
		4701	Register Access Error
		4901	Register Access Error
		4A01	Register Access Error
		4B01	Register Access Error
		4C01	Register Access Error
		4C0F	Illegal Parameter Error occurred
		4D01	Register Access Error
		4E01	Register Access Error
		4F01	Register Access Error
		4F04	CHK4 Error occurred

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1764		Flash contents exchange
		5001	Register Access Error
		5101	Register Access Error
		5201	Register Access Error
		5301	Register Access Error
		5401	Register Access Error
		5404	CHK4 Error occurred
		540E	Time Out Error occurred
		540F	Illegal Parameter Error occurred
		5501	Register Access Error
		5609	Semaphore Lock Failure
		5709	Register Write Error
		570D	Flash protection value Error
		5809	Erase expected value Error
		5A09	Illegal Version value Error
		5B09	Version disagreement
		5E01	Register Access Error
		5E02	Burst Transfer Error
		5F01	Register Access Error
		6001	Register Access Error
		6004	CHK4 Error occurred
		600E	Time Out Error occurred
		6109	Erase expected value Error
		6201	Register Access Error
		6204	CHK4 Error occurred
		6208	Check SUM Error
		620D	Contents comparison Error
		6309	Signature value checks Error
		6401	Register Access Error
		6501	Register Access Error
		6601	Register Access Error
		6609	Read Flag Status Register is value Error
		6701	Register Access Error
		6801	Register Access Error
		6804	CHK4 Error occurred
		680E	Time Out Error occurred
		680F	Illegal Parameter Error occurred

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1764		Flash contents exchange
		6901	Register Access Error
		6909	RISC to Host Interrupt Request is value Error
		6A01	Register Access Error
		6A09	RISC Paused
		6B01	Register Access Error
		6B09	Asynchronous event of an Error notification
		6B0E	Read count over
		6C01	Register Access Error
		6C09	Completion checks Error
		6D01	Register Access Error
		6E01	Register Access Error
		7607	Command completion code Error
		7701	Register Access Error
		7704	CHK4 Error occurred
		7707	Command completion code Error
		7708	Check SUM Error
		7709	Contents Size Error
		7807	Command completion code Error
		7809	MAC Address Value Error
		7A01	Register Access Error
		7A09	MAC Address Value Error
		7B01	Register Access Error
		7D01	Register Access Error
	1765		ROM Firmware command Test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error
		6804	CHK4 check detects an error (2)
		680E	Polling time-out error
		680F	Parameter error (2)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1765		ROM Firmware command Test
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error(1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error(2)
		6E01	Dummy read error (4)
		7007	Command completion code error(1)
		7307	Command completion code error(2)
		730D	Data mismatch
	1766		Memory Read/Write Test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		2A01	Register access error(1)
		2A04	CHK4 check detects an error (2)
		2A0E	Polling time-out error(1)
		2A0F	Parameter error (2)
		3104	CHK4 check detects an error (before memory test)
		3201	Flash access error(1)
		3204	CHK4 check detects an error (after memory test)
		330D	Data comparison error

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1766		Memory Read/Write Test
		4801	Flash access error(2)
		4808	Sum value error
		4C0F	Parameter error (3)
		4D01	Data register '0' clear error
		4E01	Address register Write error
		4F01	Register Read error
		4F04	CHK4 check detects an error (3)
		5001	Flash access error(3)
		6301	Flash Read error
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error(2)
		6804	CHK4 check detects an error (4)
		680E	Polling time-out error(2)
		680F	Parameter error (4)
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error(1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error(2)
		6E01	Dummy read error (4)
		7409	Initial value error
		7707	Command completion code error(1)
		7807	Command completion code error(2)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1767		FC Firmware Boot Test
		010C	Internal processing is inconsistent (1)
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		3401	FC Firmware RISC_RAM_1 address acquisition error
		3402	FC Firmware_1 burst transfer error from LM to DXBF
		3404	CHK4 check detects an error (2)
		3502	FC Firmware_2 burst transfer error from LM to DXBF
		350D	FC Firmware_1 Comparison error
		360D	FC Firmware_2 Comparison error
		3704	CHK4 check detects an error (3)
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error
		6804	CHK4 check detects an error (4)
		680E	Polling time-out error
		680F	Parameter error (2)
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error (1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error (2)
		6E01	Dummy read error (4)
		7207	Command completion code error (1)
		720C	Internal processing is inconsistent (2)
		7407	Command completion code error (2)
		7707	Command completion code error (3)
		7807	Command completion code error (4)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1768		FC Firmware command Test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error
		6804	CHK4 check detects an error (2)
		680E	Polling time-out error
		680F	Parameter error (2)
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error (1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error
		6E01	Dummy read error (4)
		7007	Command completion code error (1)
		7307	Command completion code error (2)
		730D	Data mismatch
	1769		FC Firmware version acquisition test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		3802	Parameter block transfer error to DXBF
		3809	Mismatch of Load source and Ver. value
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error
		6804	CHK4 check detects an error (2)
		680E	Polling time-out error
		680F	Parameter error (2)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	1769		FC Firmware version acquisition test
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error (1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error (2)
		6E01	Dummy read error (4)
		7507	Command completion code error (1)
		7509	Version value error
		7A07	Command completion code error (2)
	176A		FC Firmware configuration setting
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		6401	Incoming Mailbox Write error
		6501	Dummy read error (1)
		6601	Host Command and Control register Write error
		6701	Dummy read error (2)
		6801	Register access error
		6804	CHK4 check detects an error (2)
		680E	Polling time-out error
		680F	Parameter error (2)
		6901	RISC to Host Status register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error (1)
		6A09	RISC Paused
		6B01	Dummy read error (3)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox Read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error (2)
		6E01	Dummy read error (4)

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	176A		FC Firmware configuration setting
		6E07	Command completion code error (1)
		7907	Command completion code error (2)
		7B07	Command completion code error (3)
		8007	Command completion code error (4)
		8107	Command completion code error (5)
		8207	Command completion code error (6)
		8209	Firmware status is other than Ready
	176C		IOCB Test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		7101	RQQ In_Pointer Read error (1)
		8601	RQQ In_Pointer Read error (2)
		8602	Burst transfer error of IOCB request parameter
		8701	RQQ In_Pointer update Write error
		8707	Command completion code error (1)
		870D	Response data mismatch
		8B01	Dummy read error
		8B09	STATUS failed
		8C01	ATQ Out_Pointer Read error
		8C09	No IOCB response
		8D04	CHK4 check detects an error (4)
		8D0E	Polling time-out error
		8D0F	Parameter error (2)
		8E01	ATQ OUT_Pointer update Write error
		8E07	No IOCB response
		8E09	Response of the same HANDLE
		8F01	RQQ In_Pointer Read error (3)
		8F07	Command completion code error (2)
		8F09	Unexpected asynchronous event

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	176D		DMA function check/indirect register test
		010C	Internal processing is inconsistent
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		1E01	GPIO Data Register Write error
		1F01	GPIO Enable Register Write error
		2001	Dummy read error (1)
		5604	CHK4 check detects an error (After DMA transfer)
		6104	CHK4 check detects an error (Before DMA transfer)
		6304	CHK4 check detects an error (2)
		6C0D	Data comparison error
		7201	RQQ In_Pointer Read error (1)
		7401	RQQ In_Pointer Read error (2)
		8601	RQQ In_Pointer Read error (3)
		8602	Burst transfer error of IOCB request parameter
		8604	CHK4 check detects an error (4)
		8607	Command completion code error (1)
		8609	Register Read value error (1)
		8701	RQQ In_Pointer update Write error
		8704	CHK4 check detects an error (5)
		8709	Register Read value error (2)
		8B01	Dummy read error (2)
		8B07	Command completion code error (2)
		8B09	STATUS failed

LSI type	Test ID	Error Code	Error Contents
Hilda (FC16G)	176D		DMA function check/indirect register test
		8C01	ATQ Out_Pointer Read error
		8C09	IOCB_HANDLE mismatch
		8D04	CHK4 check detects an error (9)
		8D0E	Polling time-out error
		8D0F	Parameter error (2)
		8E01	ATQ OUT_Pointer update Write error
		8E07	No IOCB response
		8E09	Response of the same HANDLE
		8F09	Unexpected asynchronous event
	176E		Data transfer function check
		010C	Internal processing is inconsistent
		010F	Parameter error
		1504	CHK4 check detects an error (1)
		5807	Mailbox Command: Diagnostic Loopback (0x0045) error (1)
		5A07	Mailbox Command: Diagnostic Loopback (0x0045) error (2)
		5C07	Mailbox Command: Diagnostic Loopback (0x0045) error (3)
		6404	CHK4 check detects an error (2)
		6504	CHK4 check detects an error (3)
		6704	CHK4 check detects an error (4)
		6707	Mailbox Command: Diagnostic Loopback (0x0045) error (4)
		6B04	CHK4 check detects an error (5)
		6B07	Mailbox Command: Diagnostic Loopback (0x0045) error (5)
		6D09	Latter part of user data is mismatched
		6D0D	Data comparison mismatch
	176E		Data transfer function check
		7004	CHK4 check detects an error (6)
		7604	CHK4 check detects an error (7)
		760D	Data comparison mismatch
	176F		FC16G diagnosis is completed

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1A10		iSCSI10G diagnosis starts
		01xC *1	Internal processing is inconsistent (1)
		01xF *1	Parameter error
		02xC *1	Internal processing is inconsistent (2)
		14x4 *1	CHK4 check detects an error (1)
		15x4 *1	CHK4 check detects an error (2)
		26x4 *1	CHK4 check detects an error (3)
		26xC *1	Internal processing is inconsistent (3)
		2Cx4 *1	CHK4 check detects an error (4)
	1A20		Initialization completion check
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		40x1 *1	Address register dummy read error (at the end)
		41x1 *1	Address register '0' Write error (at the head)
		42x1 *1	Address register dummy read error (at the head)
		43x1 *1	Data register Write error
		44x1 *1	Data register Read error
		45xD *1	Comparison error
		46x1 *1	Address register '0' Write error (at the end)
	1A30		Warm Boot Test
		01xC *1	Internal processing is inconsistent
		01xF *1	Parameter error (1)
		15x4 *1	CHK4 check detects an error (1)
		1Cx1 *1	Dummy read error (1)
		1Cx4 *1	CHK4 check detects an error (2)
		1Dx1 *1	Soft Reset register Write error
		1Ex1 *1	GPIO Data Register Write error
		1Fx1 *1	GPIO Enable Register Write error
		20x1 *1	Dummy read error (2)
		21x1 *1	Register Write error
		22x1 *1	Register access error
		22x4 *1	CHK4 check detects an error (3)
		22xE *1	Polling time-out error
		22xF *1	Parameter error (2)
		27x2 *1	Burst transfer error
		27x4 *1	CHK4 check detects an error (4)
		2Dx1 *1	Save error of registers

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1A40		No operation Test
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A41		Mailbox Register Test
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A50		About Firmware Test
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
		5Bx9 *1	Ver. mismatch
	1A60		Firmware default setting
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A61		Stop Firmware Test
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A70		Internal Loopback test
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		28x7 *1	IOCB no response
		29x7 *1	IOCB status error
		45xD *1	Comparison error

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1A80		Test Flash ROM
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A81		Test DDR Read/Write
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A82		Test OCM Read/Write
		01xC *1	Internal processing is inconsistent
		15x4 *1	CHK4 check detects an error
		22xE *1	Polling time-out error
		27x7 *1	MailBox Status error
		45xD *1	Comparison error
	1A83		Flash replacement
		010C	Internal processing is inconsistent (1)
		010F	Parameter error (1)
		1504	CHK4 check detects an error (1)
		1604	CHK4 check detects an error (2)
		2504	CHK4 check detects an error (3)
		2501	Hilda reboot error
		250E	Hilda reboot time-out error
		2A01	Register access error (1)
		2A04	CHK4 check detects an error (4)
		2A0E	Polling time-out (1)
		2A0F	Parameter error (2)
		4501	Register access error (2)
		4701	Dummy read error (1)
		4709	Flash Read Expectation error
		4901	Data register '0' clear error (1)

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1A83		Flash replacement
		4A01	Address register Write error (1)
		4B01	Address register Read error
		4C01	Data register Read error
		4C0F	Parameter error (3)
		4D01	Data register '0' clear error (2)
		4E01	Address register Write error (2)
		4F01	Register Read error
		4F04	CHK4 check detects an error (5)
		5001	Flash access error (1)
		5101	Address value error
		5201	Data register Write error
		5301	Address register Write error (3)
		5401	Register access error (3)
		5404	CHK4 check detects an error (6)
		540E	Polling time-out (2)
		540F	Parameter error (4)
		5501	Flash access error (2)
		5609	Semaphore Lock failure
		5709	Write to Status Register cannot be confirmed
		5809	Erase Expectation error (1)
		5B09	Ver. unmatched
		5E01	Ver. Value Read error
		5E02	Transfer error
		5F01	Flash access error (3)
		6001	Flash access error (4)
		6004	CHK4 check error was detected during polling
		600E	Polling time-out (3)
		6109	Erase Expectation error (2)
		6201	Flash access error (5)
		6204	CHK4 check detects an error (7)
		6208	Sum value error
		620D	Contents comparison error

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1A83		Flash replacement
		6401	Incoming Mailbox Write error
		6501	Dummy read error (2)
		6601	Host Command and Control Register Write error
		6609	Read Flag Status Register is error value
		6701	Dummy read error (3)
		6801	Register access error (4)
		6804	CHK4 check detects an error (8)
		680E	Polling time-out (4)
		680F	Parameter error (5)
		6901	RISC to Host Status Register Read error
		6909	RISC to Host Interrupt Request is OFF
		6A01	Clear RISC to PCI Interrupt Write error (1)
		6A09	RISC Paused
		6B01	Dummy read error (4)
		6B09	Asynchronous event of error notice
		6C01	Outgoing Mailbox read error
		6C09	Completion check error
		6D01	Clear RISC to PCI Interrupt Write error (2)
		6E01	Dummy read error (5)
		7607	Command completion code error (1)
		7707	Command completion code error (2)
		7807	Command completion code error (3)
	1a90		PHY firmware exchange
		0004	Time Out Error occurred
		00ff	Internal processing contradiction
		010f	Parameter error
		450d	Comparison error
	1aa0		IFPK reset/P-Path recovery
		0001	P-Path recovery error
		000c	Internal processing contradiction
		000e	Time Out Error occurred

LSI type	Test ID	Error Code	Error Contents
DIMM	0C01		DIMM configuration check
		0001	Unsupported DIMM installed
		0002	DIMM installation position mismatch
		0003	DIMM type mismatch
	0C02		DIMM configuration check 2
		0001	DIMM training error was detected
		0002	Abnormal DIMM installation pattern
	0D02		Memory WT/RD Test
		0002	Data comparison error has occurred
		0040	CHK1 error has occurred
	0D1A		PD Check
		0002	DIMM type mismatch error has occurred
		0040	CHK1 error has occurred
	0D1B		DIMM information read
		0040	CHK1 error has occurred
	0D1C		DIMM SPROM information Read
		0040	CHK1 error has occurred
CFM	0BF1		Diagnostic parameter check
		0100	Abnormal diagnostic parameter
	0BF2		CFM installation check
		0100	SPEC information read error
		0200	Configuration information acquisition error
		0300	CFM type mismatch
		0040	CHK1 error has occurred
	0BF3		CFM FRU number check
		0100	Configuration information acquisition error
		0200	CFM FRU number disagreement
	0BF8		CFM WT/RD Test
		0001	Periodic diagnosis CMD terminates abnormally
		0002	Excessive access competition
		0040	CHK1 error has occurred

LSI type	Test ID	Error Code	Error Contents
CPU	1270		I pass diagnosis (A)
		0100	I pass write access error
		0200	I pass read access error
		0300	Data comparison error
		0400	Failure detected in I path Linkup check
		0F00	Diagnosis completion time-out
		0040	CHK1 error has occurred
	1271		I pass diagnosis (B)
		0100	I pass write access error
		0200	I pass read access error
		0300	Data comparison error
		0400	Failure detected in I path Linkup check
		0040	CHK1 error has occurred
LANC	0401		LANC diagnosis
		0100	LANCTL failure detected
		0200	Link error between PCH and LANCTL

LSI type	Test ID	Error Code	Error Contents
Tachyon	1960		Tachyon diagnosis
		01xC *1	Internal processing contradiction
		01xF *1	Invalid Port# parameter
		11x4 *1	Beginning CHK4 check error detected
		12x4 *1	Last CHK4 check error detected
		13xC *1	Phase processing number contradiction
		15xC *1	Internal processing contradiction (excessive LOOP count)
		17xF *1	Invalid IFPK# parameter
		18xF *1	Invalid Port# parameter
		1Cx F *1	Invalid LOOP mode parameter
		1DxF *1	Invalid transfer speed parameter
		22xC *1	Beginning processing: Phase processing number contradiction
		23x4 *1	Beginning processing: CHK4 check error detected
		27xC *1	End processing: Phase processing number contradiction
		28x4 *1	End processing: CHK4 check error detected
		40x4 *1	Tachyon_LINKUP processing: CHK4 check error detected (at the end)
		42x1 *1	Register test: Register save error
		43x1 *1	Register test: Register Write error
		44x1 *1	Register test: Register Read error
		46x1 *1	Register test: Register recovery error
		47x1 *1	Register setting value addition: QE8_Config_2 register Write error
		48x1 *1	Register setting value addition: QE8_Config_1 register Read error
		49x1 *1	Register setting value addition: QE8_Config_1 register Write error
		4Bx1 *1	Tachyon_LINKUP processing: Frame Manager Link Status 1 Register(BAD CHAR CHK) acquisition failed
		4BxE *1	Tachyon_LINKUP processing: BAD CHAR CHK time-out error
		4Cx1 *1	Tachyon_LINKUP processing: Frame Manager Primitive Register setting failed
		4Dx1 *1	Tachyon_LINKUP processing: Frame Manager Control Register(LIP) setting failed
		4Ex1 *1	Tachyon_LINKUP processing: Dummy read after setting Frame Manager Control Register(LIP) failed
		4Fx1 *1	Tachyon_LINKUP processing: Frame Manager Control Register(Offline) setting failed

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1960		
		50x1 *1	Tachyon_LINKUP processing: Dummy read after setting Frame Manager Control Register(Offline) failed
		51x1 *1	Tachyon_LINKUP processing: Frame Manager Control Register(Initialize) setting failed
		52x1 *1	Tachyon_LINKUP processing: Dummy read after setting Frame Manager Control Register(Initialize) failed
		53xE *1	Tachyon_LINKUP processing: IMQ_P-Index does not become the expected value after Initialize setting and time-out error
		54x1 *1	Tachyon_LINKUP processing: The setting (+1) of Tachyon IMQ_C-Index failed
		55x1 *1	Tachyon_LINKUP processing: Dummy read after updating IMQ_C-Index failed
		57x1 *1	Tachyon_LINKUP processing: The setting (+1) (2) of Tachyon IMQ_C-Index failed
		58x1 *1	Tachyon_LINKUP processing: Tachyon SFQ_C-Index setting failed
		59x1 *1	Tachyon_LINKUP processing: Frame Manager Status Register acquisition failed
		5Ax9 *1	Tachyon_LINKUP processing: Frame Manager Status Register value is outside the expected value
		5Bx1 *1	Tachyon_LINKUP processing: Frame Manager Received AL_PA Register acquisition failed
		5Cx9 *1	Tachyon_LINKUP processing: Frame Manager Received AL_PA Register ACQ_ALPA value is outside the expected value
		5Dx1 *1	Tachyon_LINKUP processing: My ID Register setting failed
		5Ex1 *1	Tachyon_LINKUP processing: Frame Manager Status Register(INT STS CLR) setting failed
		5Fx1 *1	Tachyon_LINKUP processing: Frame Manager Link Status 1 Register(LINK STS CLR) acquisition failed
		60x9 *1	Tachyon_LINKUP processing: Link Status is not cleared
		61x1 *1	Tachyon_LINKUP processing: Frame Manager Link Status 1 Register(LINK STS CLR CHECK) acquisition failed
		62x9 *1	Tachyon_LINKUP processing: Link Status is not cleared
		68x9 *1	Tachyon_LINKUP processing: Other than Completion_Message was returned
		69x9 *1	Tachyon_LINKUP processing: Other than Completion_Message was returned

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1960		
		73x7 *1	Beginning processing: Protocol initial setting result error
		74x4 *1	Beginning processing: Protocol initial setting CHK4 check error detected
		74x7 *1	Beginning processing: Protocol initial setting result is INIT_STATUS error
		79x4 *1	Beginning/End processing: Port initial setting CHK4 check error detected
		7Ax1 *1	End processing (optical port closed): Frame Manager Configuration 5 Register setting failed
		7Bx1 *1	End processing (optical port closed): Frame Manager Configuration 5 Register dummy read failed
		83x7 *1	End processing: Protocol blockade result error
		84x4 *1	End processing: Protocol blockade CHK4 check error detected
		84x7 *1	End processing: Protocol blockade result is STATUS error
		87x7 *1	End processing: Protocol installation information acquisition result error
		88x4 *1	End processing: Protocol installation information acquisition CHK4 check error detected
		88x7 *1	End processing: Protocol initial setting result is INIT_STATUS error
		B5x1 *1	Beginning processing: S_ID entry Write error in MP search setting
		B6x1 *1	Beginning processing: S_ID entry register enable set error in MP search setting
		B7x2 *1	Beginning processing: MP search table Write error in MP search setting

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1960		
		C8x1 *1	Beginning processing: MP search data Read error in register save
		C9x1 *1	Beginning processing: MP search register Read error in register save
		CAX1 *1	End processing: MP search data Write error in register recovery
		CBx1 *1	End processing: MP search register Write error in register recovery
		CCx1 *1	Beginning processing: S_ID entry register table Read error in register save
		CDx1 *1	End processing: S_ID entry register table Write error in register recovery
	1961		Tachyon transfer: LM → LM
		02xD *1	Data transfer comparison error
		13xD *1	Data comparison error
		14x4 *1	Last CHK4 check error detected
		19xD *1	Data comparison error
		41x1 *1	Dummy read after updating IMP_C-Index failed
		45xD *1	Data comparison error
		63x1 *1	Virtual IMQ_C-Index acquisition failed
		64x2 *1	Data transfer to SEST from TRE[SEST] failed
		65x2 *1	Data transfer to sfs from FCHS failed
		66x2 *1	Data transfer to SEST from IRE[SEST] failed
		67x1 *1	Data transfer to ERQ from IRB[ERQ] failed
		69xE *1	Virtual IMQ_P-Index is not updated and time-out error
		6Ax9 *1	The content of the first IMQ failed because it is outside the expected value
		6Bx1 *1	The setting (+1) of IMQ_C-Index failed
		6CxE *1	Virtual IMQ_P-Index (2) is not updated and time-out error
		6Dx9 *1	The content of the second IMQ failed because it is outside the expected value
		6Ex1 *1	The setting (+1) (2) of IMQ_C-Index failed
		70x4 *1	CHK4 check error detected
		CEx1 *1	Burst transfer access error from LM to XR
		CEx2 *1	Burst transfer error from LM to XR
		CEx9 *1	Burst transfer check data error from LM to XR
		CExE *1	Burst transfer time-out error from LM to XR
		CExF *1	Burst transfer parameter error from LM to XR

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Tachyon	1963		Tachyon transfer: T10-DIF
		02xD *1	Transfer data comparison error
		13xD *1	Data comparison error
		19x4 *1	Last CHK4 check error detected
		19xD *1	Data comparison error
		1Ax1 *1	T10mode2 UDT4-5 Read error
		1Bx1 *1	T10mode2 UDT0-3 Read error
		1Ex1 *1	T10mode3 UDT4-5 Read error
		1Fx1 *1	T10mode3 UDT0-3 Read error
		41x1 *1	Dummy read after updating IMQ_C-Index failed
		45xD *1	Data comparison error
		63x1 *1	Virtual IMQ_C-Index acquisition failed
		64x2 *1	Data transfer to SEST from TRE[SEST] failed
		65x2 *1	Data transfer to sfs from FCHS failed
		66x2 *1	Data transfer to SEST from IRE[SEST] failed
		67x1 *1	Data transfer to ERQ from IRB[ERQ] failed
		69xE *1	Virtual IMQ_P-Index is not updated and time-out error
		6Ax9 *1	The content of the first IMQ failed because it is outside the expected value
		6Bx1 *1	The setting (+1) of IMQ_C-Index failed
		6Cx9 *1	Virtual IMQ_P-Index (2) is not updated and time-out error
		6Dx9 *1	The content of the second IMQ failed because it is outside the expected value
		6Ex1 *1	The setting (+1) (2) of IMQ_C-Index failed
		70x4 *1	CHK4 check error detected
		CEx1 *1	Burst transfer access error from LM to XR
		CEx2 *1	Burst transfer error from LM to XR
		CEx9 *1	Burst transfer check data error from LM to XR
		CExE *1	Burst transfer time-out error from LM to XR
		CExF *1	Burst transfer parameter error from LM to XR

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1560		iSCSI10G diagnosis starts/Flash contents exchange
		00x2 *1	PCI Config setting error (iSCSI10G diagnosis starts)
		00x3 *1	PCI Config setting error (iSCSI10G diagnosis starts)
		00x4 *1	PCI Config setting error (iSCSI10G diagnosis starts)
		00x5 *1	PCI Config setting error (iSCSI10G diagnosis starts)
		00x6 *1	PCI Config setting error (iSCSI10G diagnosis starts)
		01xC *1	Internal processing contradiction (1) (iSCSI10G diagnosis starts)
		01xF *1	Illegal Parameter Error occurred
		02xC *1	Internal processing contradiction (2) (iSCSI10G diagnosis starts)
		11x8 *1	Flash data SUM check Error
		14x4 *1	CHK4 Error occurred (iSCSI10G diagnosis starts)
		15x4 *1	CHK4 Error occurred
		16x4 *1	CHK4 Error occurred
		25x1 *1	PCI Reset Error end
		25x4 *1	CHK4 Error occurred
		25x7 *1	LRP Mailbox Error
		25xC *1	Internal processing contradiction
		25xE *1	PCI Reset Time Out Error occurred
		26x4 *1	CHK4 Error occurred (iSCSI10G diagnosis starts)
		26xC *1	Internal processing contradiction (3) (iSCSI10G diagnosis starts)
		2Ax1 *1	Register Access Error
		2Ax4 *1	CHK4 Error occurred
		2AxE *1	Time Out Error occurred
		2AxF *1	Illegal Parameter Error occurred
		2Cx4 *1	CHK4 Error occurred (iSCSI10G diagnosis starts)
		37x1 *1	Register Access Error
		40x1 *1	Address register dummy read error (at the end) (iSCSI10G diagnosis starts)
		41x1 *1	Address register '0' Write error (at the head) (iSCSI10G diagnosis starts)
		42x1 *1	Address register dummy read error (at the head) (iSCSI10G diagnosis starts)
		43x1 *1	Data register Write error (iSCSI10G diagnosis starts)
		44x1 *1	Data register Read error (iSCSI10G diagnosis starts)
		45xD *1	Comparison error (iSCSI10G diagnosis starts)

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1560		iSCSI10G diagnosis starts/Flash contents exchange
		46x1 *1	Address register '0' Write error (at the end) (iSCSI10G diagnosis starts)
		47x1 *1	Register Access Error
		49x1 *1	Register Access Error
		4Ax1 *1	Register Access Error
		4Bx1 *1	Register Access Error
		4Cx1 *1	Register Access Error
		4CxF *1	Illegal Parameter Error occurred
		4Dx1 *1	Register Access Error
		4Ex1 *1	Register Access Error
		4Fx1 *1	Register Access Error
		4Fx4 *1	CHK4 Error occurred
		50x1 *1	Register Access Error
		51x1 *1	Register Access Error
		52x1 *1	Register Access Error
		53x1 *1	Register Access Error
		54x1 *1	Register Access Error
		54x4 *1	CHK4 Error occurred
		54xE *1	Time Out Error occurred
		54xF *1	Illegal Parameter Error occurred
		55x1 *1	Register Access Error
		56x9 *1	Semaphore Lock Failure
		57x9 *1	Register Write Error
		57xD *1	Flash protection value Error
		58x9 *1	Erase expected value Error
		5Ax9 *1	Illegal Version value Error
		5Bx9 *1	Version disagreement
		5Ex1 *1	Register Access Error
		5Ex2 *1	Burst Transfer Error
		5Fx1 *1	Register Access Error

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1560		iSCSI10G diagnosis starts/Flash contents exchange
		60x1 *1	Register Access Error
		60x4 *1	CHK4 Error occurred
		60xE *1	Time Out Error occurred
		61x9 *1	Erase expected value Error
		62x1 *1	Register Access Error
		62x4 *1	CHK4 Error occurred
		62x8 *1	Check SUM Error
		62xD *1	Contents comparison Error
		63x9 *1	Signature value checks Error
		64x1 *1	Register Access Error
		65x1 *1	Register Access Error
		66x1 *1	Register Access Error
		66x9 *1	Read Flag Status Register is value Error
		67x1 *1	Register Access Error
		68x1 *1	Register Access Error
		68x4 *1	CHK4 Error occurred
		68xE *1	Time Out Error occurred
		68xF *1	Illegal Parameter Error occurred
		69x1 *1	Register Access Error
		69x9 *1	RISC to Host Interrupt Request is value Error
		6Ax1 *1	Register Access Error
		6Ax9 *1	RISC Paused
		6Bx1 *1	Register Access Error
		6Bx9 *1	Asynchronous event of an Error notification
		6BxE *1	Read count over
		6Cx1 *1	Register Access Error
		6Cx9 *1	Completion checks Error
		6Dx1 *1	Register Access Error
		6Ex1 *1	Register Access Error

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1560		iSCSI10G diagnosis starts/Flash contents exchange
		76x7 *1	Command completion code Error
		77x1 *1	Register Access Error
		77x4 *1	CHK4 Error occurred
		77x7 *1	Command completion code Error
		77x8 *1	Check SUM Error
		77x9 *1	Contents Size Error
		78x7 *1	Command completion code Error
		78x9 *1	MAC Address Value Error
		7Ax1 *1	Register Access Error
		7Ax9 *1	MAC Address Value Error
		7Bx1 *1	Register Access Error
		7Dx1 *1	Register Access Error
	1561		Warm Boot test
		01xC *1	Internal processing contradiction
		01xF *1	Parameter error (1)
		15x4 *1	CHK4 check error detected (1)
		1Cx1 *1	Dummy read error (1)
		1Cx4 *1	CHK4 check error detected (2)
		1Dx1 *1	Soft Reset register Write error
		1Ex1 *1	GPIO Data Register Write error
		1Fx1 *1	GPIO Enable Register Write error
		20x1 *1	Dummy read error (2)
		21x1 *1	Register Write error

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
Hilda (iSCSI)	1561		Warm Boot test
		22x1 *1	Register access error
		22x4 *1	CHK4 check error detected (3)
		22xE *1	Polling time-out error
		22xF *1	Parameter error (2)
		27x2 *1	Burst transfer error
		27x4 *1	CHK4 check error detected (4)
		2Dx1 *1	Register save error
	1562		No operation test / Mailbox Register test / About Firmware test
		01xC *1	Internal processing contradiction
		15x4 *1	CHK4 check error detected
		22xE *1	Polling time-out error
		27x7 *1	MailBox status error
		45xD *1	Comparison error
		5Bx9 *1	Ver. mismatch
	1563		Firmware initial setting / Stop Firmware test
		01xC *1	Internal processing contradiction
		15x4 *1	CHK4 check error detected
		22xE *1	Polling time-out error
		27x7 *1	MailBox status error
		45xD *1	Comparison error
	1564		Internal Loopback test
		01xC *1	Internal processing contradiction
		15x4 *1	CHK4 check error detected
		22xE *1	Polling time-out error
		27x7 *1	MailBox status error
		28x7 *1	No IOCB response
		29x7 *1	IOCB status error
		45xD *1	Comparison error
	1565		Peripheral memory test
		01xC *1	Internal processing contradiction
		15x4 *1	CHK4 check error detected
		22xE *1	Polling time-out error
		27x7 *1	MailBox status error
		45xD *1	Comparison error

*1 : x: Port Number

LSI type	Test ID	Error Code	Error Contents
SW/SCM	3001		HW-strap setting check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3002		DeviceError check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3003		VID/DID check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3004		vSW setting check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3005		EEPROM access check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3006		Upstream-Port check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3007		SW initial setting value check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred

LSI type	Test ID	Error Code	Error Contents
SW/SCM	3008		SW register read access check (configuration space)
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3009		SW register read access check (MMIO space)
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3010		SW register write access check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3011		SW PCIe path check
		0040	CHK1 error occurred
		0100	SW register expected value mismatch error occurred
		0200	SW register read error occurred
		0300	SW register write error occurred
	3020		e environment microcomputer diagnosis result check
		0040	CHK1 error occurred
		0100	e environment microcomputer diagnosis error was detected
	3021		eGCTL initial setting value check
		0040	CHK1 error occurred
		0100	eGCTL initial setting value and expected value mismatch error occurred
	3030		SW type check
		0040	CHK1 error occurred
		0100	SW type mismatch error occurred
	30F0	0000	Diagnosis parameter error (PK type)
	30F1	0000	Diagnosis parameter error (SW type)
	30F2	0000	Diagnosis phase error
GCTL	1277		Diagnosis of control signal (Emergency signal) between clusters
		1000	Expected value mismatch error of control signal (Emergency signal) between clusters has occurred
		E100	Synchronous data transmission error between clusters has occurred
		E200	Synchronous time-out between clusters has occurred

LSI type	Test ID	Error Code	Error Contents
Baker (FC16G, FC32G)	1D01		FC32G diagnosis control
		0001	Internal processing contradiction
		0002	Illegal Parameter Error occurred
		0003	Illegal HARD ADPMODE Error occurred
	1D10		Submain (EP Configuration/Register Test/Soft Reset)
		0001	Internal processing contradiction
	1D11		EP Configuration
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
	1D12		Register Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
	1D13		Soft Reset
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D20		Submain (ROM Firmware/Flash exchange)
		0001	Internal processing contradiction
	1D21		ROM Firmware Boot Test
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D22		ROM Firmware Command Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D30		Submain (Memory Read-Write Test/POST Test)
		0001	Internal processing contradiction
	1D31		Memory Read-Write Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D32		POST Test
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D33		Soft Reset (After Post Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D34		ROM Firmware Boot Test(After Post Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D40		Submain (FC Firmware)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D41		FC Firmware Boot Test
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
		0008	FC Firmware SUM check Error
		000B	FC Firmware Illegal Parameter Error occurred
	1D42		FC Firmware Command Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D43		FC Firmware Version Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
		000B	FC Firmware Illegal Parameter Error occurred

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D44		FC Firmware Parameter Setting
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D45		FC Firmware Initialize Setting
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D50		Submain (IOCB Test)
		0001	Internal processing contradiction
	1D51		IOCB Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
		000A	IOCB Error end
	1D52		DMA Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
		000A	IOCB Error end
	1D61		Flash exchange
		0001	Internal processing contradiction
	1D62		Flash Access Test
		0001	Internal processing contradiction

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D63		Flash contents SUM check
		010F	Illegal Parameter Error occurred
		1108	Flash data SUM check Error
		1504	CHK4 Error occurred
		1604	CHK4 Error occurred
		2504	CHK4 Error occurred
		2507	LRP Mailbox Error
		250C	Internal processing contradiction
		2A01	Register Access Error
		2A04	CHK4 Error occurred
		2A0E	Time Out Error occurred
		2A0F	Illegal Parameter Error occurred
		3701	Register Access Error
		4701	Register Access Error
		4901	Register Access Error
		4A01	Register Access Error
		4B01	Register Access Error
		4C01	Register Access Error
		4C0F	Illegal Parameter Error occurred
		4D01	Register Access Error
		4E01	Register Access Error
		4F01	Register Access Error
		4F04	CHK4 Error occurred
		5001	Register Access Error
		5101	Register Access Error
		5201	Register Access Error
		5301	Register Access Error
		5401	Register Access Error
		5404	CHK4 Error occurred
		540E	Time Out Error occurred
		540F	Illegal Parameter Error occurred
		5501	Register Access Error
		5609	Semaphore Lock Failure
		5709	Register Write Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D63		Flash contents SUM check
		570D	Flash protection value Error
		5809	Erase expected value Error
		5A09	Illegal Version value Error
		5B09	Version disagreement
		5E01	Register Access Error
		5E02	Burst Transfer Error
		5F01	Register Access Error
		6001	Register Access Error
		6004	CHK4 Error occurred
		600E	Time Out Error occurred
		6109	Erase expected value Error
		6201	Register Access Error
		6204	CHK4 Error occurred
		6208	Check SUM Error
		620D	Contents comparison Error
		6309	Signature value checks Error
		6401	Register Access Error
		6501	Register Access Error
		6601	Register Access Error
		6609	Read Flag Status Register is value Error
		6701	Register Access Error
		6801	Register Access Error
		6804	CHK4 Error occurred
		680E	Time Out Error occurred
		680F	Illegal Parameter Error occurred
		6901	Register Access Error
		6909	RISC to Host Interrupt Request is value Error
		6A01	Register Access Error
		6A09	RISC Paused
		6B01	Register Access Error
		6B09	Asynchronous event of an Error notification
		6B0E	Read count over
		6C01	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D63		Flash contents SUM check
		6C09	Completion checks Error
		6D01	Register Access Error
		6E01	Register Access Error
		7607	Command completion code Error
		7701	Register Access Error
		7704	CHK4 Error occurred
		7707	Command completion code Error
		7708	Check SUM Error
		7709	Contents Size Error
		7807	Command completion code Error
		7809	MAC Address Value Error
		7A01	Register Access Error
		7A09	MAC Address Value Error
		7B01	Register Access Error
		7D01	Register Access Error
	1D64		Flash contents exchange
		010F	Illegal Parameter Error occurred
		1108	Flash data SUM check Error
		1504	CHK4 Error occurred
		1604	CHK4 Error occurred
		2504	CHK4 Error occurred
		2507	LRP Mailbox Error
		250C	Internal processing contradiction
		2A01	Register Access Error
		2A04	CHK4 Error occurred
		2A0E	Time Out Error occurred
		2A0F	Illegal Parameter Error occurred
		3701	Register Access Error
		4701	Register Access Error
		4901	Register Access Error
		4A01	Register Access Error
		4B01	Register Access Error
		4C01	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D64		Flash contents exchange
		4C0F	Illegal Parameter Error occurred
		4D01	Register Access Error
		4E01	Register Access Error
		4F01	Register Access Error
		4F04	CHK4 Error occurred
		5001	Register Access Error
		5101	Register Access Error
		5201	Register Access Error
		5301	Register Access Error
		5401	Register Access Error
		5404	CHK4 Error occurred
		540E	Time Out Error occurred
		540F	Illegal Parameter Error occurred
		5501	Register Access Error
		5609	Semaphore Lock Failure
		5709	Register Write Error
		570D	Flash protection value Error
		5809	Erase expected value Error
		5A09	Illegal Version value Error
		5B09	Version disagreement
		5E01	Register Access Error
		5E02	Burst Transfer Error
		5F01	Register Access Error
		6001	Register Access Error
		6004	CHK4 Error occurred
		600E	Time Out Error occurred
		6109	Erase expected value Error
		6201	Register Access Error
		6204	CHK4 Error occurred
		6208	Check SUM Error
		6309	Signature value checks Error
		620D	Contents comparison Error
		6401	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D64		Flash contents exchange
		6501	Register Access Error
		6601	Register Access Error
		6609	Read Flag Status Register is value Error
		6701	Register Access Error
		6801	Register Access Error
		6804	CHK4 Error occurred
		680E	Time Out Error occurred
		680F	Illegal Parameter Error occurred
		6901	Register Access Error
		6909	RISC to Host Interrupt Request is value Error
		6A01	Register Access Error
		6A09	RISC Paused
		6B01	Register Access Error
		6B09	Asynchronous event of an Error notification
		6B0E	Read count over
		6C01	Register Access Error
		6C09	Completion checks Error
		6D01	Register Access Error
		6E01	Register Access Error
		7607	Command completion code Error
		7701	Register Access Error
		7704	CHK4 Error occurred
		7707	Command completion code Error
		7708	Check SUM Error
		7709	Contents Size Error
		7807	Command completion code Error
		7809	MAC Address Value Error
		7A01	Register Access Error
		7A09	MAC Address Value Error
		7B01	Register Access Error
		7D01	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D65		PCI Reset
		0005	Time Out Error occurred
		000C	PCI Reset Error end
	1D66		Flash default value check
		010F	Illegal Parameter Error occurred
		1108	Flash data SUM check Error
		1504	CHK4 Error occurred
		1604	CHK4 Error occurred
		2504	CHK4 Error occurred
		2507	LRP Mailbox Error
		250C	Internal processing contradiction
		2A01	Register Access Error
		2A04	CHK4 Error occurred
		2A0E	Time Out Error occurred
		2A0F	Illegal Parameter Error occurred
		3701	Register Access Error
		4701	Register Access Error
		4901	Register Access Error
		4A01	Register Access Error
		4B01	Register Access Error
		4C01	Register Access Error
		4C0F	Illegal Parameter Error occurred
		4D01	Register Access Error
		4E01	Register Access Error
		4F01	Register Access Error
		4F04	CHK4 Error occurred
		5001	Register Access Error
		5101	Register Access Error
		5201	Register Access Error
		5301	Register Access Error
		5401	Register Access Error
		5404	CHK4 Error occurred
		540E	Time Out Error occurred
		540F	Illegal Parameter Error occurred

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D66		Flash default value check
		5501	Register Access Error
		5609	Semaphore Lock Failure
		5709	Register Write Error
		570D	Flash protection value Error
		5809	Erase expected value Error
		5A09	Illegal Version value Error
		5B09	Version disagreement
		5E01	Register Access Error
		5E02	Burst Transfer Error
		5F01	Register Access Error
		6001	Register Access Error
		6004	CHK4 Error occurred
		600E	Time Out Error occurred
		6109	Erase expected value Error
		6201	Register Access Error
		6204	CHK4 Error occurred
		6208	Check SUM Error
		620D	Contents comparison Error
		6309	Signature value checks Error
		6401	Register Access Error
		6501	Register Access Error
		6601	Register Access Error
		6609	Read Flag Status Register is value Error
		6701	Register Access Error
		6801	Register Access Error
		6804	CHK4 Error occurred
		680E	Time Out Error occurred
		680F	Illegal Parameter Error occurred
		6901	Register Access Error
		6909	RISC to Host Interrupt Request is value Error
		6A09	RISC Paused
		6A01	Register Access Error
		6B01	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D66		Flash default value check
		6B09	Asynchronous event of an Error notification
		6B0E	Read count over
		6C01	Register Access Error
		6C09	Completion checks Error
		6D01	Register Access Error
		6E01	Register Access Error
		7607	Command completion code Error
		7701	Register Access Error
		7704	CHK4 Error occurred
		7707	Command completion code Error
		7708	Check SUM Error
		7709	Contents Size Error
		7807	Command completion code Error
		7809	MAC Address Value Error
		7A01	Register Access Error
		7A09	MAC Address Value Error
		7B01	Register Access Error
		7D01	Register Access Error
	1D67		Flash SUM check
		010F	Illegal Parameter Error occurred
		1108	Flash data SUM check Error
		1504	CHK4 Error occurred
		1604	CHK4 Error occurred
		2504	CHK4 Error occurred
		2507	LRP Mailbox Error
		250C	Internal processing contradiction
		2A01	Register Access Error
		2A04	CHK4 Error occurred
		2A0E	Time Out Error occurred
		2A0F	Illegal Parameter Error occurred
		3701	Register Access Error
		4701	Register Access Error
		4901	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D67		Flash SUM check
		4A01	Register Access Error
		4B01	Register Access Error
		4C01	Register Access Error
		4C0F	Illegal Parameter Error occurred
		4D01	Register Access Error
		4E01	Register Access Error
		4F01	Register Access Error
		4F04	CHK4 Error occurred
		5001	Register Access Error
		5101	Register Access Error
		5201	Register Access Error
		5301	Register Access Error
		5401	Register Access Error
		5404	CHK4 Error occurred
		540E	Time Out Error occurred
		540F	Illegal Parameter Error occurred
		5501	Register Access Error
		5609	Semaphore Lock Failure
		5709	Register Write Error
		570D	Flash protection value Error
		5809	Erase expected value Error
		5A09	Illegal Version value Error
		5B09	Version disagreement
		5E01	Register Access Error
		5E02	Burst Transfer Error
		5F01	Register Access Error
		6001	Register Access Error
		6004	CHK4 Error occurred
		600E	Time Out Error occurred
		6109	Erase expected value Error
		6201	Register Access Error
		6204	CHK4 Error occurred
		6208	Check SUM Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D67		Flash SUM check
		620D	Contents comparison Error
		6309	Signature value checks Error
		6401	Register Access Error
		6501	Register Access Error
		6601	Register Access Error
		6609	Read Flag Status Register is value Error
		6701	Register Access Error
		6801	Register Access Error
		6804	CHK4 Error occurred
		680E	Time Out Error occurred
		680F	Illegal Parameter Error occurred
		6901	Register Access Error
		6909	RISC to Host Interrupt Request is value Error
		6A01	Register Access Error
		6A09	RISC Paused
		6B01	Register Access Error
		6B09	Asynchronous event of an Error notification
		6B0E	Read count over
		6C01	Register Access Error
		6C09	Completion checks Error
		6D01	Register Access Error
		6E01	Register Access Error
		7607	Command completion code Error
		7701	Register Access Error
		7704	CHK4 Error occurred
		7707	Command completion code Error
		7708	Check SUM Error
		7709	Contents Size Error
		7807	Command completion code Error
		7809	MAC Address Value Error
		7A01	Register Access Error
		7A09	MAC Address Value Error
		7B01	Register Access Error
		7D01	Register Access Error

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D70		Submain (Loopback Test1)
		0001	Internal processing contradiction
	1D71		Data transfer Test
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D72		T10CRC Data transfer Test (32G Mode)
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D80		Submain (16G Mode Boot Test)
		0001	Internal processing contradiction
	1D81		Soft Reset (16G Mode Boot Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D82		ROM Firmware Boot Test (16G Mode Boot Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
	1D83		FC Firmware Boot Test (16G Mode Boot Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
		0008	FC Firmware SUM check Error
		000B	FC Firmware Illegal Parameter Error occurred
	1D84		FC Firmware Command Test (16G Mode Boot Test)
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end

LSI type	Test ID	Error Code	Error Contents
Baker (FC32G)	1D85		FC Firmware Parameter Setting (16G Mode Boot Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D86		FC Firmware Initialize Setting (16G Mode Boot Test)
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D90		Submain (Loopback Test2)
		0001	Internal processing contradiction
	1D91		T10CRC Data transfer Test (16G Mode)
		0001	Internal processing contradiction
		0004	Data Compare Error occurred
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end
	1D92		DIF Error Test
		0001	Internal processing contradiction
		0005	Time Out Error occurred
		0006	Mailbox Command Uncompleted
		0007	Mailbox Command Error end

LSI type	Test ID	Error Code	Error Contents
HIE	4000		HIE register test
		1001	Abnormal end
		1002	Expected value Error
		1003	CHK3 error
		1010	PKID error
	4001		MDMA internal RAM test
		1001	Abnormal end
		1002	Expected value Error
		1010	PKID error
	4002		XDMA internal RAM test
		1001	Abnormal end
		1002	Expected value Error
		1010	PKID error
	4003		LRP4 DS internal RAM test
		1001	Abnormal end
		1002	Expected value Error
		1010	PKID error
	4004		PARB internal RAM test
		1001	Abnormal end
		1002	Expected value Error
		1010	PKID error
	4005		QDMA internal RAM test
		1001	Abnormal end
		1002	Expected value Error
		1010	PKID error
	4006		SMDMA transfer test
		1201	Abnormal end
		1202	Expected value Error
		1203	CHK3 error
		1204	Transfer time out
		1205	Status NG
		1206	Parameter setting NG
		1207	CAS failure
		1210	PKID error

LSI type	Test ID	Error Code	Error Contents
HIE	4007		XDMA transfer test
		1301	Abnormal end
		1302	Expected value Error
		1303	CHK3 error
		1304	Transfer time out
		1305	Status NG
		1306	Parameter setting NG
		1310	PKID error
	4008		MFDMA transfer test
		1401	Abnormal end
		1402	Expected value Error
		1404	Transfer time out
		1405	Status NG
		1406	Parameter setting NG
		1410	Initial setting error
	4100		ISW test between CTL and CM
		2001	Abnormal end
		2002	Another X path error
		2003	ISW status error
	4101		HIE register test between CTL and MPU
		3001	Abnormal end
		3002	Comparison error
		3003	CHK3 error
	4102		Memory write and read test between CTL and MPU
		3101	Abnormal end
		3102	Comparison error
		3104	Transfer time out
		3105	Status NG
		3106	Parameter setting NG
	4103		SMDMA memory write and read test between CTL and MPU
		3201	Abnormal end
		3202	Comparison error
		3204	Transfer time out
		3205	Status NG
		3206	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
HIE	4104		XDMA memory write and read test between CTL and MPU
		3301	Abnormal end
		3302	Comparison error
		3304	Transfer time out
		3305	Status NG
		3306	Parameter setting NG
		3301	Abnormal end
	4105		MFDMA memory write and read test between CTL and MPU
		3401	Abnormal end
		3402	Expected value Error
		3404	Transfer time out
		3405	Status NG
		3406	Parameter setting NG
	4200		ISW test between MPU and CM
		2001	Abnormal end
		2002	Another X path error
		2003	ISW status error
	4201		HIE register test between MPU and CM
		3001	Abnormal end
		3002	Comparison error
		3003	CHK3 error
	4202		Memory write and read test between MPU and CM
		3101	Abnormal end
		3102	Comparison error
		3104	Transfer time out
		3105	Status NG
		3106	Parameter setting NG
	4203		SMDMA memory write and read test between MPU and CM
		3201	Abnormal end
		3202	Comparison error
		3204	Transfer time out
		3205	Status NG
		3206	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
HIE	4204		XDMA memory write and read test between MPU and CM
		3301	Abnormal end
		3302	Comparison error
		3304	Transfer time out
		3305	Status NG
		3306	Parameter setting NG
		3301	Abnormal end
	4205		MFDMA memory write and read test between MPU and CM
		3401	Abnormal end
		3402	Expected value Error
		3404	Transfer time out
		3405	Status NG
		3406	Parameter setting NG
	4300		ISW test between CTL and CM
		2001	Abnormal end
		2002	Another X path error
		2003	ISW status error
	4301		HIE register test between CTL and CM
		3001	Abnormal end
		3002	Comparison error
		3003	CHK3 error
	4302		Memory write and read test between CTL and CM
		3101	Abnormal end
		3102	Comparison error
		3104	Transfer time out
		3105	Status NG
		3106	Parameter setting NG
	4303		SMDMA memory write and read test between CTL and CM
		3201	Abnormal end
		3202	Comparison error
		3204	Transfer time out
		3205	Status NG
		3206	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
HIE	4304		XDMA memory write and read test between CTL and CM
		3301	Abnormal end
		3302	Comparison error
		3304	Transfer time out
		3305	Status NG
		3306	Parameter setting NG
		3301	Abnormal end
	4305		MFDMA memory write and read test between CTL and CM
		3401	Abnormal end
		3402	Expected value Error
		3404	Transfer time out
		3405	Status NG
		3406	Parameter setting NG
	4400		ISW test between MPU and MPU
		2001	Abnormal end
		2002	Another X path error
		2003	ISW status error
	4401		HIE register test between MPU and MPU
		3001	Abnormal end
		3002	Comparison error
		3003	CHK3 error
	4402		Memory write and read test between MPU and MPU
		3101	Abnormal end
		3102	Comparison error
		3104	Transfer time out
		3105	Status NG
		3106	Parameter setting NG
	4403		SMDMA memory write and read test between MPU and MPU
		3201	Abnormal end
		3202	Comparison error
		3204	Transfer time out
		3205	Status NG
		3206	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
HIE	4404		XDMA memory write and read test between MPU and MPU
		3301	Abnormal end
		3302	Comparison error
		3304	Transfer time out
		3305	Status NG
		3306	Parameter setting NG
		3301	Abnormal end
	4405		MFDMA memory write and read test between MPU and MPU
		3401	Abnormal end
		3402	Expected value Error
		3404	Transfer time out
		3405	Status NG
		3406	Parameter setting NG
ISW	5000		ISW test
		2001	Abnormal end
		2002	Another X path error
		2003	ISW status error
FIVE-FX	1011		FCDG3D parameter check 2
		1030	CHK1 error
		1070	Parameter error
		1000	Invalid HTP bit
	1012		FCDG3 parameter check 2
		2030	CHK1 error
		2070	Parameter error
		2000	Invalid HTP bit
	1013		FCDG3_LS parameter check 2
		0030	CHK1 error
		0070	Parameter error
		2000	Invalid HTP bit

LSI type	Test ID	Error Code	Error Contents
FIVE-FX	1800		FCDG3D_LS link check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1801		FCDG3D_LS PCI memory space test 0
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1802		FCDG3D_LS PCI memory space test 1
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1803		FCDG3D_LS PCI memory space test 2
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1804		FCDG3D_LS PCI memory space test 3
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1805		FCDG3D_LS PCI memory space test 4
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
FIVE-FX	1806		FCDG3D_LS PCI scan space test 0
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1807		FCDG3D_LS PCI scan space test 1
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1809		FCDG3D_LS PCI direct access test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	180A		FCDG3D_LS PCI indirect access test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	180F		FCDG3D_LS parameter check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1810		FCDG3D link check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG

LSI type	Test ID	Error Code	Error Contents
FIVE-FX	1811		FCDG3D RAM access test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	181F		FCDG3D parameter check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1820		FCDG3 link check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1821		FCDG3 function test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1822		FCDG3 MSREQ test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG
	1822		FCDG3 burst test
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG

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LSI type	Test ID	Error Code	Error Contents
FIVE-FX	182F		FCDG3 parameter check
		0001	Abnormal end
		0002	Expected value Error
		0003	Status NG
		0004	Time Out Error occurred
		00FF	Parameter setting NG