

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'07': Uncorrectable error when MP reads/writes LM data							
9	RCHK::ERADR							
	←-----Address from L bus-----→							
10	RCHK::ERADR							
	←-----Address from L bus-----→							
11	RCHK::ERADR							
	←-----Address from L bus-----→							
12	RCHK::ERADR							
	←-----Address from L bus-----→						(Not used)	(Not used)
13	RCHK::ERORD							
	←-----Data before correction -- -- -- -- --→							
14	RCHK::ERORD							
	←-----Data before correction -- -- -- -- --→							
15	RCHK::ERORD							
	←-----Data before correction -- -- -- -- --→							
16	RCHK::ERORD							
	←-----Data before correction -- -- -- -- --→							
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'08': Write protection violated when MP accesses to LM							
9	RCHK::ERADR							
	←-----Address from L bus-----→							
10	RCHK::ERADR							
	←-----Address from L bus-----→							
11	RCHK::ERADR							
	←-----Address from L bus-----→							
12	RCHK::ERADR							
	←-----Address from L bus-----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'09': Out of mounting area accessed when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERST0							
	CMP ERR	(Not used)	READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0 Bank 1								
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
Bank 0 Bank 1								
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'0A': Write protection violated when LANC accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'0B': Out of mounting area accessed when LANC accesses to LM							
9	RCHK::ERADR							
	-----Address from L bus----->							
10	RCHK::ERADR							
	-----Address from L bus----->							
11	RCHK::ERADR							
	-----Address from L bus----->							
12	RCHK::ERADR							
	-----Address from L bus----->						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTSO							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0 Bank 1								
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
Bank 0 Bank 1								
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

0	1	2	3	4	5	6	7
Format (x'8')				Message (x'E')			
Subcode: x'0C': MIC ready timeout							
9	RCHK::IFTMRCTL						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
10	RCHK::IFTMRCTL						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
11	RCHK::IFTMRCTL						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
12	RCHK::IFTMRCTL						
	Update control information for READY timeout detection timer						
13	Not used						
	0	0	0	0	0	0	0
14	RCHK::TOSTS						
	MIC READYI timeout	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTS0						
	Inconsist- ency in i960 writing operation	(Not used)	MIC READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan error	(Not used)
16	RCHK::SCANNER						
	Scanning → Uncorrectable error detected in accessing to LM	(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
17	RCHK::CHK1ASTS						
	i960 → Uncorrectable error detected in accessing to LM	i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
18	Module ID						
19	Routine ID						
20	Failed Processor No.				Message code (x'0')		
21	SSID (low order)						
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)						
23	Symptom code (low order) (x'8E')						

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'10': Uncorrectable error when MP reads/writes LM data							
9	RCHK::ERADR							
	←-----Address from L bus-----→							
10	RCHK::ERADR							
	←-----Address from L bus-----→							
11	RCHK::ERADR							
	←-----Address from L bus-----→							
12	RCHK::ERADR							
	←-----Address from L bus-----→						(Not used)	(Not used)
13	RCHK::ERORD							
	←-----Data before correction-----→							
14	RCHK::ERORD							
	←-----Data before correction-----→							
15	RCHK::ERORD							
	←-----Data before correction-----→							
16	RCHK::ERORD							
	←-----Data before correction-----→							
17	RCHK::ERORC							
	←-----Check bit before correction-----→							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'11': Write protection violated when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87L/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'12': Out of mounting area accessed when MP accesses to LM							
9	RCHK::ERADR							
	-----Address from L bus----->							
10	RCHK::ERADR							
	-----Address from L bus----->							
11	RCHK::ERADR							
	-----Address from L bus----->							
12	RCHK::ERADR							
	-----Address from L bus----->						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTS0							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
	Bank 0	Bank 1						
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU871/x'EF' : LDEV type = DKU861)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1/WCHK1A)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'13': Write protection violated when LANC accesses to LM							
9	RCHK::ERADR							
	←-----Address from L bus-----→							
10	RCHK::ERADR							
	←-----Address from L bus-----→							
11	RCHK::ERADR							
	←-----Address from L bus-----→							
12	RCHK::ERADR							
	←-----Address from L bus-----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'14': Out of mounting area accessed when LANC accesses to LM							
9	RCHK::ERADR							
	----- Address from L bus -----							
10	RCHK::ERADR							
	----- Address from L bus -----							
11	RCHK::ERADR							
	----- Address from L bus -----							
12	RCHK::ERADR							
	----- Address from L bus ----- (Not used) (Not used)							
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (higher order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERST0							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0 Bank 1								
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
Bank 0 Bank 1								
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU871/x'EF' : LDEV type = DKU861)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'15': MIC ready timeout							
9	RCHK::IFTMRCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
10	RCHK::IFTMRCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
11	RCHK::IFTMRCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
12	RCHK::IFTMRCTL							
	Update control information for READY timeout detection timer							
13	Not used							
	0	0	0	0	0	0	0	0
14	RCHK::TOSTS							
	MIC READYI timeout	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTSO							
	Inconsist- ency in i960 writing operation	(Not used)	MIC READYI timeout	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
	Bank 0	Bank 1						
17	RCHK::ERCRC							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'21': RCHA CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	CHA::MPCHK1B							
	XR address 2 parity error	XR address 3 parity error	DBUS write data parity error 0	DBUS write data parity error 1	BBUS read data parity error 0	BBUS read data parity error 1	XR address 2 decode error	XR address 3 decode error
12	CHA::MPCHK1B							
	CHK clock failure	Micro access error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'22': SMP CHK 1B: XR bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	MIC::CHK1B							
	Address byte 0 parity error	Address byte 1 parity error	Address byte 2 parity error	Address byte 3 parity error	Data byte 0 parity error	Data byte 1 parity error	Data byte 2 parity error	Data byte 3 parity error
12	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERS							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUUSSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87L/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'23': ADP CHK1B (XR bus parity error)							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK CHK1B
11	EADP::CCK10							
	XRAPER3	BEPER	DBUSPER3	BBUSPER3	XRADDECER	(RSV)	(RSV)	(RSV)
12	EADP::CCK11							
	i960 I/F address parity error	i960 I/F WT DT(H) parity error	i960 I/F WT DT(L) parity error	i960 I/F RD DT(H) parity error	i960 I/F RD DT(L) parity error	(RSV)	(RSV)	(RSV)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPER							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'24': BSA0 CHK1B: MP access error/bus mode parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	BSA0::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
12	BSA1::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'25': BSA1 CHK1B: MP access error/bus mode parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	BSA0::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
12	BSA1::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'26': MIC CHK1B: internal counter timer error/LAN hold timeout/XR wait timeout/XR bus access error/L bus data parity error/arbiter data parity							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
12	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
13	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
14	MIC::ABTERR							
	SQST08	SQST09	SQST10	(Not used)	(Not used)	Transfer start timeout error	LIVE INS delay timer parity error	Transfer start timeout timer parity error
15	MIC::XADRSPER							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XR address parity error (24-27)	XR address parity error (30-37)
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'x')				Message (x'E')			
8	Subcode: x'27': RCHK CHK1B: MP LM data error/XR register R/W address error/LANC access error/refresh address error/L bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	RCHK::CHK1BSTS							
	i960 → Correctable error is over the specified value in accessing to LM		i960 → Invalid address in reading from XR	i960 → Invalid address in writing to XR	(Not used)	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1						
12	RCHK::CHK1BSTS							
	LANC → Correctable error is over the specified value in accessing to LM		LANC → Uncorrectable error detected in accessing to LM		LANC → XR invalid address	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1	Bank 0	Bank 1				
13	RCHK::CHK1BSTS							
	Refresh address register parity error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
15	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'31': i960 LM correctable error 0 (address = 400-3FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'32': i960 LM correctable error 1 (address = 40000-7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'33': LANC LM correctable error 0 (address = 400-3FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'34': LANC LM correctable error 1 (address = 40000-7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	ERADR36	ERADR37
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'35': LANC LM uncorrectable error 0 (address = 4000-3FFFF)							
9	RCHK::ERADR							
	ERADT00	ERADT01	ERADT02	ERADT03	ERADT04	ERADT05	ERADT06	ERADT07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADT10	ERADT11	ERADT12	ERADT13	ERADT14	ERADT15	ERADT16	ERADT17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADT20	ERADT21	ERADT22	ERADT23	ERADT24	ERADT25	ERADT26	ERADT27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADT30	ERADT31	ERADT32	ERADT33	ERADT34	ERADT35	ERADT36	ERADT37
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'36': LANC LM uncorrectable error 1 (address = 40000-7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'37': i960 XR read address error							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADPER							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'38': i960 XR write address error							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'39': LANC XR access error							
9	RCHK::ERADR							
	ERADR00 (Address from L bus)	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
10	RCHK::ERADR							
	ERADR10 (Address from L bus)	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
11	RCHK::ERADR							
	ERADR20 (Address from L bus)	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
12	RCHK::ERADR							
	ERADR30 (Address from L bus)	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU871/x'EF' : LDEV type = DKU861)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'41': DRR CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
12	DDR::STATUS							
	CHK2 typical error	DDR data transfer CHK2	DDR control CHK2	BSA0 CHK2 (A/C/D)	BSA1 CHK2 (A/C/D)	CHK1B	Transfer forcibly terminated	Pseudo- failure occurred
13	DDR::ESTA0							
	Unused register write error	Read- dedicated register write error	A register write error in execution of side-A	B register write error in execution of side-B	Address parity error	Byte access error	Input data parity error	Output data parity error
14	DDR::CK1BSTS0							
	Address decode error	Clock phase error	(RSV)	(RSV)	(RSV)	(RSV)	BSA0 CHK1 error	BSA1 CHK1 error
15	BSA0::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
16	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
17	DDR::MODE4							
	Micro I/F address parity reversed	Micro I/F input data parity reversed	Micro I/F output data parity reversed	S-BUS grant ID parity reversed	BSA I/F mode signal parity reversed	BSA I/F command ID parity reversed	BSA I/F input data parity reversed	BSA I/F output data parity reversed
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'42': SMP CHK1B (XR bus parity error)							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SMP::CHK1B							
	Address byte 0 parity error	Address byte 1 parity error	Address byte 2 parity error	Address byte 3 parity error	Data byte 0 parity error	Data byte 1 parity error	Data byte 2 parity error	Data byte 3 parity error
12	MIC::TIMERR							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'43': SCA0 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA0::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA0::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA0::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'44': SCA1 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA1::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA1::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'45': SCA2 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA2::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA2::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA2::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA2::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87L/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'46': SCA3 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA3::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA3::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA3::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA3::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'47': MIC CHK1B: internal counter timer error/LAN hold timeout/XR wait timeout/XR bus access error/L bus data parity error/arbitrator data parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
12	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbitrator data parity error	XR address parity error	Local bus data parity error	Data parity error
13	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
14	MIC::ABTERR							
	SQST08	SQST09	SQST10	(Not used)	(Not used)	Transfer start timeout error	LIVE INS delay timer parity error	Transfer start timeout timer parity error
15	MIC::XADSPER							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XR address parity error (24-27)	XR address parity error (30-37)
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSDTPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87L/x'EF' : LDEV type = DKU86L)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'48': RCHK CHK1B: MP LM data error/XR register R/W address error/LANC access error/refresh address error/L bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	RCHK::CHK1BSTS							
	i960 → Correctable error is over the specified value in accessing to LM		i960 → Invalid address in reading from XR	i960 → Invalid address in writing to XR	(Not used)	(Not used)	(Not used)	(Not used)
12	RCHK::CHK1BSTS							
	LANC → Correctable error is over the specified value in accessing to LM		LANC → Uncorrectable error detected in accessing to LM		LANC → XR invalid address	(Not used)	(Not used)	(Not used)
13	RCHK::CHK1BSTS							
	Refresh address register parity error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
15	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSDTPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'51': i960 LM correctable error 0 (address = 400-3FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'52': i960 LM correctable error 1 (address = 40000-7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'53': LANC LM correctable error 0 (address = 400-3FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'54': LANC LM correctable error 1 (address = 40000-7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	ERADR36	ERADR37
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'55': LANC LM uncorrectable error 0 (address = 400-3FFFF)							
9	RCHK::ERADR							
	ERADT00	ERADT01	ERADT02	ERADT03	ERADT04	ERADT05	ERADT06	ERADT07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADT10	ERADT11	ERADT12	ERADT13	ERADT14	ERADT15	ERADT16	ERADT17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADT20	ERADT21	ERADT22	ERADT23	ERADT24	ERADT25	ERADT26	ERADT27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADT30	ERADT31	ERADT32	ERADT33	ERADT34	ERADT35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORC							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'56': LANC LM uncorrectable error 1 (address = 40000- 7FFFF)							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	RCHK::ERORD							
	ERORD00	ERORD01	ERORD02	ERORD03	ERORD04	ERORD05	ERORD06	ERORD07
	(Data before correction)							
14	RCHK::ERORD							
	ERORD10	ERORD11	ERORD12	ERORD13	ERORD14	ERORD15	ERORD16	ERORD17
	(Data before correction)							
15	RCHK::ERORD							
	ERORD20	ERORD21	ERORD22	ERORD23	ERORD24	ERORD25	ERORD26	ERORD27
	(Data before correction)							
16	RCHK::ERORD							
	ERORD30	ERORD31	ERORD32	ERORD33	ERORD34	ERORD35	ERORD36	ERORD37
	(Data before correction)							
17	RCHK::ERORD							
	ERORDC0	ERORDC1	ERORDC2	ERORDC3	ERORDC4	ERORDC5	ERORDC6	ERORDC7
	(Check bit before correction)							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'57': i960 XR read address error							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'58': i960 XR write address error							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'59': LANC XR access error							
9	RCHK::ERADR							
	ERADR00	ERADR01	ERADR02	ERADR03	ERADR04	ERADR05	ERADR06	ERADR07
	(Address from L bus)							
10	RCHK::ERADR							
	ERADR10	ERADR11	ERADR12	ERADR13	ERADR14	ERADR15	ERADR16	ERADR17
	(Address from L bus)							
11	RCHK::ERADR							
	ERADR20	ERADR21	ERADR22	ERADR23	ERADR24	ERADR25	ERADR26	ERADR27
	(Address from L bus)							
12	RCHK::ERADR							
	ERADR30	ERADR31	ERADR32	ERADR33	ERADR34	ERADR35	(RSV)	(RSV)
	(Address from L bus)							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold timeout	Wait timeout	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADPER							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER + RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'60': MP check error							
9	MIC:MPCERRSTS							
	(Not used)	WCHK1 detected at MPCHK	CHK1A detected at MPCHK	parity error at timer	A/B REG inconsistent check error	A/B REG write time- out	A/B REG access sequence error	
10	MIC::AREG							
	←----- Consistent data A register -----→							
11	MIC::AREG							
	←----- Consistent data A register -----→							
12	MIC::AREG							
	←----- Consistent data A register -----→							
13	MIC::AREG							
	←----- Consistent data A register -----→							
14	MIC::BREG							
	←----- Consistent data B register -----→							
15	MIC::BREG							
	←----- Consistent data B register -----→							
16	MIC::BREG							
	←----- Consistent data B register -----→							
17	MIC::BREG							
	←----- Consistent data B register -----→							
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'61': Uncorrectable error when MP reads/writes LM data							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::ERODR							
	←----- Data before correction -----→							
14	RCHK::ERODR							
	←----- Data before correction -----→							
15	RCHK::ERODR							
	←----- Data before correction -----→							
16	RCHK::ERODR							
	←----- Data before correction -----→							
17	MIC::BREG							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'62': Write protection violated when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM		(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'63': Out of mounting area accessed when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTS0							
	CMP ERR	(Not used)	READYI time-out	CHK1A cause detected	CHK1B cause detected	Scan error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0 Bank 1								
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
Bank 0 Bank 1								
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'64': Write protection violated when LANC accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'65': Out of mounting area accessed when LANC accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTS0							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI time-out	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0	Bank 1							
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'66': LM sequence error							
9	MIC::C1WSTS							
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	CHK1A detected in processing CHK1A	micro force WCHK1 detected
10	MIC::LASTIP							
	←----- last IP address -----→							
11	MIC::LASTIP							
	←----- last IP address -----→							
12	MIC::LASTIP							
	←----- last IP address -----→							
13	MIC::LASTIP							
	←----- last IP address -----→							
14	MIC::LAST1ADR							
	←----- last access address -----→							
15	MIC::LAST1ADR							
	←----- last access address -----→							
16	MIC::LAST1ADR							
	←----- last access address -----→							
17	MIC::LAST1ADR							
	←----- last access address -----→							
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'67': LM sequencer parity error							
9	MIC:C1WSTS							
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	CHK1A detected in processing CHK1A	micro force WCHK1 detected
10	MIC::LAST1IP							
	←----- last IP address -----→							
11	MIC::LAST1IP							
	←----- last IP address -----→							
12	MIC::LAST1IP							
	←----- last IP address -----→							
13	MIC::LAST1IP							
	←----- last IP address -----→							
14	MIC::LAST1ADR							
	←----- last access address -----→							
15	MIC::LAST1ADR							
	←----- last access address -----→							
16	MIC::LAST1ADR							
	←----- last access address -----→							
17	MIC::LAST1ADR							
	←----- last access address -----→							
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'68': SRAM CHK1A							
9	MIC:C1WSTS						MIC::SRMMODE	
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	size of SRAM mounting area	
10	MIC::SRMERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHK1A detected at SRAM	CHK1B detected at SRAM	LAN access error detected
11	MIC::SRMERRSTS							
	sequencer state error detected	sequencer parity error detected	Out of mounting area in accessing to SRAM	Write protected area in accessing to SRAM	parity error detected in reading from SRAM (00-07)	parity error detected in reading from SRAM (10-17)	parity error detected in reading from SRAM (20-27)	parity error detected in reading from SRAM (30-37)
12	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
13	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
14	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
15	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→						Write/Read flg in accessing to SRAM	command/ data flg in accessing to SRAM
16	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
17	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK CHK1B
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'69': DRAM CHK1A							
9	MIC::C1WSTS						MIC::SRMMODE	
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	size of SRAM mounting area	
10	MIC::ERADR							
	←----- Address from L bus -----→							
11	MIC::ERADR							
	←----- Address from L bus -----→							
12	MIC::ERADR							
	←----- Address from L bus -----→							
13	MIC::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
14	RCHK::ERSYD							
	←----- Syndrom when LM access error detected -----→							
15	RCHK::ERWR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	L bus W/R signal
16	RCHK::CECNT							
	CECNT00	CECNT01	CECNT02	CECNT03	CECNT04	CECNT05	CECNT06	CECNT07
17	RCHK::CECNT							
	CECNT10	CECNT11	CECNT12	CECNT13	CECNT14	CECNT15	CECNT16	CECNT17
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'71': Uncorrectable error when MP reads/writes LM data							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::ERORD							
	←----- Data before correction -----→							
14	RCHK::ERORD							
	←----- Data before correction -----→							
15	RCHK::ERORD							
	←----- Data before correction -----→							
16	RCHK::ERORD							
	←----- Data before correction -----→							
17	RCHK::ERORC							
	←----- Check bit before correction -----→							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'72': Write protection violated when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960→ Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'73': Out of mounting area accessed when MP accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	High limit address of LM mounting area (higher order)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (lower order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTSO							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI time-out	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANNER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
Bank 0	Bank 1							
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
Bank 0	Bank 1							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'74': Write protection violated when LANC accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMWRADRU							
	(Not used)	High limit address of writable LM mounting area (higher order)						
14	RCHK::LMWRADRU							
	High limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
15	RCHK::LMWRADRL							
	(Not used)	Low limit address of writable LM mounting area (higher order)						
16	RCHK::LMWRADRL							
	Low limit address of writable LM mounting area (lower order)						(Not used)	(Not used)
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960→ Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'75': Out of mounting area accessed when LANC accesses to LM							
9	RCHK::ERADR							
	←----- Address from L bus -----→							
10	RCHK::ERADR							
	←----- Address from L bus -----→							
11	RCHK::ERADR							
	←----- Address from L bus -----→							
12	RCHK::ERADR							
	←----- Address from L bus -----→						(Not used)	(Not used)
13	RCHK::LMADRLMT							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	RCHK::LMADRLMT							
	High limit address of LM mounting area (higher order)				(Not used)	(Not used)	(Not used)	(Not used)
15	RCHK::ERSTS0							
	Inconsistency in i960 writing operation	(Not used)	MIC READYI time-out	CHK1A cause detected	CHK1B cause detected	Scan LM/XR access error	(Not used)	(Not used)
16	RCHK::SCANER							
	Scanning → Uncorrectable error detected in accessing to LM		(Not used)	Scanning → Out of mounting area in accessing to LM	Scanning → Invalid address in reading from XR	Scanning → Invalid address in writing to XR	(Not used)	(Not used)
	Bank 0	Bank 1						
17	RCHK::CHK1ASTS							
	i960 → Uncorrectable error detected in accessing to LM		i960 → Write protected area in accessing to LM	i960 → Out of mounting area in accessing to LM	(Not used)	(Not used)	LANC → Write protected area in accessing to LM	LANC → Out of mounting area in accessing to LM
	Bank 0	Bank 1						
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'76': LM sequence error							
9	MIC:C1WSTS							
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	CHK1A detected in processing CHK1A	micro force WCHK1 detected
10	MIC::LASTIP							
	←----- last IP address -----→							
11	MIC::LASTIP							
	←----- last IP address -----→							
12	MIC::LASTIP							
	←----- last IP address -----→							
13	MIC::LASTIP							
	←----- last IP address -----→							
14	MIC::LAST1ADR							
	←----- last access address -----→							
15	MIC::LAST1ADR							
	←----- last access address -----→							
16	MIC::LAST1ADR							
	←----- last access address -----→							
17	MIC::LAST1ADR							
	←----- last access address -----→							
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'77': LM sequencer parity error							
9	MIC::C1WSTS							
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	CHK1A detected in processing CHK1A	micro force WCHK1 detected
10	MIC::LASTIP							
	←----- last IP address -----→							
11	MIC::LASTIP							
	←----- last IP address -----→							
12	MIC::LASTIP							
	←----- last IP address -----→							
13	MIC::LASTIP							
	←----- last IP address -----→							
14	MIC::LAST1ADR							
	←----- last access address -----→							
15	MIC::LAST1ADR							
	←----- last access address -----→							
16	MIC::LAST1ADR							
	←----- last access address -----→							
17	MIC::LAST1ADR							
	←----- last access address -----→							
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'78': SRAM CHK1A							
9	MIC:C1WSTS						MIC::SRMMODE	
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	size of SRAM mounting area	
10	MIC::SRMERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHK1A detected at SRAM	CHK1B detected at SRAM	LAN access error detected
11	MIC::SRMERRSTS							
	sequencer state error detected	sequencer parity error detected	Out of mounting area in accessing to SRAM	Write protected area in accessing to SRAM	parity error detected in reading from SRAM (00-07)	parity error detected in reading from SRAM (10-17)	parity error detected in reading from SRAM (20-27)	parity error detected in reading from SRAM (30-37)
12	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
13	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
14	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
15	RCHK::ERADR							
	←----- Error address in accessing to SRAM -----→						Write/Read flg in accessing to SRAM	command/ data flg in accessing to SRAM
16	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
17	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK CHK1B
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHK1A/WCHK1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'79': SRAM CHK1A							
9	MIC::C1WSTS						MIC::SRMMODE	
	other CHK1A detected	other WCHK1 detected	CHK1A detected at MPCHK	CHK1A detected at SRAM	CHK1A detected at RCHK	WCHK1 detected at MPCHK	size of SRAM mounting area	
10	MIC::SRMERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHK1A detected at SRAM	CHK1B detected at SRAM	LAN access error detected
11	MIC::SRMERRSTS							
	sequencer state error detected	sequencer parity error detected	Out of mounting area in accessing to SRAM	Write protected area in accessing to SRAM	parity error detected in reading from SRAM (00-07)	parity error detected in reading from SRAM (10-17)	parity error detected in reading from SRAM (20-27)	parity error detected in reading from SRAM (30-37)
12	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
13	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
14	MIC::SRMERRADR							
	←----- Error address in accessing to SRAM -----→							
15	RCHK::ERADR							
	←----- Error address in accessing to SRAM -----→						Write/Read flg in accessing to SRAM	command/ data flg in accessing to SRAM
16	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
17	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK CHK1B
18	Module ID							
19	Routine ID							
20	Failed Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'81': RCHA CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	CHA::MPCHK1B							
	XR address 2 parity error	XR address 3 parity error	DBUS write data parity error 0	DBUS write data parity error 1	BBUS read data parity error 0	BBUS read data parity error 1	XR address 2 decode error	Xr address 3 decode error
12	CHA::MPCHK1B							
	CHK clock failure	Micro access error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'82': SMP CHK 1B: XR bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	MIC::CHK1B							
	Address byte 0 parity error	Address byte 1 parity error	Address byte 2 parity error	Address byte 3 parity error	Data byte 0 parity error	Data byte 1 parity error	Data byte 2 parity error	Data byte 3 parity error
12	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'83': ADP CHK1B: XR bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	EADP::CCK10							
	XRAPER3	BEPER	DBUSPER3	BBUSPER3	XRADDECER	(RSV)	(RSV)	(RSV)
12	EADP::CCK11							
	i960 I/F address parity error	i960 I/F WT DT(H) parity error	i960 I/F WT DT(L) parity error	i960 I/F RD DT(H) parity error	i960 I/F RD DT(L) parity error	(RSV)	(RSV)	(RSV)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPER							
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'84': BSA0 CHK1B: MP access error/bus mode parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	BSA0::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
12	BSA1::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'85': BSA1 CHK1B: MP access error/bus mode parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	BSA0::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
12	BSA1::CK1BST0							
	MP address parity error	MP WR data (H) parity error	MP WR data (L) parity error	MP RD data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'86': MIC CHK1B: internal counter timer error/LAN hold time-out/XR wait time-out/XR bus access error/L bus data parity error/arbiter data parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
12	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
13	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
14	MIC::ABTERR							
	SQST08	SQST09	SQST10	(Not used)	(Not used)	Transfer start time- out error	LIVE INS delay timer parity error	Transfer start time- out timer parity error
15	MIC::XADSPER							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XR address parity error (24-27)	XR address parity error (30-37)
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'87': RCHK CHK1B: MP LM data error/XR register R/W address error/LANC access error/refresh address error/L bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	RCHK::CHK1BSTS							
	i960 → Correctable error is over the specified value in accessing to LM		i960 → Invalid address in reading from XR	i960 → Invalid address in writing to XR	(Not used)	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1						
12	RCHK::CHK1BSTS							
	LANC → Correctable error is over the specified value in accessing to LM		LANC → Uncorrectable error detected in accessing to LM		LANC → XR invalid address	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1	Bank 0	Bank 1				
13	RCHK::CHK1BSTS							
	Refresh address register parity error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	RCHK::ERORD							
	←----- Data before correction -----→							
15	RCHK::ERORD							
	←----- Data before correction -----→							
16	RCHK::ERORD							
	←----- Data before correction -----→							
17	RCHK::ERORD							
	←----- Data before correction -----→							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: CHA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'88': PBA CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/PBA CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 CHK1B (0)	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	Not used							
12	Not used							
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRP							
16	MIC::XBUSDTPR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'91': DRR CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
12	DDR::STATUS							
	CHK2 typical error	DDR data transfer CHK2	DDR control CHK2	BSA0 CHK2 (A/C/D)	BSA1 CHK2 (A/C/D)	CHK1B	Transfer forcibly terminated	Pseudo- failure occurred
13	DDR::STATUS							
	Unused register write error	Read- dedicated register write error	A register write error in execution of side-A	B register write error in execution of side-B	Address parity error	Byte access error	Input data parity error	Output data parity error
14	DDR::CK1BSTS0							
	Address decode error	Clock phase error	(RSV)	(RSV)	(RSV)	(RSV)	BSA0 CHK1 error	BSA1 CHK1 error
15	BSA0::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
16	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
17	DDR::MODE4							
	Micro I/F address parity reversed	Micro I/F input data parity reversed	Micro I/F output data parity reversed	S-BUS grant ID parity reversed	BSA I/F mode signal parity reversed	BSA I/F command ID parity reversed	BSA I/F input data parity reversed	BSA I/F output data parity reversed
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'92': SMP CHK1B: XR bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SMP::CHK1B							
	Address byte 0 parity error	Address byte 1 parity error	Address byte 2 parity error	Address byte 3 parity error	Data byte 0 parity error	Data byte 1 parity error	Data byte 2 parity error	Data byte 3 parity error
12	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
13	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
14	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
15	MIC::XRADRPERR							
16	MIC::XBUSDTPERR							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'93': SCA0 CHK1B: XR bus access error/internal clock error /micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA0::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA0::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA0::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'94': SCA1 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA1::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA1::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA1::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'95': SCA2 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA2::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA2::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA2::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA2::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'96': SCA3 CHK1B: XR bus access error/internal clock error/micro access error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	SCA3::CHK1B							
	XR address (H) parity error	XR address (L) parity error	Write data 0 parity error	Write data 1 parity error	Read data 0 parity error	Read data 1 parity error	Address decode error (H)	Address decode error (L)
12	SCA3::CHK1B							
	8-phase clock error	Invalid address access	BSA0 CHK1	BSA1 CHK1	10-phase clock error	(Not used)	(Not used)	(Not used)
13	BSA3::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
14	BSA3::CK1BSTS0							
	MP address parity error	MP write data H-side parity error	MP write data L-side parity error	MP read data parity error	Higher bus mode parity error	(Not used)	(Not used)	(Not used)
15	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
16	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbiter data parity error	XR address parity error	Local bus data parity error	Data parity error
17	MIC::MBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'97': MIC CHK1B: internal counter timer error/LAN hold time-out/XR wait time-out/XR bus access error/L bus data parity error/arbitrator data parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (SCA#0) CHK1B	XR1 (SCA#1) CHK1B	XR2 (SCA#2) CHK1B	XR3 (SCA#3) CHK1B	XR4 CHK1B	XR5 CHK1B	(Not used)	RCHK1 CHK1B
11	MIC::MICCHK1STS							
	(Not used)	(Not used)	(Not used)	(Not used)	MPU reset counter parity error	Hard timer parity error	Wait timer parity error	Hold timer parity error
12	MIC::MICCHK1STS							
	LAN hold time-out	Wait time- out	XR decoder error (29A)	XR decoder error (3B)	Arbitrator data parity error	XR address parity error	Local bus data parity error	Data parity error
13	MIC::TIMER							
	Timer #0 parity error	Timer #1 parity error	Timer #2 parity error	Timer #3 parity error	Timer #4 parity error	Timer #5 parity error	Timer #6 parity error	(Not used)
14	MIC::ABTERR							
	SQST08	SQST09	SQST10	(Not used)	(Not used)	Transfer start time- out error	LIVE INS delay timer parity error	Transfer start time- out timer parity error
15	MIC::XADRSPER							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XR address parity error (24-27)	XR address parity error (30-37)
16	MIC::XBUSDTPER							
	(Not used)	(Not used)	(Not used)	(Not used)	XR bus data parity error (00-07)	XR bus data parity error (10-17)	XR bus data parity error (20-27)	XR bus data parity error (30-37)
17	MIC::LBUSDTPER+RCHK::CHK1BSTS							
	Local bus data parity error (00-07)	Local bus data parity error (10-17)	Local bus data parity error (20-27)	Local bus data parity error (30-37)				
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8. Message E (processor failure: DKA CHK1B)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode: x'98': RCHK CHK1B: MP LM data error/XR register R/W address error/LANC access error/refresh address error/L bus parity error							
9	MIC::CHK1BSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHA/DRR CHK1B	SMP CHK1B	MIC CHK1B
10	MIC::CHK1BSTS							
	XR0 (EADP) CHK1B	XR1 (BSA0) CHK1B	XR2 (BSA1) CHK1B	XR3 CHK1B (0)	XR4 CHK1B (0)	XR5 CHK1B (0)	(Not used)	RCHK1 CHK1B
11	RCHK::CHK1BSTS							
	i960 → Correctable error is over the specified value in accessing to LM		i960 → Invalid address in reading from XR	i960 → Invalid address in writing to XR	(Not used)	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1						
12	RCHK::CHK1BSTS							
	LANC → Correctable error is over the specified value in accessing to LM		LNAC → Uncorrectable error detected in accessing to LM		LANC → XR invalid address	(Not used)	(Not used)	(Not used)
	Bank 0	Bank 1	Bank 0	Bank 1				
13	RCHK::CHK1BSTS							
	Refresh address register parity error	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
14	RCHK::ERORD							
	←----- Data before correction -----→							
15	RCHK::ERORD							
	←----- Data before correction -----→							
16	RCHK::ERORD							
	←----- Data before correction -----→							
17	RCHK::ERORD							
	←----- Data before correction -----→							
18	Module ID							
19	Routine ID							
20	Processor No.				Message code (x'0')			
21	SSID (low order)							
22	Symptom code (high order) (x'FF' : LDEV type = DKU87I/x'EF' : LDEV type = DKU86I)							
23	Symptom code (low order) (x'8E')							

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	Failure section information							
11								
12	Bus mode information							
13	Shared memory data information							
14								
15								
16	CHK3 status information							
17	Master error information							
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'32': Error procedure aid.

Routine ID: x'51': CHK3 in initializing.

x'52': CHK3 in scanning.

x'53': Unknown CHK3.

x'54': CHK3 threshold exceeded.

x'55': CHK3 in main recovery process.

x'56': CHK3 in slave process.

x'57': Other function failed in CHK3 process.

x'58': Reconstruction failed in CHK3 process.

x'59': Making SSB failed in CHK3 interruption process.

x'5A': Making SSB failed in CHK3 process.

x'5B': Making SSB failed in CHK3 error process.

x'5C': System down in CHK3 process.

x'5D': MP down because reset CHK3 impossibly.

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	Not used							
11	Not used							
12	Not used							
13	Not used							
14	Not used							
15	Not used							
16	Not used							
17	Not used							
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'32': Error procedure aid.

Routine ID: x'81': Unknown CHA CHK2.

x'82': Unknown DKA CHK2.

x'F1': CHK1A threshold exceeded.

x'F4': CHK1B threshold exceeded.

x'F7': None MP.

x'F8': None logical error information when pseudo-WCHK1 detected.

x'F8': CHK1A detected (execute REBOOT).

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	Return value of arbiter process							
11								
12								
13								
14	Not used							
15	Not used							
16	Not used							
17	Not used							
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'32': Error procedure aid.

Routine ID: x'C1': Arbiter process failed in cluster failure.

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	Access test bit map (initial value)							
11								
12								
13								
14	Access test bit map for succeeded section							
15								
16								
17								
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'32': Error procedure aid.

Routine ID: x'C2': No last path in cluster failure.

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	Interruption information							
11								
12								
13								
14	CHK1B status information							
15								
16								
17								
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'32': Error procedure aid.

Routine ID: x'F5': CHK1B in CHK3.

x'F6': No error CHK3.

x'F9': Reset CHK3 impossibly.

Format 8, Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	INITYPER (x'49 4E 49 54 59 50 45 52')							
11								
12								
13								
14								
15								
16								
17								
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

Note 2: Module ID: x'80': CHA/DKA kernel.

Routine ID: x'00': Invalid reset cause.

Format 8. Message E (processor failure: Micro WCHK1) (Note 1)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Subcode (x'F0': Micro WCHK1)							
9	Not used							
10	WCHK1 type code							
11	Reserved							
12	Initialize interface information							
13								
14	Reset type code							
15								
16	1st reset type code							
17	Reset SSB type code							
18	Module ID (Note 2)							
19	Routine ID (Note 2)							
20	Failed Processor No.				Message code (x'0')			
21	SSID of self subsystem (low order)							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note 1: This format is specified by bytes 18 and 19.

(Note 2) Module ID : x'16' : Reset
 Routine ID : x'27' : CHK1B in initializing
 Routine ID : x'28' : Force Reset in initializing
 Routine ID : x'29' : Internal Reset in initializing
 Routine ID : x'2f' : CHK3 in initializing
 Routine ID : x'32' : Logical Inconsistency Reset in initializing
 Routine ID : x'33' : Live Ins in initializing
 Routine ID : x'34' : Power Failure Reset in initializing
 Routine ID : x'37' : Fault Reset in initializing
 Routine ID : x'60' : Internal Reset Loop in Resetting
 Routine ID : x'61' : Logical Inconsistency Reset Loop in Resetting
 Routine ID : x'62' : CHK1B Loop in Resetting
 Routine ID : x'63' : CHK3 Loop in Resetting
 Routine ID : x'64' : CHK1B unrecoverable
 Routine ID : x'65' : Fault Reset Loop in Resetting
 Routine ID : x'66' : CHK2A Reset
 Routine ID : x'8e' : Force WCHK1 (by another MP)

Format 8, Message E (Selective Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	LPN							
9	DCN status							
	Force device busy	Channel connect request	Command chain execute	Path reserve	Stack status	Sense pending	Long Busy (Command process)	Processor connect
10	DCN status							
	Single path mode	Gurant path mode	MCB queuing	(RSV)	Load balance control	(RSV)	(RSV)	(RSV)
11	DCN flag							
	RST NTF/RST ALG SSB PND	Pin Data pending	(RSV)	(RSV)	ODE pending	RSQ pending	SCI/SCE pending	PCH pending
12	LCT initiation cause							
	0: Ready 1: Not ready	0: Disable 1: Enable	0: No SNS 1: 64 SNS pending	0: Mount 1: Not mount	0: No RST ALG LPN 1: Exist	0: No SYS RST LPN 1: Exist	0: No stack 1: Exist	0: No Wait SNS 1: Exist
13	LCT initiation cause							
	0: No Wait-RI 1: Exist	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	0: Others 1: DEV BSY JOB
14	Command interface flag							
	Pending exist	SCE(RSP)	(RSV)	(RSV)	ODE pending	RSG pending	SCI pending	PCH pending
15	Command code							
16	Internal SSB log No.							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'1') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'Selective Reset'.

Format 8, Message E (Wait SENSE Timeout)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Current LPN							
9	LDEV number							
10*	Basic sense byte 0							
	Basic sense byte 0							
11*	Basic sense byte 1							
	Basic sense byte 1							
12*	Basic sense byte 2							
	Exception code (higher order)							
13*	Format/Message							
	Exception code (lower order)							
14*	Module ID							
	Module ID							
15*	Routine ID							
	Routine ID							
16	Internal SSB number for RESET							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'3') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

* Timeout SSB information

Upper case: 24-byte format SSB

Lower case: 32-byte format SSB

Note: Indicates the 'Wait SENSE Timeout'.

Format 8, Message E (CHK1B Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Related failure internal SSB log No. (Error procedure aid)							
9								
10	Not used (x'00')							
11	Not used (x'00')							
12	OLD PSW (interruption source address)							
13								
14								
15								
16	Internal SSB log No. (reset)							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'5') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'CHK1B Reset'.

Format 8, Message E (DKC Internal Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Not used (x'00')							
9	Not used (x'00')							
10	Not used (x'00')							
11	Not used (x'00')							
12	OLD PSW (interruption source address)							
13								
14								
15								
16	Internal SSB log No.							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'6') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'DKC Internal Reset'.

Format 8, Message E (LCP Internal Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	LPN							
9	Not used							
10	Reset cause							
11								
12*	OLD PSW (0)							
	LCP error code (higher order)							
13*	OLD PSW (1)							
	LCP error code (lower order)							
14*	OLD PSW (2)							
	x '00'							
15*	OLD PSW (3)							
	x'00'							
16	Internal SSB No.							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'7') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

* Upper case: Hot line
 Lower case: Int Sel Reset

Note: Indicates the 'LCP Internal Reset'.

Format 8, Message E (Force Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Not used (x'00')							
9	Not used (x'00')							
10	Not used (x'00')							
11	Not used (x'00')							
12	OLD PSW (interruption source address)							
13								
14								
15								
16	Internal SSB log No.							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'8') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'Force Reset'.

Format 8, Message E (Logical inconsistency reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Not used							
9	Not used							
10	Not used							
11	Not used							
12	Not used							
13	Not used							
14	Not used							
15	Not used							
16	Internal SSB log number							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'9') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'Logical inconsistency reset'

Format 8, Message E (Power failure Reset)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'E')			
8	Related failure internal SSB log No. (Error procedure aid)							
9								
10	Not used							
11	Not used							
12	OLD PSW (interruption source address)							
13								
14								
15								
16	Internal SSB log No. (RESET)							
17								
18	Detail log number for RESET							
19								
20	Processor No.				Message code (x'A') (Note)			
21	SSID (lower order) of self subsystem							
22	Symptom code (x'FF8E':LDEV type=DKU87I / x'EF8E':LDEV type=DKU86I)							
23								

Note: Indicates the 'Power failure reset'

Format 8. Message F (Microprogram detected error : Logical inconsistency)

	0	1	2	3	4	5	6	7
7	Format (x'8')				Message (x'F')			
8	Module ID							
9	Routine ID							
10	Error information							
11								
12								
13								
14								
15								
16								
17								
18								
19								
20	Processor No.				Message code (don't care)			
21	SSID (lower order) of self subsystem							
22	Symptom code (high order) (x'FF':LDEV type=DKU87I / x'EF':LDEV type=DKU86I)							
23	Symptom code (low order) (x'8F')							

Module ID (SSB8) List (1/4)

No.	Module ID	Function name	Remarks
1	00	Initialization	
2	01	Power disconnection	
3	02	CUDG	
4	10	Job management	
5	11	Synchronous management	
6	12	Communication control between processors	
7	13	Cache control	
8	14	Resource management	
9	15	Configuration control	
10	16	Reset	
11	17	SVP management	
12	18	Configuration control (RAID provided)	
13	19	Configuration control (drive recovery provided)	
14	30	Standard function	
15	31	SVP-provided function	
16	32	Error procedure aid	
17	40	CHA monitor	
18	41	CHA initiator	
19	42	CHA terminator	
20	43	CHA kernel (common function)	
21	50	Command chain start	
22	51	Command chain termination	
23	52	Command execution status analysis	
24	53	Command execution CHL I/F control	
25	54	Channel server common process 1	
26	55	Channel server common process 2	
27	56	Channel server common process 3	
28	57	Channel server common process 4	
29	58	Channel server data transfer 1	
30	59	Channel server data transfer 2	
31	5A	Read/write common process	

Module ID (SSB8) List (2/4)

No.	Module ID	Function name	Remarks
32	5B	Subsystem command common process	
33	5C	Channel server asynchronous control 1	
34	5D	Channel server asynchronous control 2	
35	5E	Channel server command queuing control	
36	5F	Channel server JOB FRR	
37	60	Read command process 1	
38	61	Read command process 2	
39	62	Read command process 3	
40	63	Read command process 4	
41	64	Read command process 5	
42	65	Read command process 6	
43	66	Write command process 1	
44	67	Write command process 2	
45	68	Write command process 3	
46	69	Write command process 4	
47	6A	Write command process 5	
48	6B	Write command process 6	
49	6D	Control command process 1	
50	6E	Control command process 2	
51	6F	Search command	
52	70	Path control command process	
53	71	Diagnostic command process	
54	72	DIAG command process	
55	73	Subsystem command process 1	
56	74	Subsystem command process 2	
57	75	Subsystem command process 3	
58	78	CHL I/F link level frame control	
59	79	CHL I/F device frame control	
60	7A	CHL I/F common control	
61	7B	LDEV format	

Module ID (SSB8) List (3/4)

No.	Module ID	Function name	Remarks
62	7E	Concurrent copy process	
63	7F	Channel server WDCP/differential (M specified)	
64	80	DKA monitor	
65	81	DKA initiator	
66	82	DKA terminator	
67	90	Staging	
68	91	LDEV destaging	
69	92	PDEV destaging	
70	93	Drive recovery	
71	94	Offline monitor	
72	95	Physical drive maintenance	
73	98	RAID synchronous type	
74	99	RAID LDEV destaging	
75	9A	RAID PDEV destaging	
76	9B	RAID drive recovery	
77	9C	RAID external provided	
78	9D	RAID 0 destaging	
79	9E	RAID 0 Error/merge	
80	9F	RAID faild slot operation	
81	A0	SCSI control	
82	A1	DRR control	
83	A2	SVP command execution	
84	A3	Test activation	
85	A4	SCSI control	
86	A5	Offline monitor	
87	A6	Physical drive mentenance	
88	A7	SCSI control	
89	A8	DRR control	
90	AE	SCSI control	
91	C0	HRC/HODM CHL/SVP I/F	
92	C1	HRC/HODM Remote copy monitor	

Module ID (SSB8) List (4/4)

No.	Module ID	Function name	Remarks
93	C2	HRC/HODM Configuration control	
94	C3	HRC/HODM common copy procedure	
95	C4	HRC Remote copy procedure	
96	C5	HODM Migration copy procedure	
97	C6	HRC/HODM channel server procedure	Note 1
98	C7	HRC/HODM Remote I/O manager	
99	C8	HRC/HODM Remote I/O 1	Note 1
100	C9	HRC/HODM Remote I/O 2	
101	CA	HRC/HODM path control	

Note 1 : No action is required for error codes, C60E, C870, C871 and C872. These error codes are reported only to have the host processor re-drive the failed CCW chain.