

# ***TROUBLE SHOOTING SECTION***

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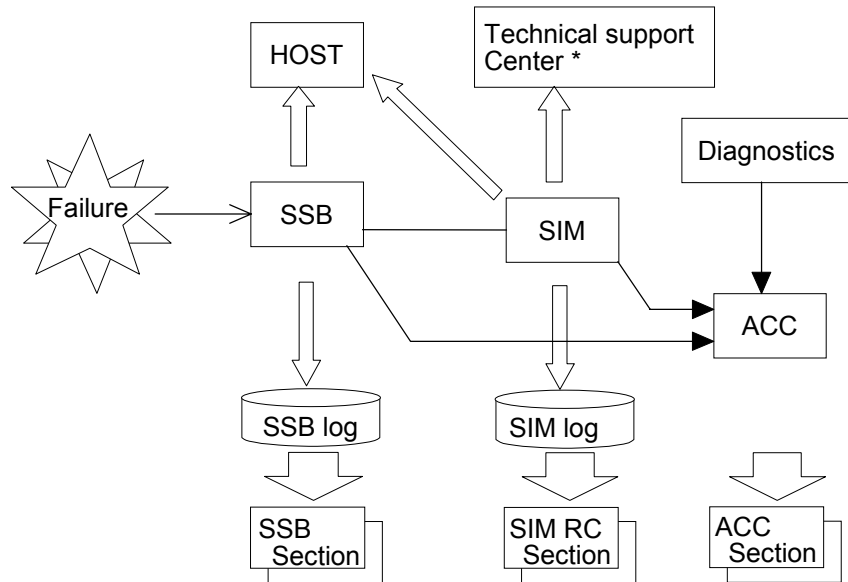
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# 1 Overview of TRBL

The figure below shows the flowchart of creating and reporting the SIM and SSB after the DKC microprogram has detected a failure. It also shows the section of maintenance manual which should be referred to.



Since the subsystem starts its maintenance work based on the SIM and ACC, if a failure occurs check the ACC first and start troubleshooting.

For the failure which does not produce the ACC, isolate the failed part depending on its phenomenon.

\* Technical Support Center: Responsible section of maintenance service and technical support.

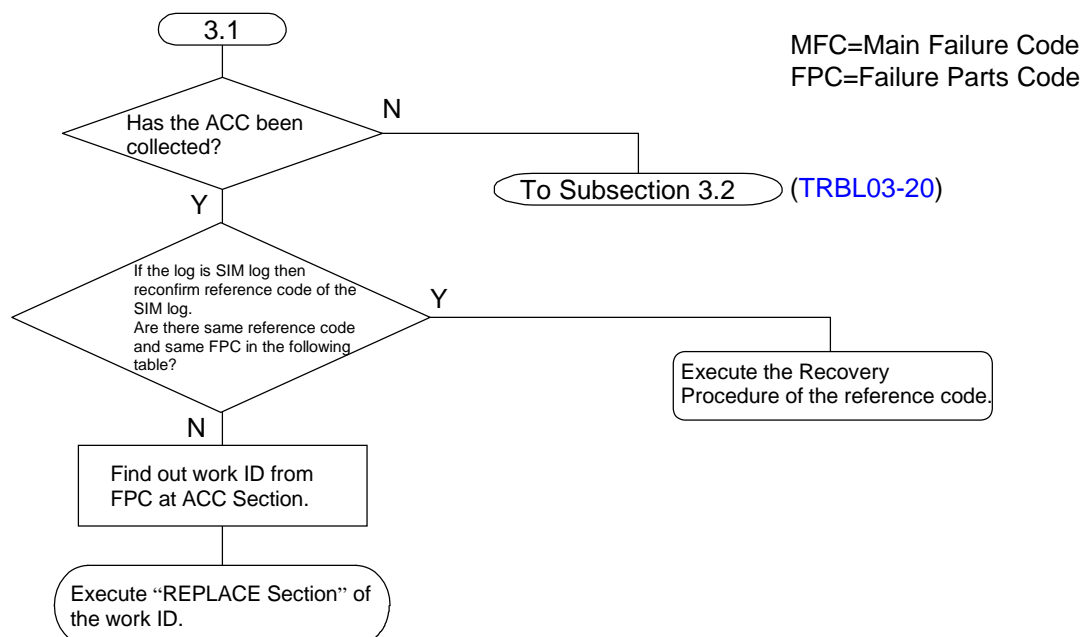
## 2 Point out a Failed Part

[1] The ACC has been collected -----See [TRBL03-10](#)

[2] The ACC has not been collected -----See [TRBL03-20](#)

## 3 Isolating a Failed Part

### 3.1 Analyze the SIM log, SSB log or Remote Diagnostic Data



Reference Code	FPC	Recovery Procedure
DF6YXX, DF7YXX	80000000	5.2 Isolation and Recovery Procedures for Common Fibre Loop Error (TRBL05-20)
DF8YXX, DF9YXX	80000000	
BF2XYY	80000000	5.12 Voltage alarm (TRBL05-340)
BF4XXX	80000000	5.14 PS warning error (TRBL05-510)
BF93XX	80000000	5.21 DKCMNs disagreement error (TRBL05-570)
BFA1XX	80000000	5.13 Environment monitors disagreement error (TRBL05-460)
BFE3A2	80000000	5.25 Duplex SVP Setup failed (TRBL05-630)
CF90XY	80000000	5.11 Recovery Procedure for LDEV Blocking (TRBL05-320)
DF8YXX, DF9YXX	80000000	
EF9YXX	80000000	
DFA0XY	60D0	Collect Dump/Log and T.S.C call
EF50XY		
EF90XX	80000000	5.11 Recovery Procedure for LDEV Blocking (TRBL05-320)
D4XYYY	80000000	6 HRC/HODM/HORC Error Recovery (TRBL06-10)
DBXYYY	80000000	
2180XY	80000000	
2190XY	80000000	10 Recovery procedure of AL_PA conflict (TRBL10-10)
47DXYY	80000000	9 HMRCF & HOMRCF Error Recovery (TRBL09-10)
47FYXX	80000000	11 HIHSM Error Recovery (TRBL11-10)
399AXY, 3D9AXY	80000000	5.22 Recovery Procedure for Warning of SM DISABLE (TRBL05-580)
399DX0, 399EX0, 3D9DX0, 3D9EX0, FFF60X, FFF70X, FF200X, FF210X	80000000	5.23 Recovery Procedure for Injustice DC voltage control and Injustice CE MODE (TRBL05-590)
BF9XXX	80000000	5.1 FSW Access Error/FSW LED Bus Test Error (TRBL05-10)
3DAXXX		
3999XY	80000000	5.4 Error Recovery Procedure during CHA/DKA replacement (TRBL05-100)

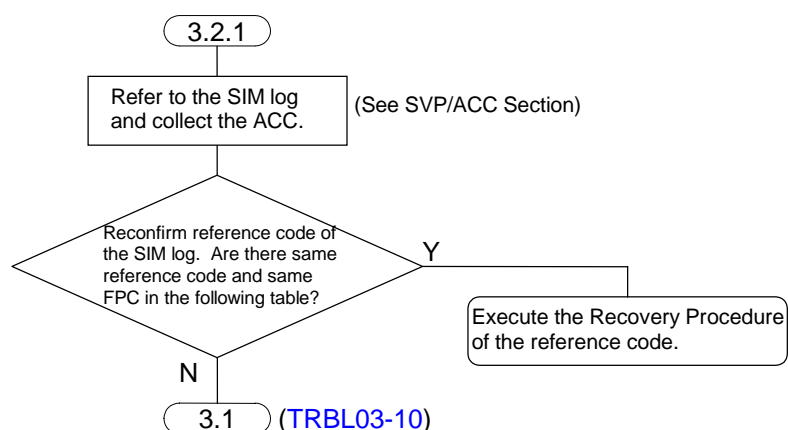
### 3.2 A failure has been reported to the customer but the DKC has not been connected to the Remote Maintenance

Isolate the failed part depending on the following phenomenon.

- [1] The SIM has been reported. ----- [TRBL03-30](#)
- [2] The SSB has been reported. ----- [TRBL03-30](#)
- [3] The OS cannot recover the subsystem error. (MIH, Job ABEND, etc.) ----- [TRBL03-40](#)
- [4] The OS has detected the subsystem error. (ICC, CC=3) ----- [TRBL03-60](#)
- [5] The lamp on the subsystem panel has failed. ----- [TRBL03-70](#)
- [7] A failure has occurred when turning on the power on. ----- [TRBL03-180](#)
- [8] The power cannot be turned off. ----- [TRBL03-190](#)
- [9] The multiple parts have failed. ----- [TRBL03-210](#)

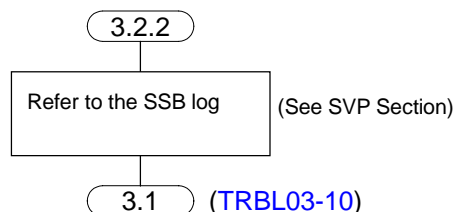


### 3.2.1 SIM has been reported

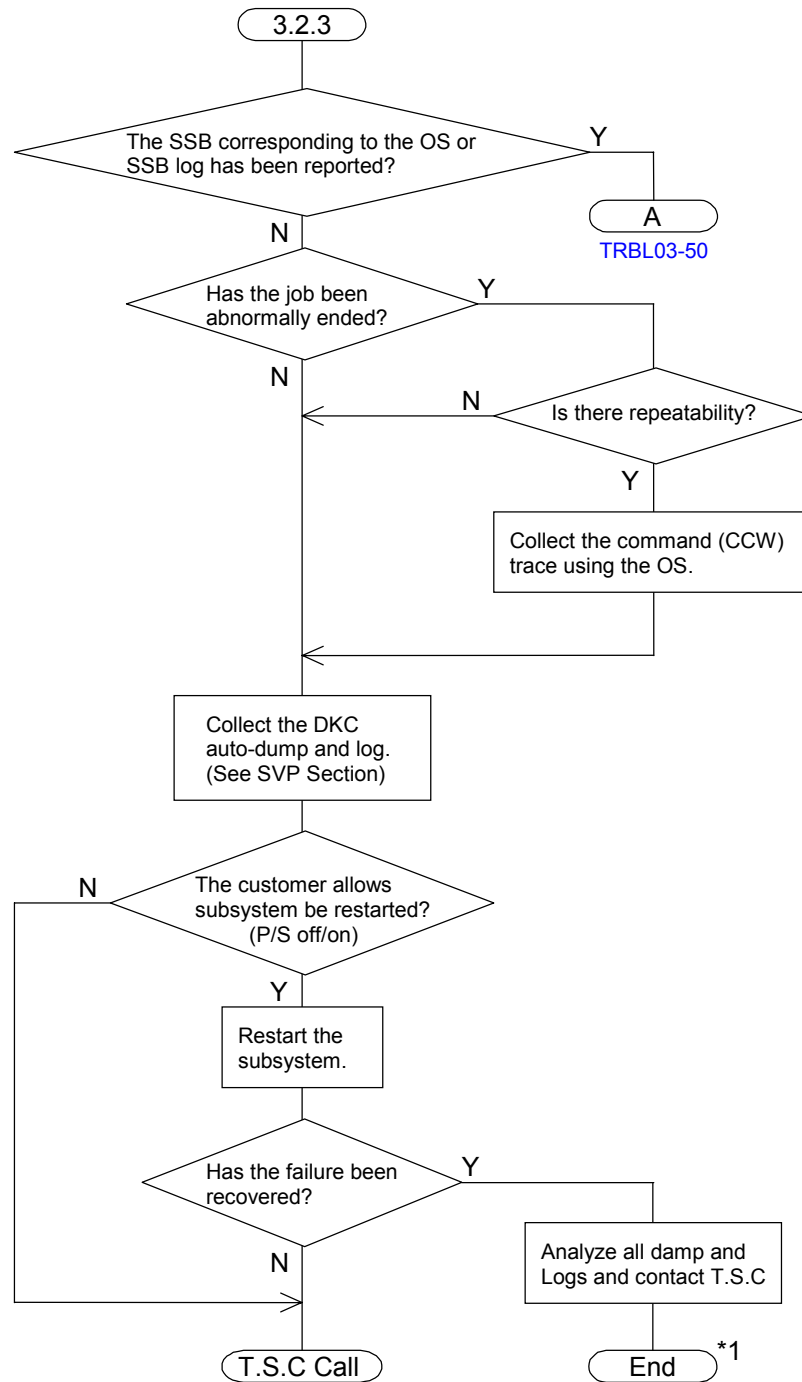


Reference Code	FPC	Recovery Procedure
DF6YXX, DF7YXX	80000000	5.2 Isolation and Recovery Procedures for Common Fibre Loop Error (TRBL05-20)
DF8YXX, DF9YXX	80000000	
BF2XYY	80000000	5.12 Voltage alarm (TRBL05-340)
BF4XXX	80000000	5.14 PS warning error (TRBL05-510)
BF93XX	80000000	5.21 DKCMNs disagreement error (TRBL05-570)
BFA1XX	80000000	5.13 Environment monitors disagreement error (TRBL05-460)
BFE3A2	80000000	5.25 Duplex SVP Setup failed (TRBL05-630)
CF90XY	80000000	
DF8YXX, DF9YXX	80000000	5.11 Recovery Procedure for LDEV Blocking (TRBL05-320)
EF9YXX	80000000	
D4XYYY	80000000	6 HRC/HODM/HORC Error Recovery (TRBL06-10)
DBXYYY	80000000	
2180XY	80000000	
2190XY	80000000	10 Recovery procedure of AL_PA conflict (TRBL10-10)
47DXYY	80000000	9 HMRCF & HOMRCF Error Recovery (TRBL09-10)
47FYXX	80000000	11 HIHSM Error Recovery (TRBL11-10)
399AXY, 3D9AXY	80000000	5.22 Recovery Procedure for Warning of SM DISABLE (TRBL05-580)
399DX0, 399EX0, 3D9DX0, 3D9EX0, FFF60X, FFF70X, FF200X, FF210X	80000000	5.23 Recovery Procedure for Injustice DC voltage control and Injustice CE MODE (TRBL05-590)
BF9XXX	80000000	5.1 FSW Access Error/FSW LED Bus Test Error (TRBL05-10)
3DAXXX		
3999XY	80000000	5.4 Error Recovery Procedure during CHA/DKA replacement (TRBL05-100)

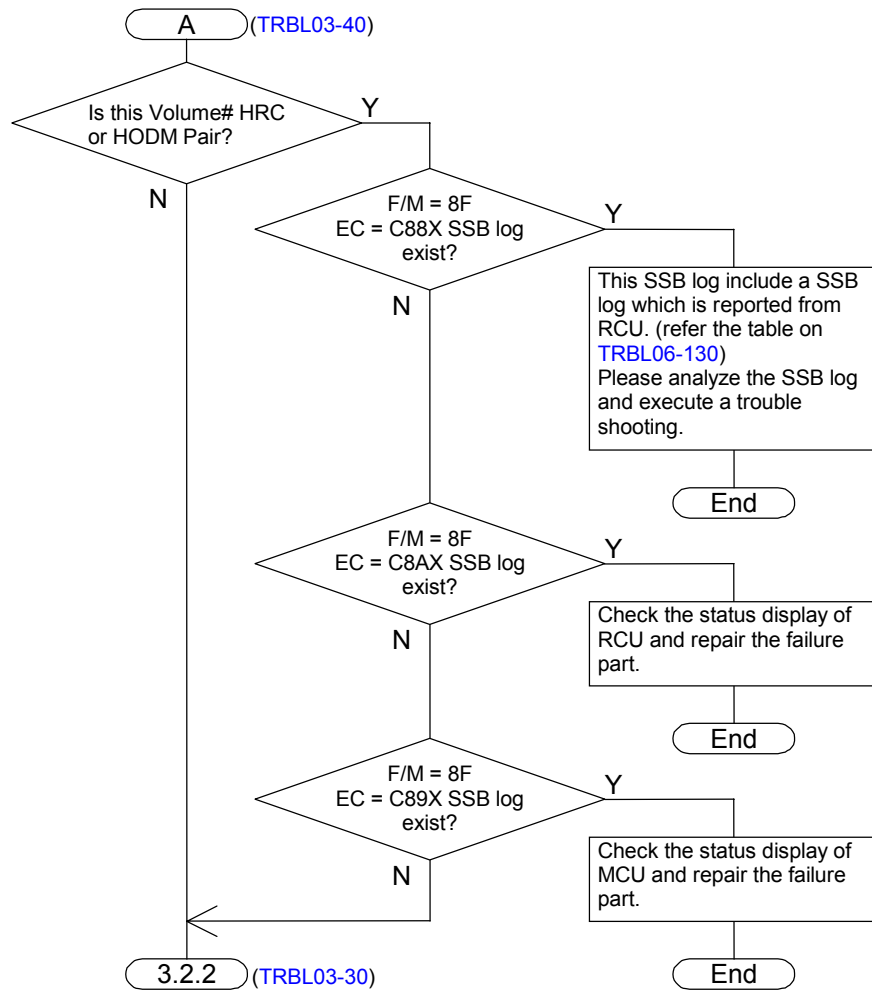
### 3.2.2 SSB has been reported



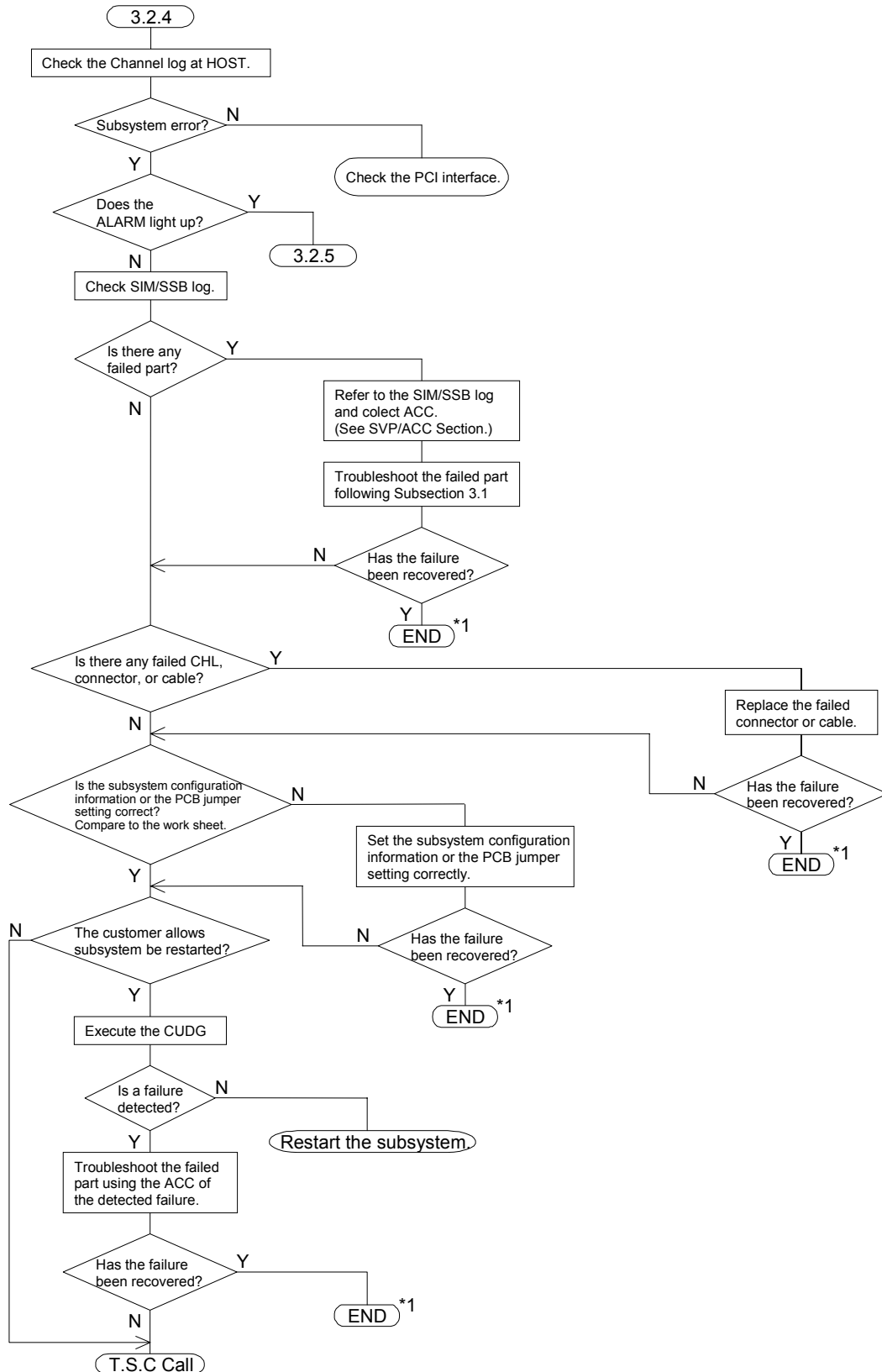
### 3.2.3 OS cannot recover the subsystem error.(MIH, Job ABEND)



\*1 : If you finished the Maintenance, delete the log and SIM complete. (Refer to [SVP02-170, 580](#))

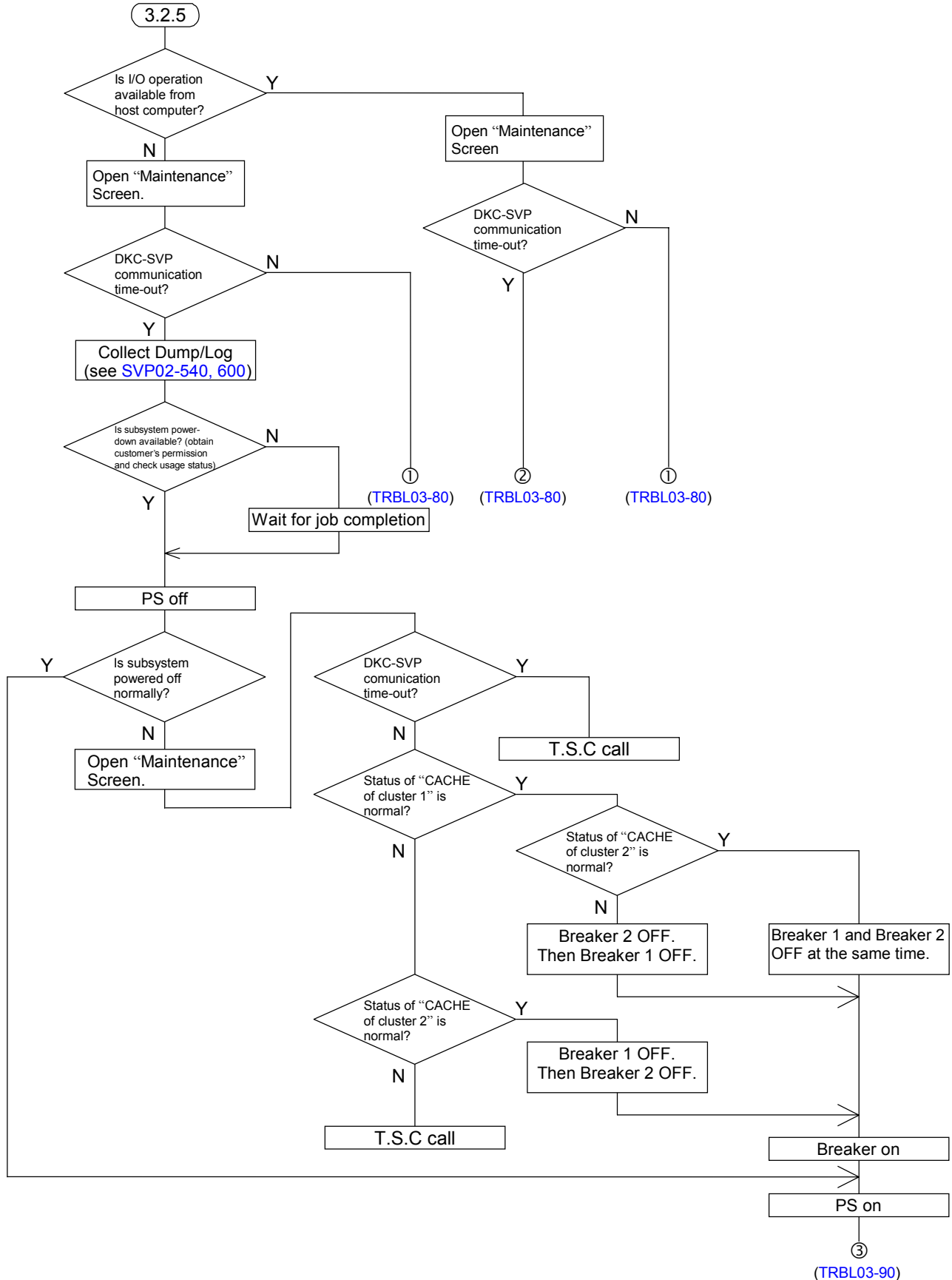


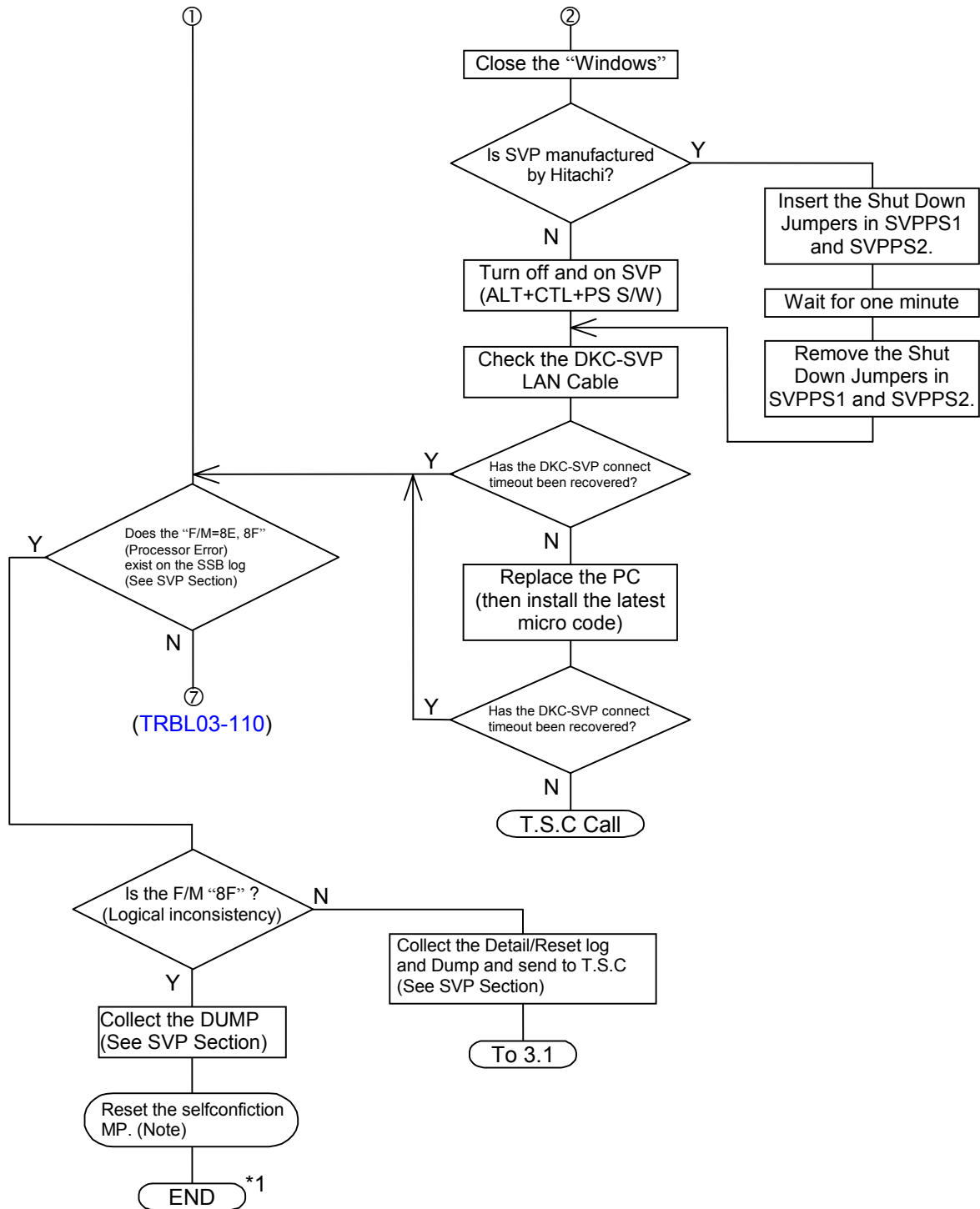
### 3.2.4 OS has detected the subsystem error (ICC, CC=3)



\*1 : If you finished the Maintenance, delete the log and SIM complete. (Refer to [SVP02-170, 580](#))

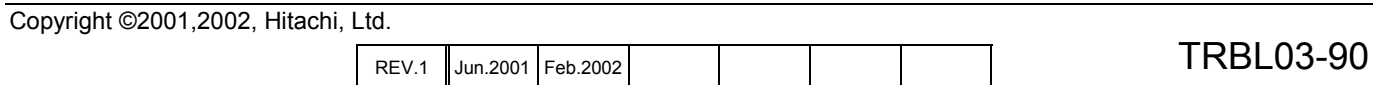
### 3.2.5 The lamp on the subsystem panel has failed

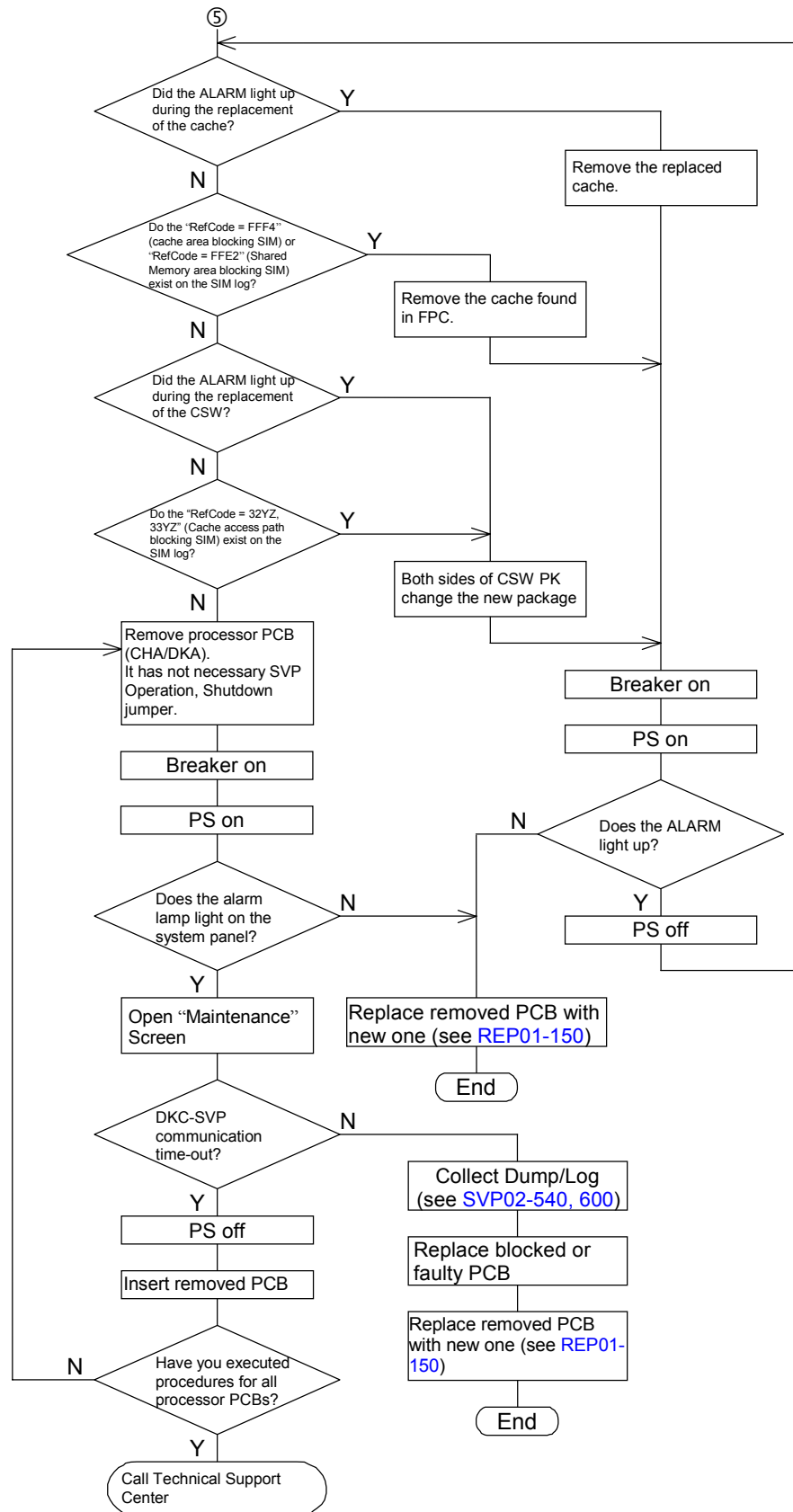




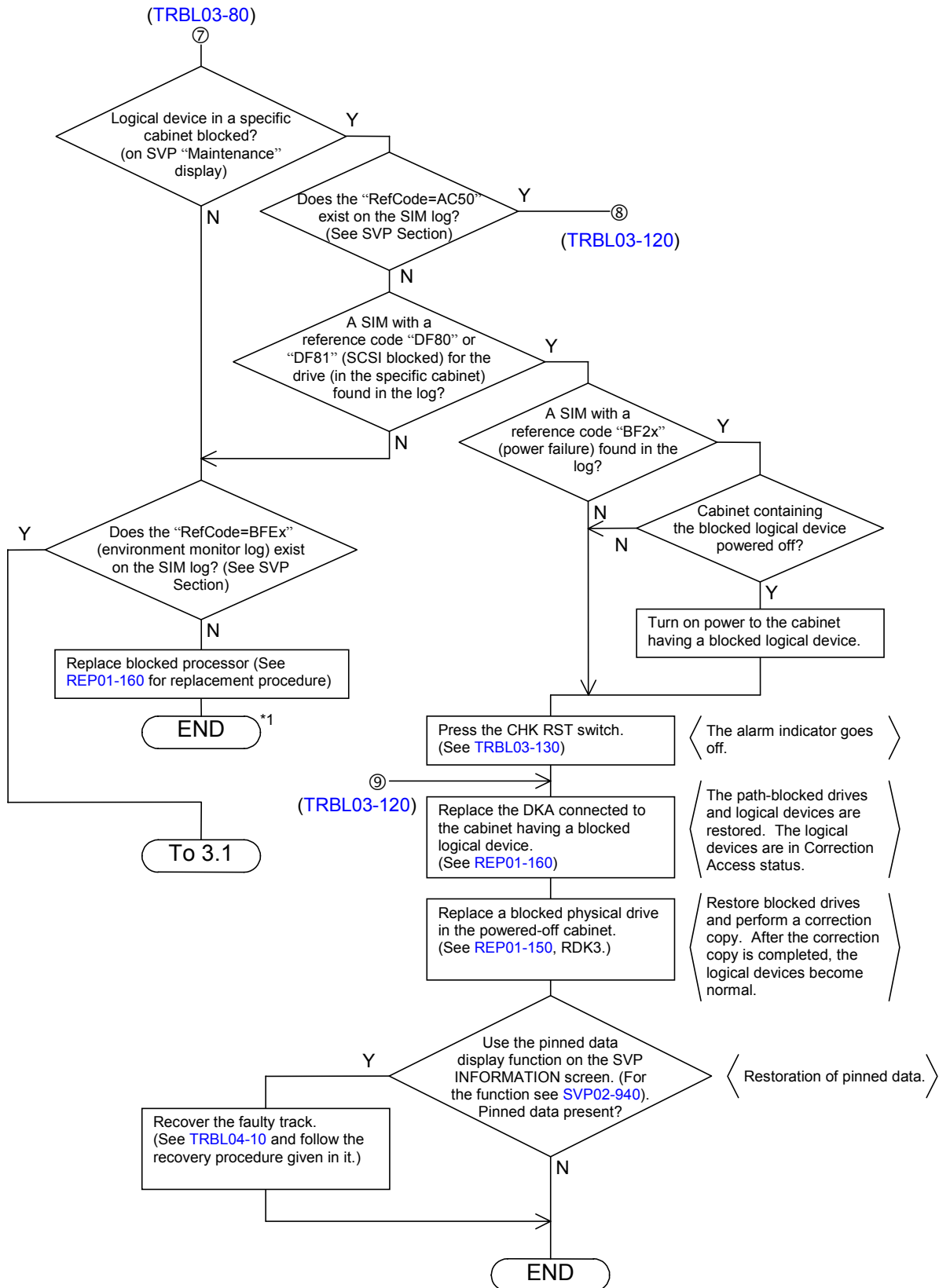
(Note) Replace the failure processor PCB.

\*1 : If you finished the Maintenance, delete the log and SIM complete. (Refer to [SVP02-170, 580](#))

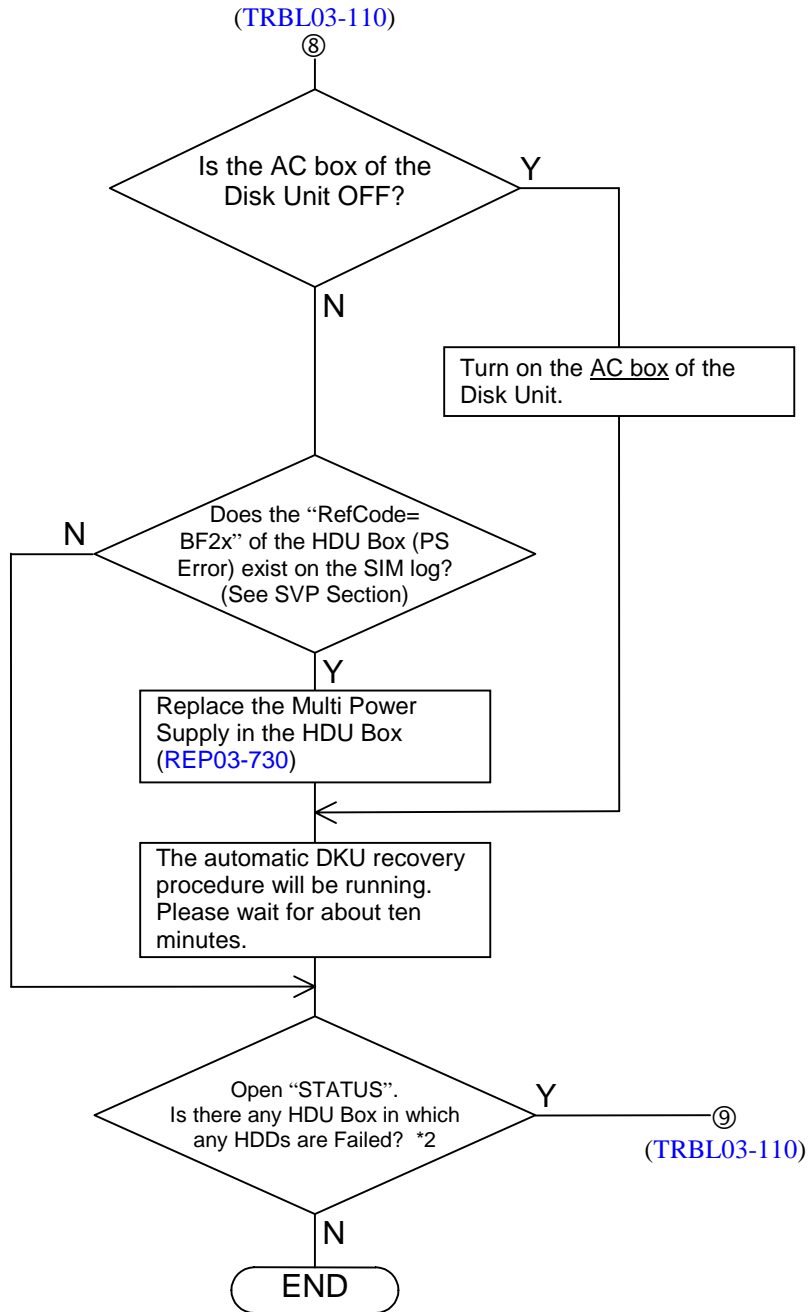




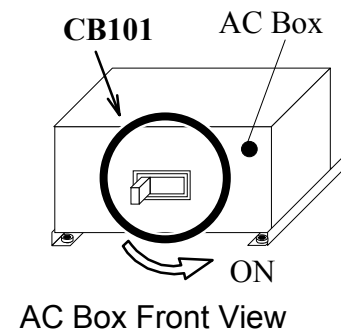
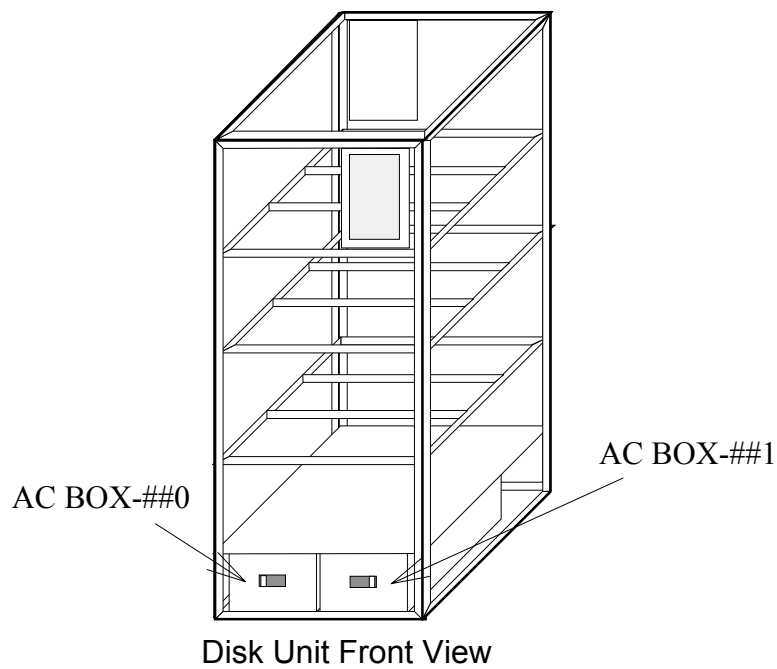
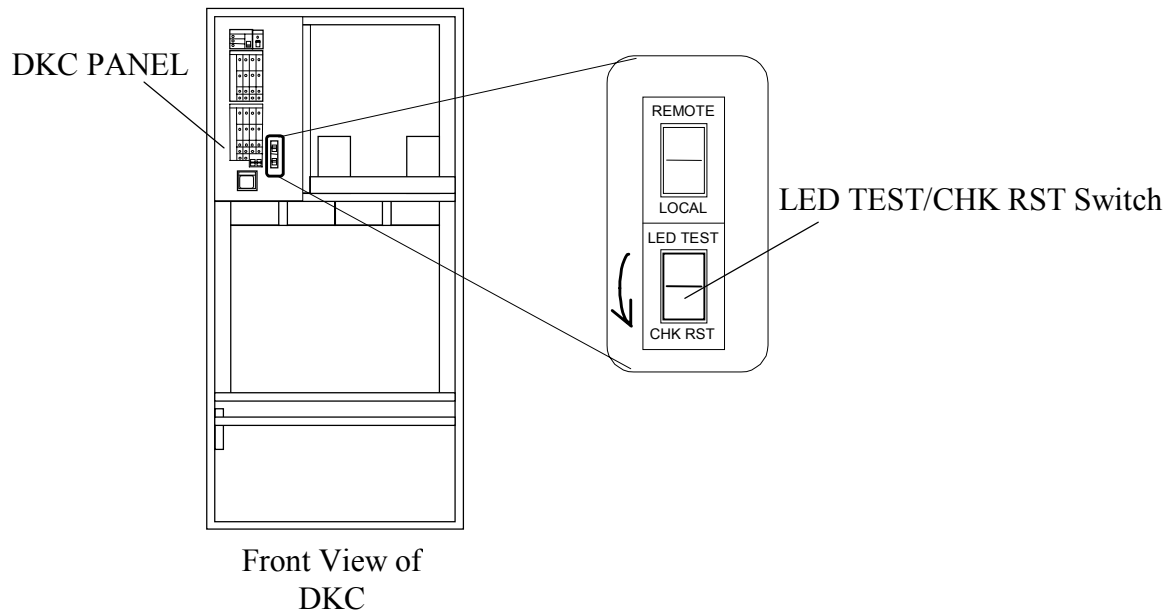




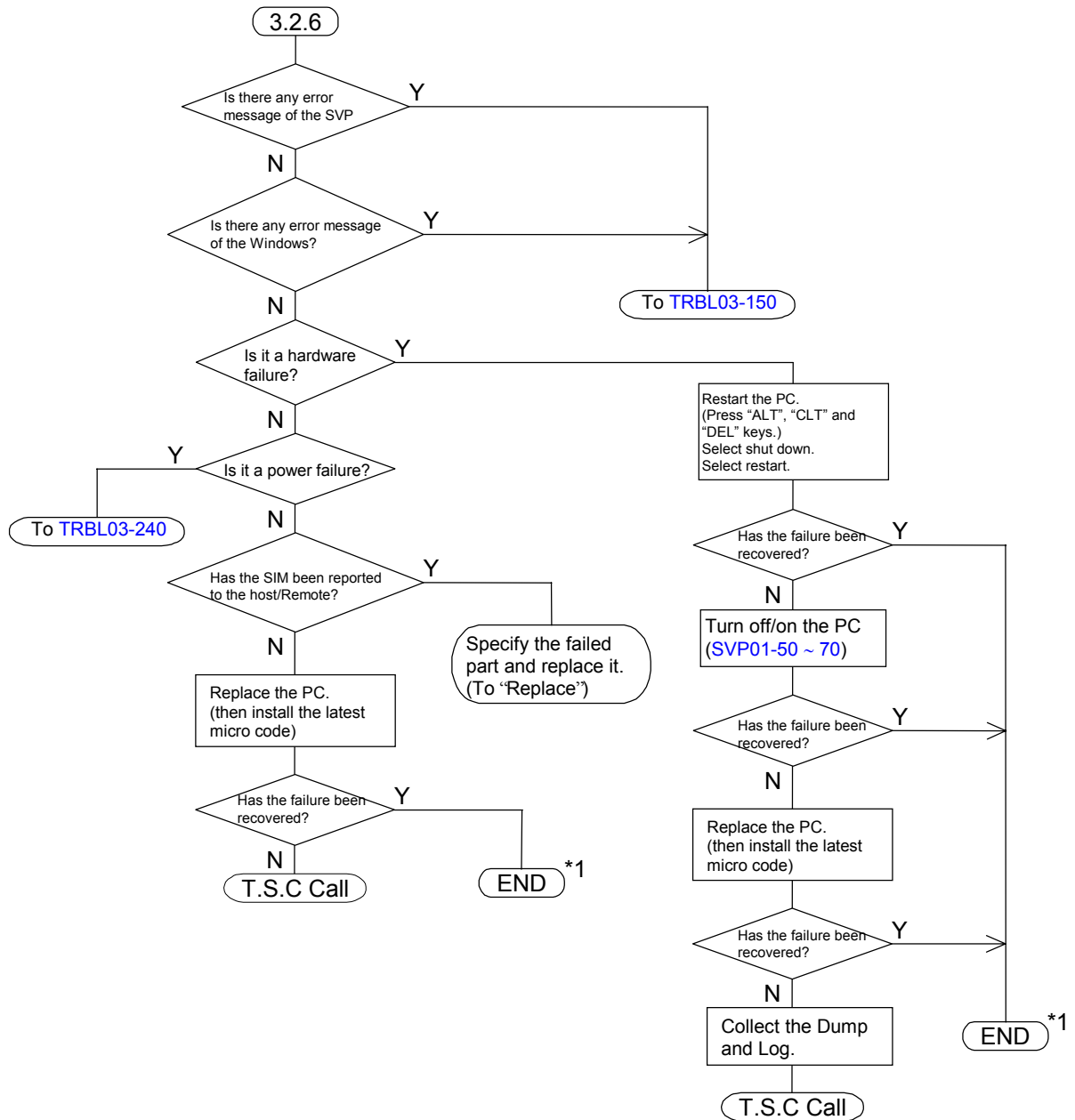
\*1 If you finished the Maintenance, delete the log and SIM complete. (Refer to SVP02-170, 580)



\*2 The automatic DKU recovery procedure is not effective for some cases. ex, very short period power down.



### 3.2.6 PC (SVP) failure recovery procedure

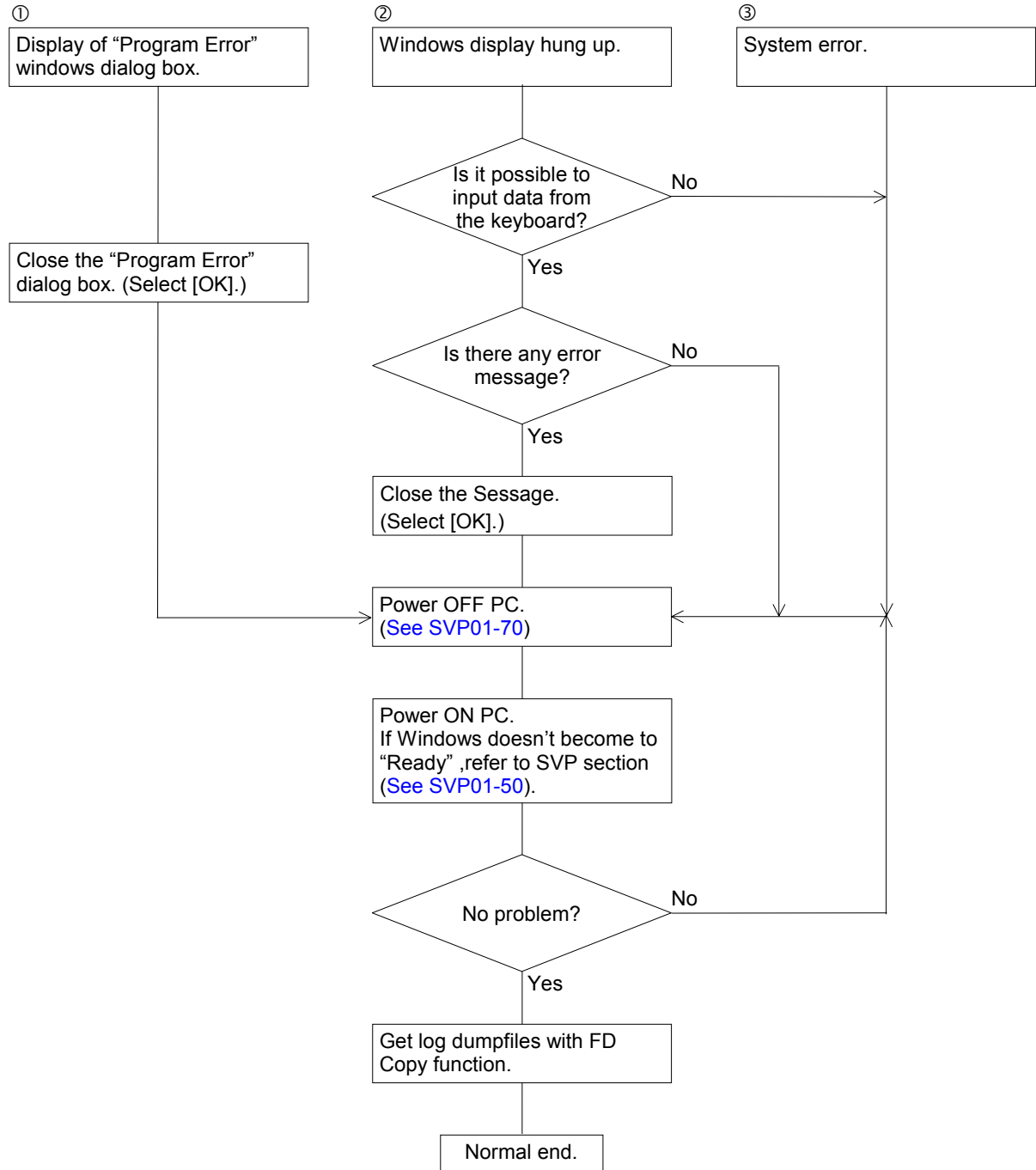


\*1 : If you finished the Maintenance, delete the log and SIM complete. (Refer to [SVP02-170, 580](#))

## (1) Types of SVP failures

- ① Display of “Program Error” windows dialog box.
- ② Windows display hang up.
  - (i) Keyboard operation possible.
  - (ii) Keyboard operation not possible.
- ③ Display of non windows error. (System error)

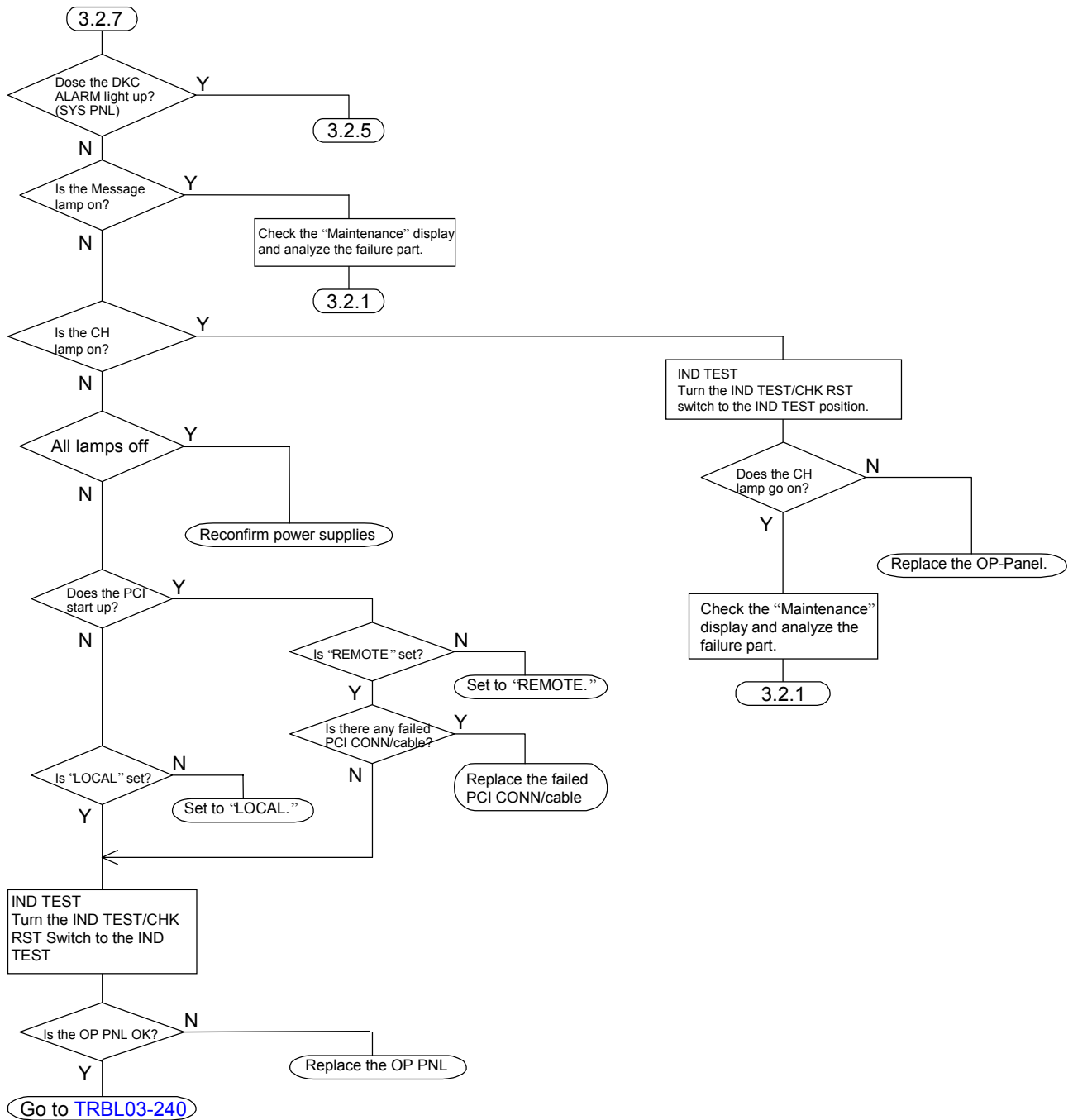
## (2) Recovery procedure based on type of failure.



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Blank Sheet

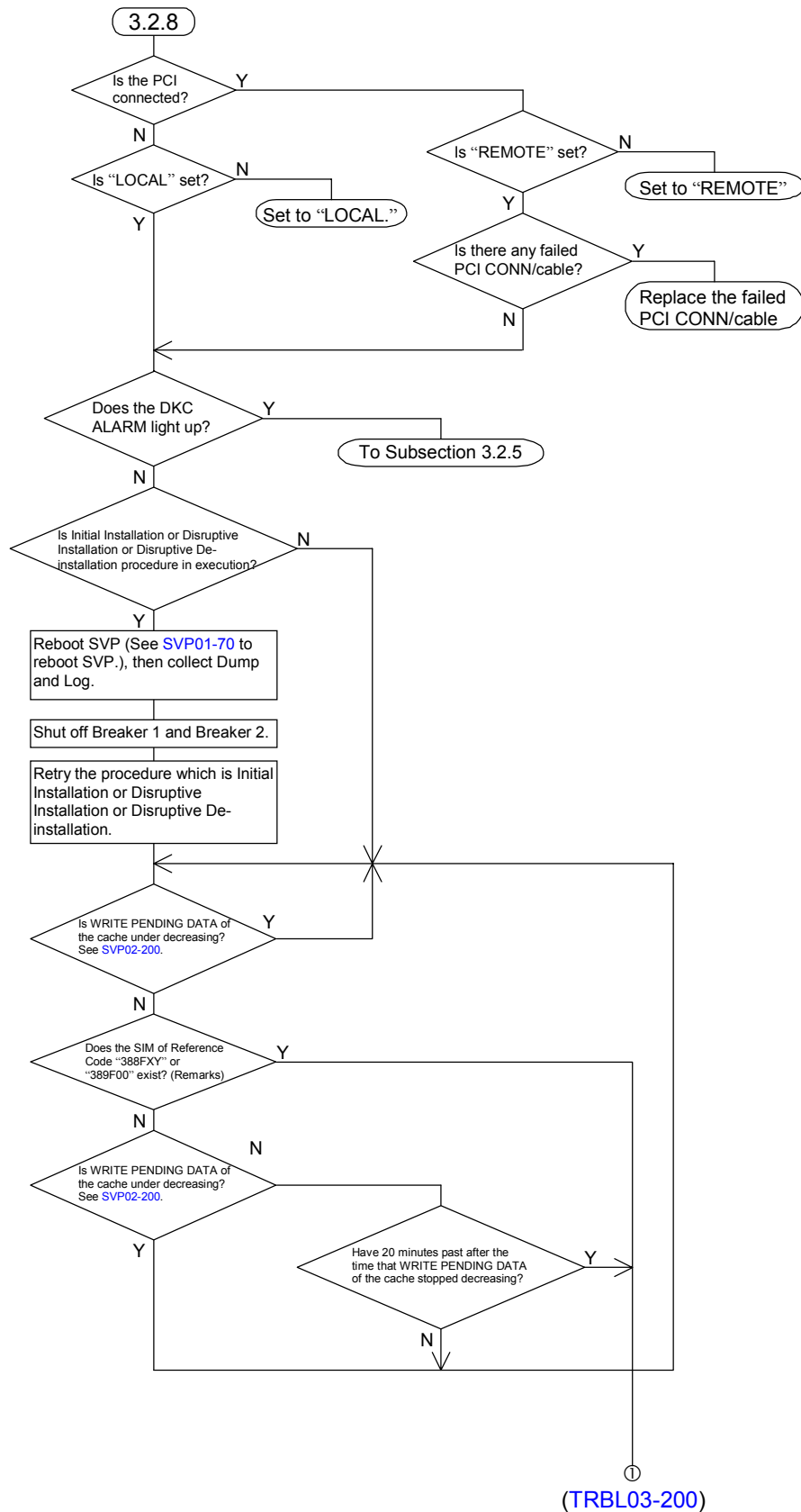
### 3.2.7 A failure has occurred when turning the power on

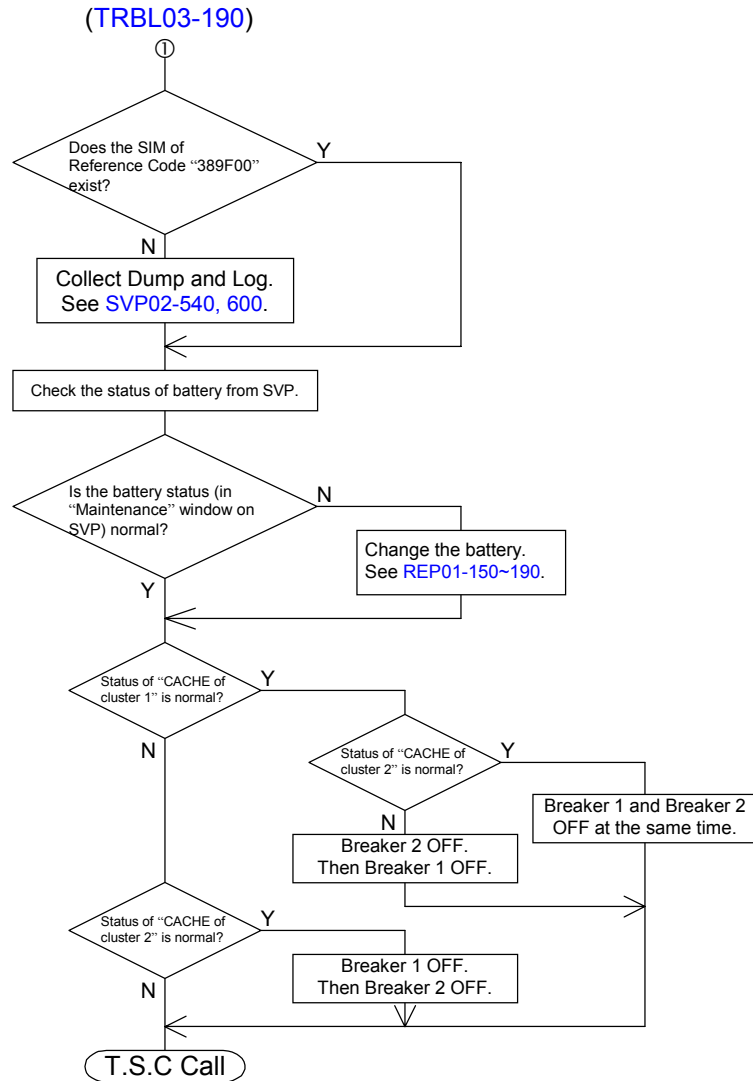


\*1 : If you finished the Maintenance, delete the log and SIM complete. (Refer to [SVP02-170, 580](#))



### 3.2.8 The power cannot be turned off





#### Remarks

- There is a case that it takes more than 10 minutes to report SIM of Reference Code “388FXY” or “389F00”, and that especially it takes about 1 hour in case that emergency destage does not complete.
- There is a case that p/s off procedure is normally finished, even after SIM of Reference Code “388FXY” or “389F00” is reported.

### 3.2.9 Multiple parts have failed

#### Maintenance Priority

If there are many parts which need maintenance in the system, you should plan the maintenance schedule under the priority mentioned in this page.

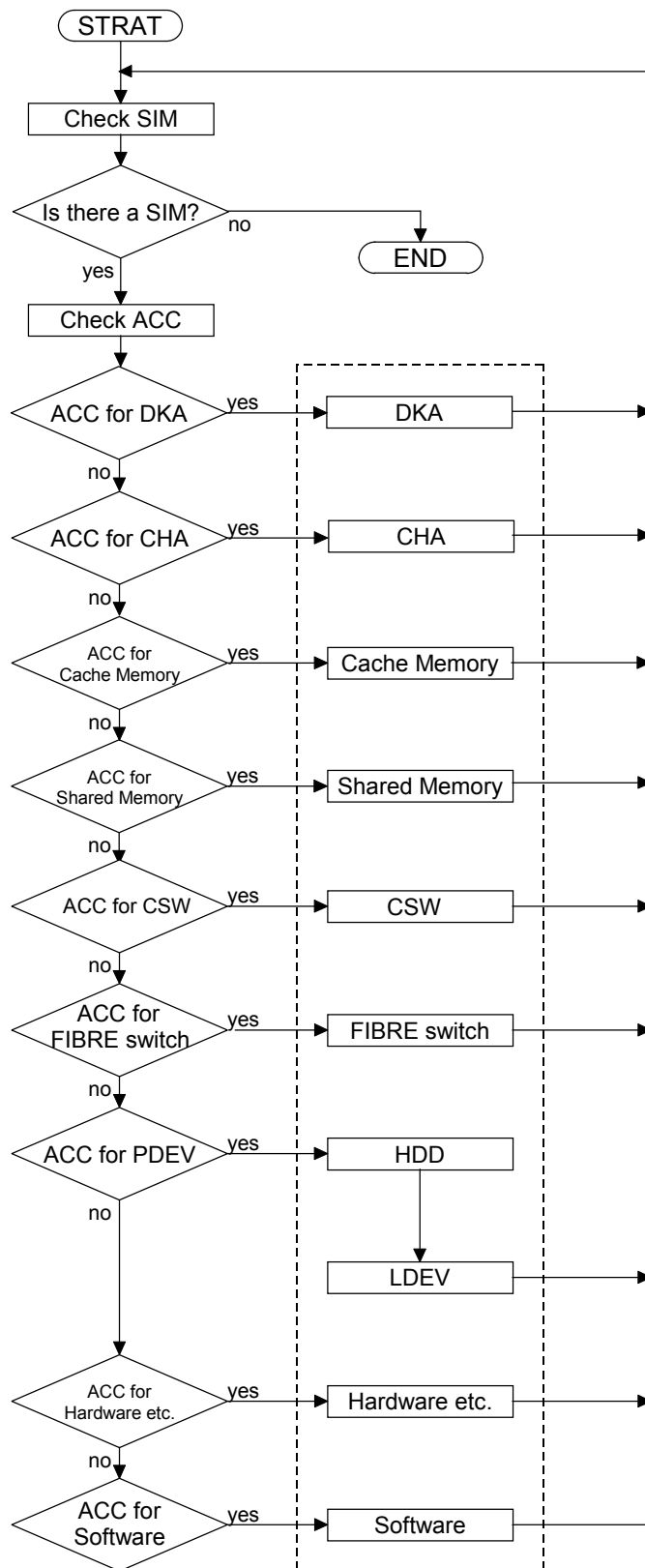
If you have to maintain two parts, first you should maintain a part whose priority is higher than the other.

Table 1 shows that a part with a smaller priority number has a higher priority.

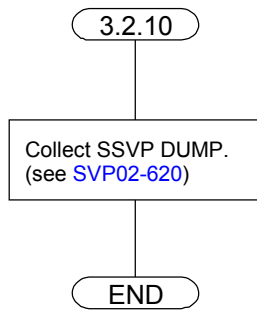
Table 1 Priority Table

Priority	Parts name	Maintenance method
1	DKA	Replace
2	CHA	Replace
3	Cache Memory	Replace
4	Shared Memory	Replace
5	CSW	Replace
6	FIBRE switch	Replace
7	HDD	Replace
8	LDEV	Format or Restore
9	Hardware etc.	Replace
10	Software	Exchange

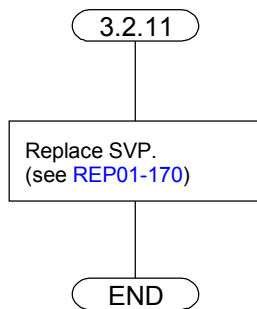
## Maintenance priority chart



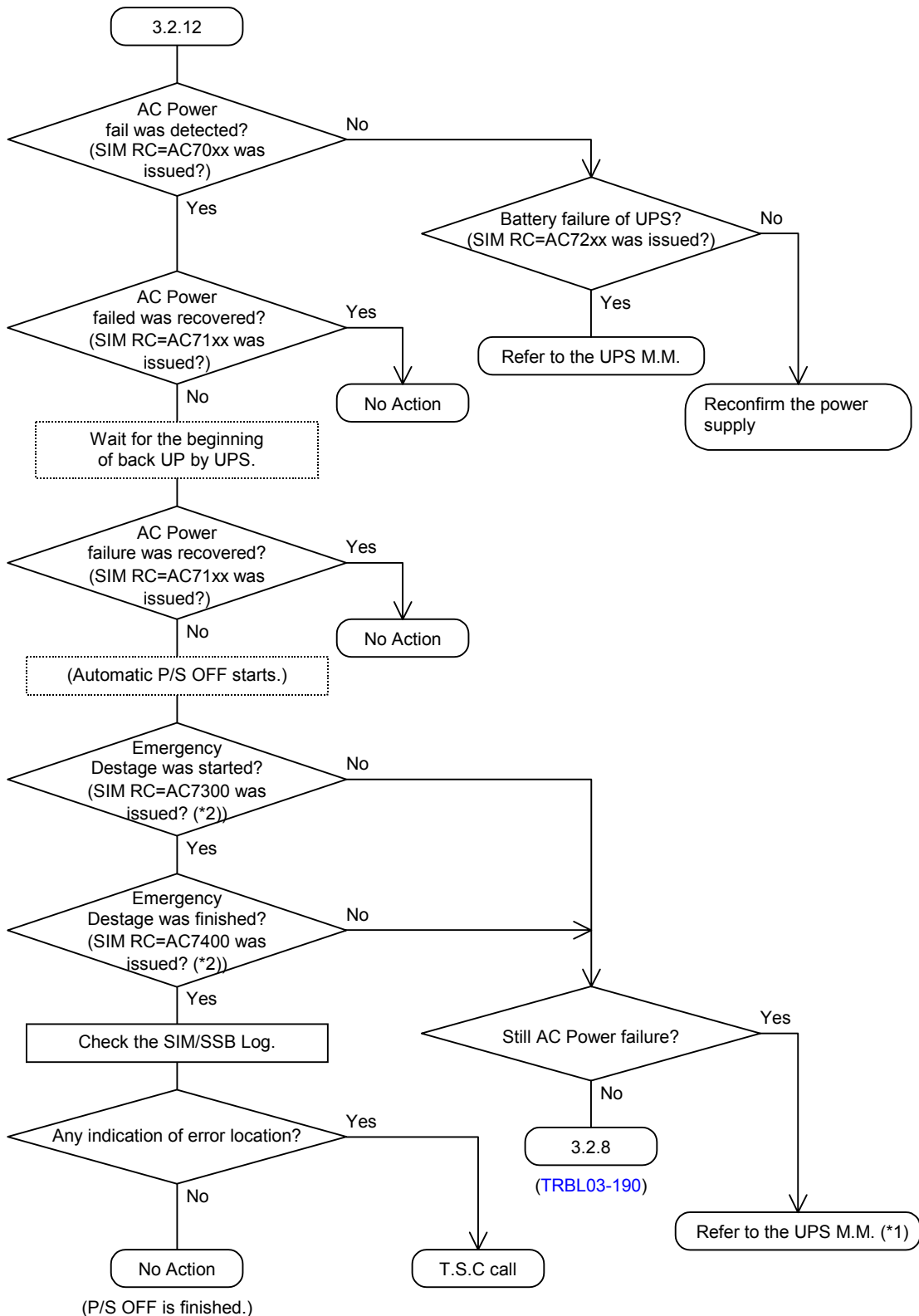
### 3.2.10 SSVP alarm lamp has been blinking or has lighted on.



### 3.2.11 MESSAGE lamp has been blinking



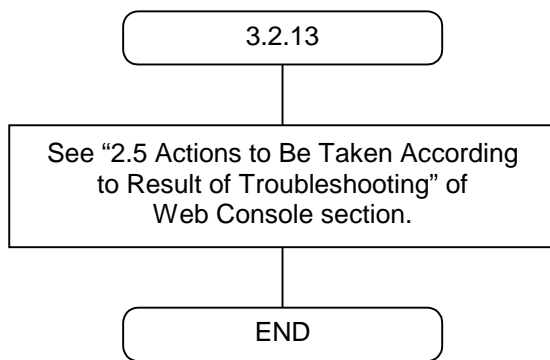
## 3.2.12 AC Power failure in the case that the UPS is connected



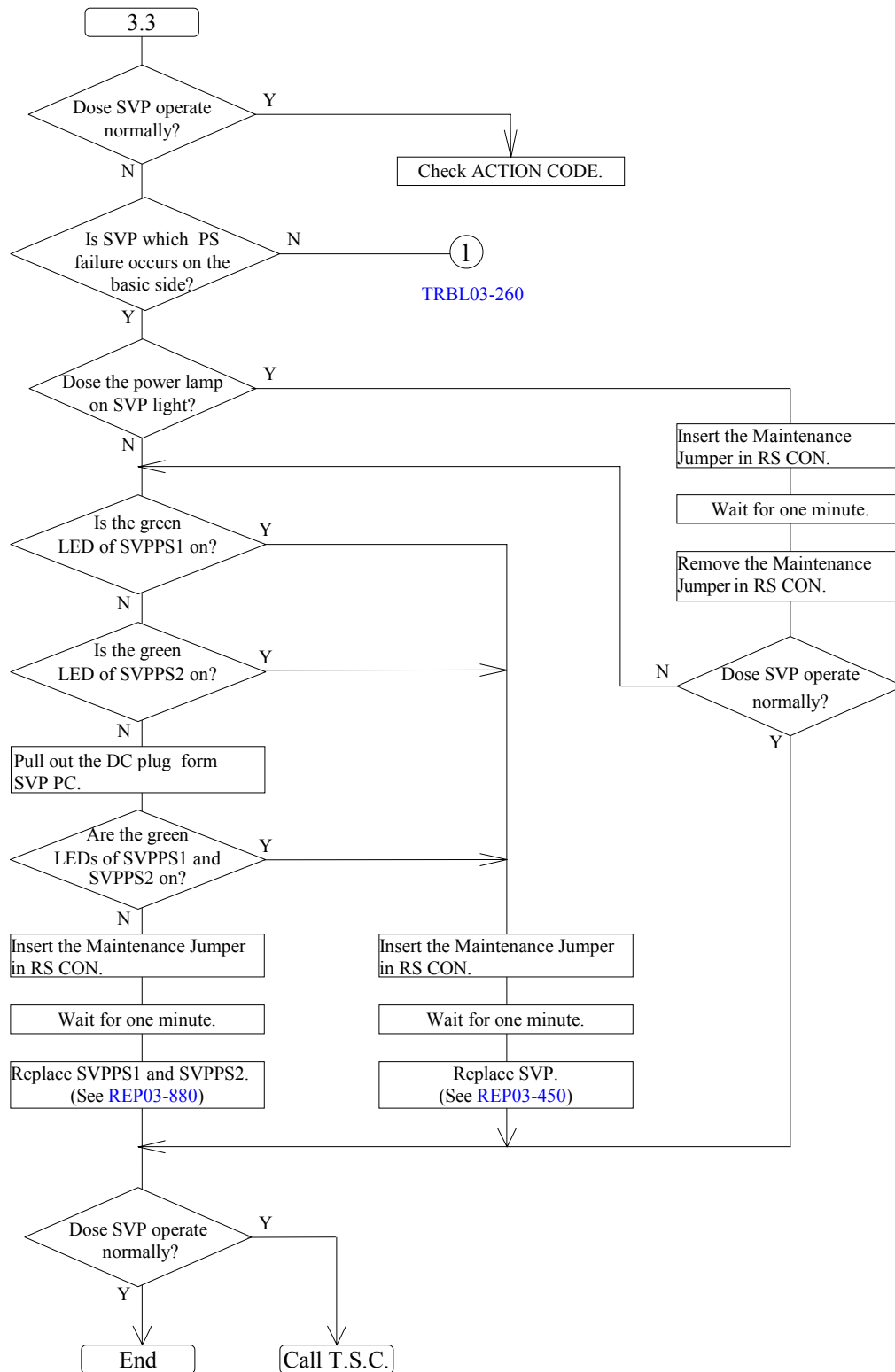
(\*1) You have to recover AC Power failure within 48 hours or Memory is being kept by battery.

(\*2) As for SIMAC7300 and SIMAC7400, The order of indication time can be inverted. The Log number continues in serial order.

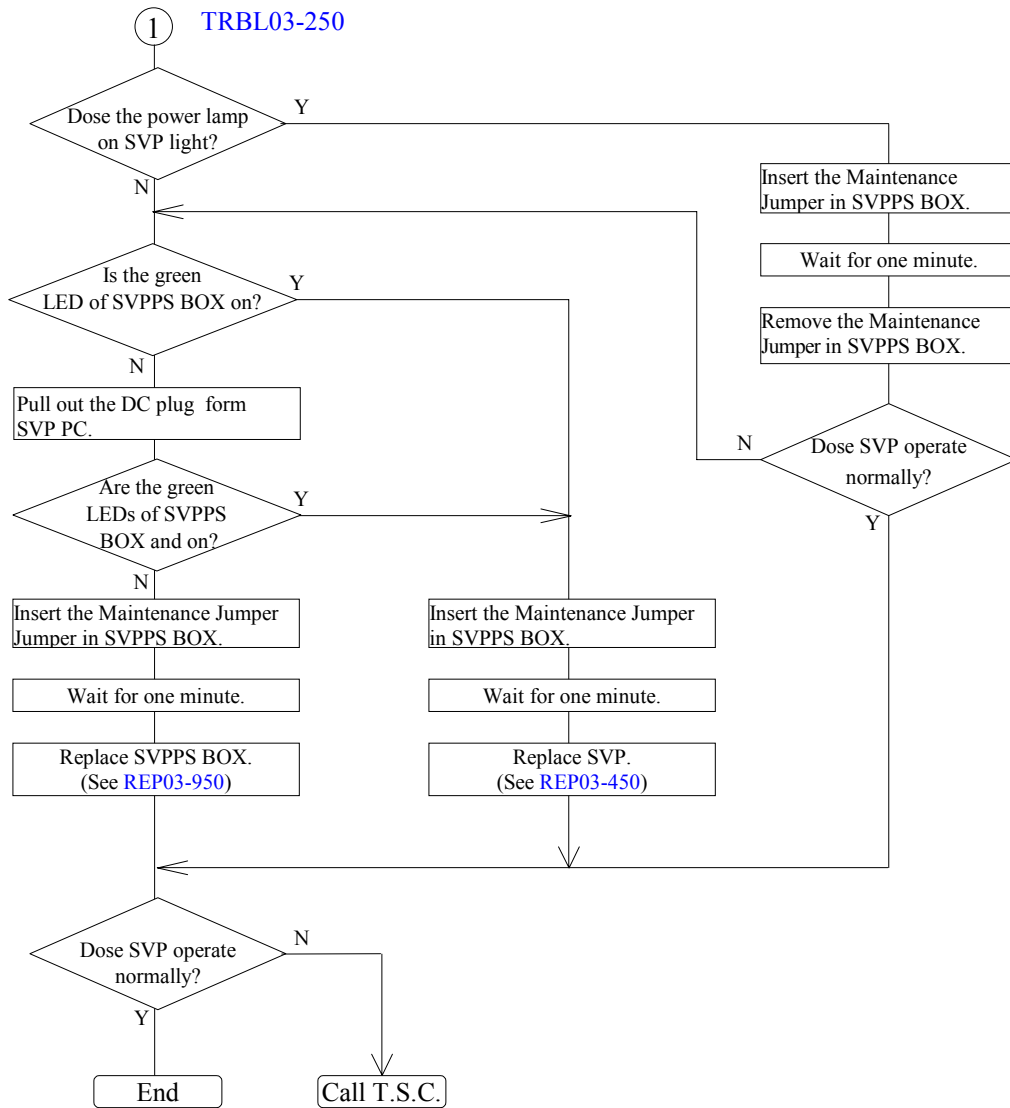
### 3.2.13 Web Console failure



### 3.3 SVP Power Trouble Shooting







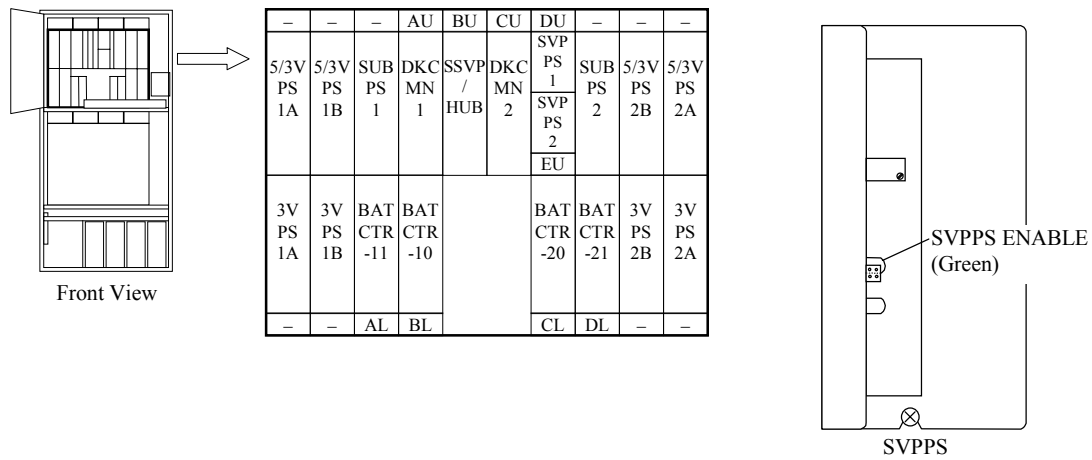


Fig. 3.3-1 Location of SVPPS

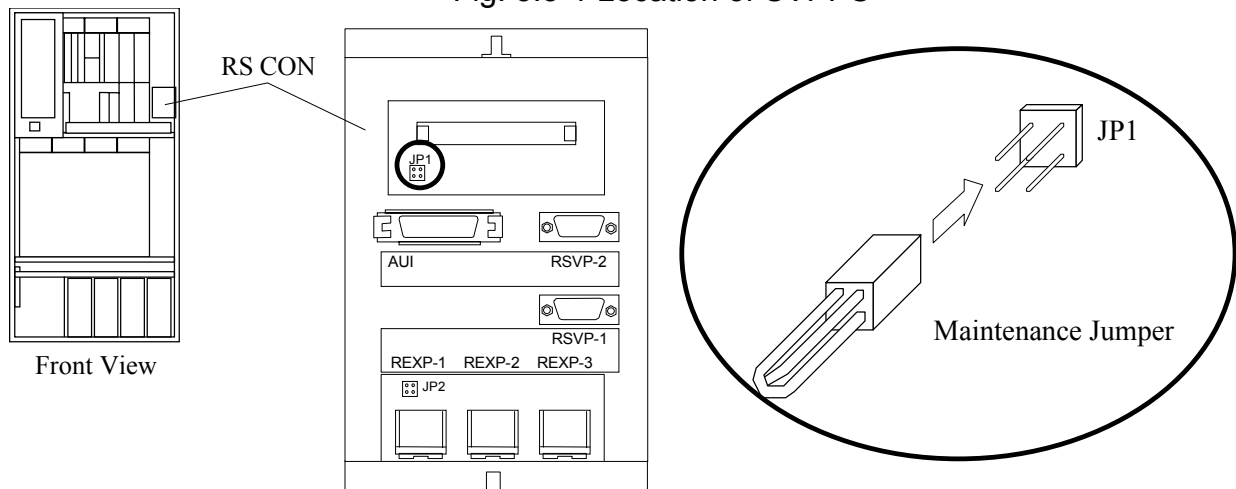


Fig. 3.3-2 Location of RS CON

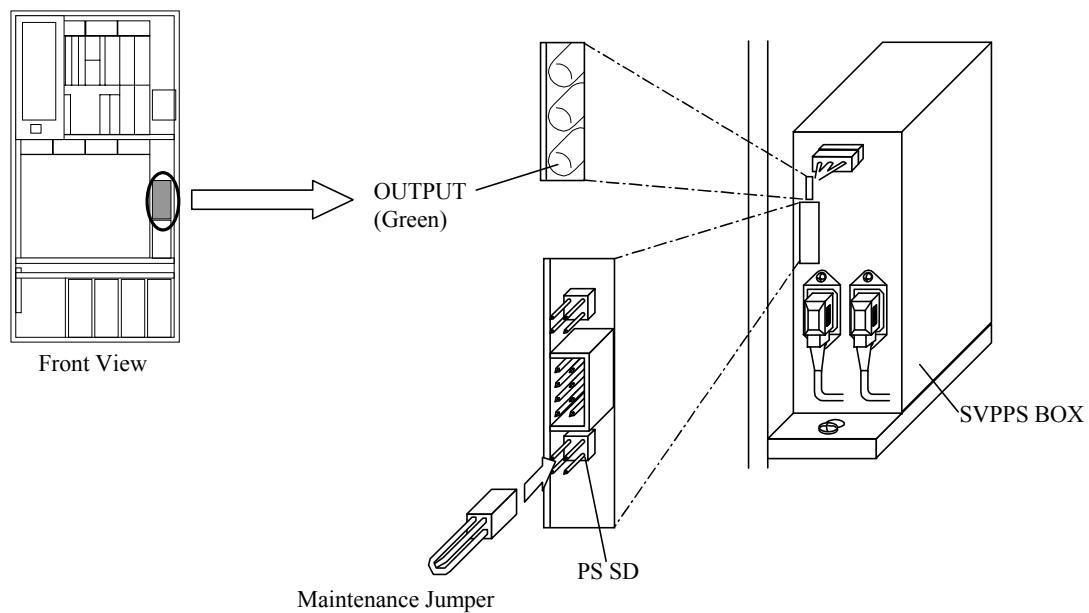


Fig. 3.3-3 Location of SVPPS BOX

## 4 Recovery for Pinned Tracks

Hardware errors sometimes cause a pinned track.

This document explains pinned tracks and how to recover from them.

The following reports inform of the occurrence of a pinned track:

- Permanent Data Check
- Repeated report of Host Adapter CHK2 on the same track
- SIM report of a pinned track

SIM REF. CODE	Meaning	Comment
EF4X-YY	Unable to write a track to a PDEV	X:CU# YY:LDEV#
FF4X-YY	Unable to process a track to or from Cache	X:CU# YY:LDEV#

To recover pinned tracks, the following information is necessary:

- Address in LDEV (LDEV number, Cylinder & Head address)
- First and last Cylinder & Head addresses of the stripe containing the pinned track
- The error type of the pinned track (Write or ECC/LRC)
- The PDEV number of the drive containing the pinned track

This information can be obtained from the “Pinned Data Display Function” ([SVP03-250](#)) of the SVP.

There are 2 types of pinned track error:

Display on SVP	Meaning	Recovery
Write Error	Unable to write a track to a PDEV	Replacement of PDEV
ECC/LRC Error	Unable to process a track to or from Cache	Data recovery

Pages TRBL04-XX explain the error types of pinned tracks and have flowcharts for recovery. For HRC or HODM volumes, the flowcharts for recovery are shown in [TRBL06-270 through TRBL06-300](#).

For HMRCF volumes, the flowcharts for recovery are shown in [TRBL09-10](#).

For OPEN volumes, the flowcharts for recovery are shown in [TRBL07-150](#).

## **ECC/LRC Error**

### **Causes:**

- (1) During a write operation new data is written into Cache. If less than a full stripe (3 data tracks & their parity track) are written, the corresponding old data and old parity tracks are staged into Cache to create a new parity track. When a new parity track is created, the new parity track and the new data tracks are ready to be destaged to the DKUs. These tracks are called "Dirty Data". An ECC/LRC pinned track will occur:
  - (a) When Dirty Data cannot be read from Cache by the host. This will lead to Permanent Data Check and the data will be lost. The data will have to be reconstructed by the host from back-up files, etc.
  - (b) When Dirty Data cannot be destaged to DKU (CHK2 errors). This type of pinned data can usually be read by the host.
- (2) An ECC/LRC type of pinned track occurs when a new parity track cannot be properly reconstructed. During the parity track creation, if any of the old data or old parity tracks cannot be staged to Cache to construct the new parity track, or if a new parity track cannot be destaged due to a drive failure, the parity track will be pinned. Data can still be read by the host.
- (3) An ECC/LRC type of pinned track occurs when a track cannot be correctly reconstructed during Correction Copy. This will lead to a Permanent Data Check and the original data will be lost.
- (4) An ECC/LRC type of pinned track occurs if; there is a write type pinned track due to drive failure; the DKC is powered down by manual operation of the P/S OFF switch; the batteries fail or are unplugged, then the Write pinned track will become an ECC/LRC pinned track.

### **Result of host I/O operation:**

- (1) When a track with an ECC/LRC error is accessed by a host I/O and the data can be read, the result will be a normal end.
- (2) If the data cannot be read, there will be a failure reported of Permanent Data Check. In this case the data is lost.
- (3) Repeated Host Adapter CHK2 errors reported. Data is usually still readable but is not being destaged from cache.

### **Recovering ECC/LRC pinned tracks:**

- (1) Execute ICKDSF ANALYZE SCAN using LDEV, CC, HH information from the SVP PINNED TRACK display to determine whether Failure is reported or not.
  - (a) No Failure. Data can be read. Read the data and save it to tape, another volume, or memory, etc.
  - (b) Failure. Data cannot be read. It must be reconstructed by host from backup files, etc.
- (2) Run ICKDSF INSPECT NOPRESRVE (NOCHECK if 3390-3R) to the affected track. This will write all zeros to the track and will clear the pinned track indication.
- (3) Restore the track with the saved data from step 1a or the reconstructed data 1b.
- (4) If a Pinned Track recurs, resolve hardware problem and repeat steps 1-3.

**Pinned Track disappearance:**

Under certain circumstances, pinned tracks may disappear with no recovery action having been taken.

This can occur from:

- (1) The entire stripe is written. This discards all the old data & parity in the stripe.
- (2) A Format Write from R1 is issued to the pinned track. This rewrites the entire stripe and all old data in the stripe is discarded.
- (3) If dirty data that could not be destaged due to CHK2 error is read successfully by the host, pinned tracks will be turned off.
- (4) If a parity track is unable to be destaged due to a drive failure and the drive is replaced using Correction Copy (drive replacement by copying to a spare will not clear Pinned Track).

**Write Error****Cause:**

A write error type of pinned track will be made when the data destaging process to a PDEV is unsuccessful due to a drive failure. When a drive failure occurs, the drive and the DKC both attempt to recover the problem. If the recovery attempts are unsuccessful, a Write type pinned track is posted. The recovery attempts are:

- (1) Media failure : Automatic reallocation of data to an alternate sector.
- (2) Other failure : Alternate path retry.

Write error count for each PDEV is stored in the DKC. If the write error count for a PDEV exceeds the threshold value, the PDEV is blocked. Only one PDEV per parity group will be blocked. One blocked PDEV in a parity group will not stop DKC operation to that parity group. However, the parity group will be in correction access mode. If a write type pinned track is accessed by the host after its PDEV has been blocked, the pinned track status will be reset.

**Result of host I/O operation:**

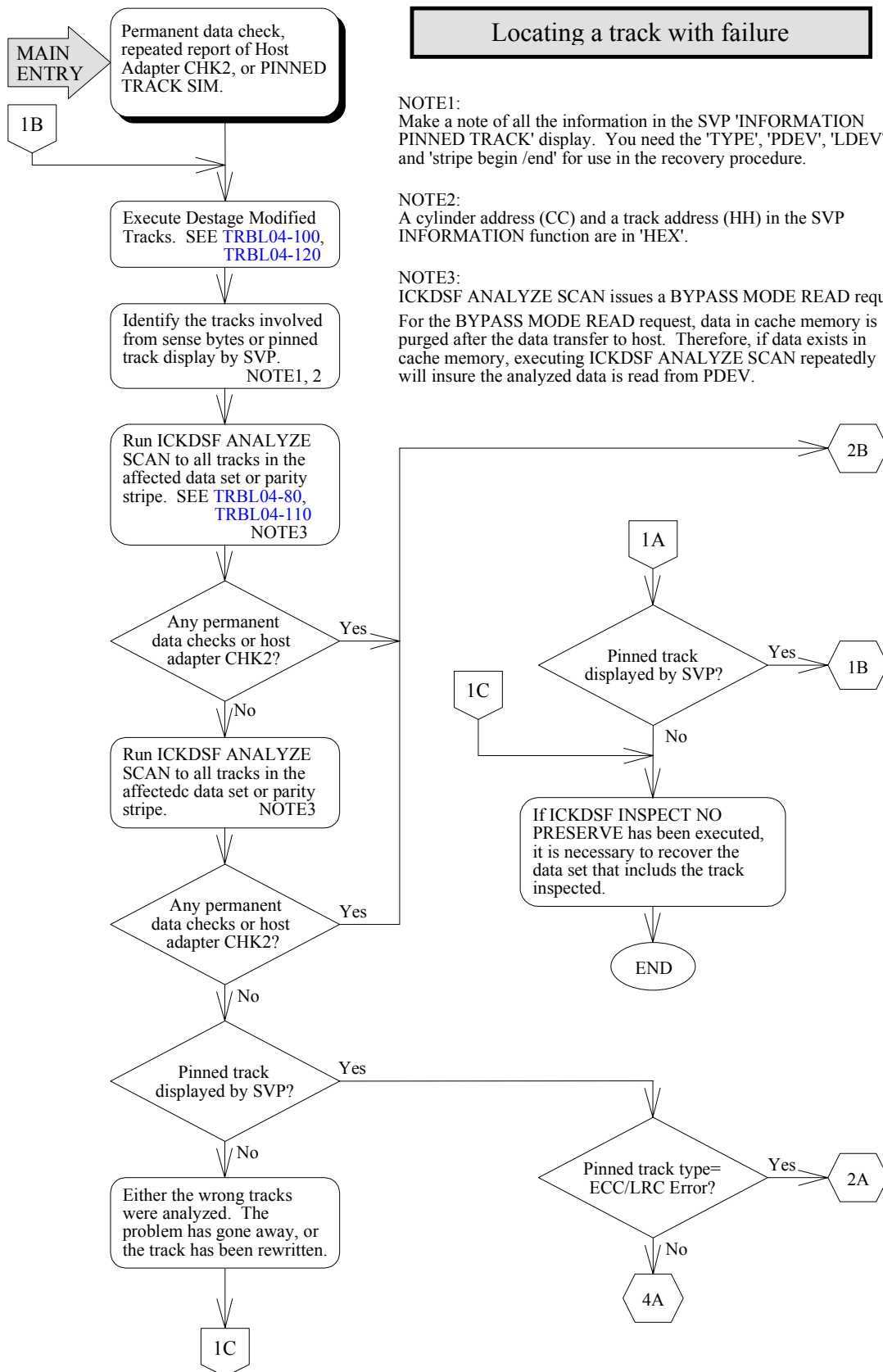
All access to write type pinned tracks will be successful and return a normal end.

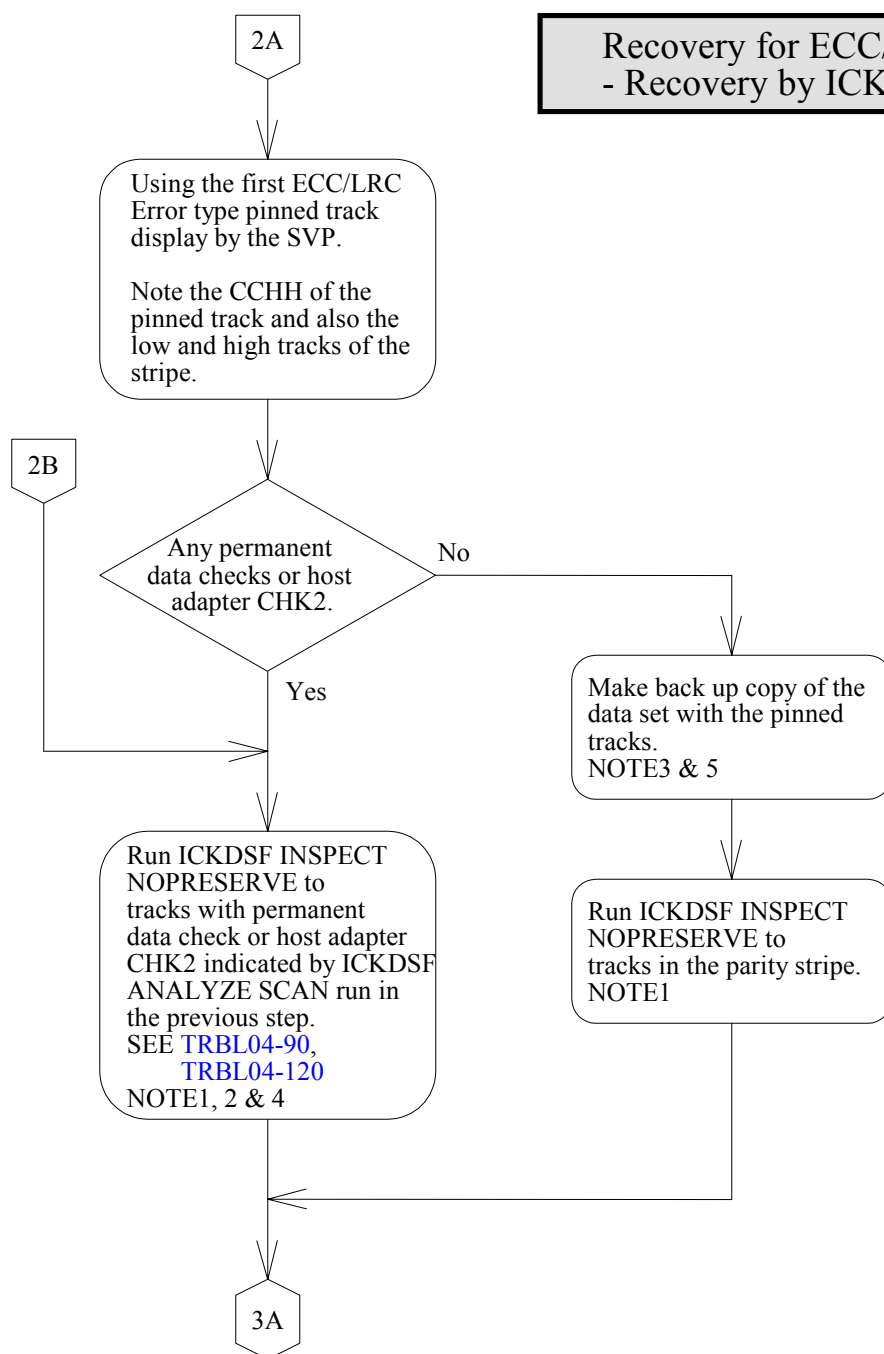
**Recovering Write Error pinned tracks:**

Replace the PDEV with the pinned track. At this time if there is already a blocked PDEV in the same parity group, replace the blocked PDEV first. Then replace the PDEV with the pinned track. If more than one PDEV in a parity group has write error pinned tracks, check the ORM display on the SVP Panel.

Replace the PDEV with the highest error rate first, then second highest, etc. The pinned track(s) will be recovered by correction copy.

## 4.1 Recovery Procedure for Pinned Tracks





NOTE1 Remember all tracks on which the ICKDSF INSPECT NO PRESERVE is run as customer data has been lost. Customer will have to recover files for each data set.

NOTE2 If you execute ICKDSF INSPECT in the tracks of VTOC, the volume should be set to offline. In this case the entire volume will have to be restored.

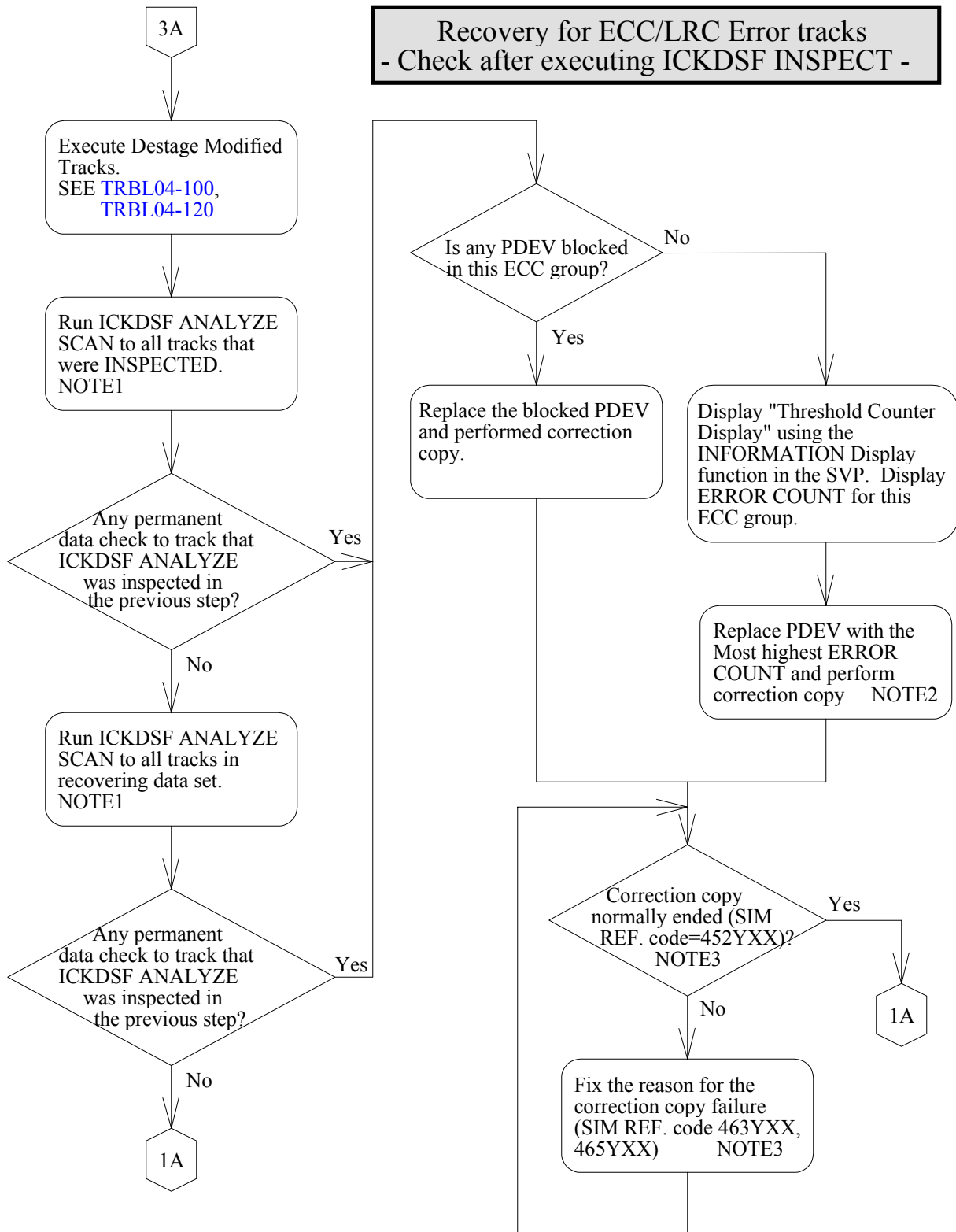
NOTE3

- During a file recovery from the backup procedure to the restore procedure of the data set, the data set can not be accessed until the recovery is complete.
- If a file recovery for PDS data is set, execute the backup/restore procedure to the data set (all members).

NOTE4 When the drive emulation type is 3390-3R, add NOCHECK to ICKDSF INSPECT parameter.

NOTE5

- ① Execute File Back up for dataset including pinned track. Store the backed-up file to another media (tape or another dasd).
- ② After executing NO PRESERVE INSPECT, restore the backed-up file to its original location.

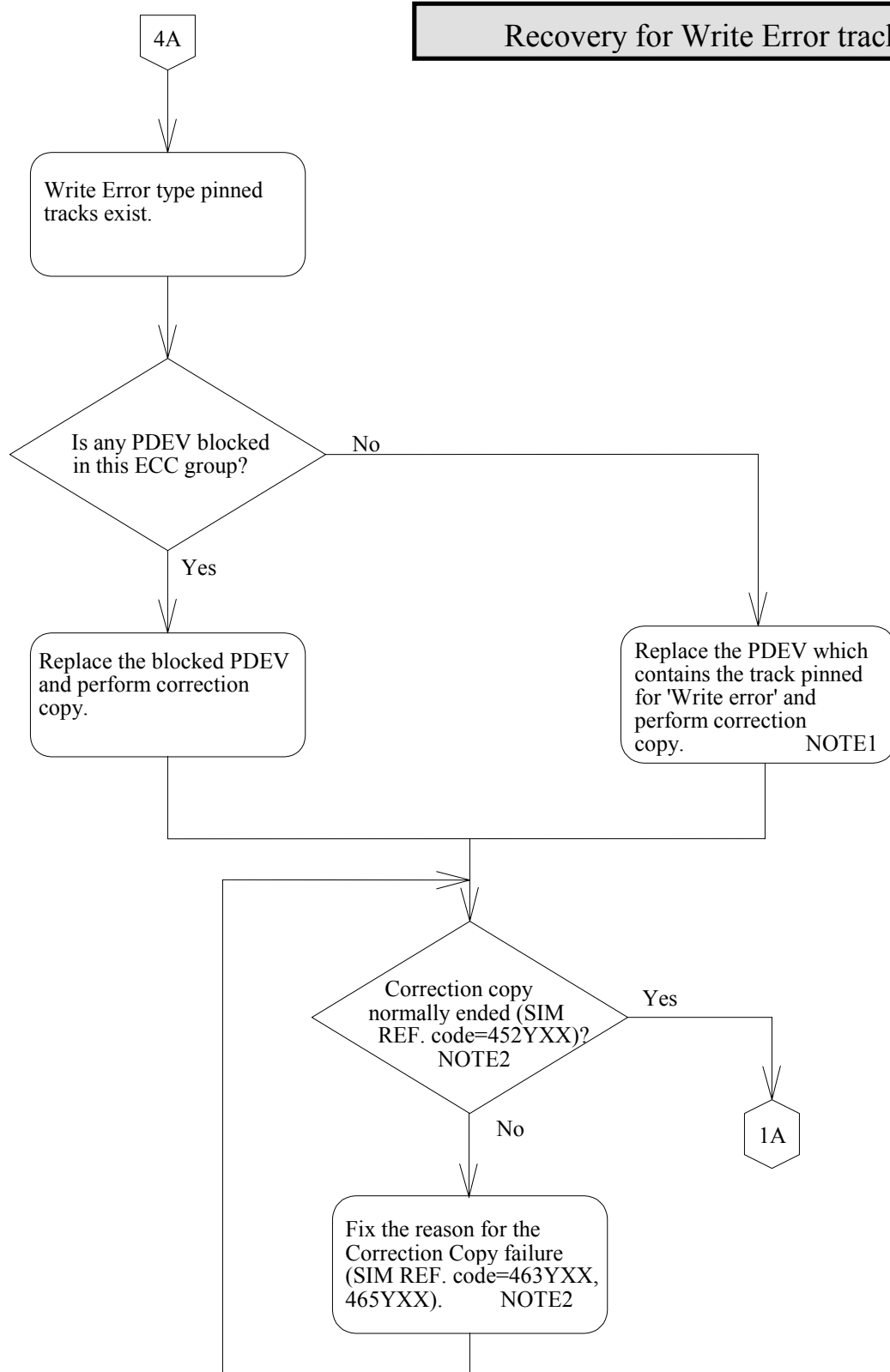


NOTE1 Repeat ICKDSF ANALYZE SCAN twice for the purpose verifying the data written on the physical device actually.

NOTE2 If Spare drives are available, you can perform Drive Copy instead of Correction copy.

NOTE3 If Drive copy ended abnormally, SIM REF. code is "463YXX" or "465YXX".





NOTE1 If spare drives are available, you can perform Drive copy instead of Correction copy.

NOTE2 If Drive copy ended abnormally, SIM REF. code is “463YXX” or “465YXX”.

JCL example (under MVS)

A. Check if any pinned track by using DSF (ANALYZE).

[In case target LDEV to be recovered is ONLINE state]

```
//ANALYZE JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//DASD DD UNIT=DASD,VOL=SER=xxxxxx,DISP=SHR
//SYSIN DD *
ANALYZE DDNAME(DASD) SCAN
/*
//
```

[In case target LDEV to be recovered is OFFLINE state]

```
//ANALYZE JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//SYSIN DD *
ANALYZE UNIT(cuu) SCAN
/*
//
```

## B. Recover pinned tracks by using DSF (INSPECT).

### (1) When the drive emulation type is other than 3390-3R.

[In case target LDEV to be recovered is ONLINE state]

```
//INSPECT JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//DASD DD UNIT=DASD,VOL=SER=xxxxxx,DISP=SHR
//SYSIN DD *
INSPECT DDNAME(DASD) NOVERIFY NOPRESERVE -
FROM(X'ccc',X'h') TO(X'ccc',X'h')
/*
//
```

[In case target LDEV to be recovered is OFFLINE state]

```
//INSPECT JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//SYSIN DD *
INSPECT UNIT(cuu) NOVERIFY NOPRESERVE -
FROM(X'ccc',X'h') TO(X'ccc',X'h')
/*
//
```

### (2) When the drive emulation type is 3390-3R.

[In case target LDEV to be recovered is ONLINE state]

```
//INSPECT JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//DASD DD UNIT=DASD,VOL=SER=xxxxxx,DISP=SHR
//SYSIN DD *
INSPECT DDNAME(DASD) NOVERIFY NOPRESERVE NOCHECK
-
TRACKS(X'ccc',X'h')
/*
//
```

[In case target LDEV to be recovered is OFFLINE state]

```
//INSPECT JOB MSGCLASS=x,MSGLEVEL=(1,1)
//STEP EXEC PGM=ICKDSF
//SYSPRINT DD SYSOUT=*
//SYSIN DD *
INSPECT UNIT(cuu) NOVERIFY NOPRESERVE NOCHKCK -
TRACKS(X'ccc',X'h')
/*
//
```

### C. Execute Destage Modified Tracks

```
//DESTDATA JOB
MSGCLASS=x,MSGLEVEL=(1,1),REGION=nnnnK
//STEP1 EXEC PGM=IDCAMS
//SYSPRINT DD SYSOUT=*
//SYSIN DD *
SETCACHE VOLUME(xxxxxx) unit(DASD) DESTAGE
/*
//
```

Operation example (under VM)

Note : This recovery procedure is executable only under VM/ESA 1.1 or higher release with DEVMAINT authority.

A. Check if any pinned track by using DSF (ANALYZE).

```
ickdsf
ICK030E DEFINE INPUT...
console
CONSOLE
ICK031E DEFINE OUTPUT...
console
CONSOLE
ICKDSF - CMS DEVICE SUPPORT FACILITY...

ENTER INPUT COMMAND:
analyze unit(cuu) scan
      .
      .
end
END
```

## B. Recover pinned tracks by using DSF(INSPECT).

### (1) When the drive emuration type is other than 3390-3R.

```

ickdsf
ICK030E DEFINE INPUT...
console
CONSOLE
ICK031E DEFINE OUTPUT...
console
CONSOLE
ICKDSF - CMS DEVICE SUPPORT FACILITY...

ENTER INPUT COMMAND:
inspect unit(cuu) norecovery nopriserve from(x'ccc',x'h'
to(x'ccc',x'h')
      .
      .
end
END

```

### (2) When the drive emuration type is 3390-3R.

```

ickdsf
ICK030E DEFINE INPUT...
console
CONSOLE
ICK031E DEFINE OUTPUT...
console
CONSOLE
ICKDSF - CMS DEVICE SUPPORT FACILITY...

ENTER INPUT COMMAND:
inspect unit(cuu) norecovery nopriserve nocheck
tracks(x'ccc',x'h')
      .
      .
end
END

```

## C. Execute Destage Modified Tracks

```
destage rdev
```

## 5 Error Recovery

### 5.1 FSW Access Error/FSW LED Bus Test Error (SIM = BF9XXX, 3DAXXX)

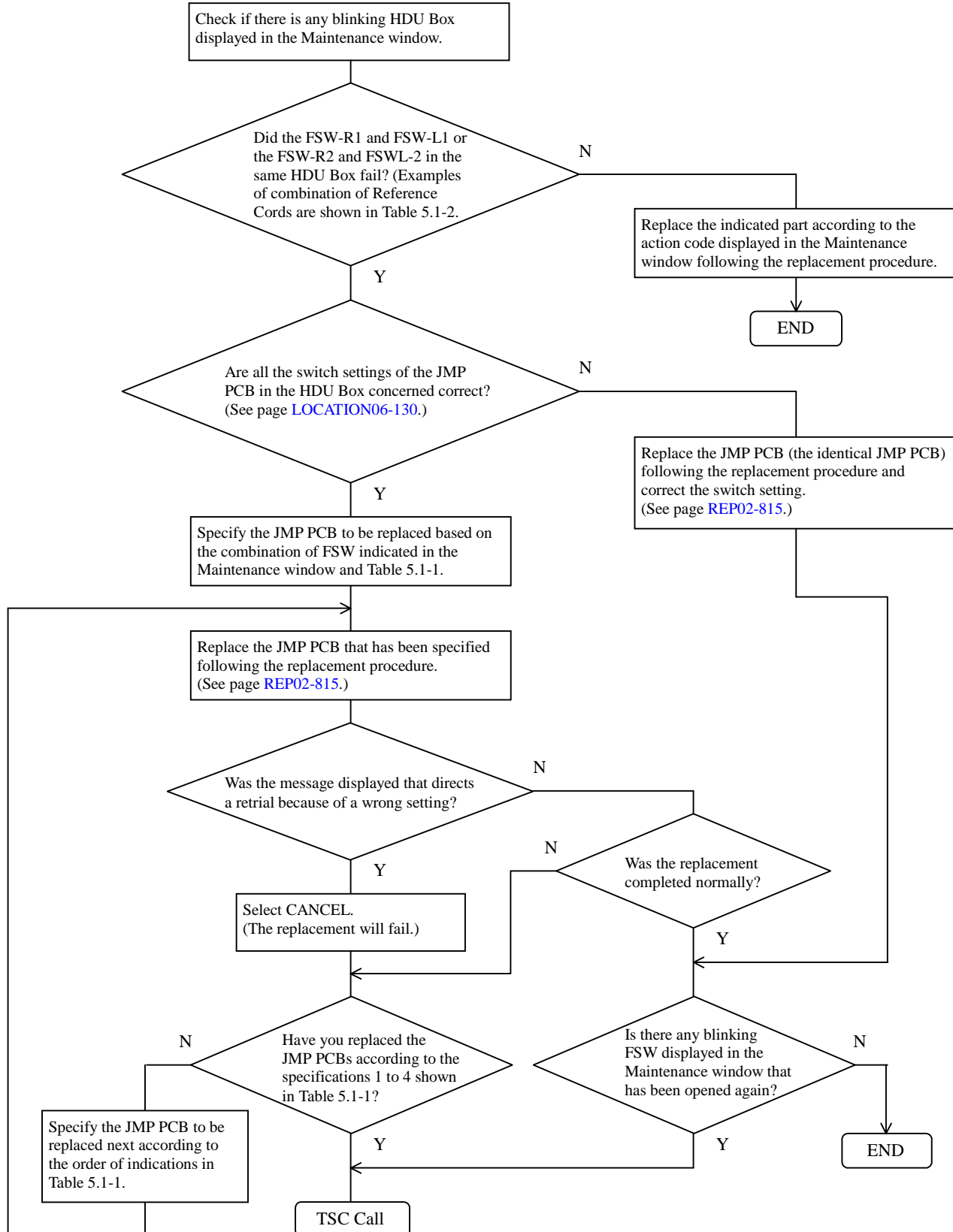


Table 5.1-1 Indication of a part to be replaced (SC Model)

No	ACC Indication part		A part to be replaced			
	CL1	CL2	Indication 1	Indication 2	Indication 3	Indication 4
1	FSW <sub>x</sub> -R1	FSW <sub>x</sub> -L1	JMP <sub>x</sub> -R1	JMP <sub>x</sub> -R2	JMP <sub>x</sub> -L1	JMP <sub>x</sub> -L2
2	FSW <sub>x</sub> -R2	FSW <sub>x</sub> -L2	JMP <sub>x</sub> -R1	JMP <sub>x</sub> -R2	JMP <sub>x</sub> -L1	JMP <sub>x</sub> -L2

(See page [LOCATION02-20](#))

Table 5.1-2 Example of a combination of the reference codes (SC Model)

No	CL1	CL2
1	3DA <sub>x</sub> 00	3DA <sub>x</sub> 40
2	3DA <sub>x</sub> 01	3DA <sub>x</sub> 41
3	3DA <sub>x</sub> 02	3DA <sub>x</sub> 42
4	3DA <sub>x</sub> 03	3DA <sub>x</sub> 43
5	3DA <sub>x</sub> 10	3DA <sub>x</sub> 50
6	3DA <sub>x</sub> 11	3DA <sub>x</sub> 51
7	3DA <sub>x</sub> 12	3DA <sub>x</sub> 52
8	3DA <sub>x</sub> 13	3DA <sub>x</sub> 53

(x: FSW PCB#)



## 5.2 Isolation and Recovery Procedures for Common Fibre Loop Error (SIM = DF6YXX, DF7YXX, DF8YXX, DF9YXX)

When a Fibre port error SIM for a drive (PDEV) is reported, this section provides the procedures for judging whether the error is caused by the pertinent drive error or common Fibre Loop error, and how to recover the error.

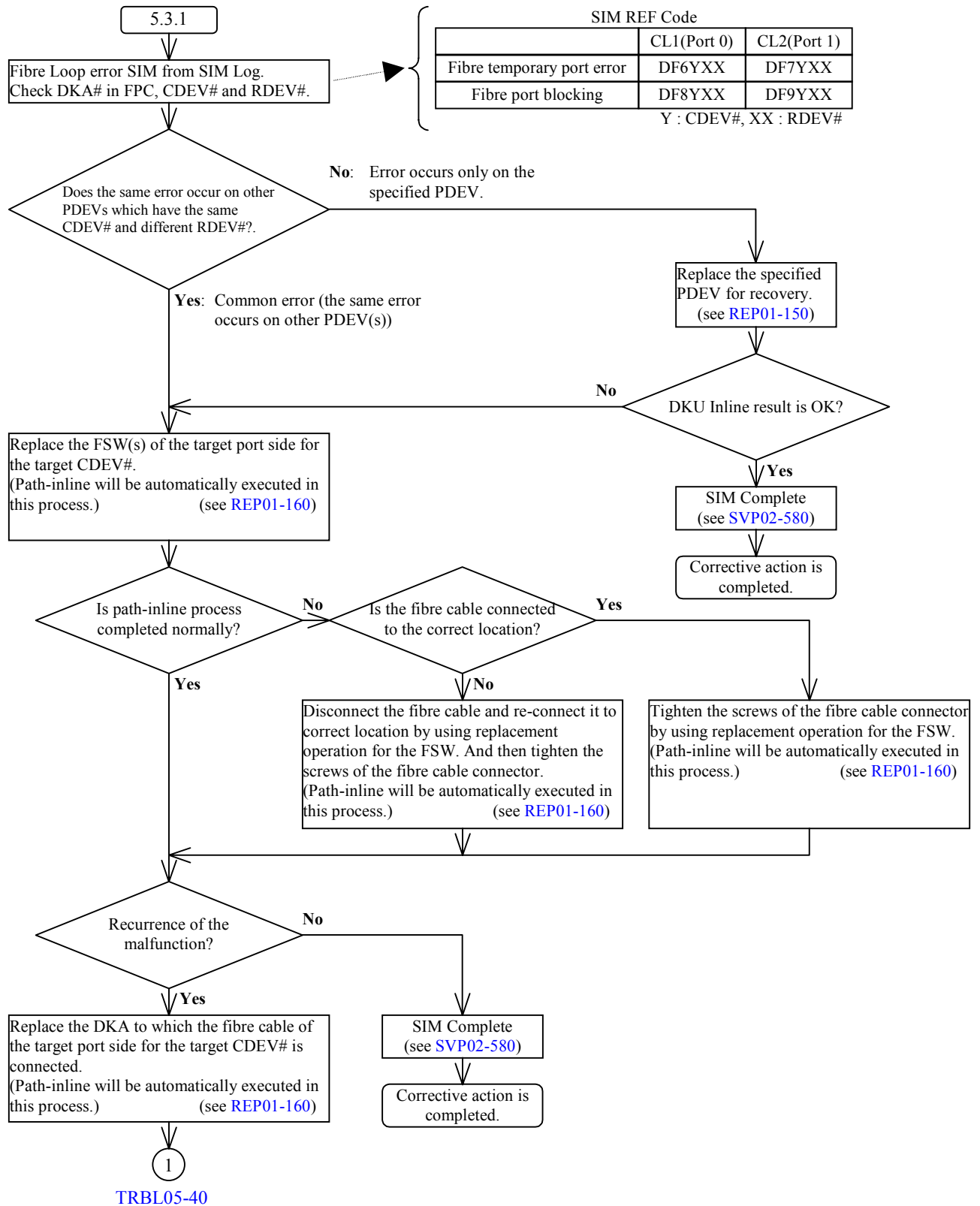
As a Fibre port error SIM managed in each PDEV, a Fibre temporary error (REF code = DF6YXX, DF7YXX) is reported when a warning is issued, and Fibre blocking (REF code = DF8YXX, DF9YXX) is reported when the equipment is blocked (Y: CDEV#, XX: RDEV#).

Possible causes for the malfunction are:

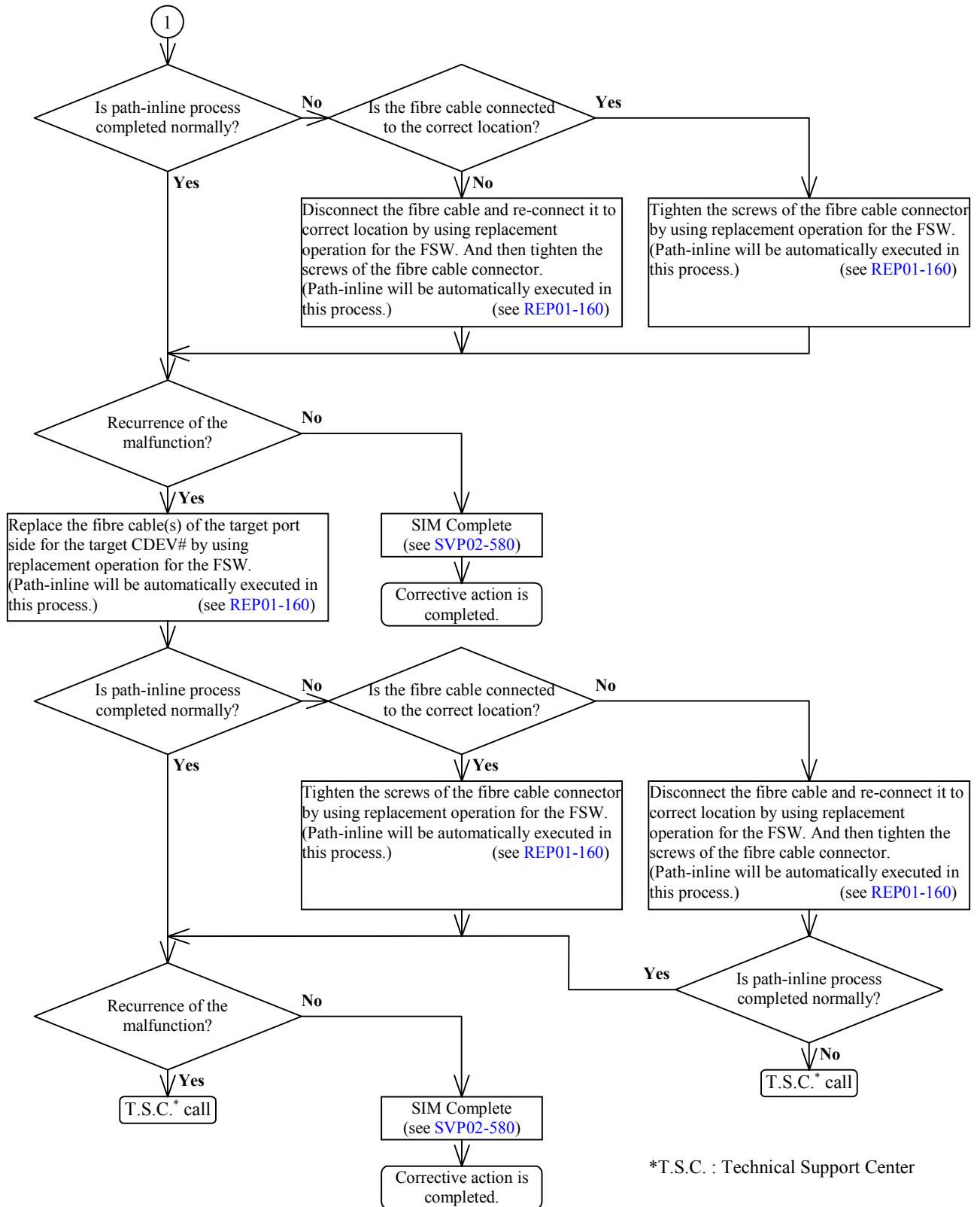
- (1) Failure of drive specified in FPC
- (2) Defects in Fibre Chip for DKF
- (3) Defects in Fibre Loop (cable, FSW(Fibre switch))
- (4) Other drive failure
- (5) HDU Box platter failure

Before performing the maintenance and replacement only for the failed drive, follow these isolation and recovery procedures to check that a common Fibre port error occurs on other PDEVs on the same Fibre Loop. If a common error exists, perform the appropriate recovery for the common error parts.

## Common Fibre Loop Error Isolation Procedure



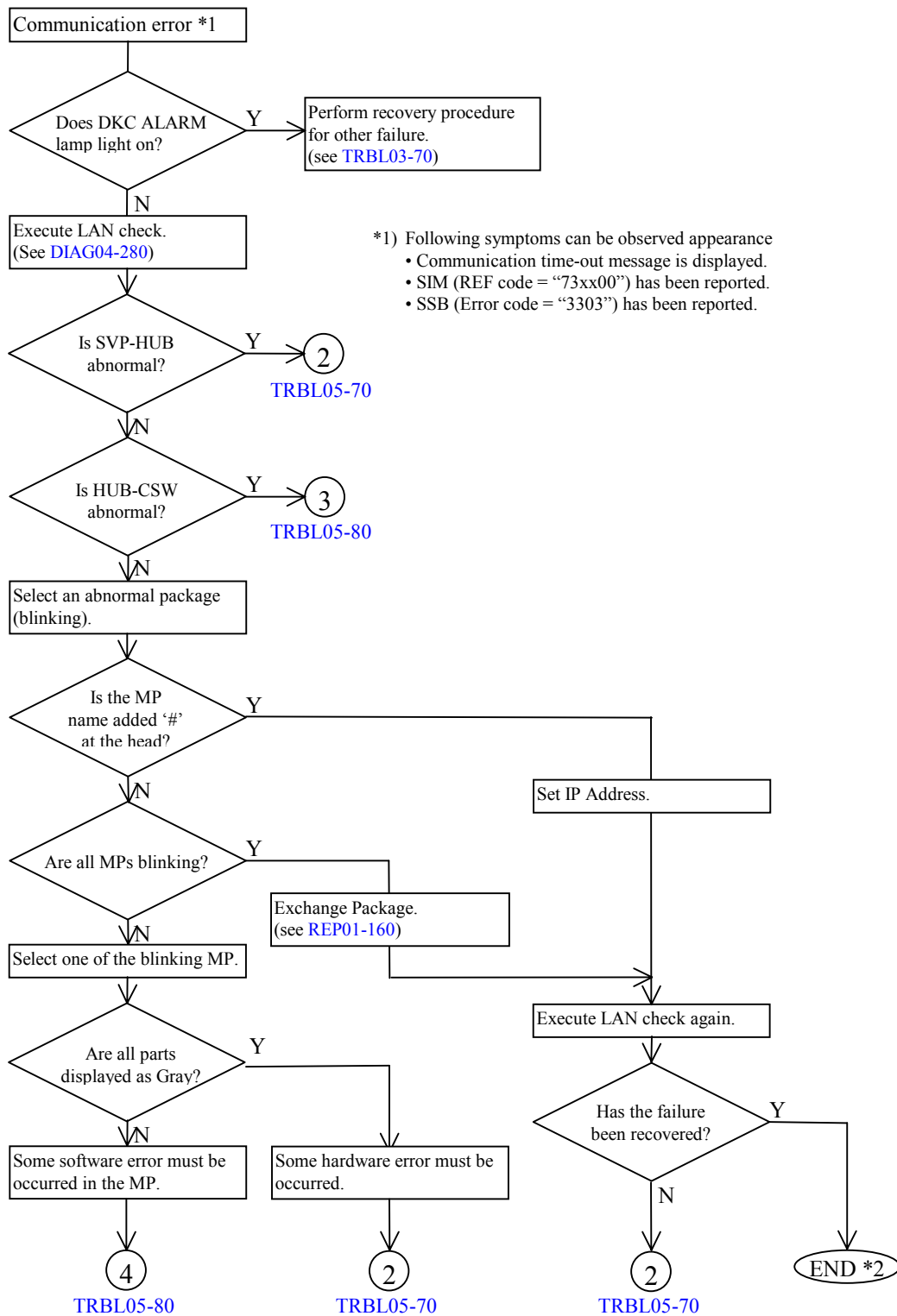
TRBL05-30



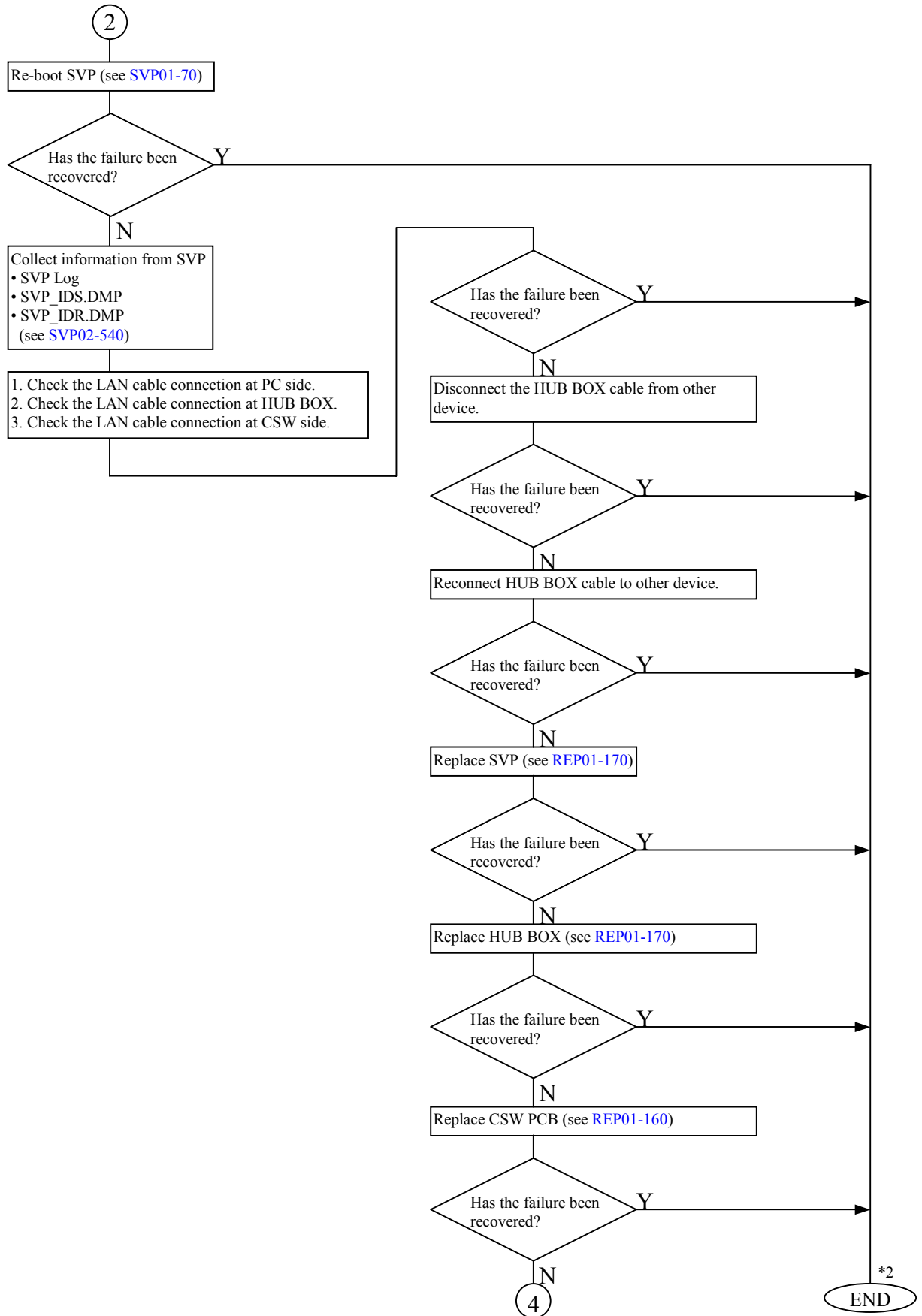
\*T.S.C. : Technical Support Center

Blank Sheet

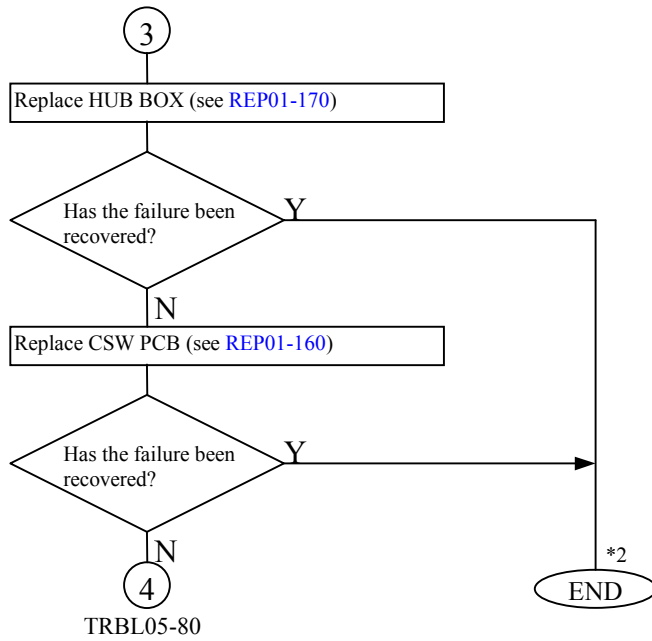
### 5.3 Recovery Procedure for LAN Error (SIM = 1400X0, 1500X0, 73XX00)



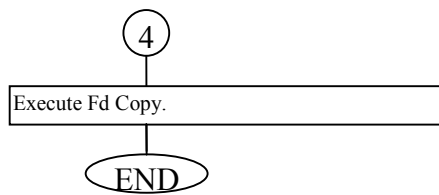
\*2) After finishing the error recovery, execute SIM complete and delete logs. (Refer to SVP02-170, 580)



TRBL05-80



\*2: After finishing the error recovery, execute SIM complete and delete logs. (see [SVP02-170, 580](#))



Blank Sheet



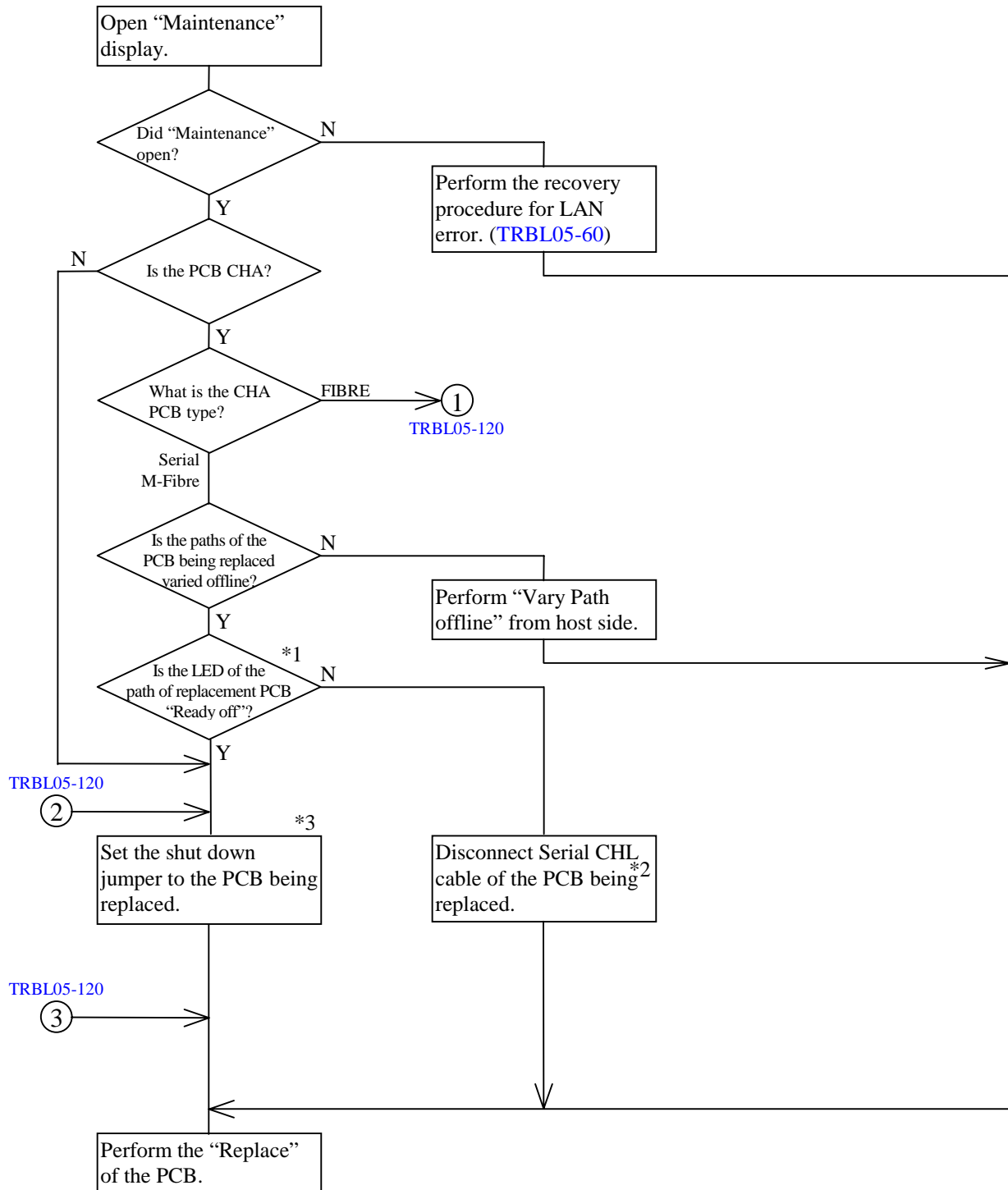
## 5.4 Error Recovery Procedure during CHA/DKA replacement

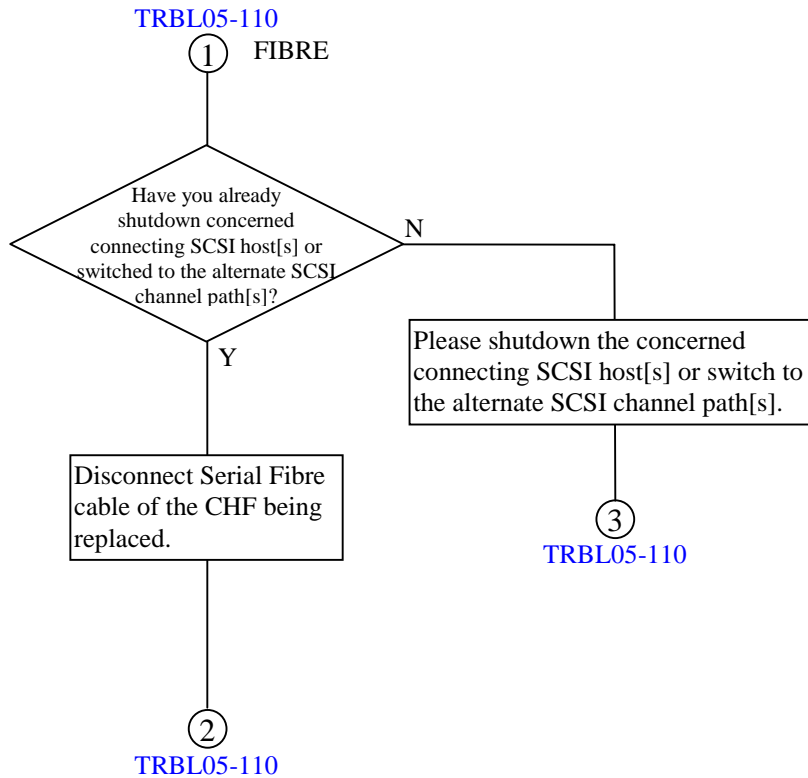
Perform the appropriate recovery procedure according to case (1) or (2).

## (1) Blocking error occurrence

If the following message is displayed when CHA/DKA is being blocked (when the message “The CHA is being blocked...” or “The DKA is being blocked...” is being displayed), perform the following recovery procedures.

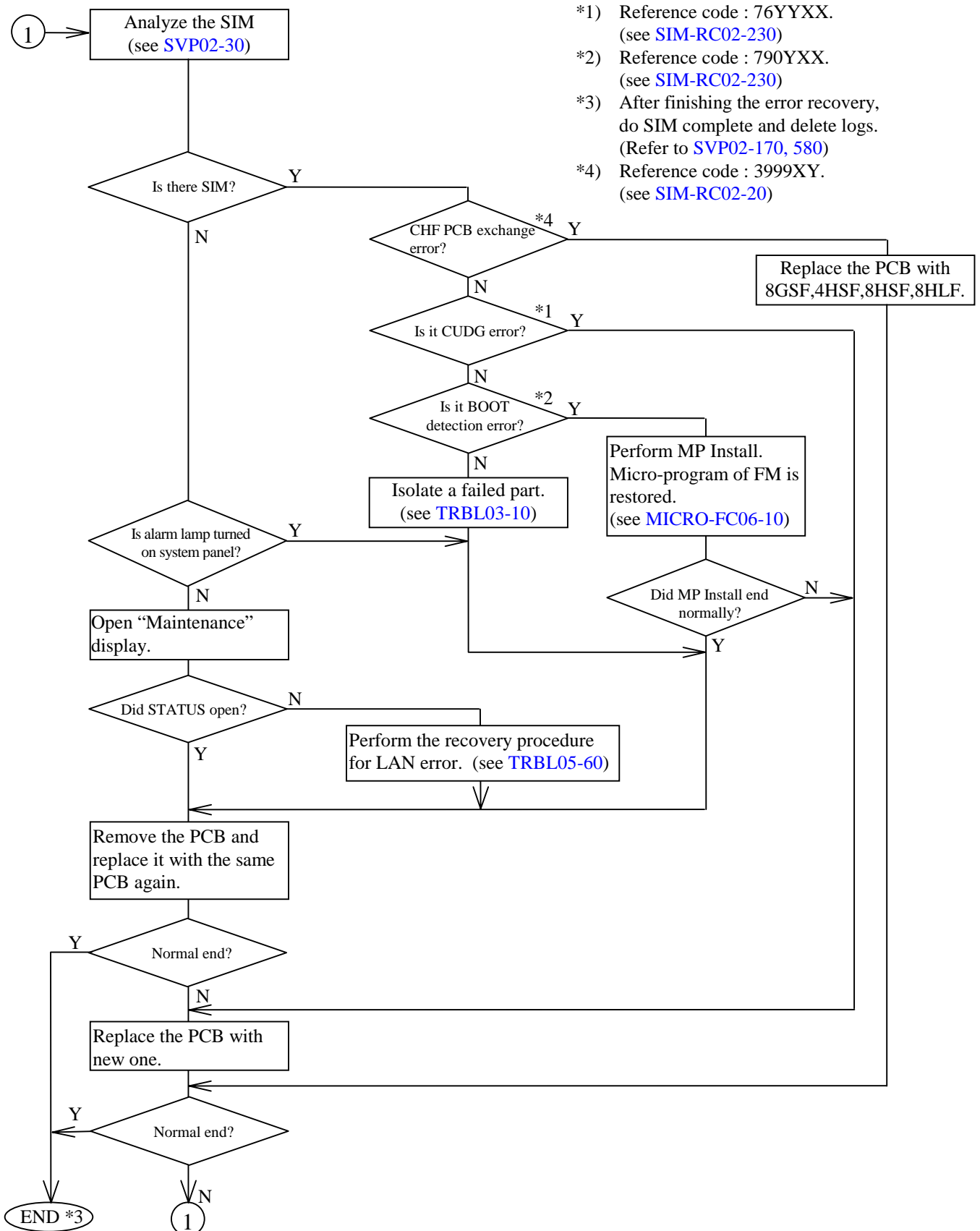
“There is no response from DKC within the time limit.”





- \*1) Verify that channel lamp of operating panel is turned off.
- \*2) After this operating, error message may be displayed at host side.
- \*3) After this operation, error SIM and SSB may occur. Refer to the following pages for shut down jumper insertion method depending on the type of PCB.
  - Serial Channel CHA : see #1 in [REP03-80](#)
  - Fibre Channel CHA : see #1 in [REP03-110](#)
  - M-Fibre Channel CHA: see #1 in [REP03-140](#)
  - DKA : see #1 in [REP03-170](#)

## (2) Other cases



## 5.5 Recovery Procedure for Cache Replace Failure (SIM = 3993XX, 3D93XX, FFE40X)

This recovery procedure is provided for cache replacement when replace failure SIM is reported.

- Cache replace failure caused by processor error

(REF code = 0x3993XY, 0x3D93XY : X = PK ID, Y = MP ID in PK)

- ① Insert the shut down jumper into the processor PCB shown in the SIM REF code.  
 (Serial channel CHA see #1 [REP03-80](#)).  
 (Fibre channel CHA see #1 [REP03-110](#)).  
 (M-Fibre channel CHA see #1 [REP03-140](#)).  
 (DKA see #1 [REP03-170](#)).

In this case, CHK3 or other equipment error are reported because no processing is performed. However, you should ignore these errors.

- ② Retry the suspended cache replacement.
- ③ Replace the PCB into which the shut down jumper inserted in Step ①.

- Cache replace failure caused by Shared Memory error

(REF code = FFE40X : X:(0) = side A, (1) = side B)

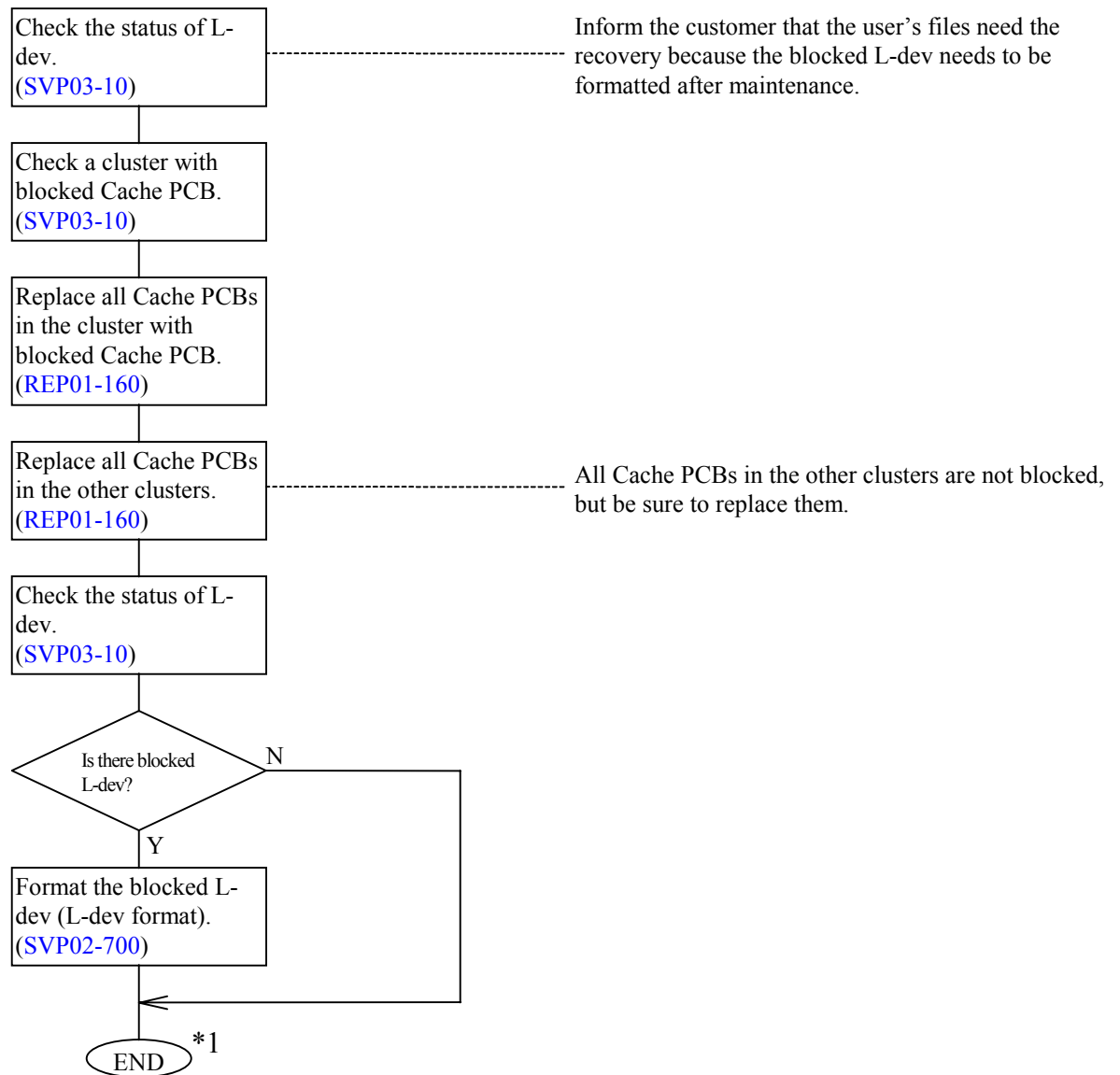
When this SIM occurs, SM failure can be the cause. But, when SM failure occurred without INLINE CUDG error, this error maybe caused by intermittent error. Therefore,

- ① Conduct cache replacement again.
- ② When normal end, cache replacement is completed.
- ③ When SM failure SIM is reported again, replace other cache P/K which was not replaced.

## 5.6 Recovery Procedure for Cache Error (Both sides) (SIM = FFF50X)

This procedure is to recover errors of the both sides of cache (SIM = FFF5) at powering on the subsystem.

At this time, if pending data (non-written data to the drive) exists on the cache, the drive will be blocked and the L-dev formatted drive will be required.



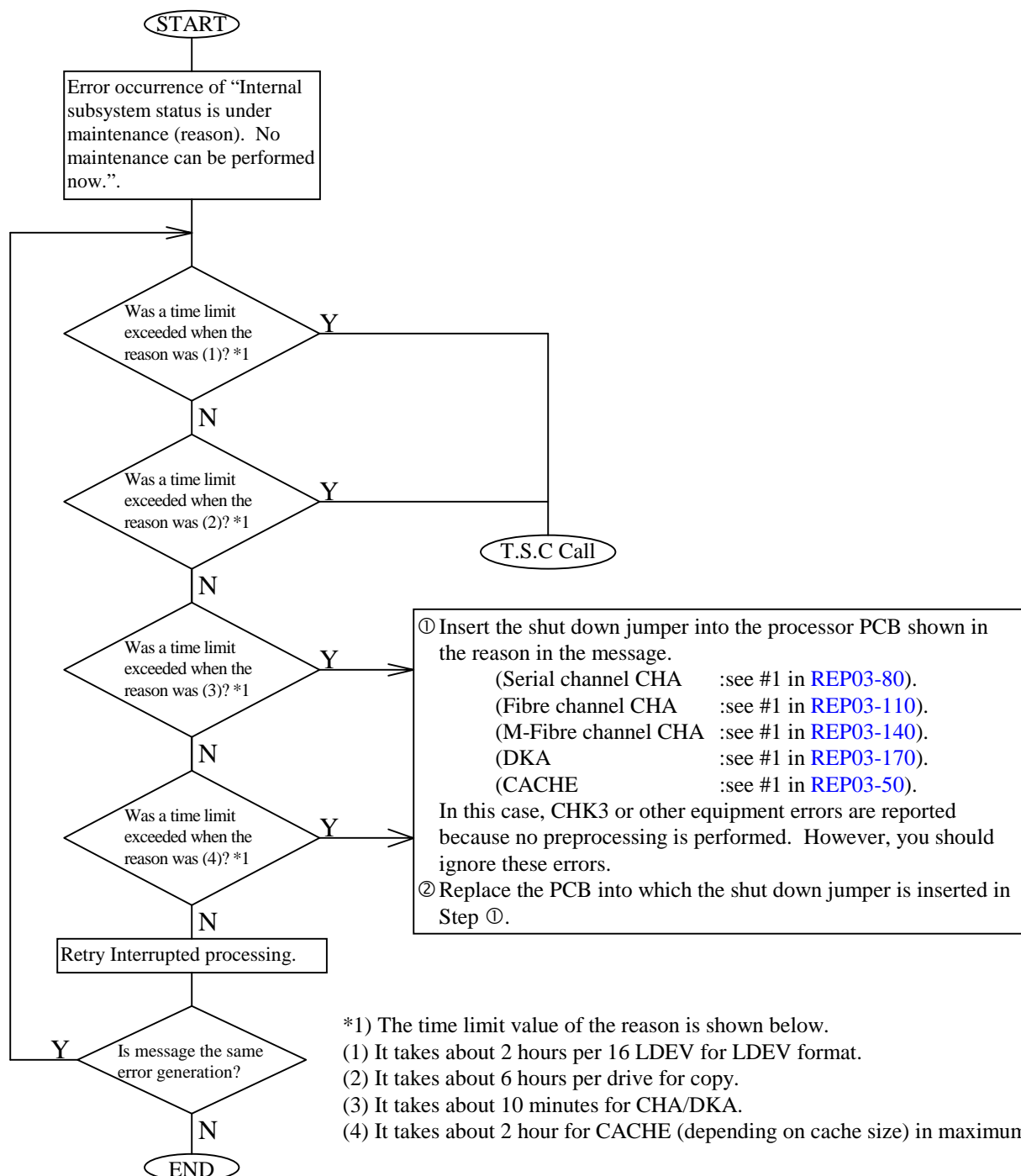
\*1) Delete Log after the end of this procedure. (SVP02-170)

## 5.7 Recovery Procedures for Status in changing is not released

The SVP protects maintenance operation while the DKC is changing its status. If the SVP detects such condition before executing maintenance process, the message as “Internal subsystem status is under maintenance (reason). No maintenance can be performed now.” is displayed. The reason is one of the following messages.

- (1) LDEV format in progress.  
(2) Copy in progress.
- (3) CHA-xx (or DKA-xx) changing the status.  
(4) Cache-xx changing the status.
- xx:package number

If one of the above message appears, try the following recovery procedure.

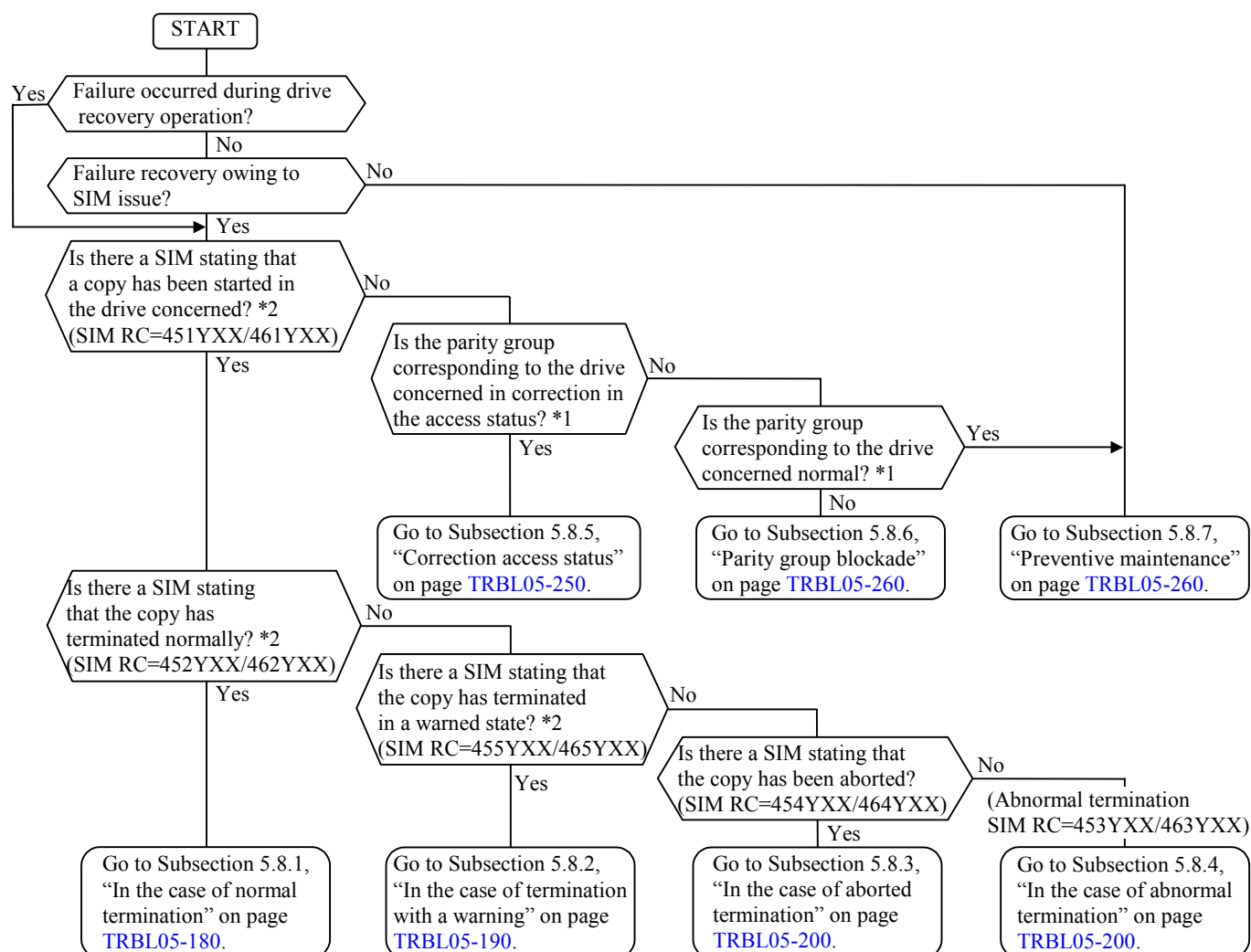


## 5.8 Drive failure recovery procedure

Explanation:

Types of the drive copy are shown below. Confirm the copy type and execute the drive recovery procedure.

Copy type	Description	Operation occasion
Correction copy	This is an operation to restore and copy the data of the blocked drive using the data of another drive and parity data when the data drive is blocked. <ul style="list-style-type: none"> <li>A correction copy to the spare drive is referred to as another drive correction copy.</li> <li>A correction copy to the replacement drive which has been installed instead of the blocked drive is referred to as a self-drive correction copy.</li> </ul>	<ul style="list-style-type: none"> <li>Automatic operation owing to a failure</li> <li>Operation instructed by the maintenance personnel</li> </ul>
Drive copy	This is an operation to copy data to the spare drive from the data drive. A copy automatically performed owing to a warning level failure is referred to as dynamic sparing.	<ul style="list-style-type: none"> <li>Automatic operation owing to a failure</li> <li>Operation instructed by the maintenance personnel</li> </ul>
Copy back	This is a copy for returning data which has been copied to the spare drive by another drive correction copy or drive copy to the original data drive.	<ul style="list-style-type: none"> <li>Operation instructed by the maintenance personnel</li> </ul>



\*1: Confirm the status of the parity group corresponding to the drive concerned by means of the SVP status.

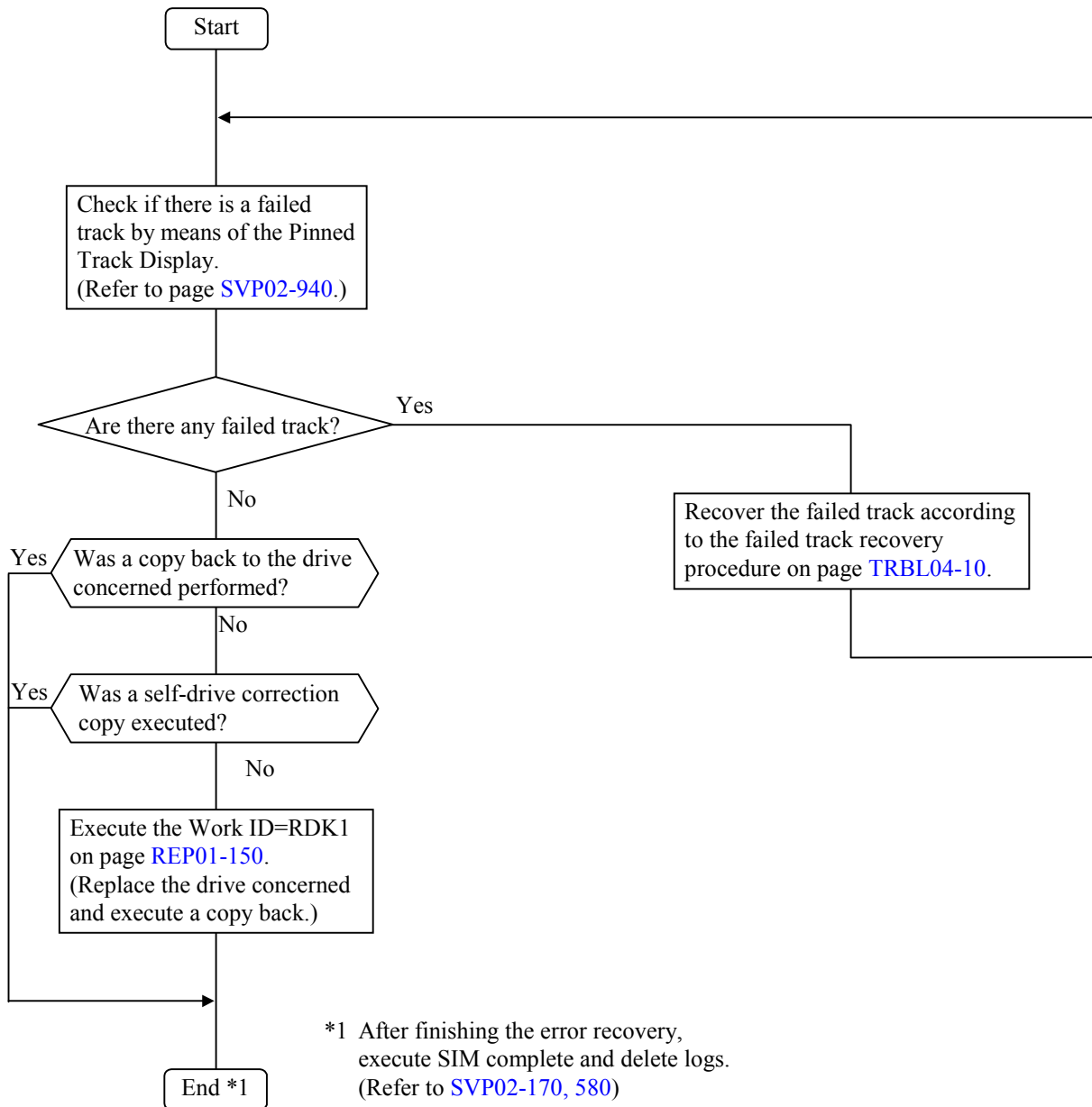
For the procedure for referring to the SVP status, refer to Section 3.8, "Logical device" on page SVP03-170.

\*2: The term "copy" means drive copy, copy back, or correction copy.



### 5.8.1 In the case of normal termination (SIM RC=452YXX/462YXX)

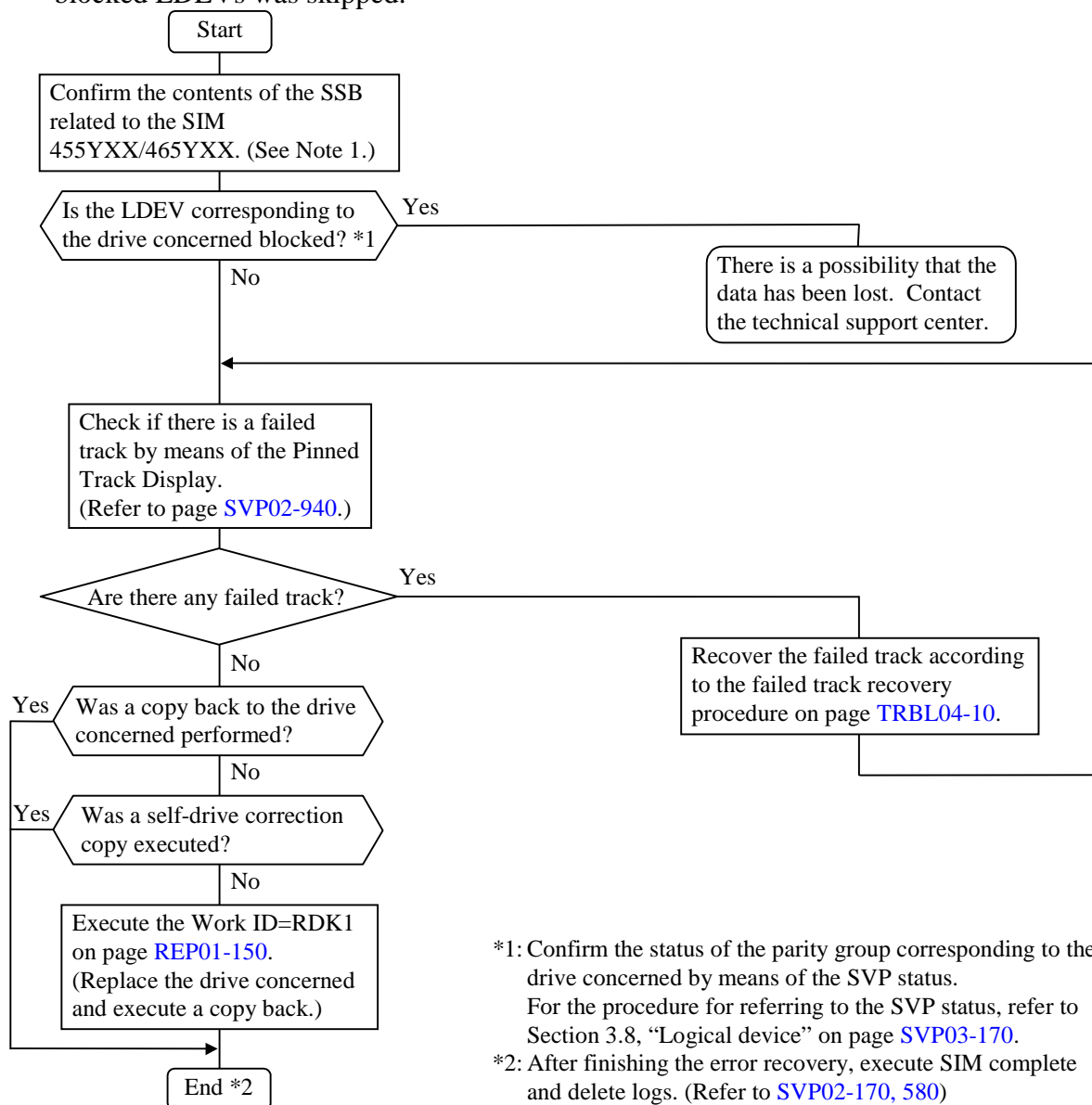
The procedure below is that used after a drive copy, copy back, or correction copy terminates normally.



### 5.8.2 In the case of termination with a warning (SIM RC=455YXX/465YXX)

The procedure explained below is that used when the drive copy, copy back, or correction copy terminates but one of the following is detected:

- (1) A failed track was detected, but the copy was continued.
- (2) One or more blocked LDEVs were detected in the same parity group. Copy of data of the blocked LDEVs was skipped.



Note 1: Meaning of bytes 40 to 7E of SSB F/M=9F EC=9355 related to SIM  
RC=455YXX/465YXX

Byte (Hex.)	Item	Meaning
40	Number of failed tracks	Number of failed tracks
41	Number of blocked LDEVs	Number of blocked LDEVs
42	Blocked LDEV #0	Blocked LDEV No. (Up to 30 LDEVs can be inputted from the top.) However, 0Xff is inputted in the unused part.
:	:	
7D	Blocked LDEV #29	
7E	E.O.D	Means the end of data

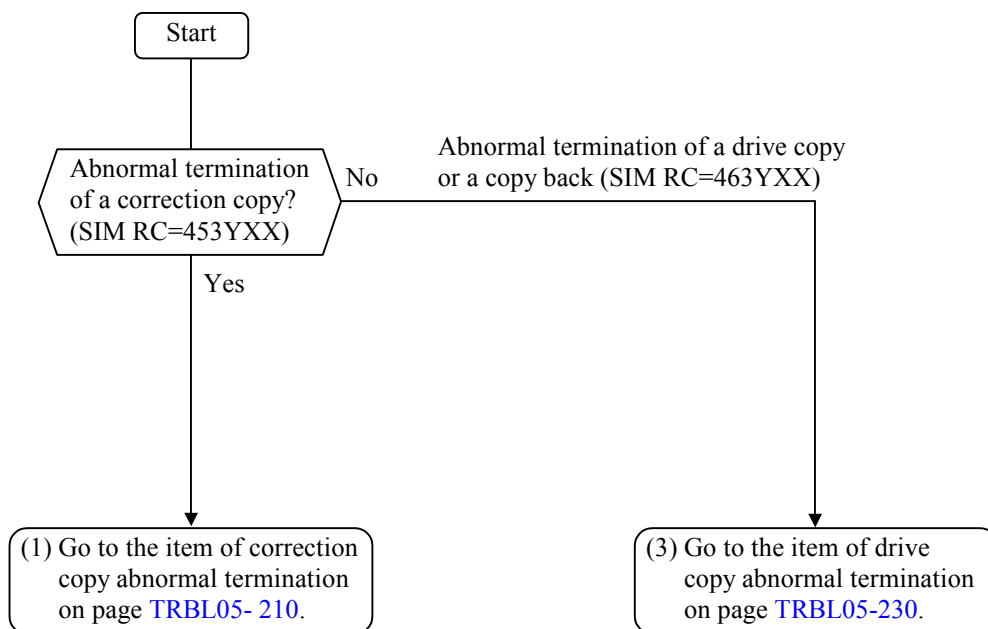
### 5.8.3 In the case of termination owing to abortion (SIM RC=454YXX/464YXX)

The procedure below is that used when a drive copy, copy back, or correction copy is aborted owing to the SVP operation by the service personnel. Since the status of the drive concerned is that before starting copy, execute the recovery operation for the same drive once again.

### 5.8.4 In the case of abnormal termination (SIM RC=453YXX/463YXX)

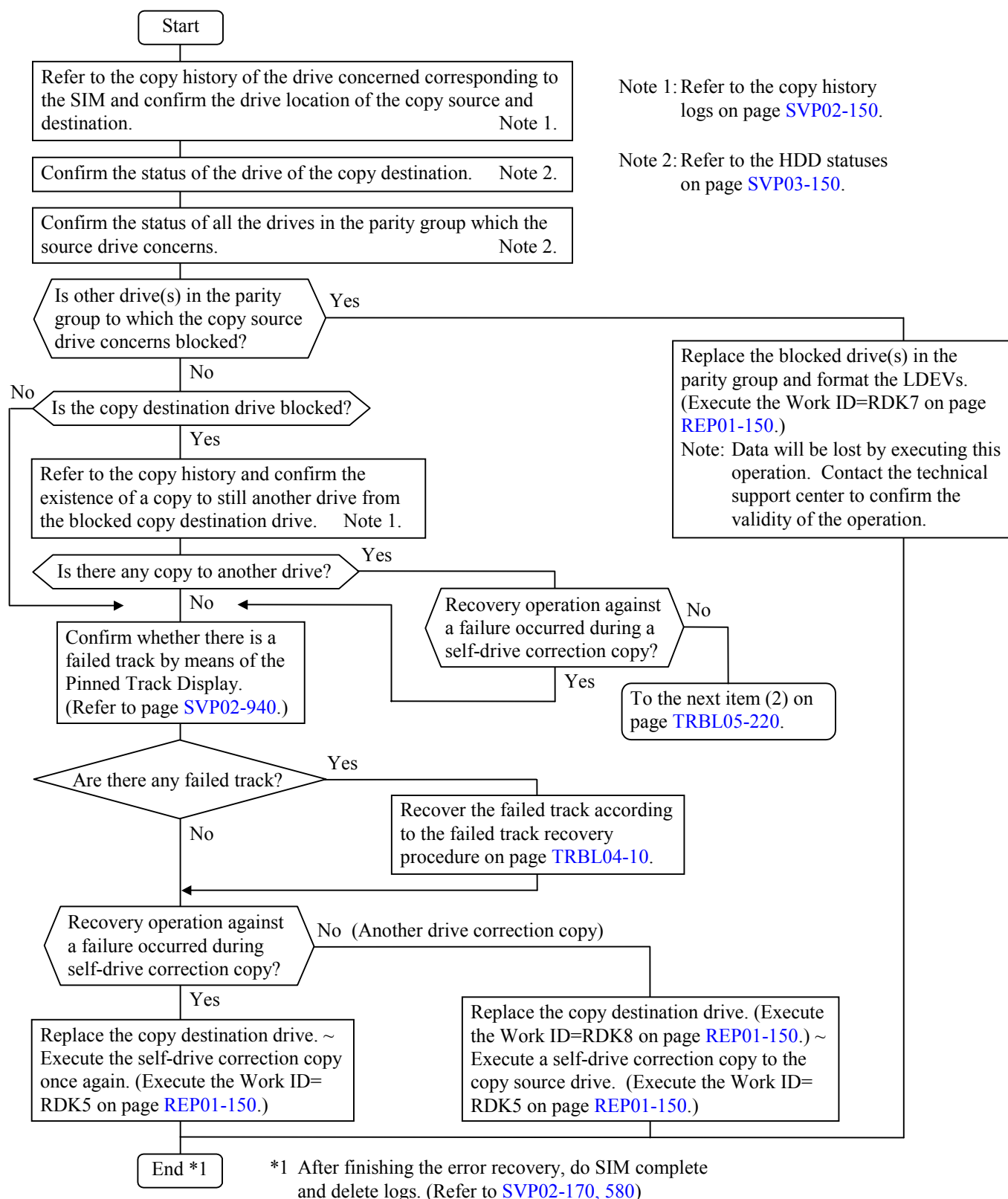
The procedure below is that used when a drive copy, copy back, or correction copy terminates abnormally.

Since the recovery procedure may differ depending on the copy type, confirm the description on page [TRBL05-170](#) and execute the following procedure.



## (1) In the case of correction copy abnormal termination

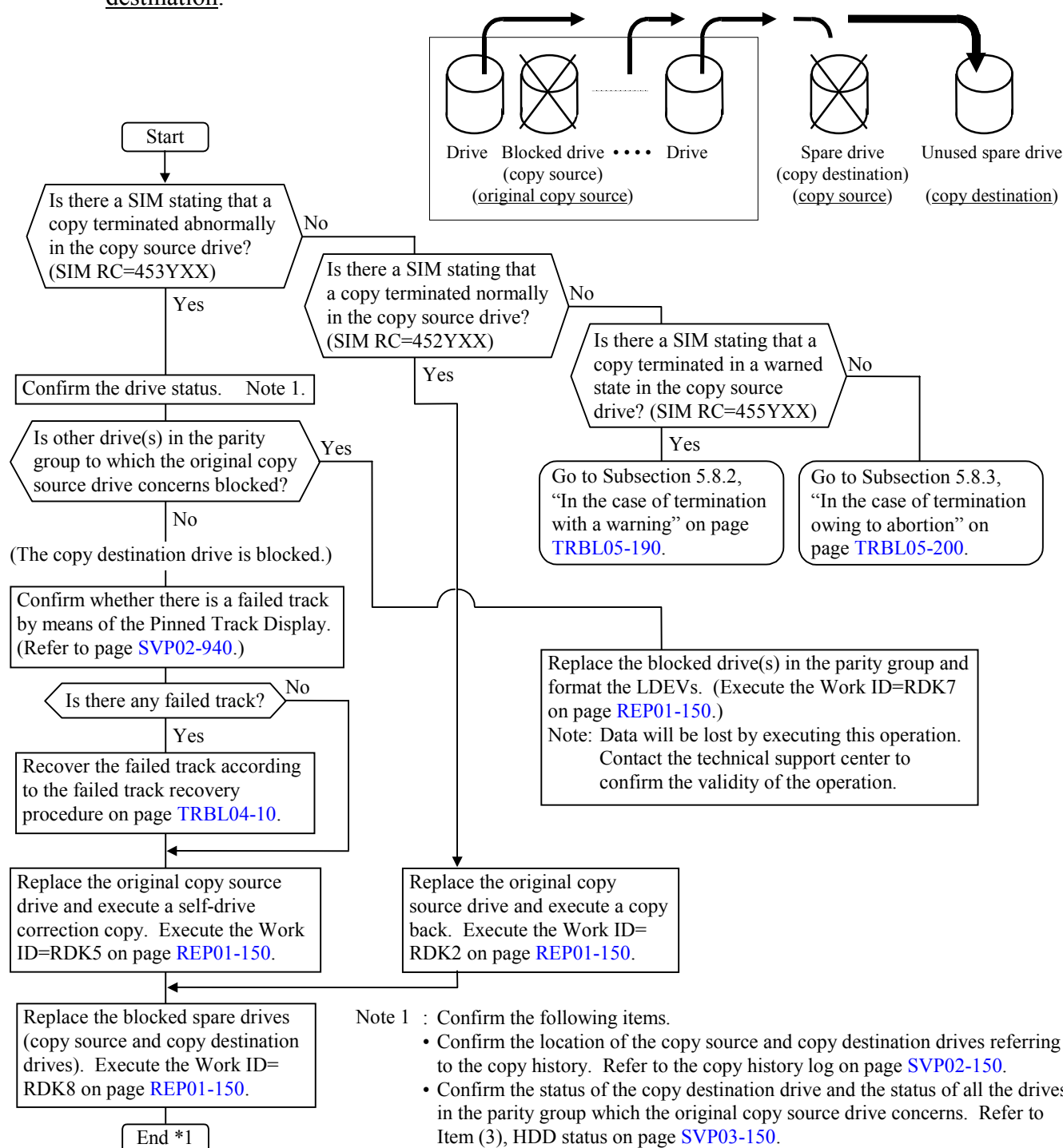
The procedure below is that used when a correction copy terminates abnormally.



- (2) In the case of correction copy abnormal termination (in which an unused spare drive is available)

This is a case in which a correction copy terminates abnormally owing to a failure in the copy destination drive. However, if an unused spare drive exists, the correction copy is automatically performed to the unused spare drive.

In the following procedure, the copy source is referred to as a original copy source, the copy destination is referred to as a copy source, and the unused spare drive is referred to as a copy destination.



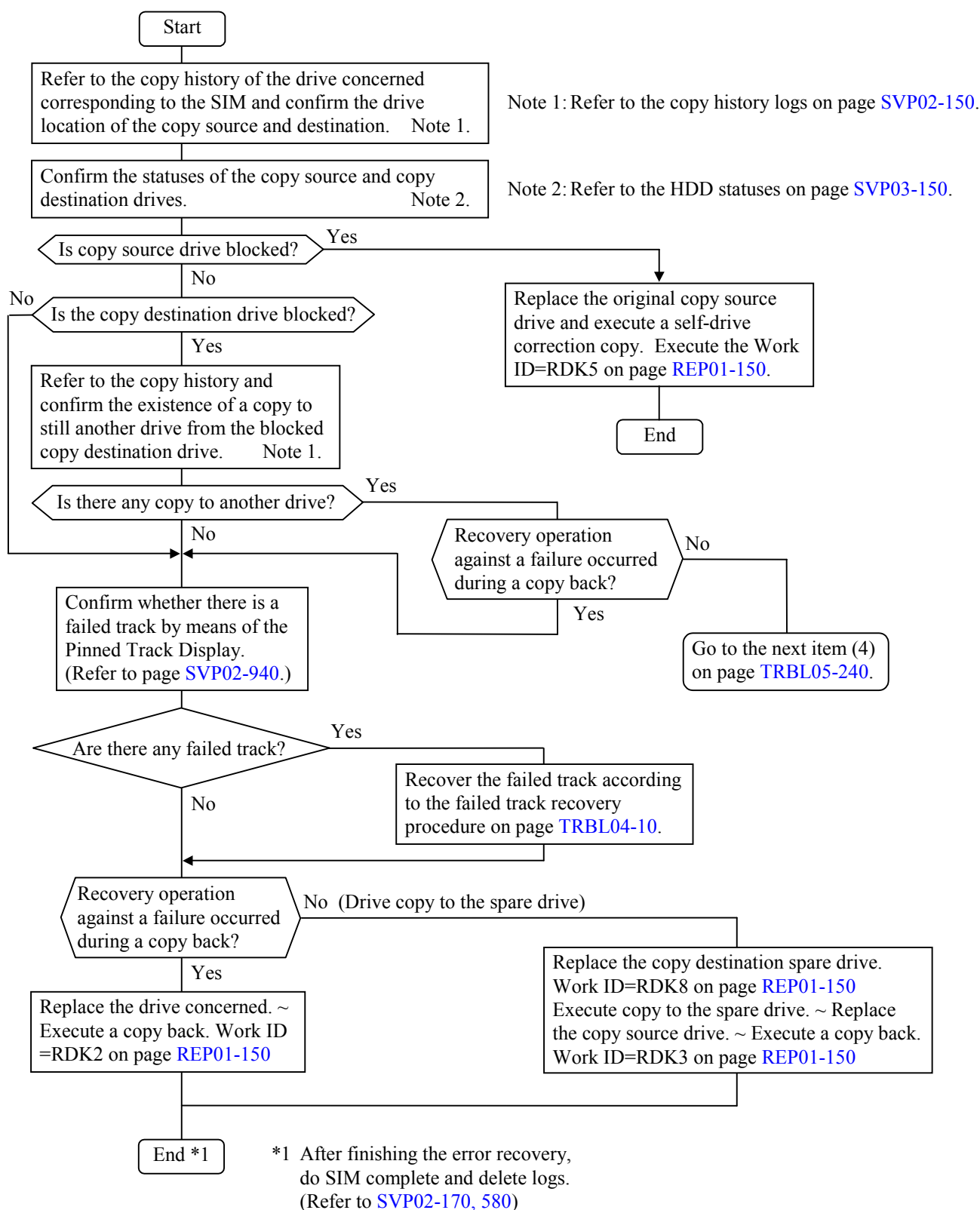
Note 1 : Confirm the following items.

- Confirm the location of the copy source and copy destination drives referring to the copy history. Refer to the copy history log on page SVP02-150.
- Confirm the status of the copy destination drive and the status of all the drives in the parity group which the original copy source drive concerns. Refer to Item (3), HDD status on page SVP03-150.

\*1 After finishing the error recovery, do SIM complete and delete logs. (Refer to SVP02-170, 580)

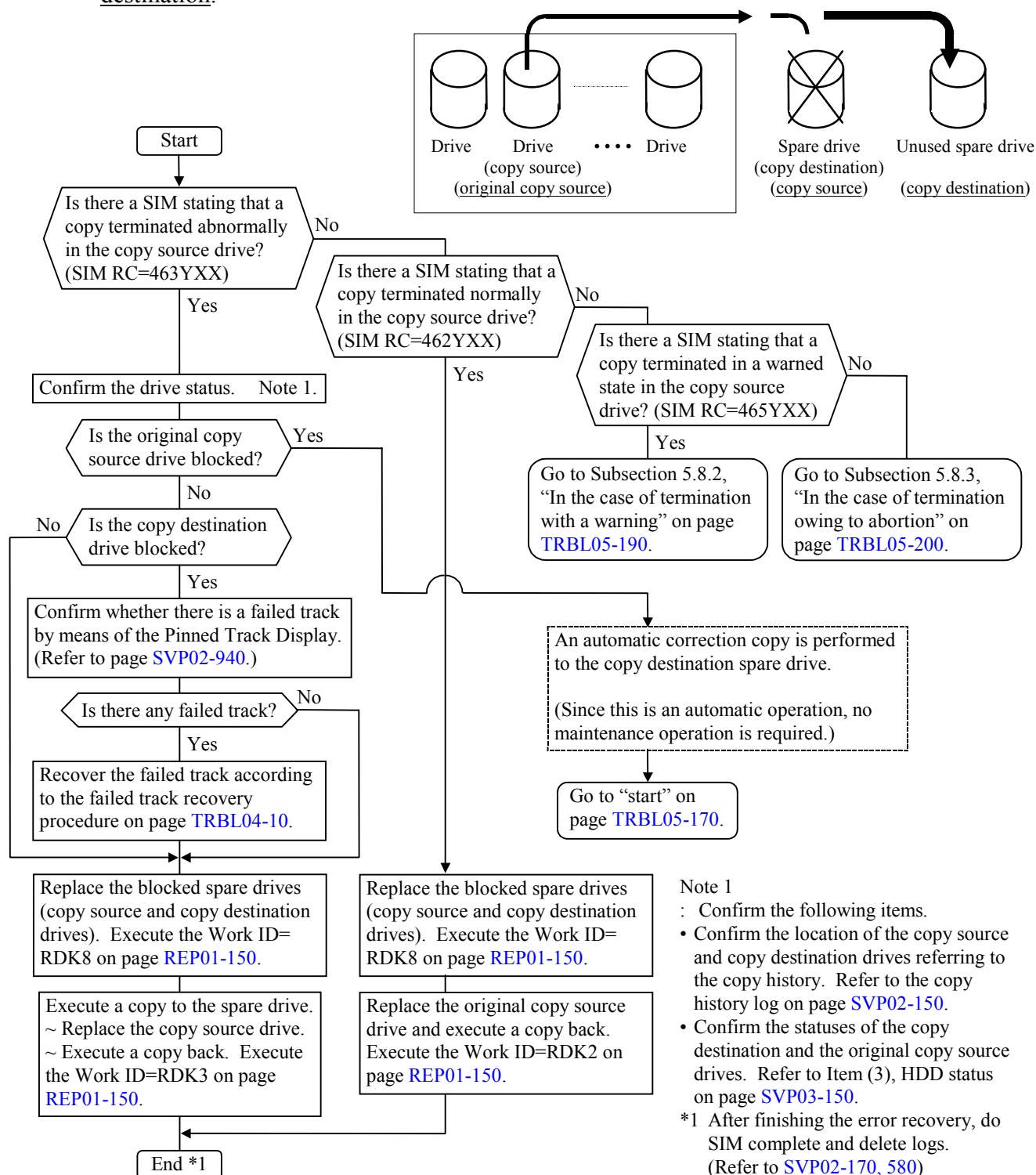
## (3) In the case of drive copy abnormal termination

The procedure below is that used when a drive copy or a copy back terminates abnormally.



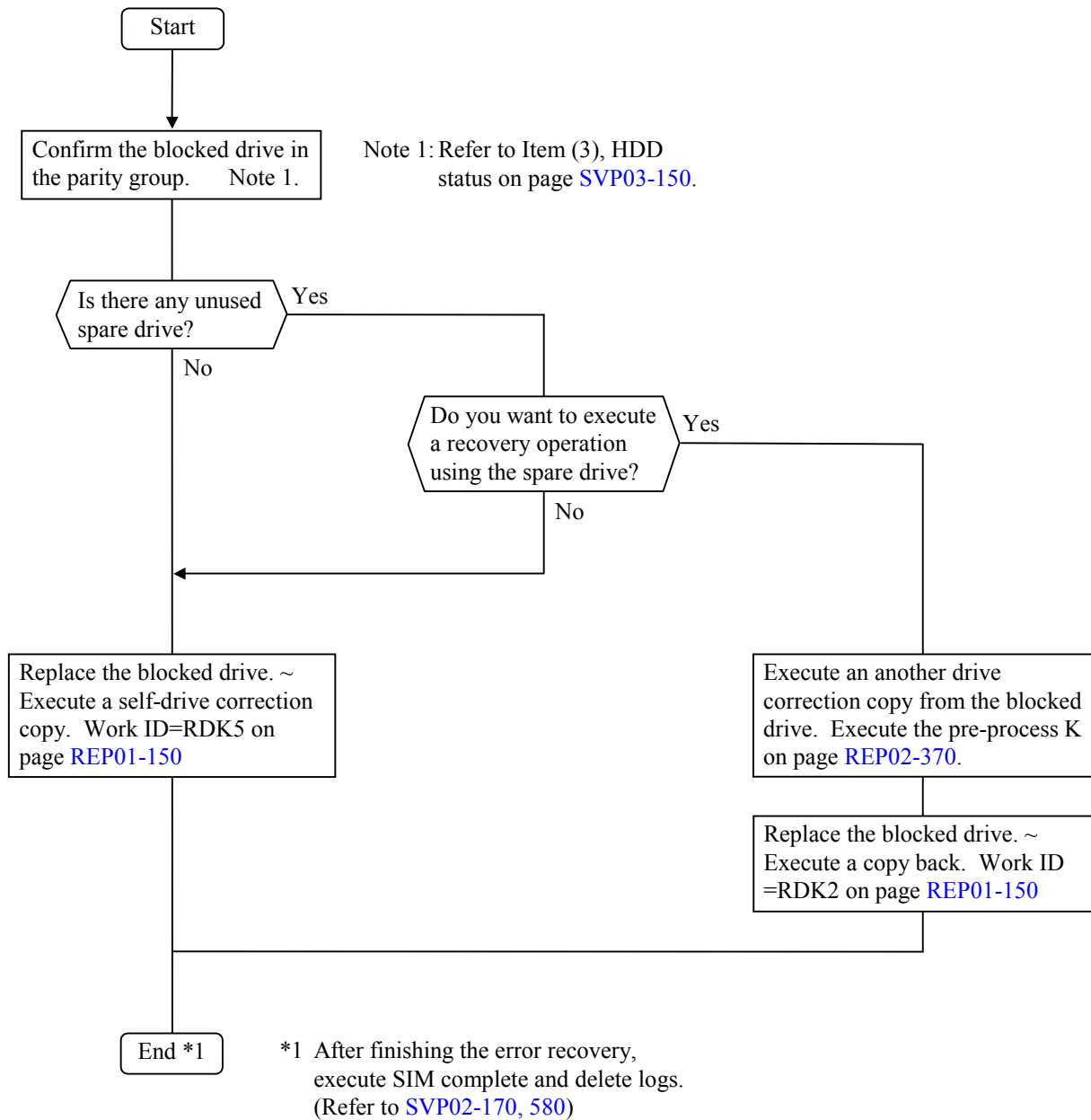
- (4) In the case of drive copy abnormal termination (in which an unused spare drive is available) This is a case in which a drive copy terminates abnormally owing to a failure in the copy destination drive. However, if an unused spare drive exists, the drive copy is automatically performed to the unused spare drive.

In the following procedure, the copy source is referred to as an original copy source, the copy destination is referred to as a copy source, and the unused spare drive is referred to as a copy destination.



## 5.8.5 Correction access status

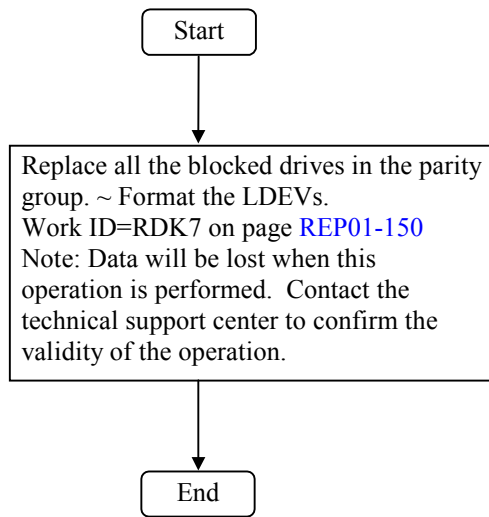
The procedure below is that used when one drive in the parity group is blocked.





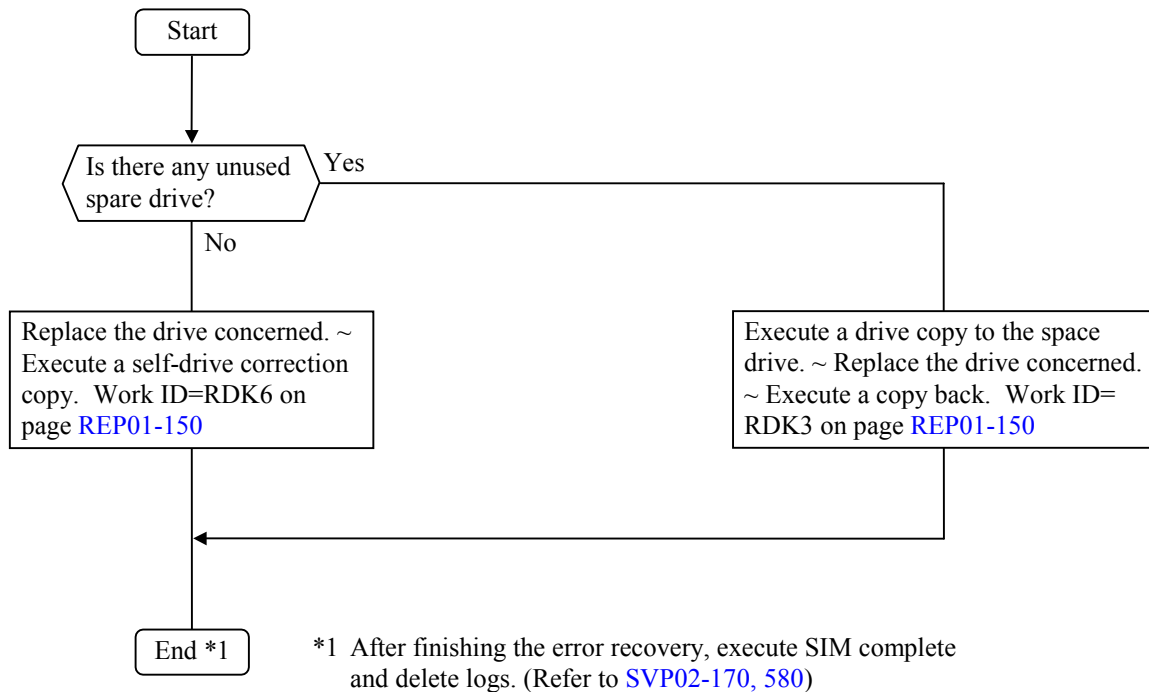
### 5.8.6 Parity group blockade

The procedure below is that used when the two or more drives are blocked in the parity group.



### 5.8.7 Preventive maintenance

The procedure below is that used when the drive is not blocked but it must be replaced.



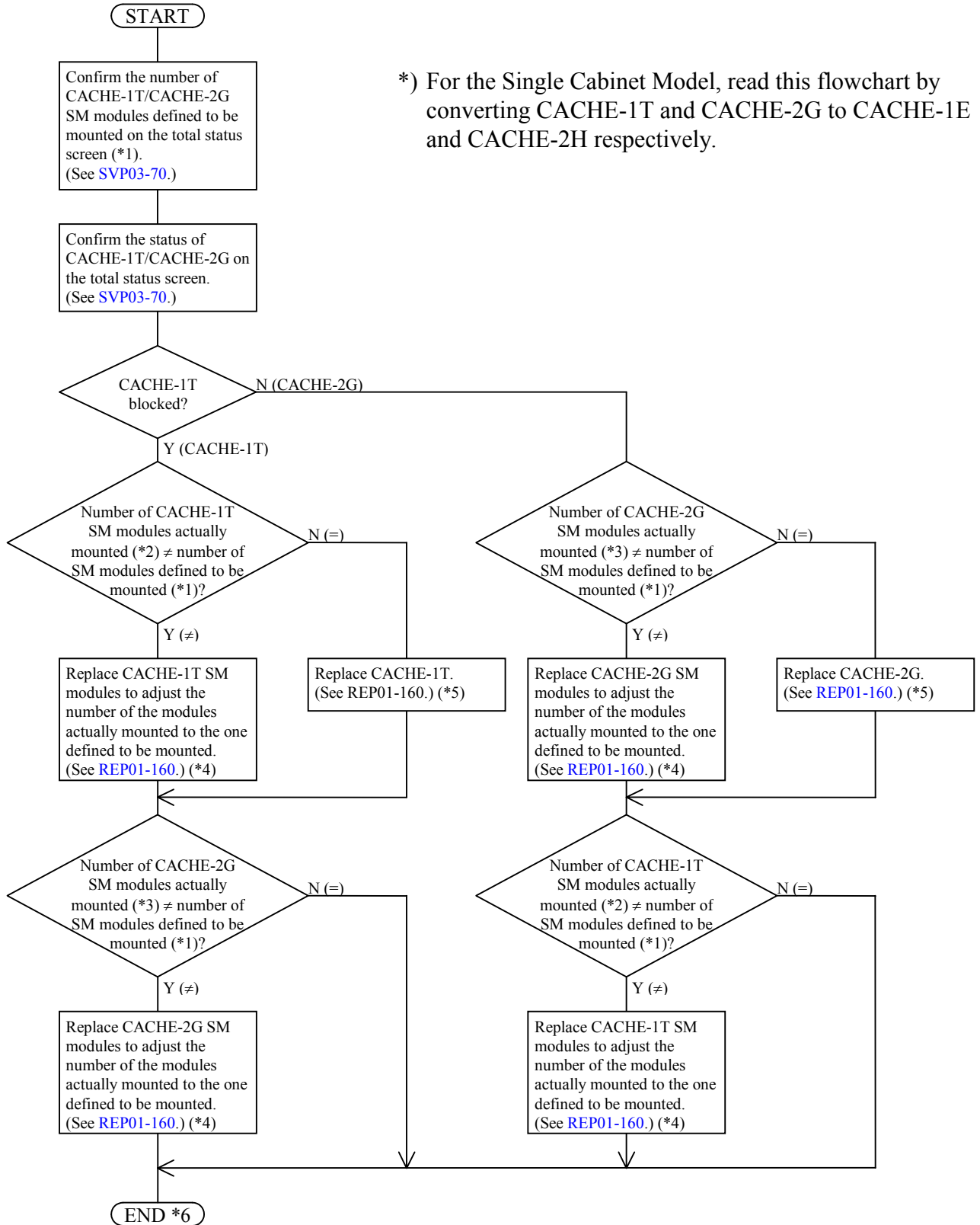
## 5.9 Recovery Procedure for SM Capacities Inequality (SIM = FFE3XY)

The mounted SM capacities inequality error (ffe3 xy/FPC = 80000000) should be recovered following the procedure below.

The number of CACHE-1T/CACHE-2G\* SM modules mounted is set in x/y respectively.

This error occurs if an SM is blocked because the mounted SM capacity differs between CACHE-1T and CACHE-2G (PCBs mounting SM modules). Therefore, the number of SM modules mounted must be adjusted correctly to recover this error.

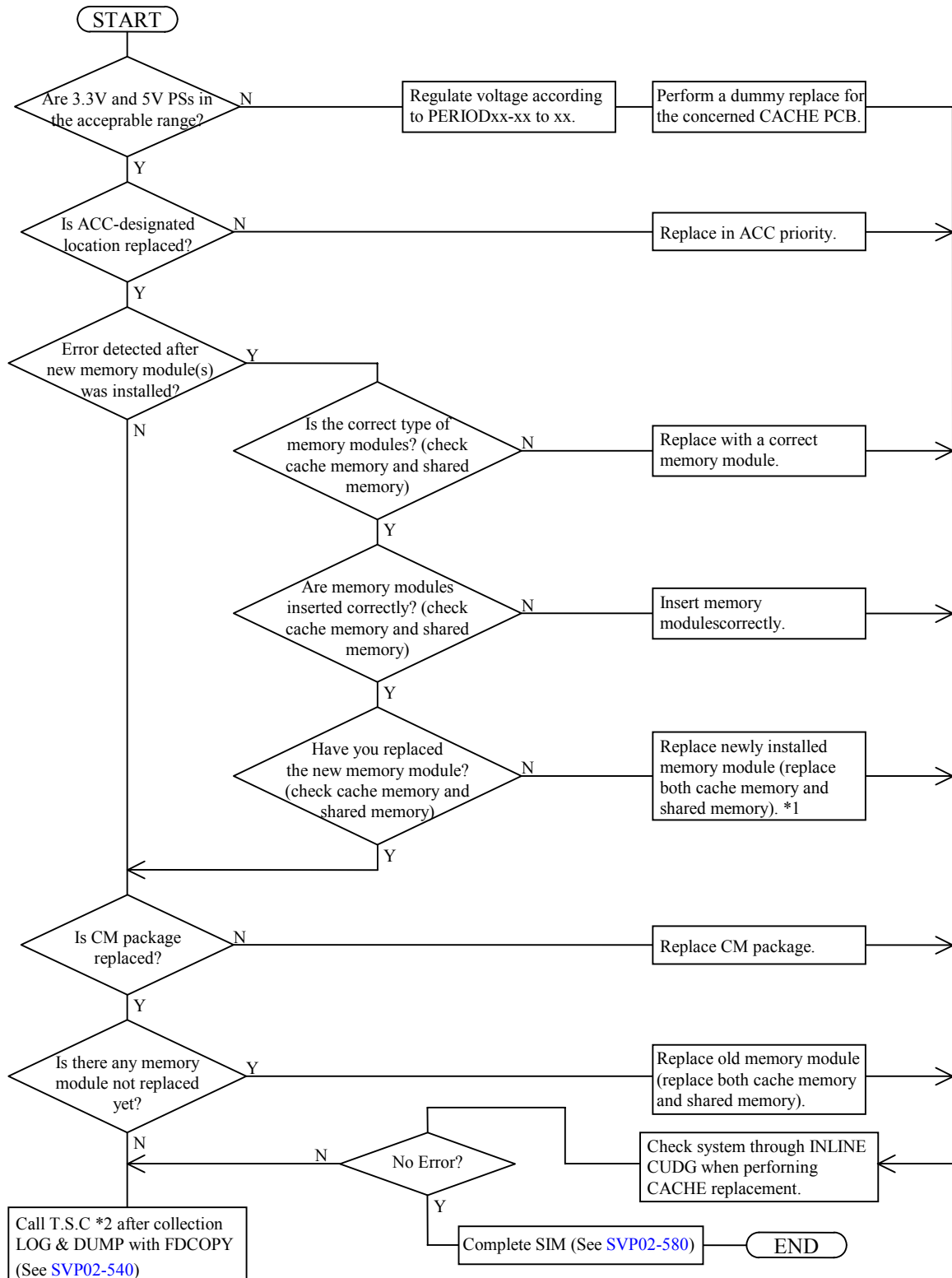
\*) For the Single Cabinet Model, read this flowchart by converting CACHE-1T and CACHE-2G to CACHE-1E and CACHE-2H respectively.



- \*1: Number of SM modules indicated not to be “not-mounted” on the status screen.
- \*2: Value x (number of SM modules) in REF-CD = FFE3xy
- \*3: Value y (number of SM modules) in REF-CD = FFE3xy
- \*4: Confirm the number of mounted SM modules to adjust it.
- \*5: No need to adjust the number of SM modules mounted nor replace any of them.
- \*6: After finishing the error recovery, do SIM complete and delete logs.  
(Refer to [SVP02-170, 580](#))

## 5.10 Cache Memory Error Isolation Procedure (SIM = FFF0XX, FFF1XX, FFF2XX, FFE0XX, FFE1XX, FFE2XX)

Isolate a cache memory or shared memory error according to the procedure given below.



- 
- \*1: If there are multiple module groups, replace one by one.  
If the symptom is not cleared even after replacement, de-install newly installed memory modules to return to the original configuration.
  - \*2: T.S.C : Technical Support Center

## 5.11 Recovery Procedure for LDEV Blocking (SIM = CF90XY, EF9YXX, DFAYXX, DFBYXX)

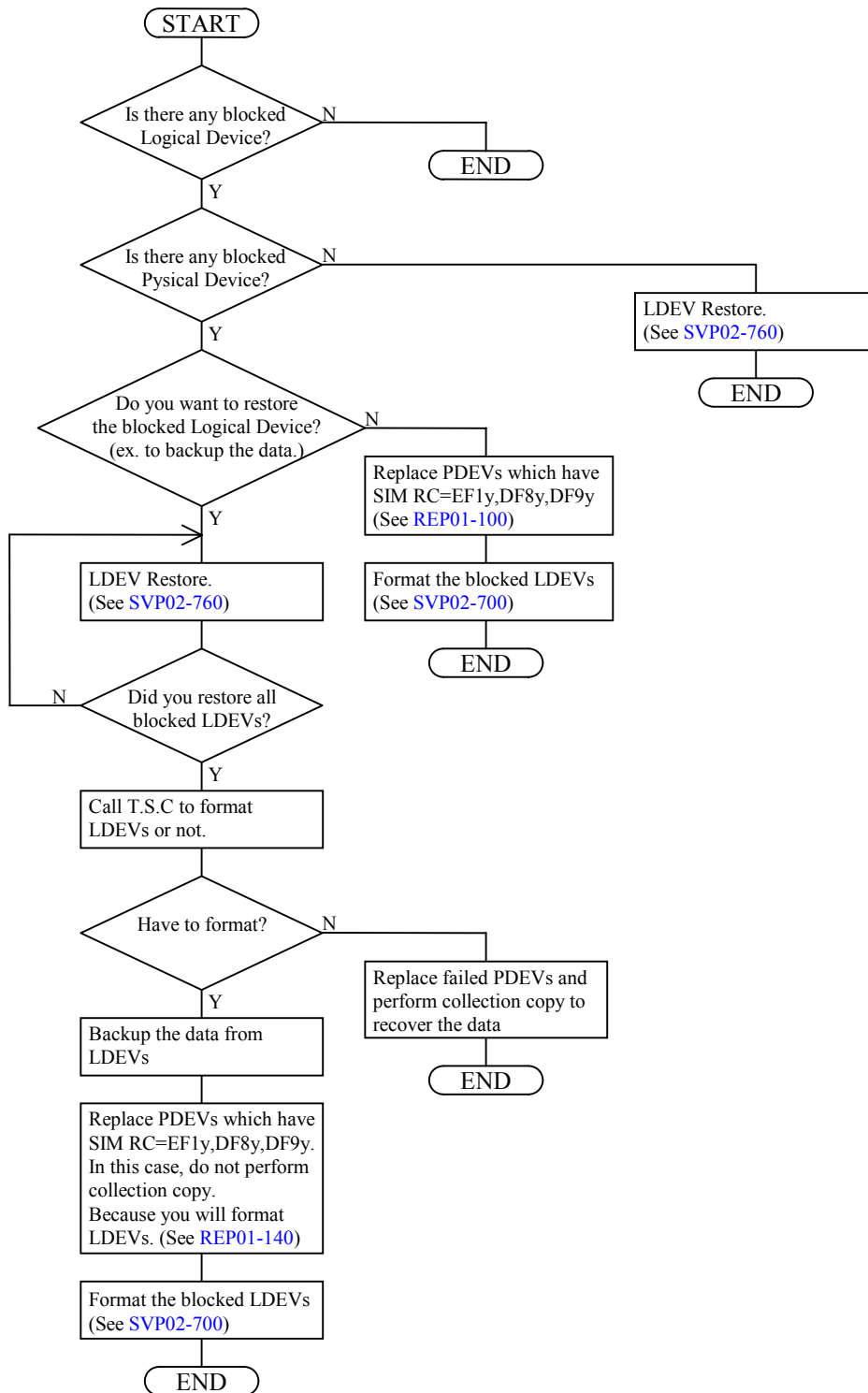
When LDEVs are blocked in the case of blocking several PDEVs (SIM RC=EF9Yxx, DFAYxx, DFBYxx), or in the case of blocking FCA (SIM RC=CF90XY), perform the following recovery procedures.

Be sure to call T.S.C. before you perform PDEV replace. It causes a DATA-LOSS in some cases.

Notice : When you execute a Recovery Procedure for LDEV Blocking, you must delete the HRC/HODM pair.

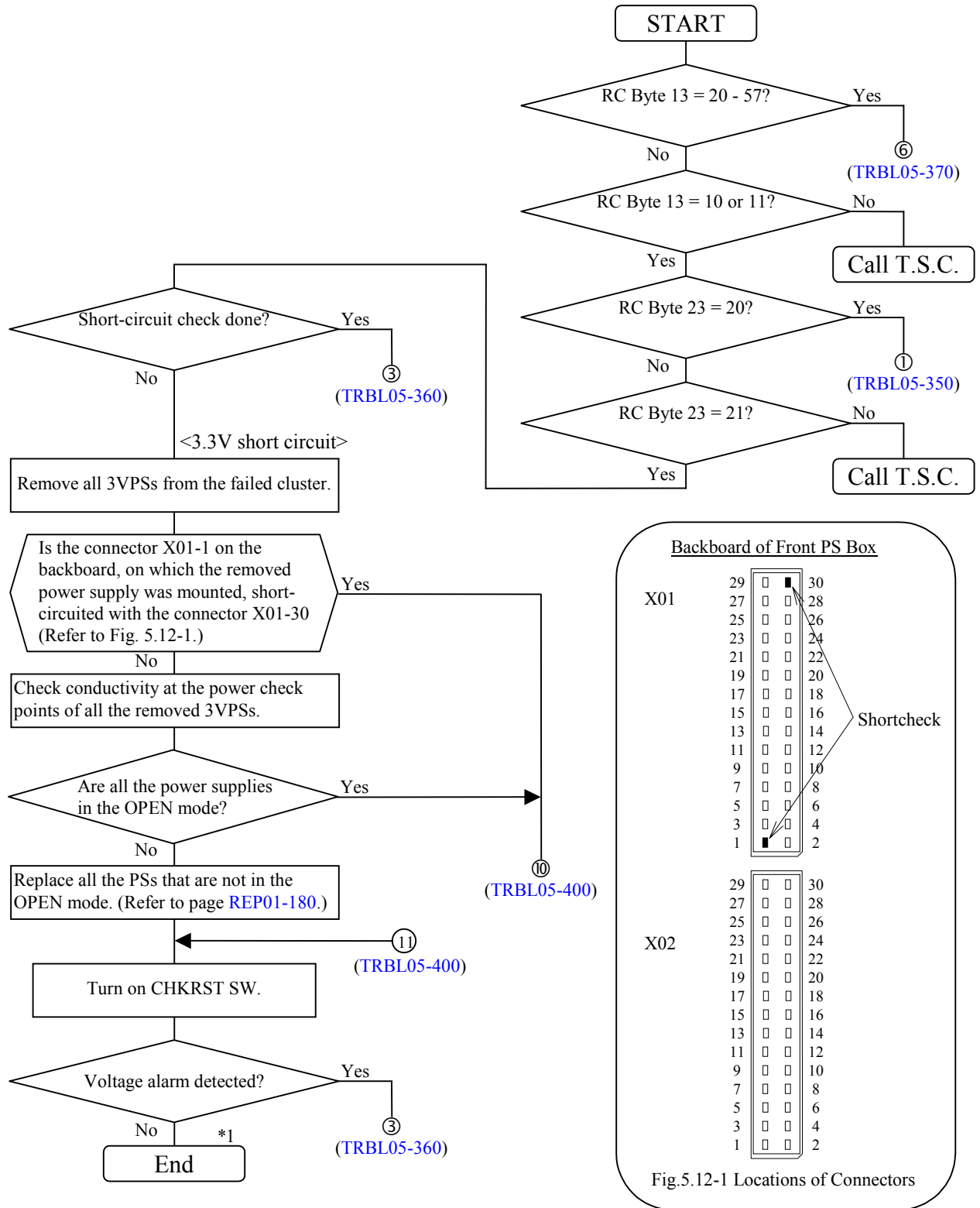
After recovering it, if necessary, you execute establish pair.

SIM RC = CF90, DF9y, DFAy, DFBy





## 5.12 Voltage alarm (SIM = BF2XYY)



\*1: If you finish the maintenance, delete the log, SIM complete and recover Cluster. (Refer to [SVP02-170](#), [580](#) and [1110](#).)

\*2: Confirm that the failed cluster is inactive, prior to checking short-circuit.

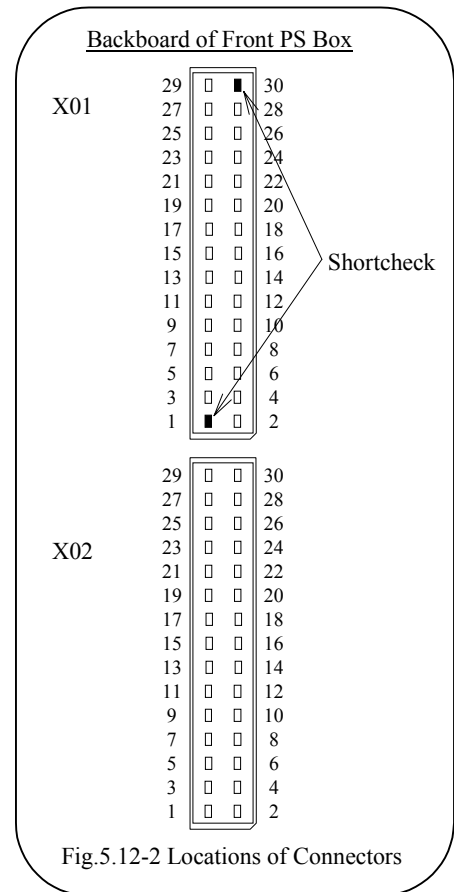
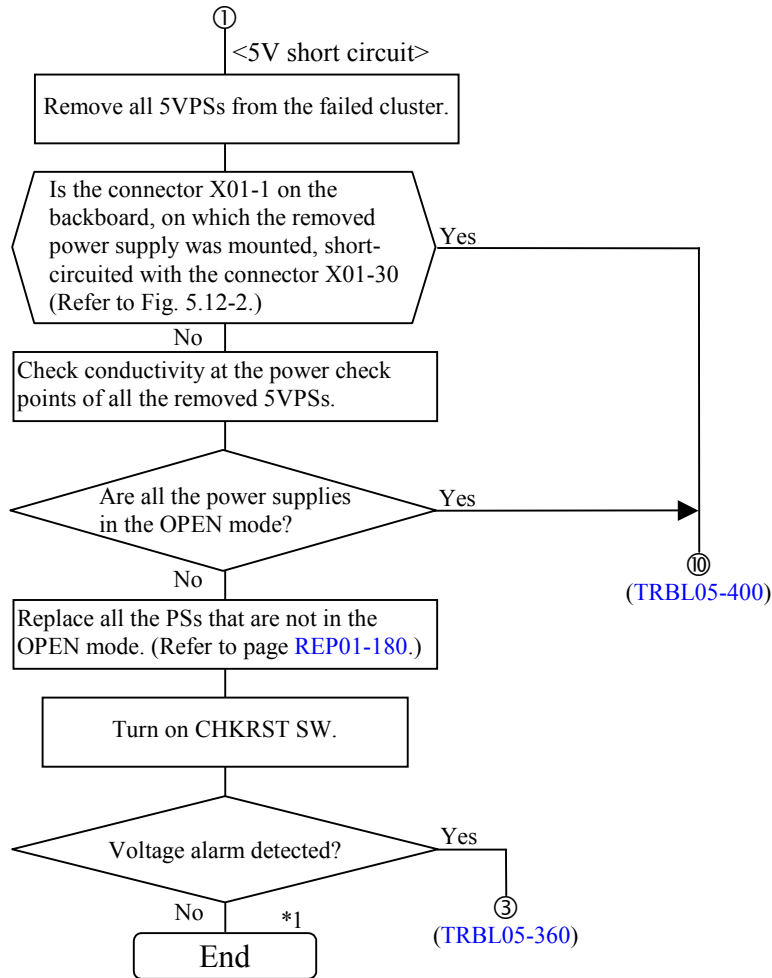
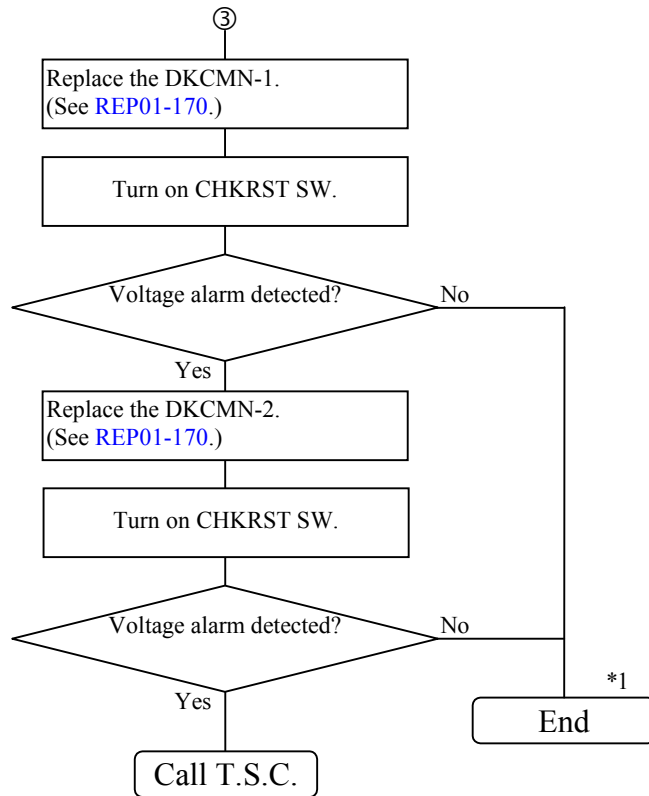
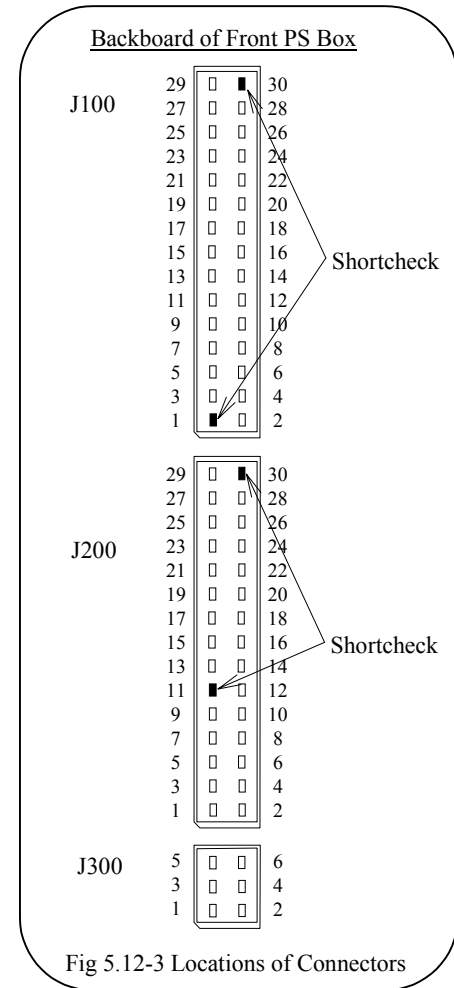
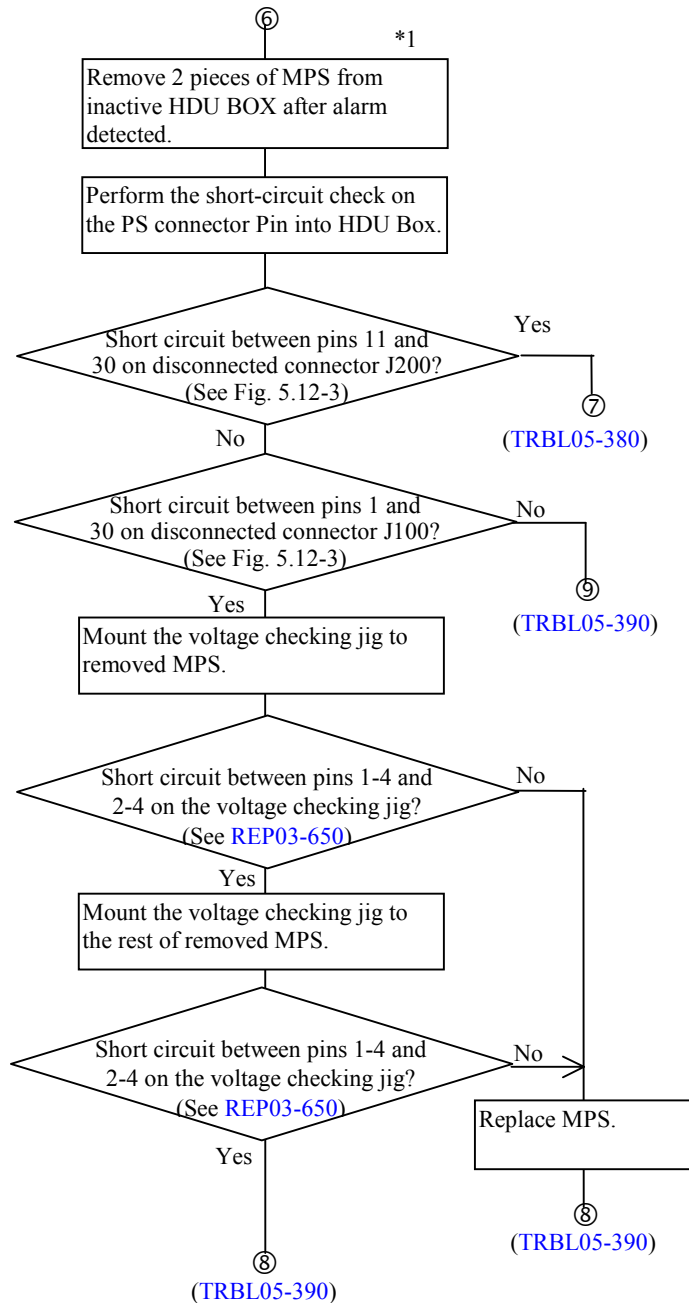


Fig.5.12-2 Locations of Connectors

\*1 : If you finished the maintenance, delete the log, SIM complete and recover Cluster.(Refer to SVP02-170, 580 and 1110.)

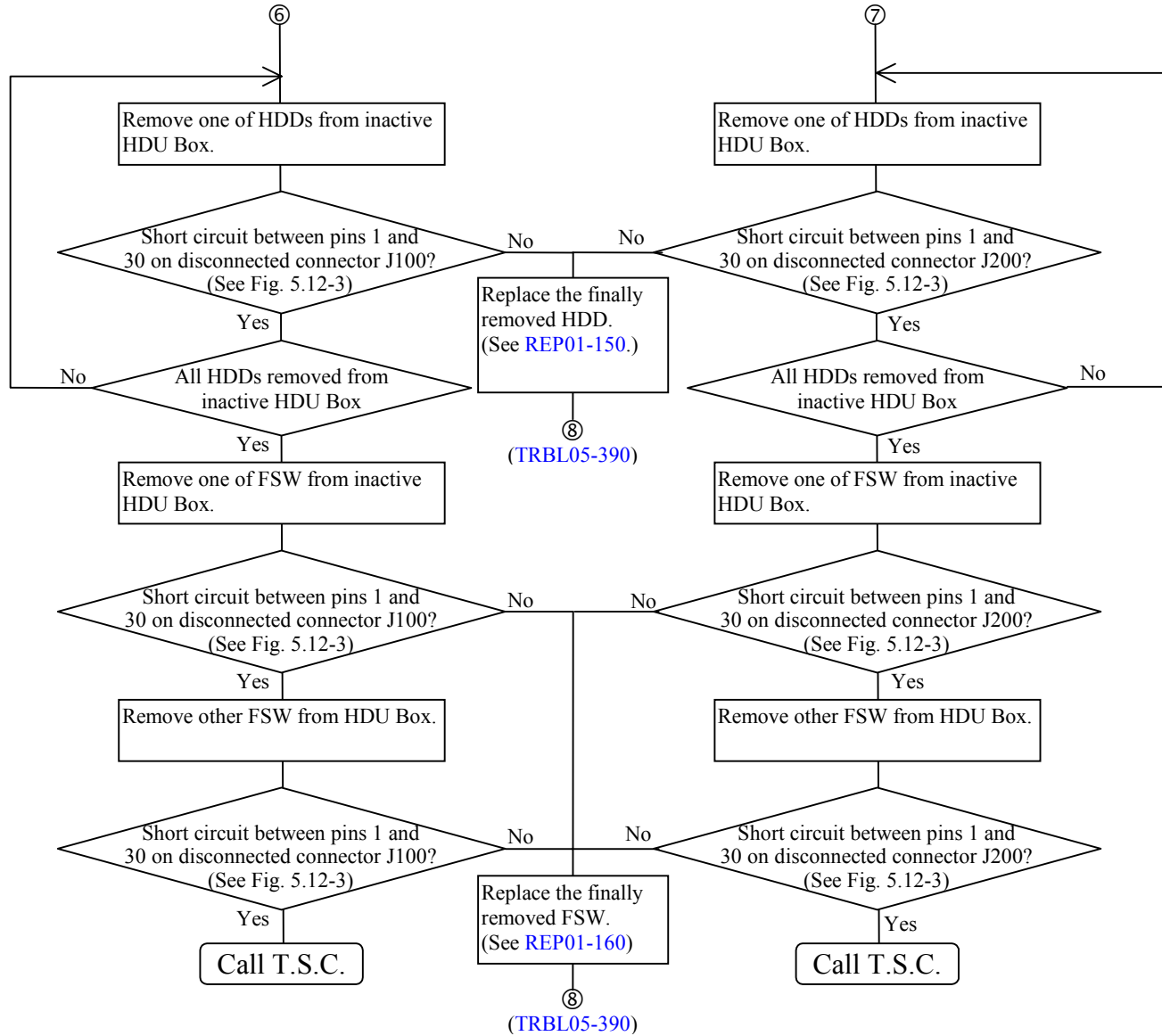


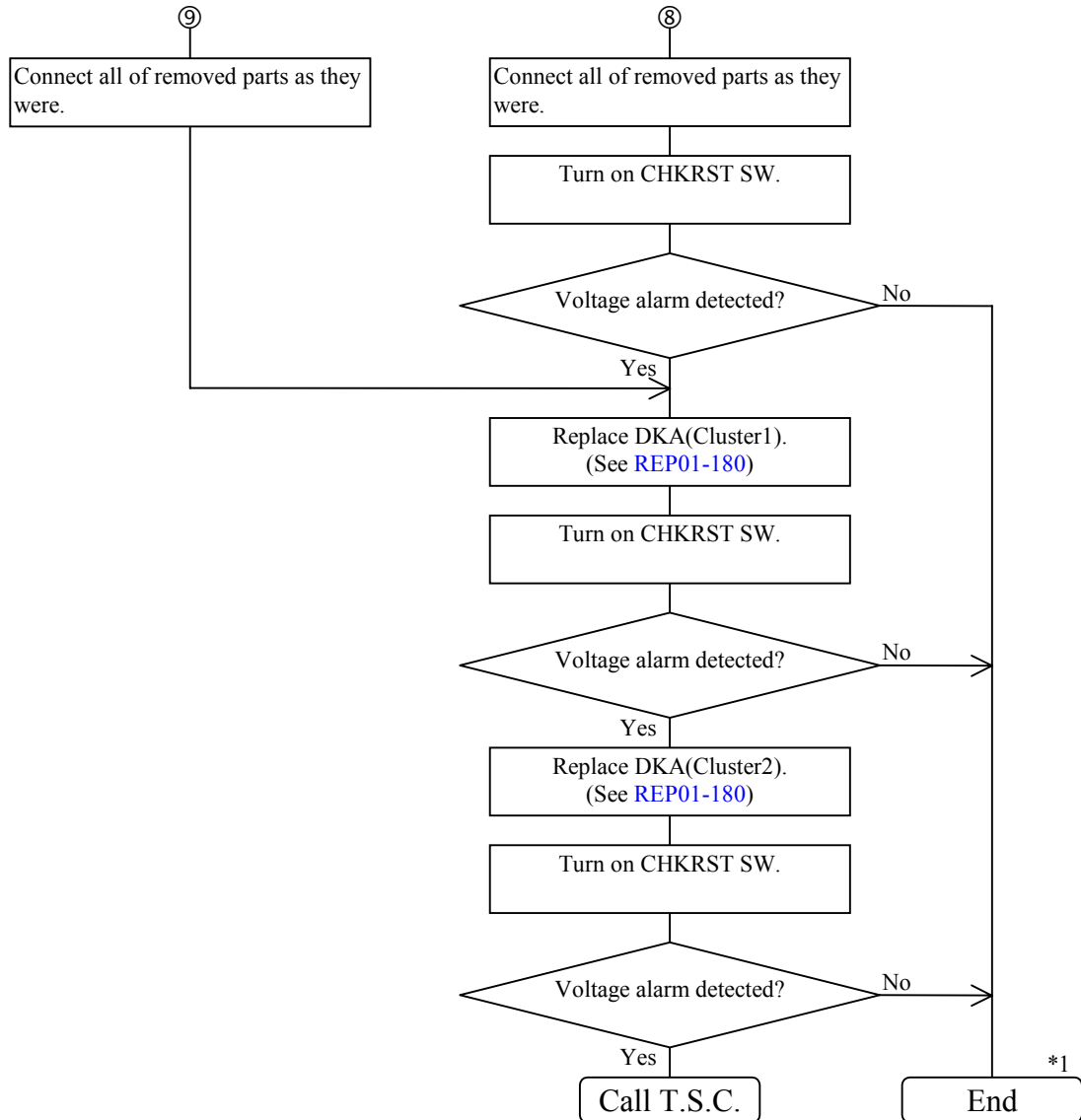
\*1 : If you finished the maintenance, delete the log, SIM complete and recover Cluster.(Refer to SVP02-170, 580 and 1110.)



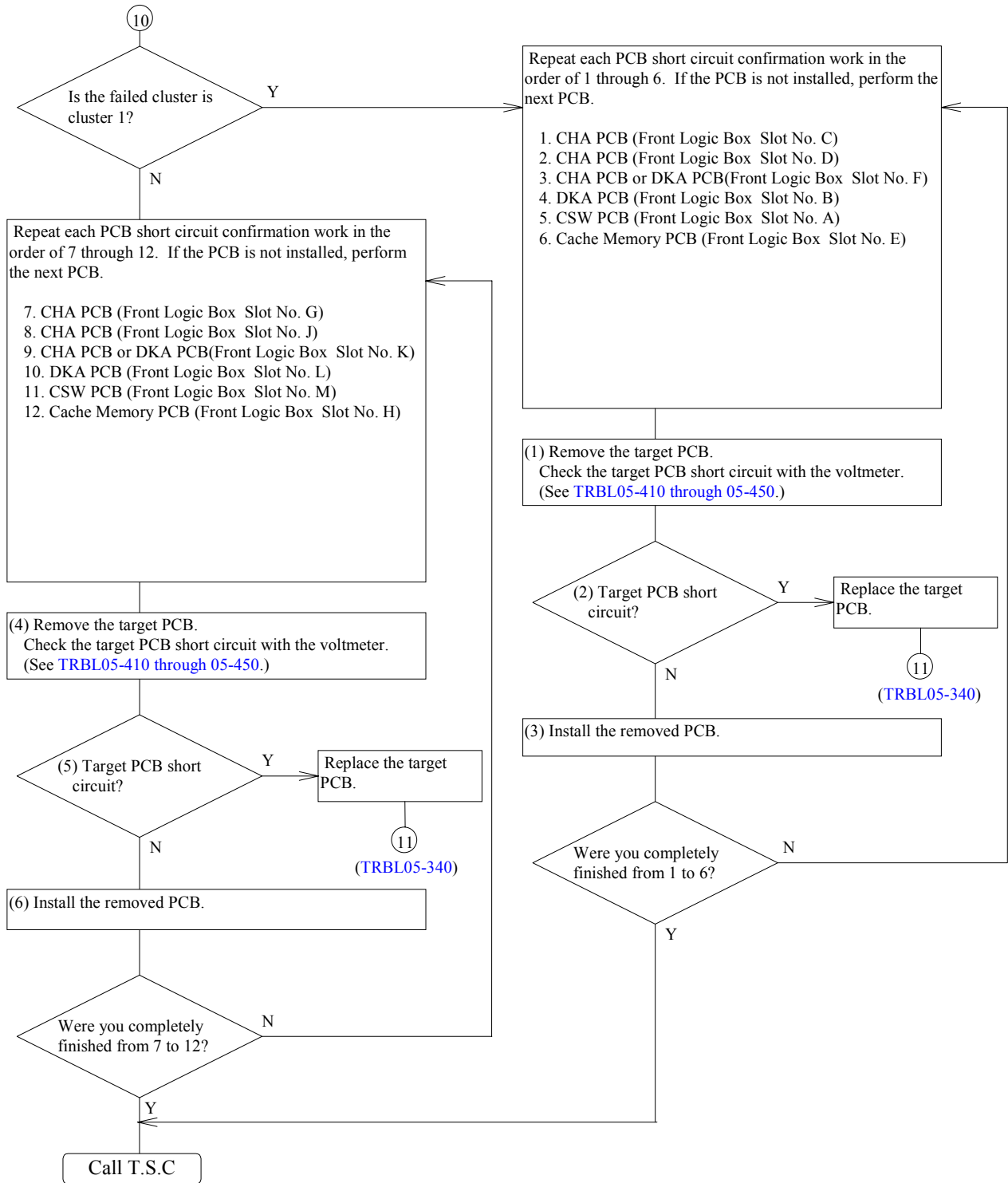
\*1: Confirm that the failed HDU Box is inactive, prior to checking short-circuit.)

\*2: When a connector is removed, SIM is detected in several. If you finish the maintenance, delete the log, SIM complete and recover Cluster (Refer to SVP02-170, 580 and 1110).





\*1: If you finish the maintenance, delete the log, SIM complete and recover Cluster. (Refer to SVP02-170, 580 and 1110.)

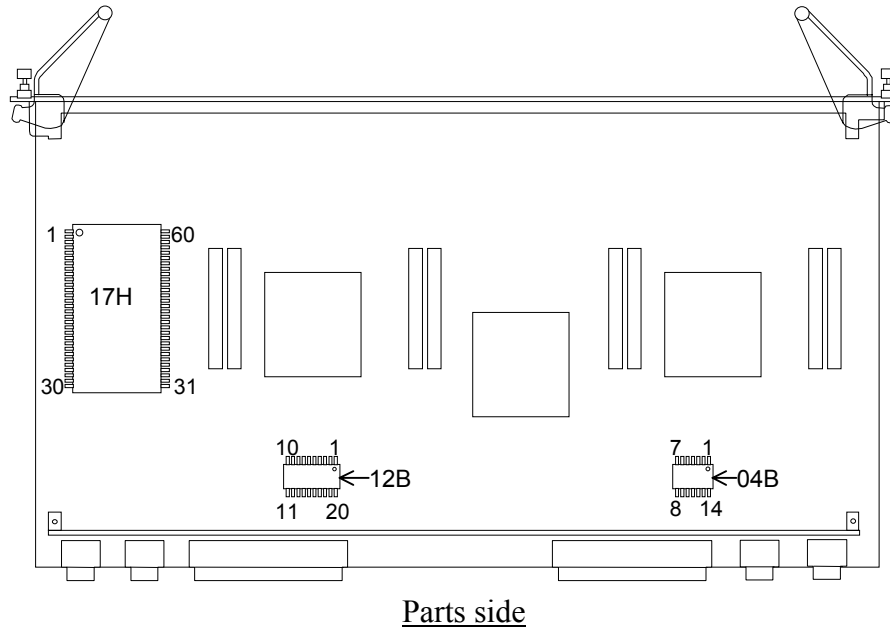


## Serial Channel Adapter PCB (WP462)

The check points of the PCB short circuit are shown in the following table.

No.	Voltage	Point (VCC)	Point (GND)	Part Name	Normal Value
1	@5VIN	17H-1pin	17H-11pin	CHIP CAPACITOR	1k $\Omega$ or more after 10 seconds
2	@3VIN	17H-33pin	17H-11pin	CHIP CAPACITOR	2k $\Omega$ or more after 10 seconds
3	@5V	04B-14pin	04B-7pin	CHIP CAPACITOR	4 $\Omega$ or more after 10 seconds
4	@3V	12B-20pin	12B-10pin	CHIP CAPACITOR	1k $\Omega$ or more after 10 seconds

Serial Channel Adapter PCB(WP462)



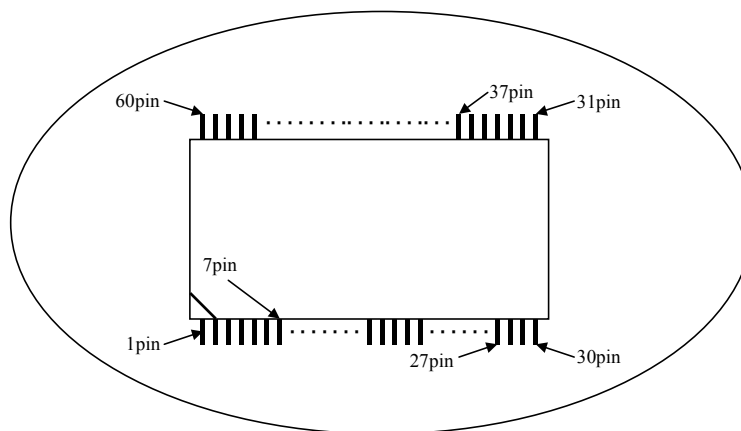
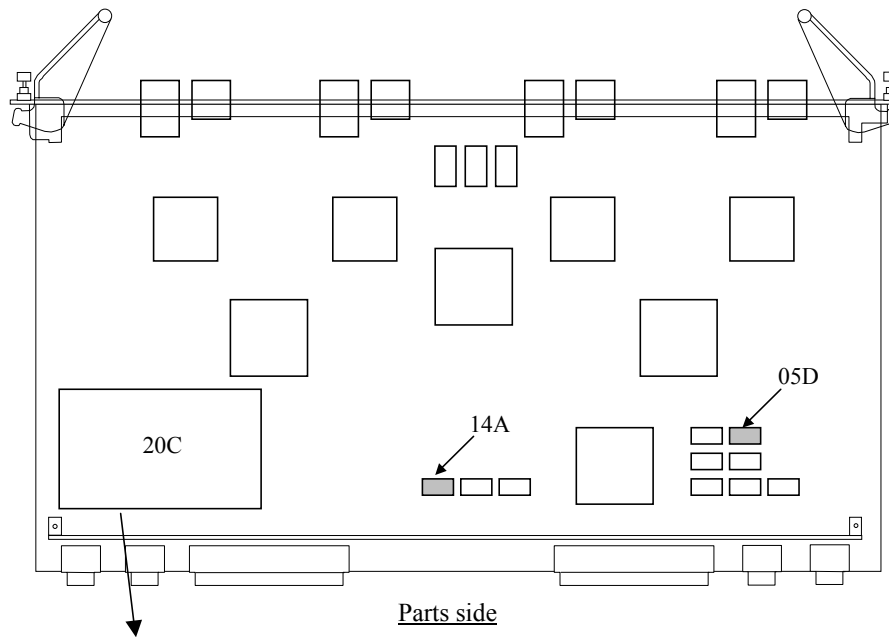


## Fibre Channel Adapter PCB (WP461)

The check points of the PCB short circuit are shown in the following table.

No.	Voltage	Point (VCC)	Point (GND)	Part Name	Normal Value
1	@5V	14D-14	14A-7	IC(TTL)	50 $\Omega$ or more after 5seconds
2	@3V	05D-14	05D-7	IC(LVA)	7 $\Omega$ or more after 5seconds
3	@2.5V	20C-60	20C-7	DC-DC converter	50 $\Omega$ or more after 5seconds
4	@1.8V	20C-37	20C-7	DC-DC converter	50 $\Omega$ or more after 5seconds
5	@1.5V	20C-27	20C-7	DC-DC converter	15 $\Omega$ or more after 5seconds

Fibre Channel Adapter PCB (WP461)



Mainframe Fibre Channel PCB(WP465)

The check points of the PCB short circuit are shown in the following table.

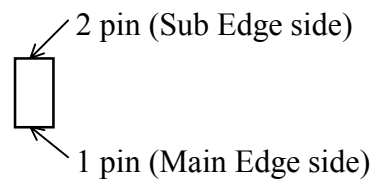
DKA PCB (WP471)

The check points of the PCB short circuit are shown in the following table.

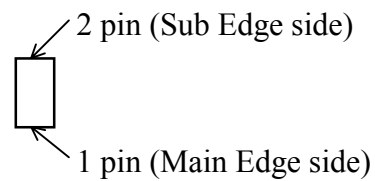
No.	Voltage	Point (VCC)	Point (GND)	Part Name	Normal Value
1	5VIN	C1320-1	C1320-2	CHIP CAPACITOR	10Ω or more after 5 seconds
2	3.3VIN	C1333-1	C1333-2	CHIP CAPACITOR	10Ω or more after 5 seconds
3	@5V	C1323-1	C1323-2	CHIP CAPACITOR	10Ω or more after 5 seconds
4	@3.3V	C548A-1	C548A-2	CHIP CAPACITOR	10Ω or more after 5 seconds

DKA PCB (WP471)

R\*\*\* (CHIP RESISTOR)



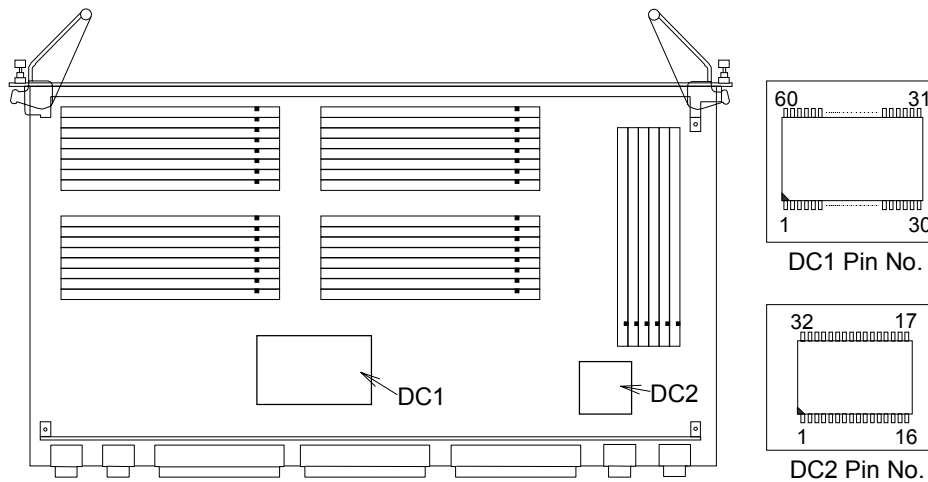
C\*\*\* (CHIP CAPACITOR)



Cache Memory PCB (WP490)

The check points of the PCB short circuit are shown in the following table.

No.	Voltage	Point (VCC)	Point (GND)	Part Name	Normal Value
1	@3VIN	DC1-33	DC1-7	DC-DC CONVERTER	100Ω or more after 5 seconds
2	@5VIN	DC1-1	DC1-7	DC-DC CONVERTER	100Ω or more after 5 seconds
3	@2.5V	DC1-60	DC1-7	DC-DC CONVERTER	100Ω or more after 5 seconds
4	@1.8V	DC1-37	DC1-7	DC-DC CONVERTER	100Ω or more after 5 seconds
5	@1.5V	DC1-28	DC1-7	DC-DC CONVERTER	10Ω or more after 5 seconds
6	@3V	DC1-15	DC1-7	DC-DC CONVERTER	10Ω or more after 5 seconds
7	@5V	DC1-16	DC1-7	DC-DC CONVERTER	100Ω or more after 5 seconds
8	@3VCM	DC2-2	DC1-7	BACKUP MODULE	100Ω or more after 5 seconds
9	@3VSM	DC2-3	DC1-7	BACKUP MODULE	100Ω or more after 5 seconds

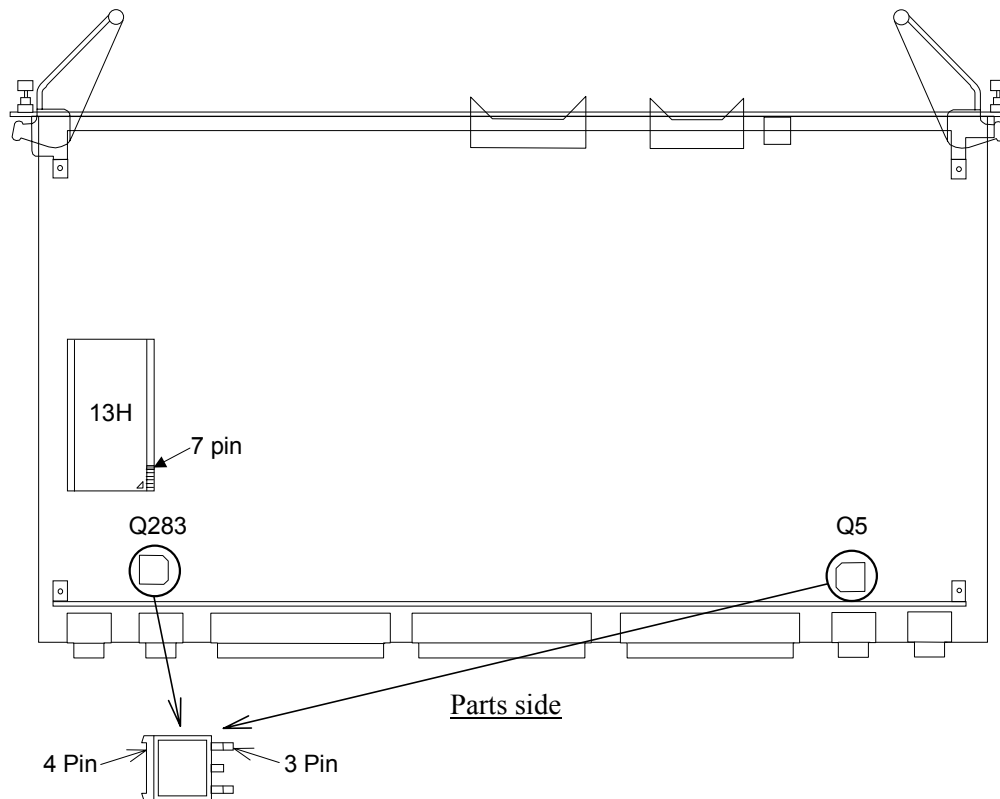
Cache Memory PCB (WP490)

Parts side

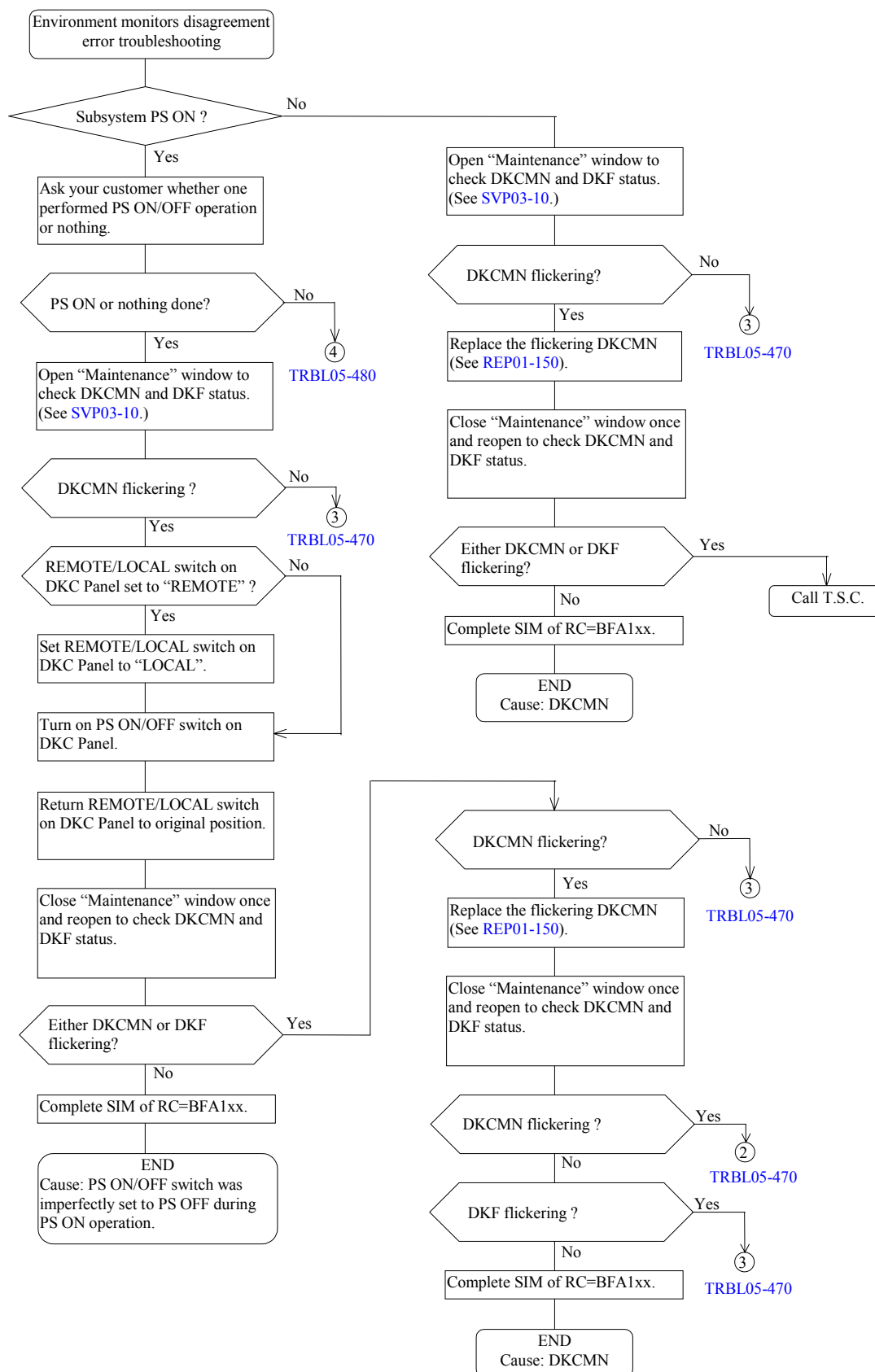
CSW PCB (WP481)

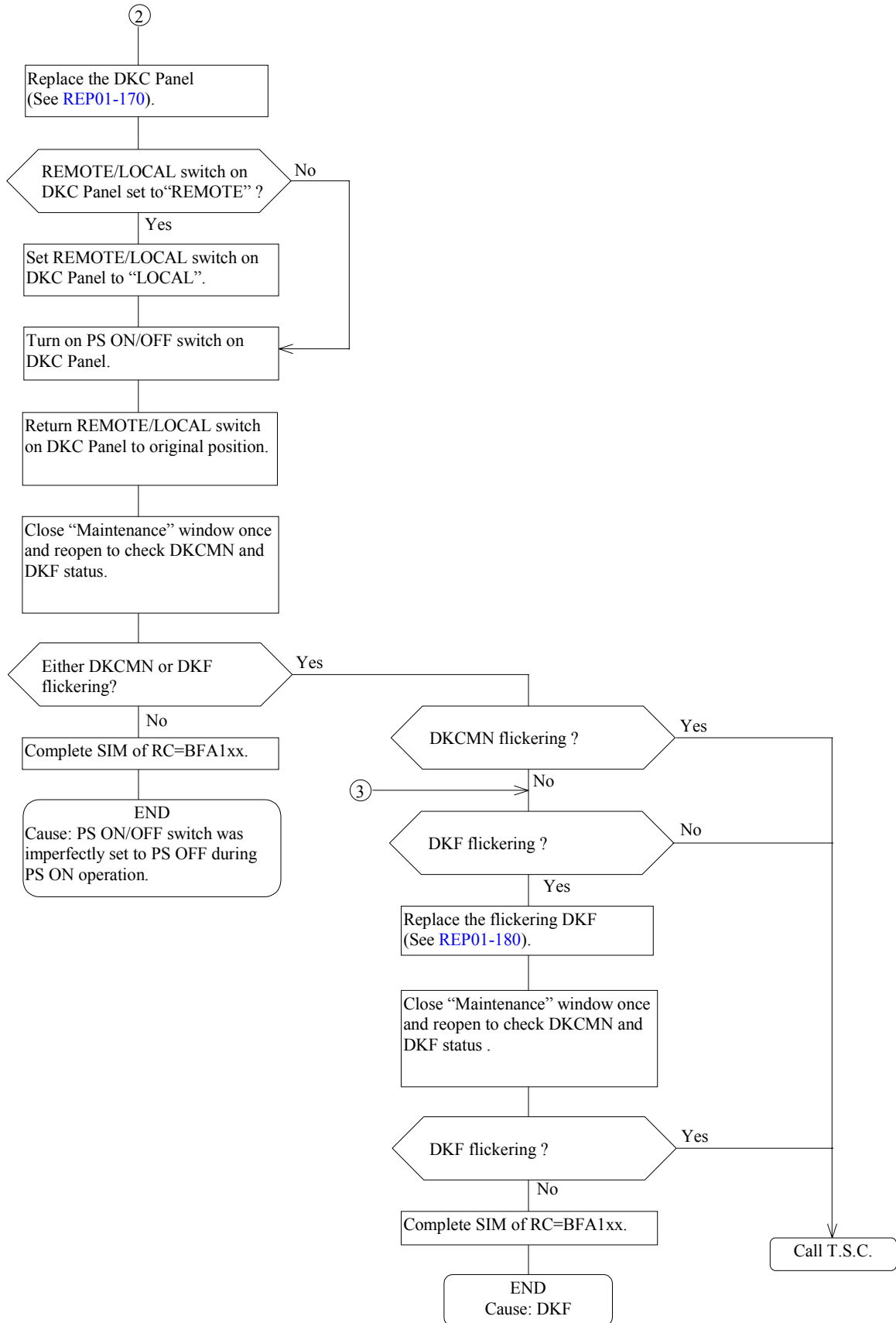
The check points of the PCB short circuit are shown in the following table.

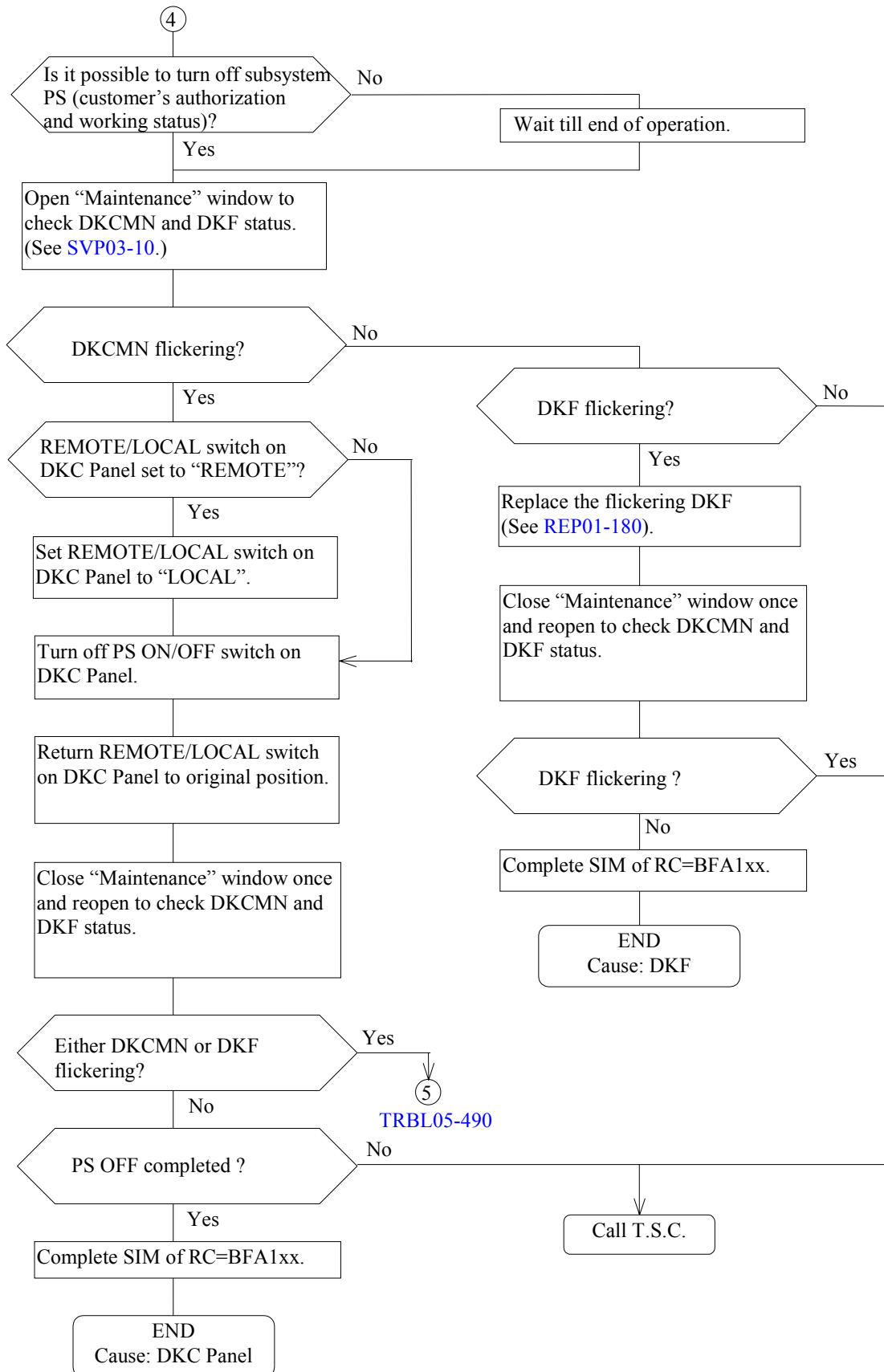
No.	Voltage	Point (VCC)	Point (GND)	Part Name	Normal Value
1	@5VIN	Q283-4	18B-7	POWER MOSFET	100Ω or more after 5 seconds
2	@3.3VIN	Q5-4	18B-7	POWER MOSFET	100Ω or more after 5 seconds
3	@5V	Q283-3	18B-7	POWER MOSFET	100Ω or more after 5 seconds
4	@3.3V	Q5-3	18B-7	POWER MOSFET	100Ω or more after 5 seconds

CSW PCB (WP481)

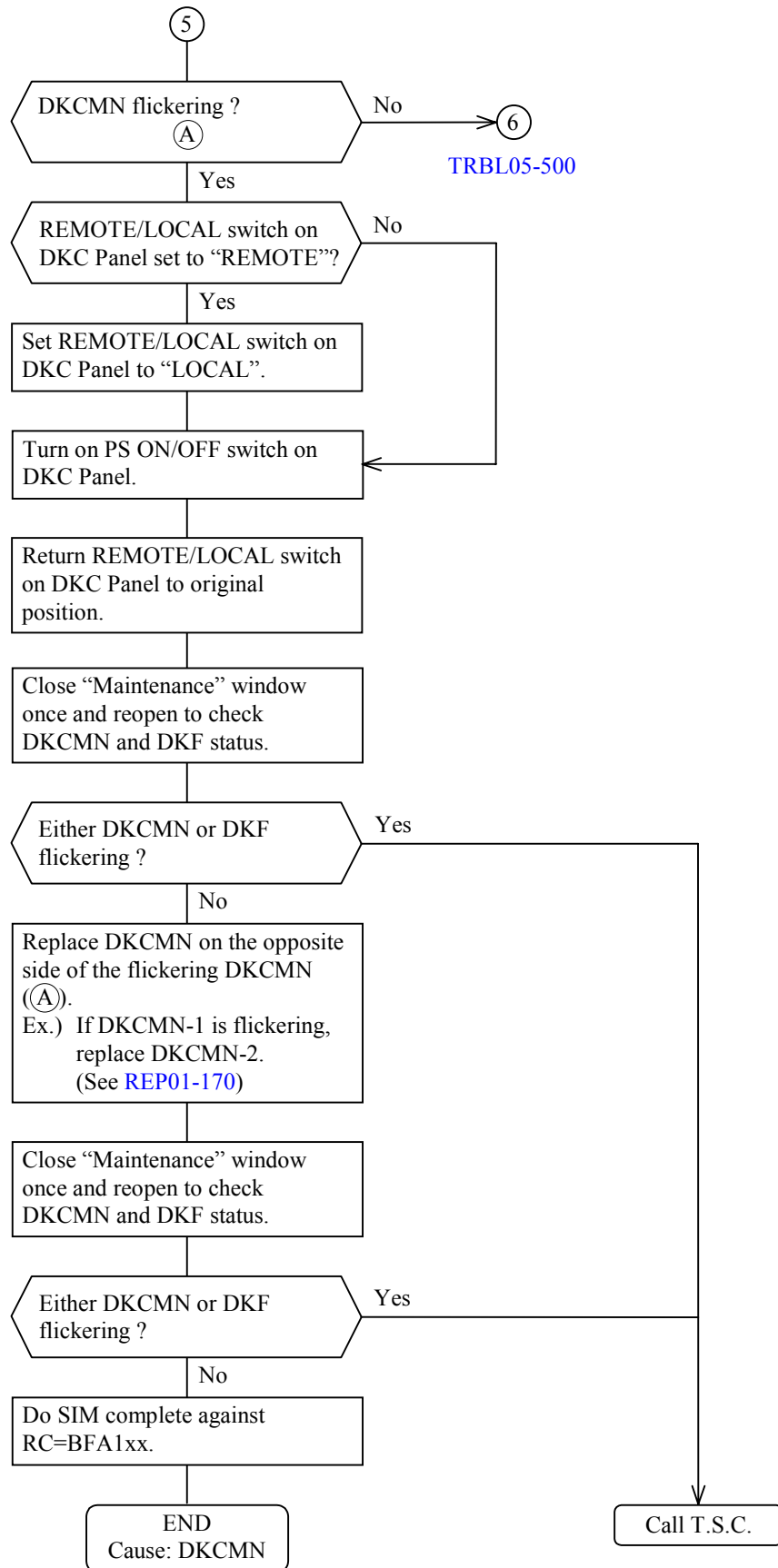
### 5.13 Environment monitors disagreement error (SIM = BFA1XX)

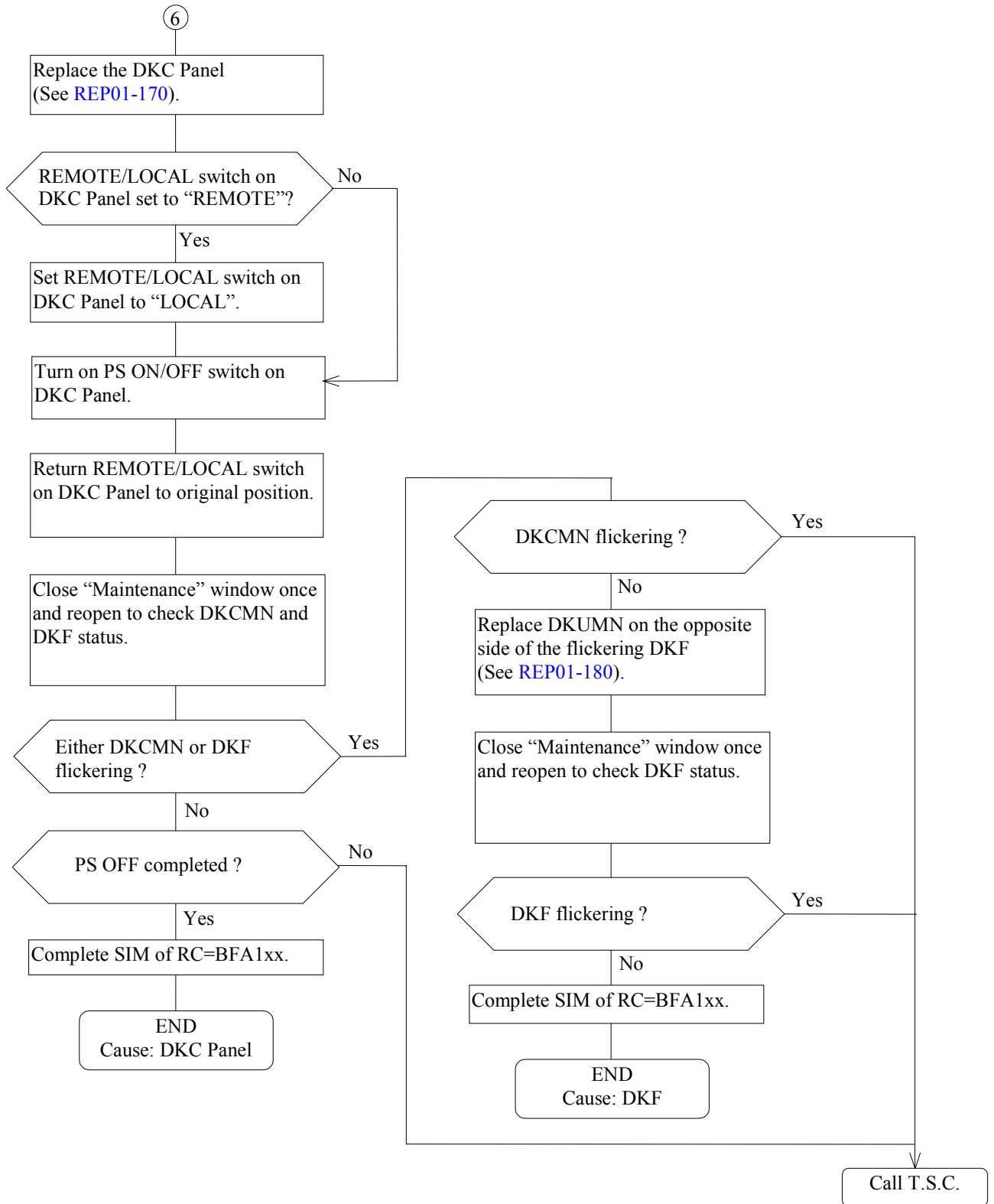




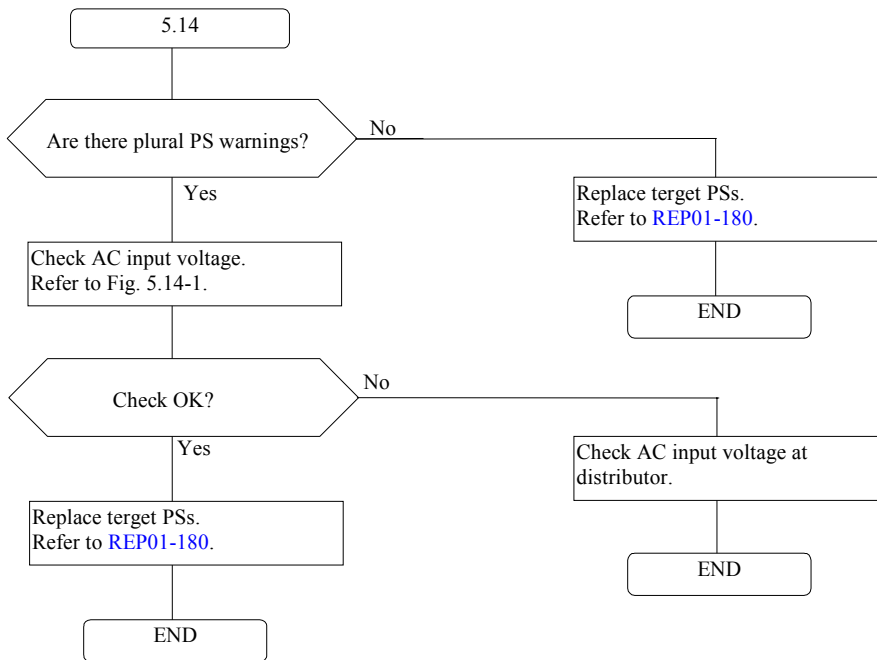








## 5.14 PS warning error (SIM=BF4XXX)



### [AC Input voltage check]

- a. Remove the INLET cable of target PS.
- b. Measure AC Input voltage at INLET cable.

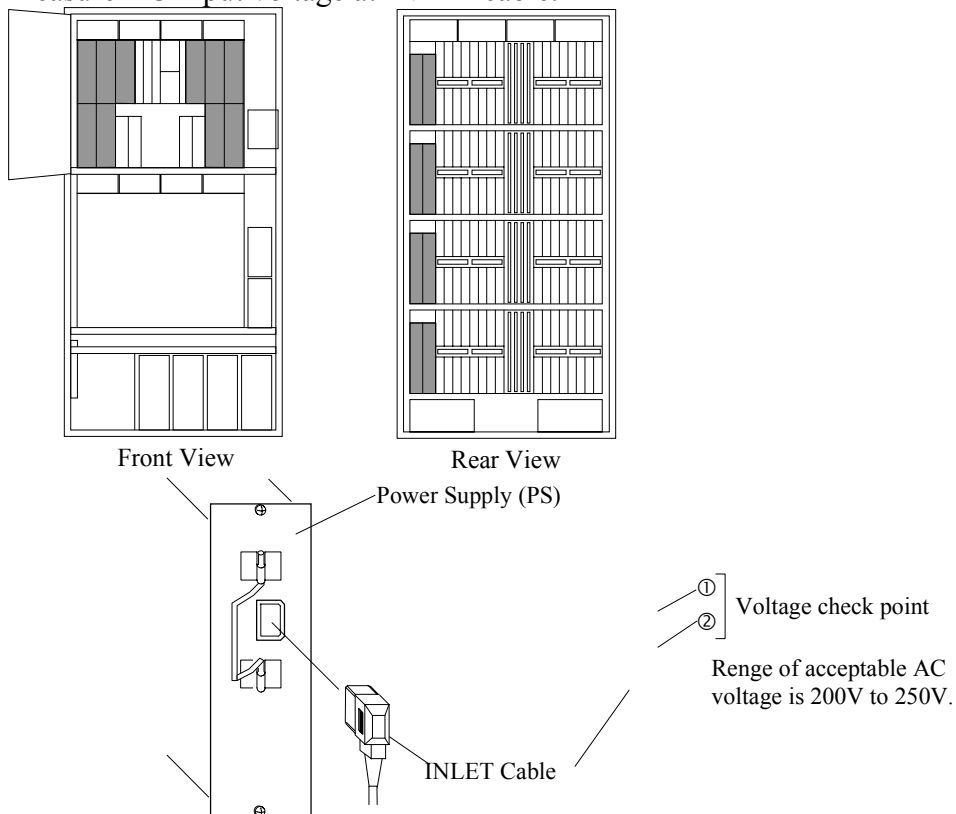
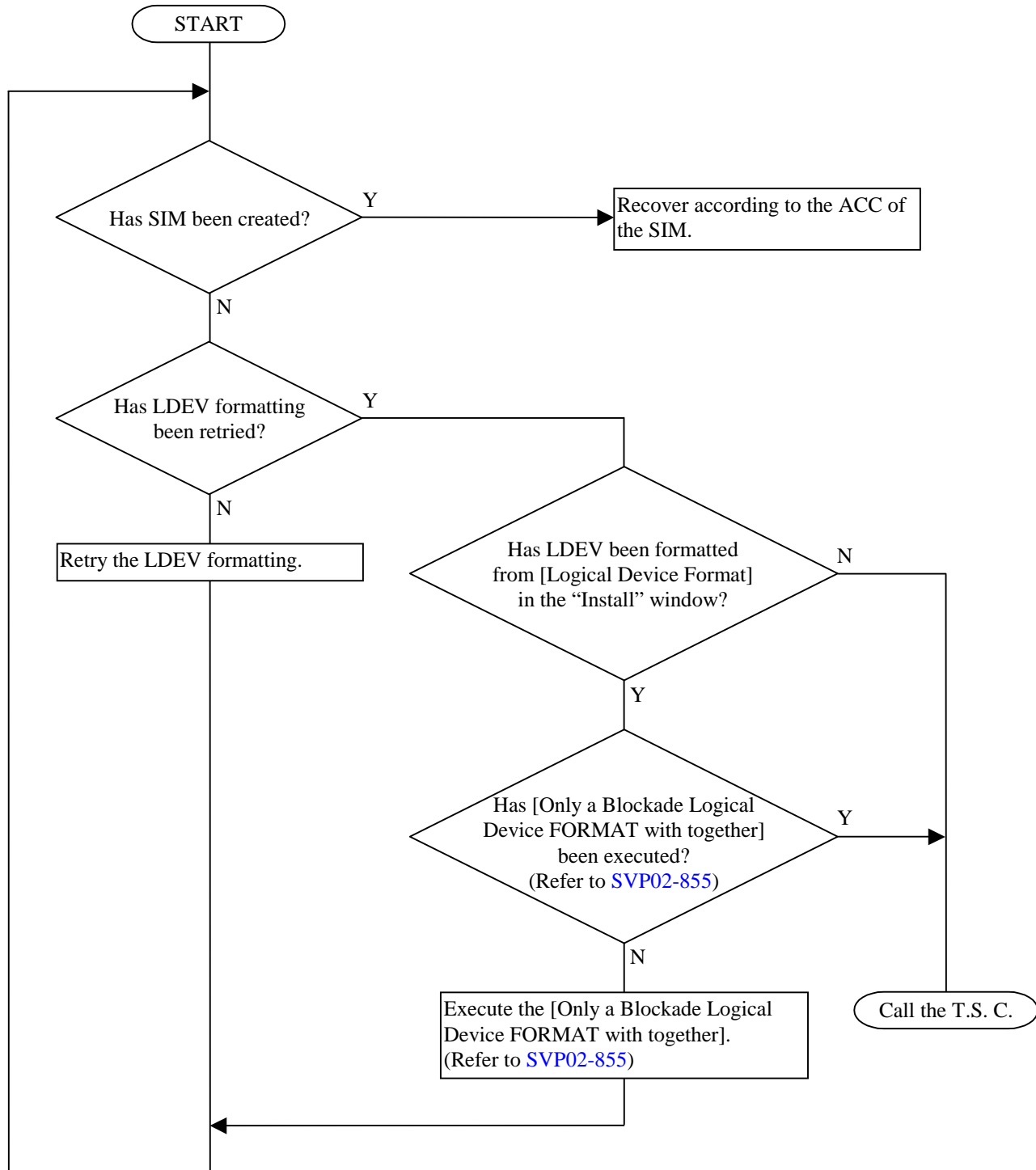


Fig. 5.14-1 AC Input Voltage Check

## 5.15 Recovery procedure when LDEV formatting failed

When “Formatting logical devices rejected by DKC.” or “Formatting the logical device is failed.” is displayed at the end of the LDEV formatting and when “Blocked” is displayed for the LDEV formatted by “Logical Device Status”, make a recovery according to the following procedure.



### 5.17 Recovery procedure when WDCP information is lost

(1) When only this SIM is reported

When this SIM is reported at the time of the subsystem powering on 192 hours or more after the previous breaker turning off, no maintenance is required.

When this SIM is reported at the time of the subsystem powering on less than 192 hours after the previous breaker turning off, (a) failure(s) may occur in the cache PCB, cache memory, battery, or battery charge. Replace the failed part(s).

(2) When another SIM is reported together with this SIM

When the SIM concerning the cache, shared memory, or battery, perform the maintenance of the failed part(s).

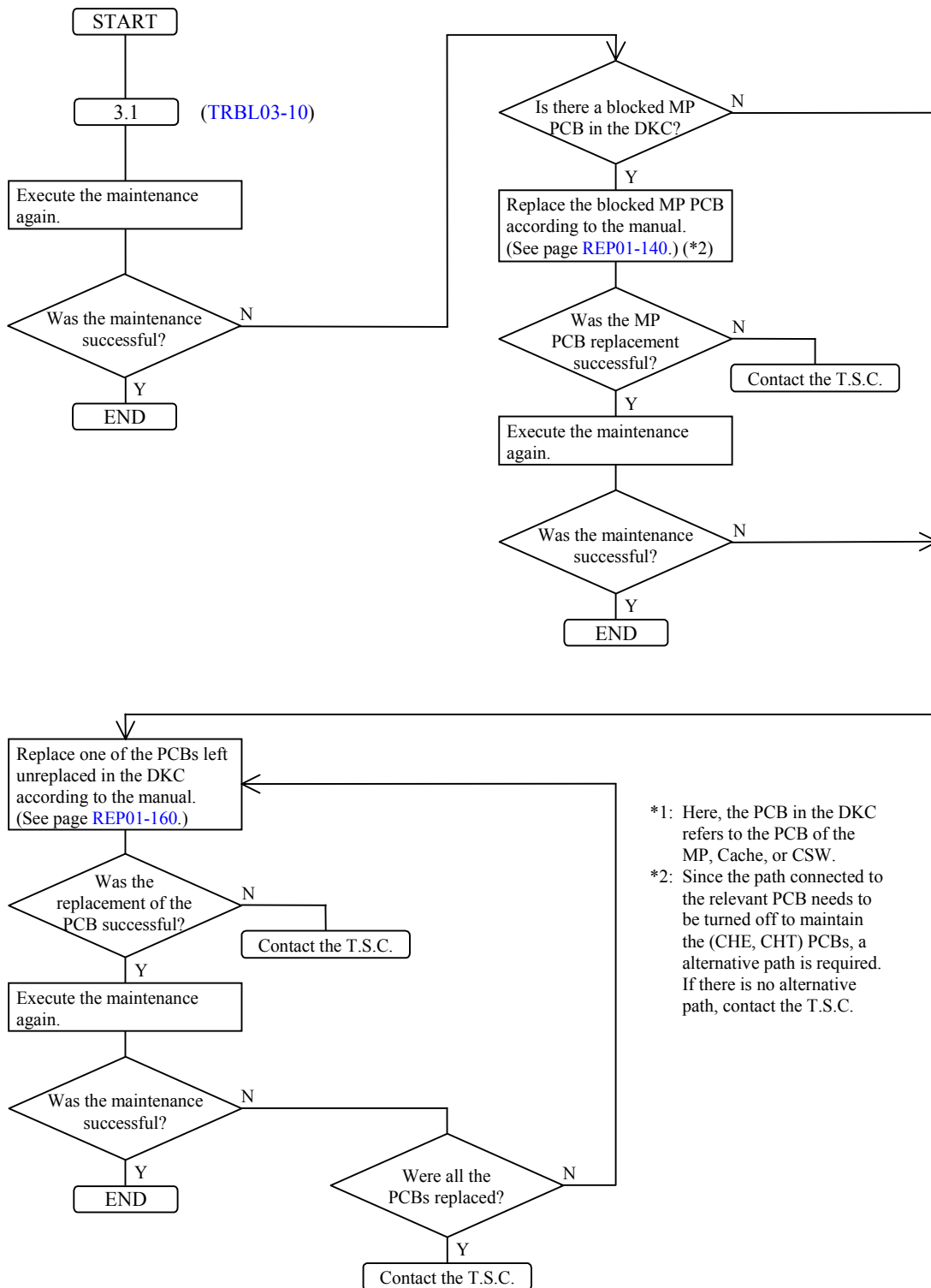
When the SIM not concerning the above is reported at the same time, perform the maintenance of the failed part(s) and see “(1) When only this SIM is reported”.

(3) WDCP system recovery procedure

For the procedure for recovering the WDCP system, see the instruction manual of the OS concerned.

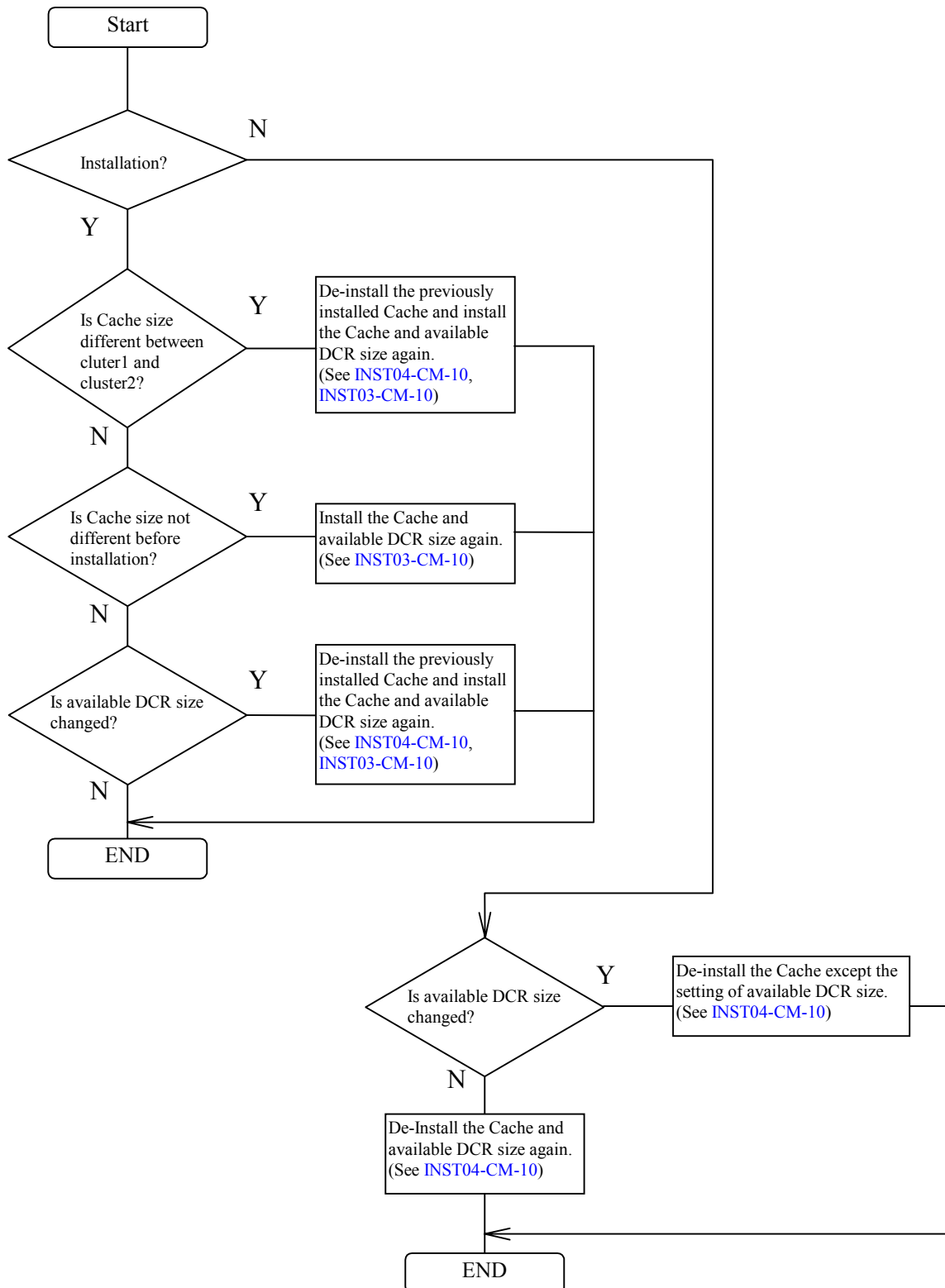
## 5.18 Recovery procedure when recovering SM is impossible

The following flowchart shows the recovery procedure when SM cannot be recovered during Cache PCB replacement, SM/CM installation/de-installation, or cluster recovery procedure .



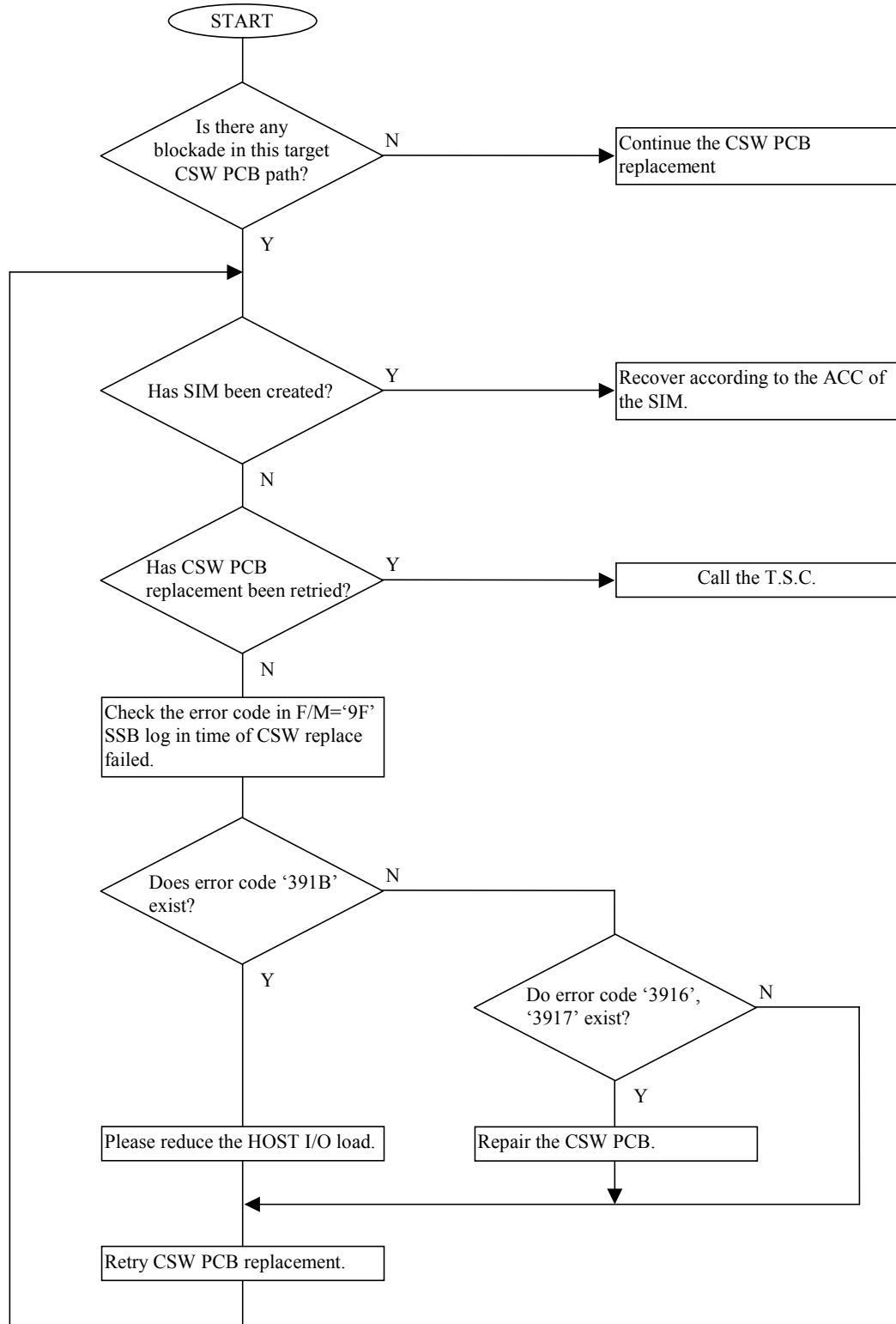
## 5.19 Recovery procedure when installation/de-installation Cache and DCR is impossible

The recovery procedure when installation/de-installation Cache and DCR available size simultaneously is impossible.



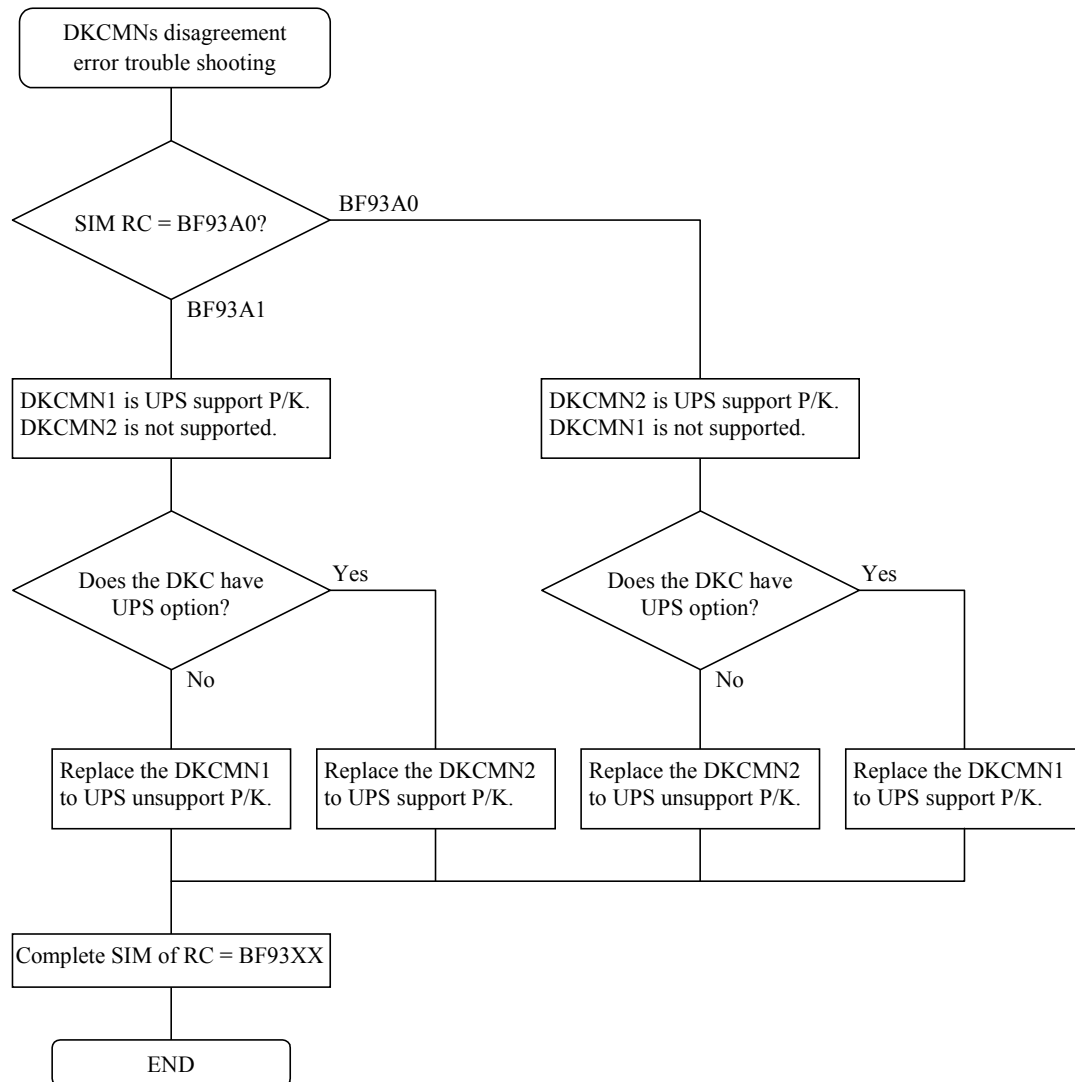
## 5.20 Recovery procedure for failed CSW PCB replacement

When “Some Memory access paths are blockade. Do you want to refer to the path Status?” is displayed at the end of the CSW PCB replacement, recover the status according to the following procedure.

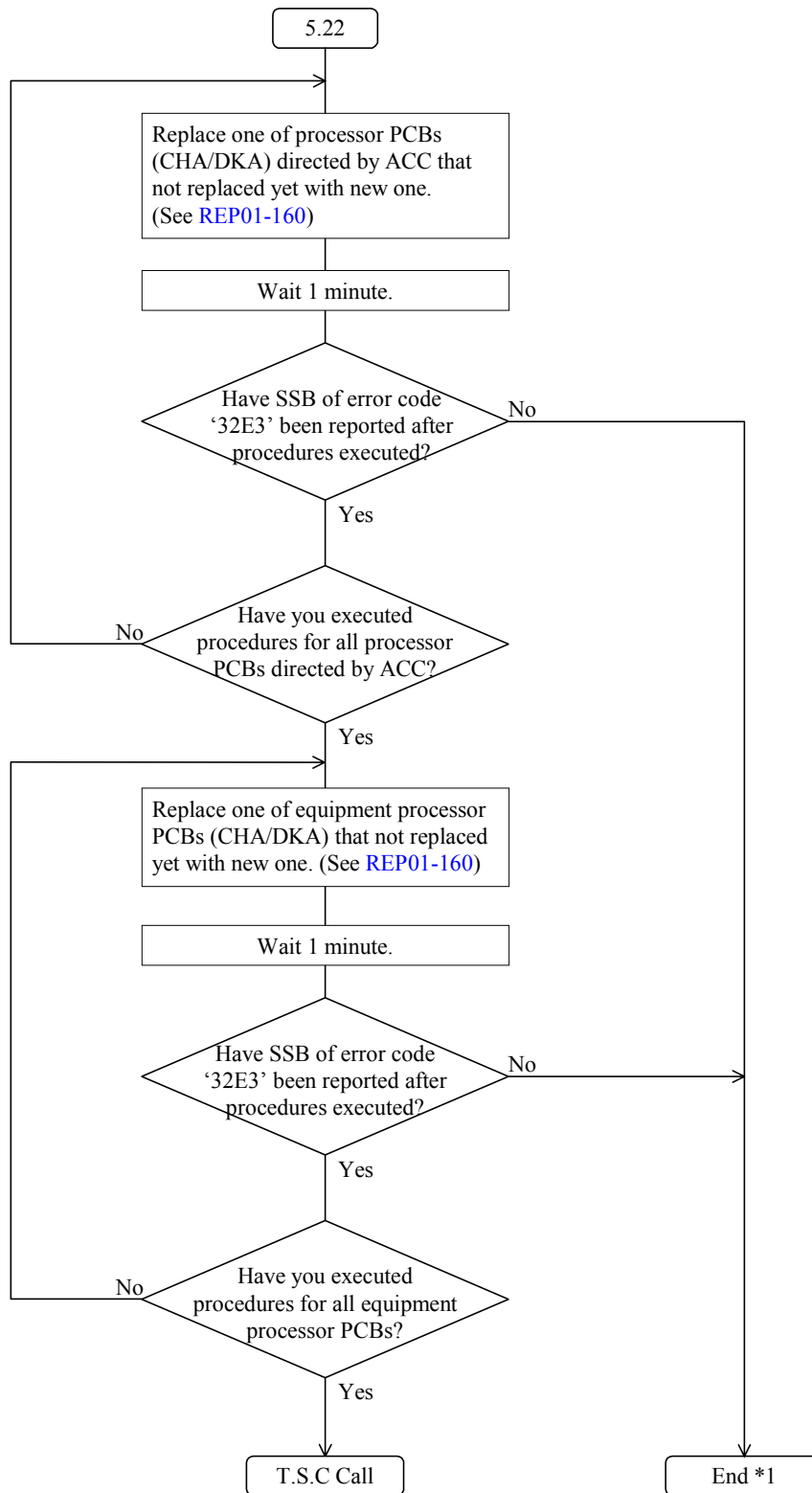




## 5.21 DKCMNs disagreement error (SIM = BF93XX)

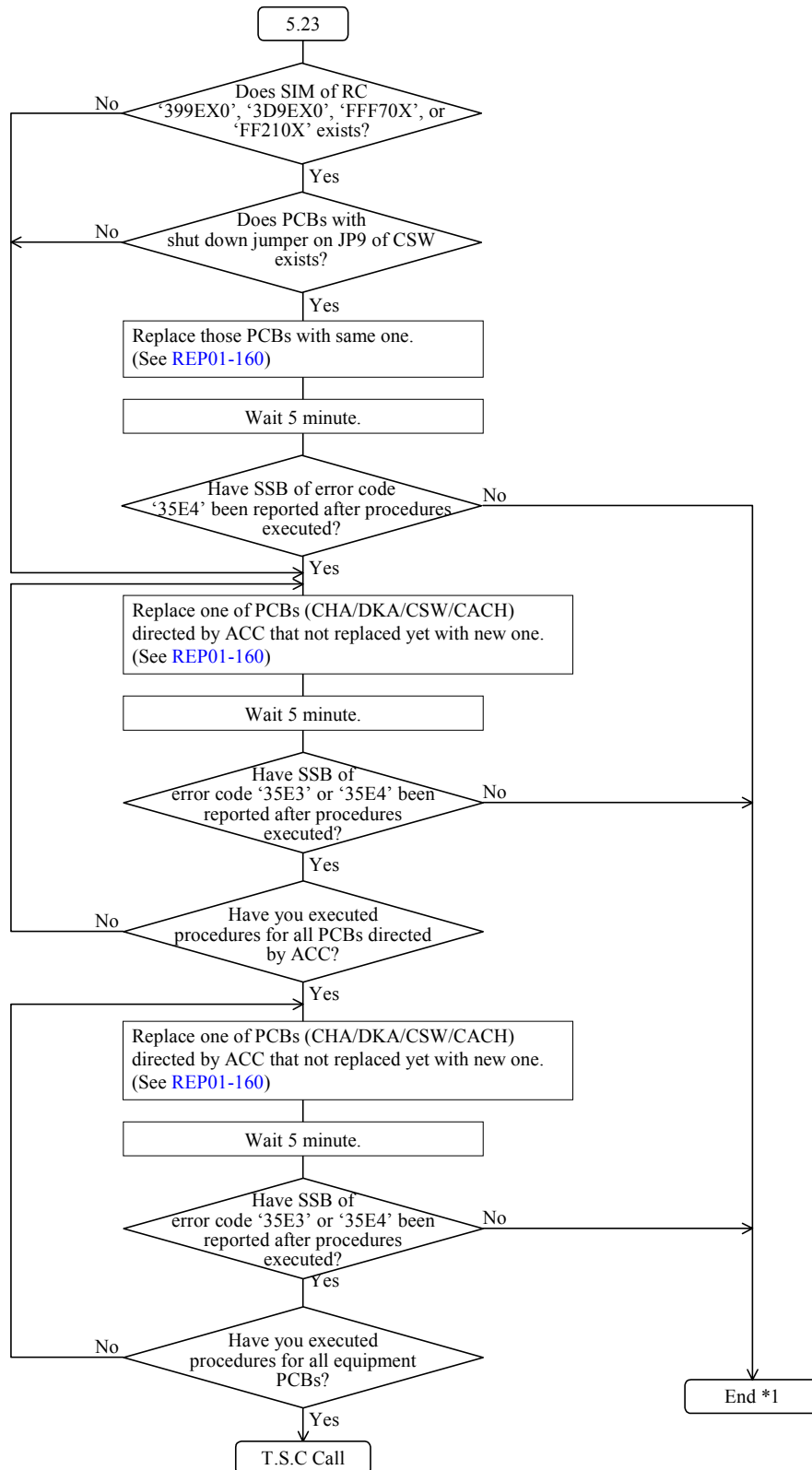


## 5.22 Recovery Procedure for Warning of SM DISABLE (SIM = 399AXY, 3D9AXY)



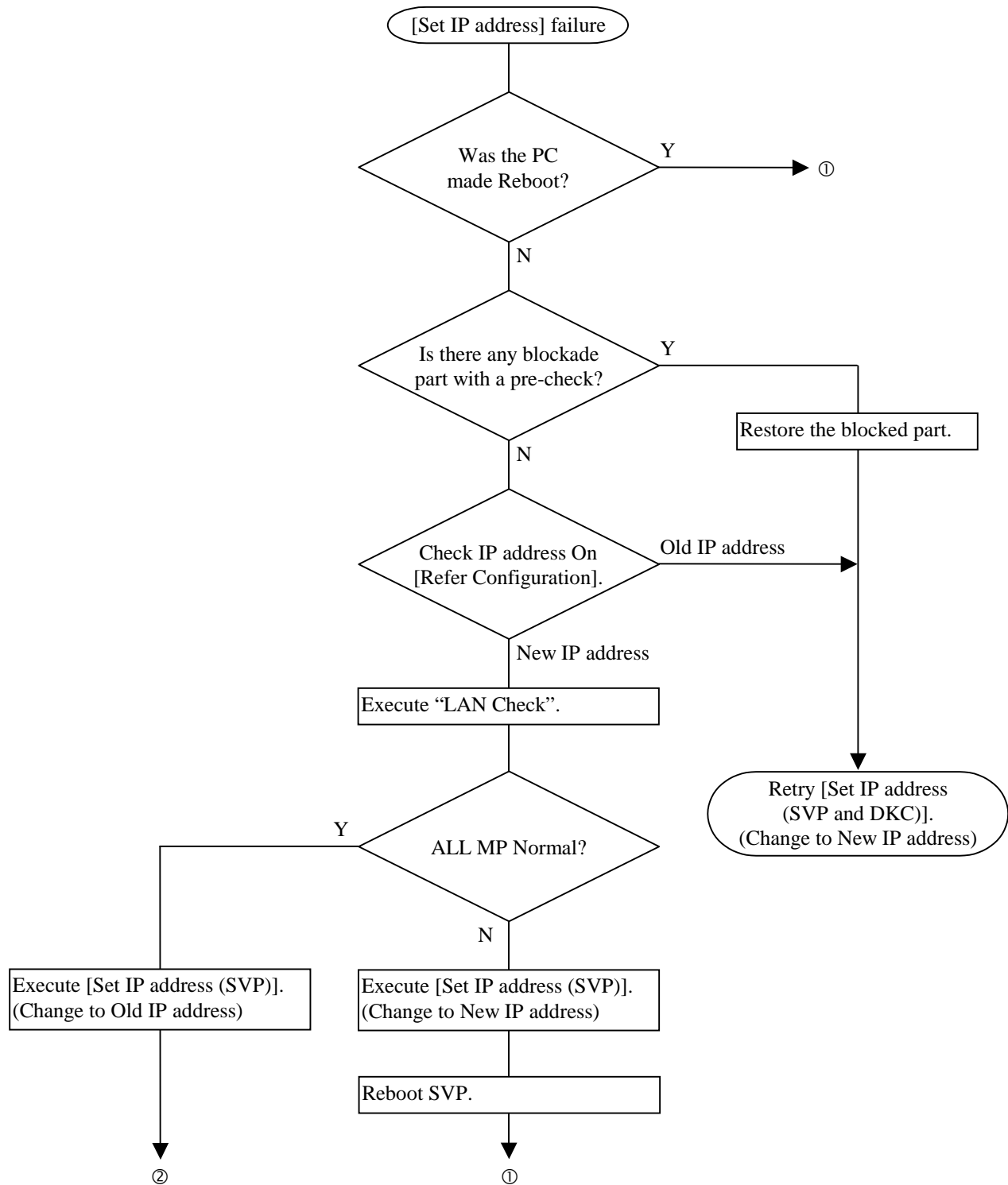
\*1 After finishing the error recovery, execute SIM complete and delete logs. (Refer to [SVP02-170, 580](#))

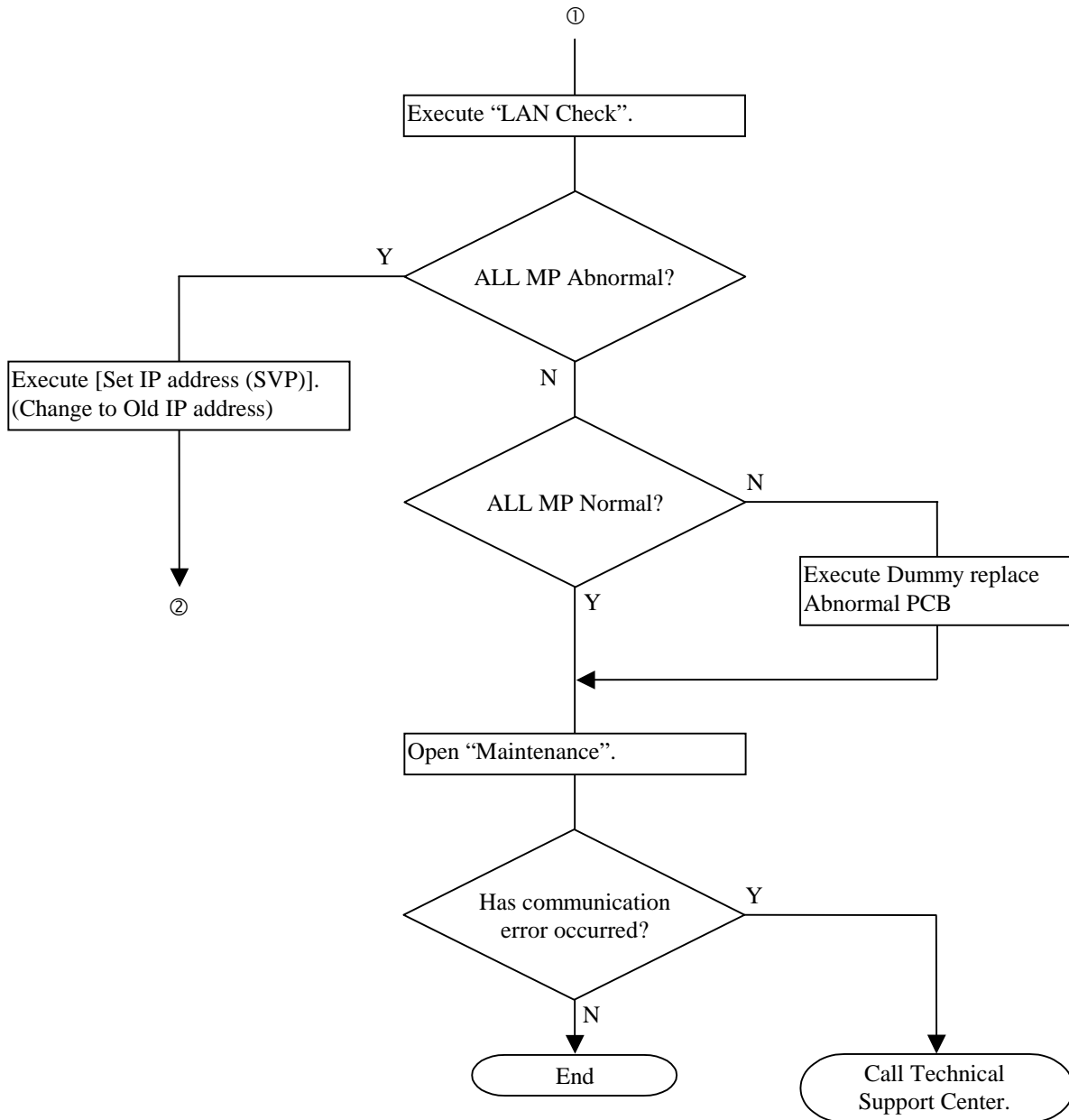
## 5.23 Recovery Procedure for Injustice DC voltage control and Injustice CE MODE (SIM = 399DX0, 399EX0, 3D9DX0, 3D9EX0, FFF60X, FFF70X, FF200X, FF210X)

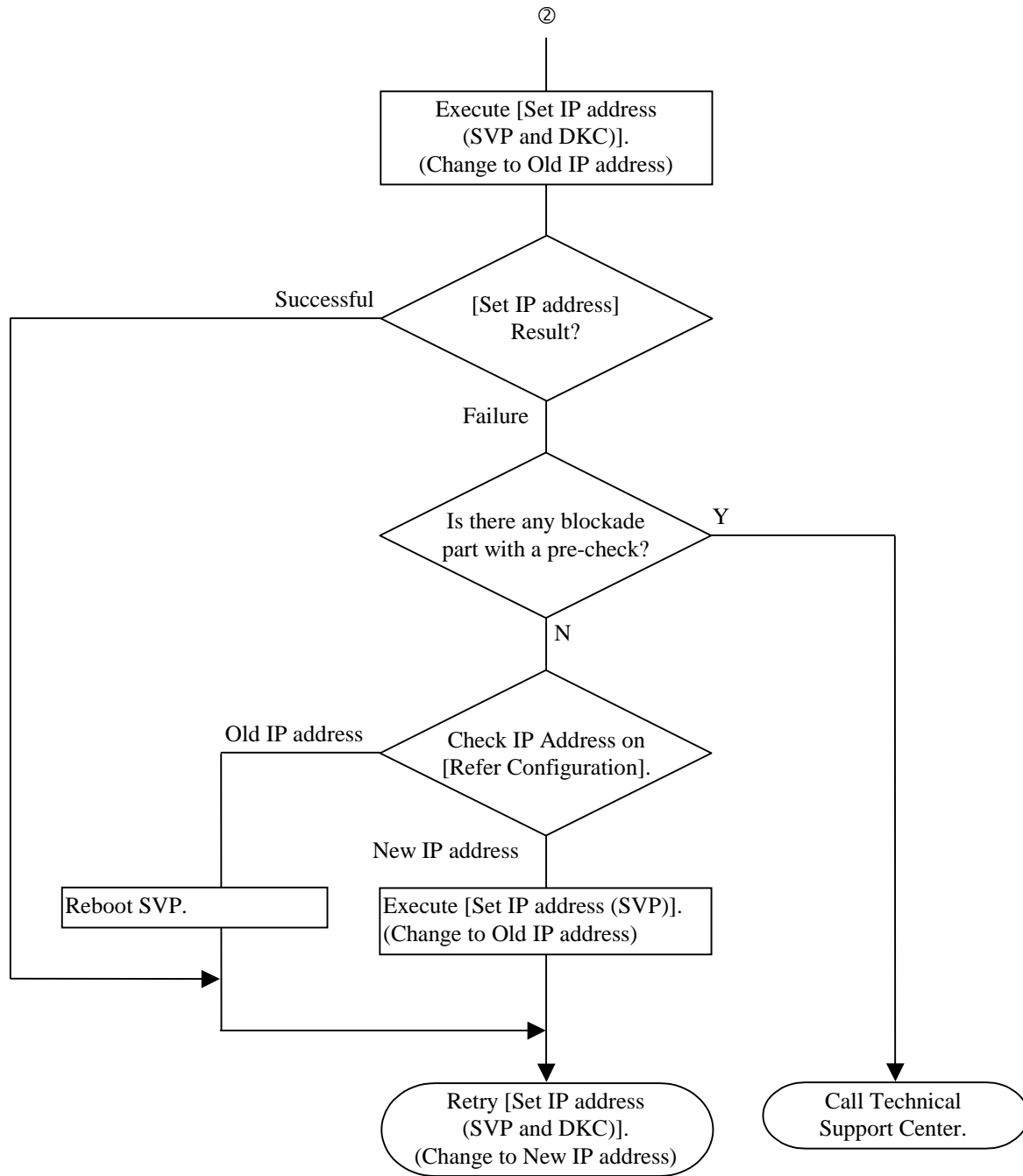


\*1 After finishing the error recovery, execute SIM complete and delete logs. (Refer to SVP02-170, 580)

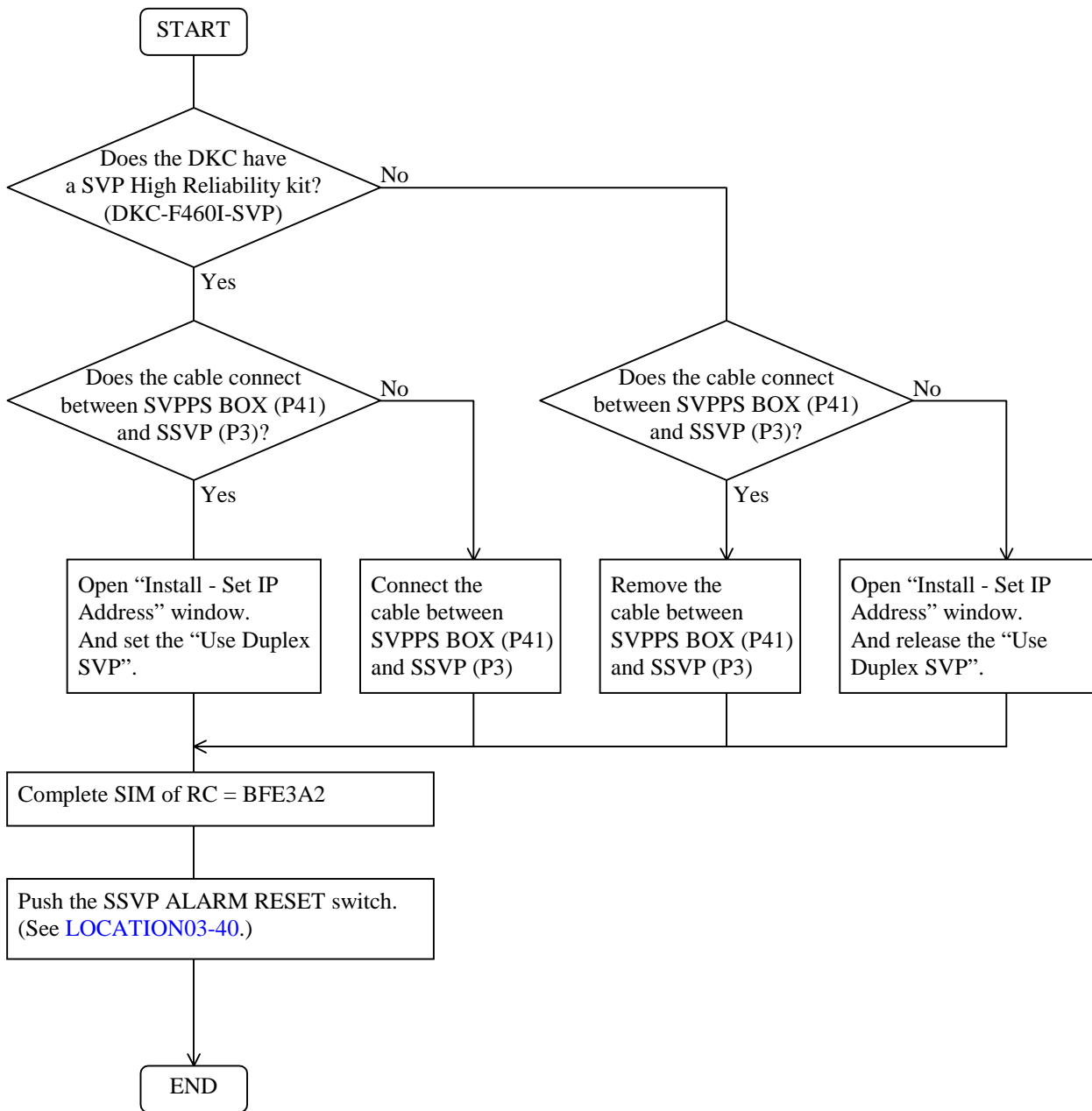
## 5.24 Recovery Procedure when Change the IP Address failed





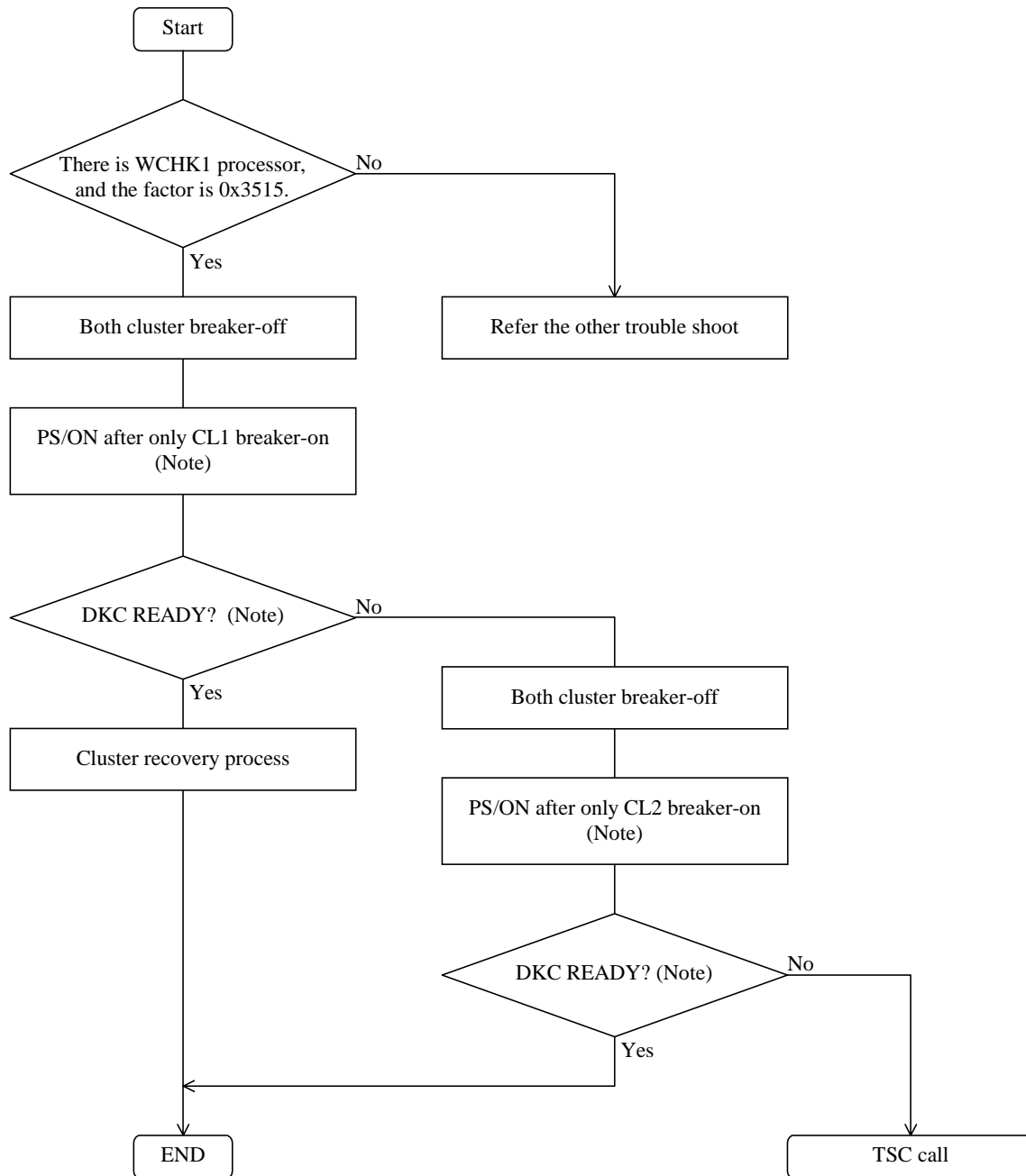


## 5.25 Duplex SVP Setup failed (SIM = BFE3A2)



## 5.26 The recovery procedure at the time of disorder generating after a power failure etc.

It is a recovery procedure at the time of the abnormalities in equipment at DKC PS/ON, such as after breaker-on.



- (Note) 1. A DKC alarm lamp is turned on in PS/ON only in a one side cluster.  
 2. Only by the one side cluster, even if 30 minutes pass, when you do not carry out DKC READY lamp lighting after PS/ON. Please judge that equipment starting is impossible.



## 6 HRC/HODM/HORC Error Recovery

### 6.1 Recovery Procedure for HRC/HODM Error

Hardware error sometimes causes HRC/HODM error which is a pair suspend or a path disable. This document explain the HRC/HODM Error and how to recover it.

Following reports indicate HRC/HODM error occurrence.

- SIM report of HRC/HODM path disable occurrence
- SIM report of HRC/HODM pair suspend occurrence
- SIM report of HODM Erase Error occurrence
- SIM report of RCU Acute or Serious Level SIM detection
- SIM report of RCU Moderate Level SIM detection

Table 6.1-1 HRC/HODM SIM REF.CODE

SIM REF. CODE	meaning	comment
D4XY-YY	HRC/HODM pair is suspended	X:0~5 or F YYY:LDEV number
DBXY-YY	HRC Asynchronous pair is suspended	X:0~8 or F YYY:LDEV number
D48Y-YY	HODM Erase Error occurred	YYY:LDEV number
D4EY-YY	RCU Acute or Serious Level SIM reported	YYY:LDEV number
DBEY-YY	RCU (Asynchronous pair) Acute or Serious Level SIM reported.	YYY:LDEV number
D4DY-YY	RCU Moderate Level SIM reported	YYY:LDEV number
DBDY-YY	RCU (Asynchronous pair) Moderate Level SIM reported.	YYY:LDEV number
2180-XY	HRC/HODM path is disabled	X:Processor No. Y:LCP No.
2182-XY	MCU has received the notification of communication line error detection from extender.	X:Processor No. Y:LCP No.

Following pages explain each error type of HRC/HODM Error and recovery flow chart for the HRC/HODM Error is showed. Concerning to the Disaster Recovery Procedures, please refer to THEORY SECTION ([THEORY03-690 ~ 790](#)).

The delete pair operation with Delete Pair by Force option is supported for HRC asynchronous recovery procedure. If hung-up conditions may occur at HRC asynchronous, this operation can be executed from SVP or Remote Console. Refer to Forcible Delete Operation([TRBL06-210](#)).

Note: Please check a fence Level Parameter for the suspended pair by SVP Pair Option. And if M-VOL Fence Level is 'R-VOL Data' or M-VOL Fence Level is 'R-VOL Status' and suspended SIM is 'D4FYYY', write I/O operations to the M-VOL will be rejected. So you must execute Delete Pair for the suspended pair, before execution of the recovery flow chart.

If you find out the F/M = '8F' SSB log which have following error code (C870, C871, C872), it is not the original cause of the suspended pair. It means that the SSB log is created by the pair status change timing. So you have no need to execute a recovery action.

## 6.2 HORC Error Recovery Procedure

A HORC pair suspension or a HORC pass blockade may occur owing to hardware errors. This section explains the recovery procedure against them. Occurrences of HORC errors can be known through the following.

- HORC error message on the Syslog outputted by the RAID manager/HORC (Note 1)
- Report of a HORC pass blockade occurrence by the SIM
- Report of a HORC pair suspension occurrence by the SIM

Table 6.2-1 HORC SIM REF.CODE

SIM REF. CODE	Meaning	Remarks
D4XY-YY	HORC pair suspend	X: 0 - 2, 4 - 5 or F YYY: LDEV number
DBXY-YY	HORC Asynchronous pair suspend	X: 0 - 8, F YYY: LDEV number
2180-XY	HORC pass blockade	X: Processor # Y: LCP#
2182-XY	MCU has received the notification of communication line error detection from extender.	X: Processor # Y: LCP#

SIM outputted when the HORC is suspended or the HORC pass is blocked has the same REF. CODE and meaning as those outputted when the HRC/HODM pair is suspended or the HRC/HODM pass is blocked.

Furthermore, the error recovery procedure is the same as that against an HRC/HODM error. Therefore, follow the procedures shown in the flowcharts on page TRBL06-30 and succeeding pages to recover from a HORC pair error.

When a message indicating that a HORC pair error has occurred is displayed on the Syslog, check the SIM log of the connected DKC and confirm the conformance of the message on the Syslog with the SIM logged on the DKC side before starting the recovery using the above flow chart.

Note 1; When the HORC pair is suspended, the RAID manager/HORC displays the following message on the Syslog.

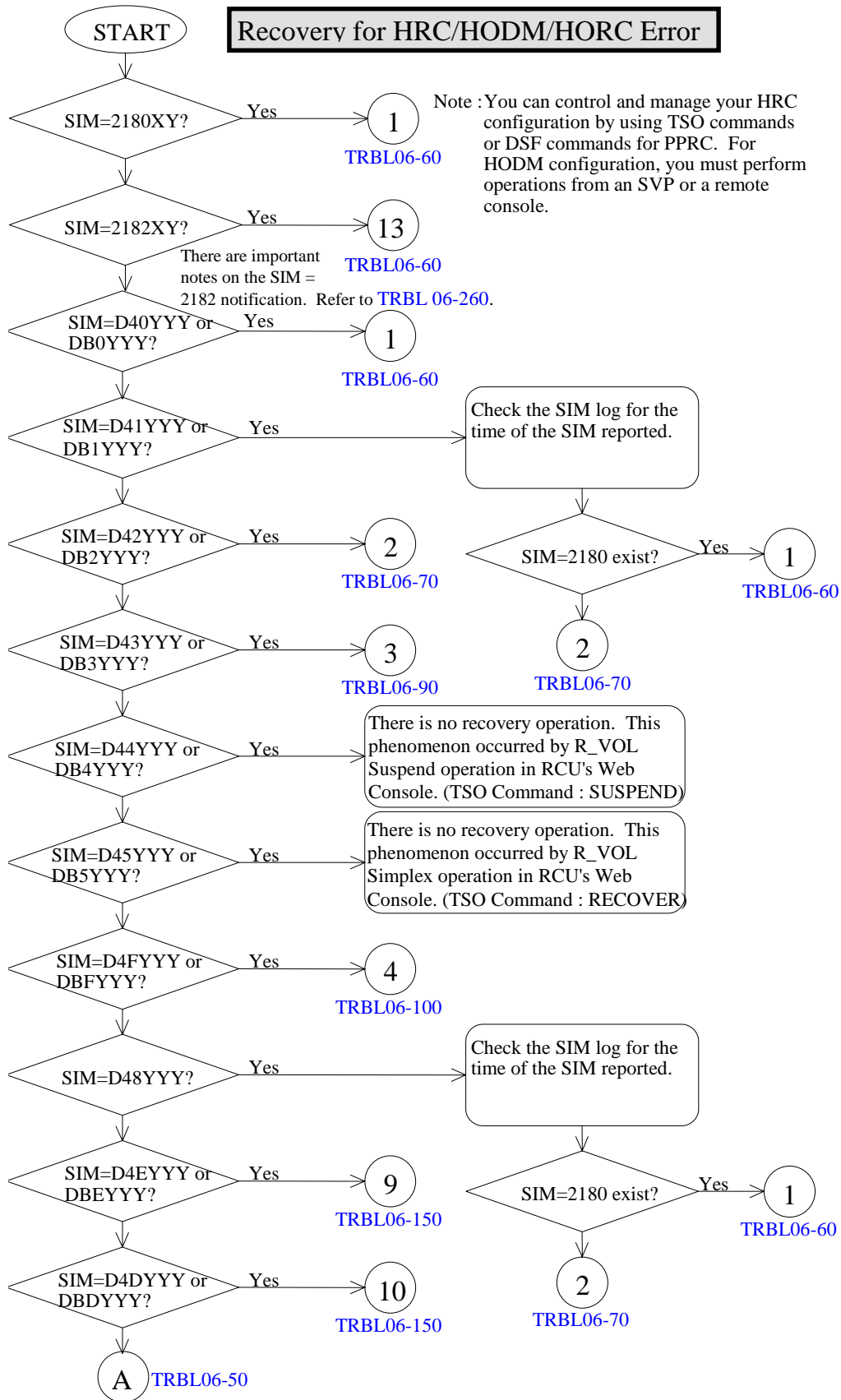
[HORCM\_102] Detected a suspending status on this paired volume  
(Volume: ○○○○, code: XXXX).

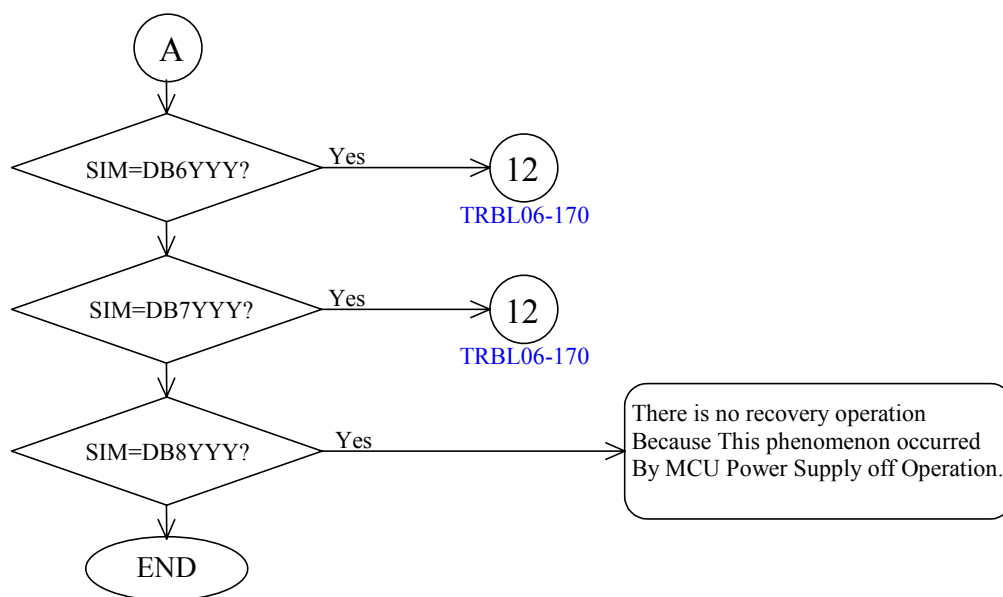
○○○○: Volume name

XXXX: Factor code

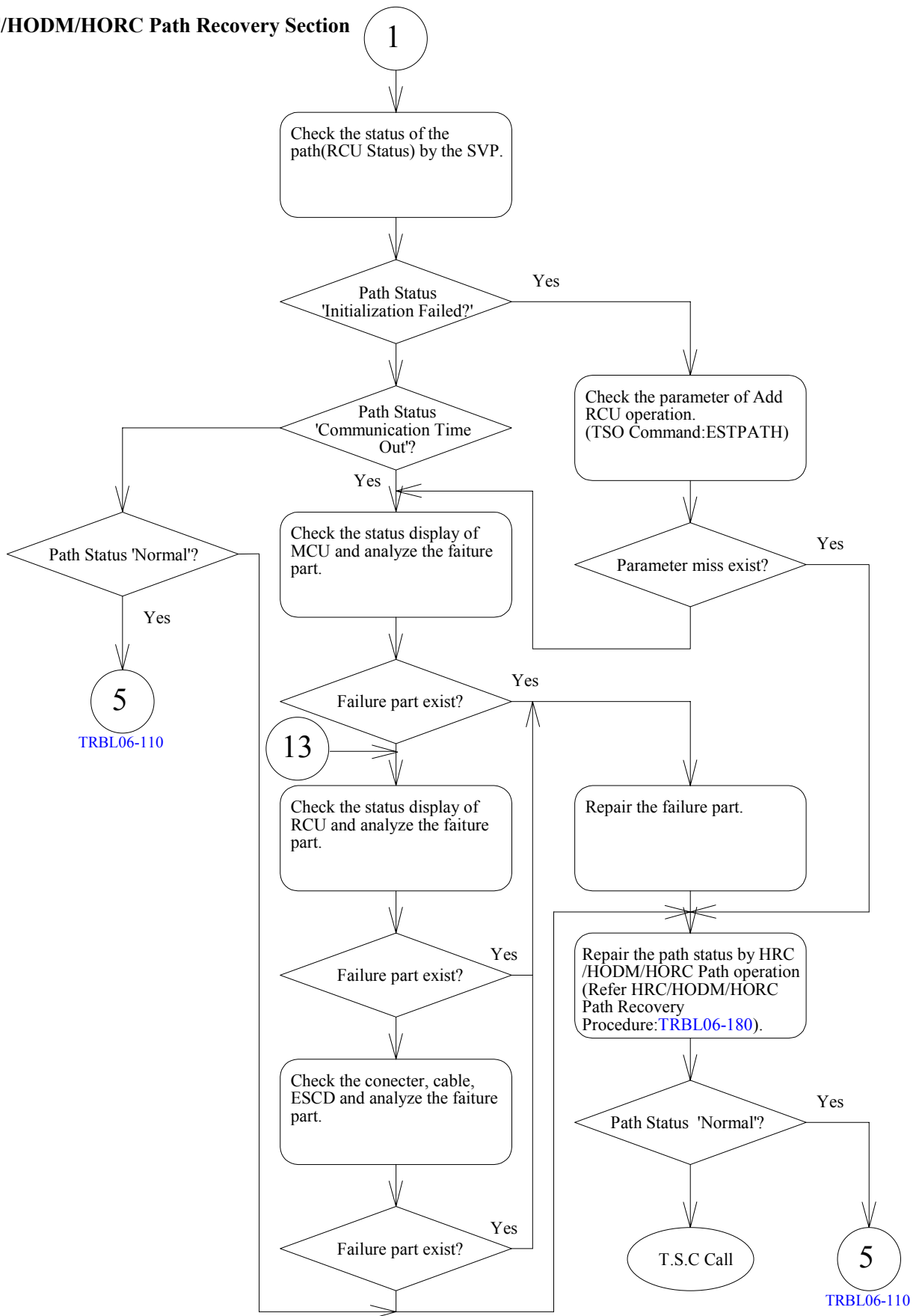
The delete pair operation with Delete Pair by Force option is supported for HORC asynchronous recovery procedure. If hung-up conditions may occur at HORC asynchronous, this operation can be executed from SVP or Remote Console. Refer to Forcible Delete Operation([TRBL06-210](#)).

When the pair status is 'Suspending' a command for creating pairs or deleting pairs from RAID manager is rejected [EX\_CMDRJE] at HORC asynchronous. In this case, retry the command after the pair status is fixed (PSUE, PFUS).





## HRC/HODM/HORC Path Recovery Section



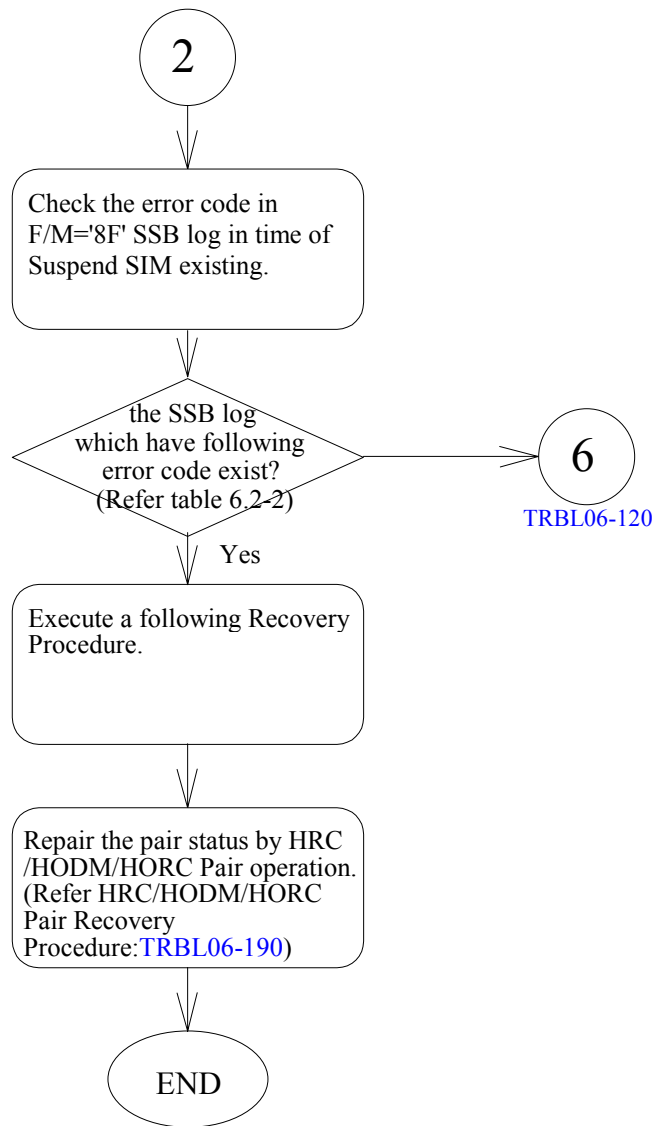


Table 6.2-2 HRC/HODM Recovery Procedure for F/M = '8F'

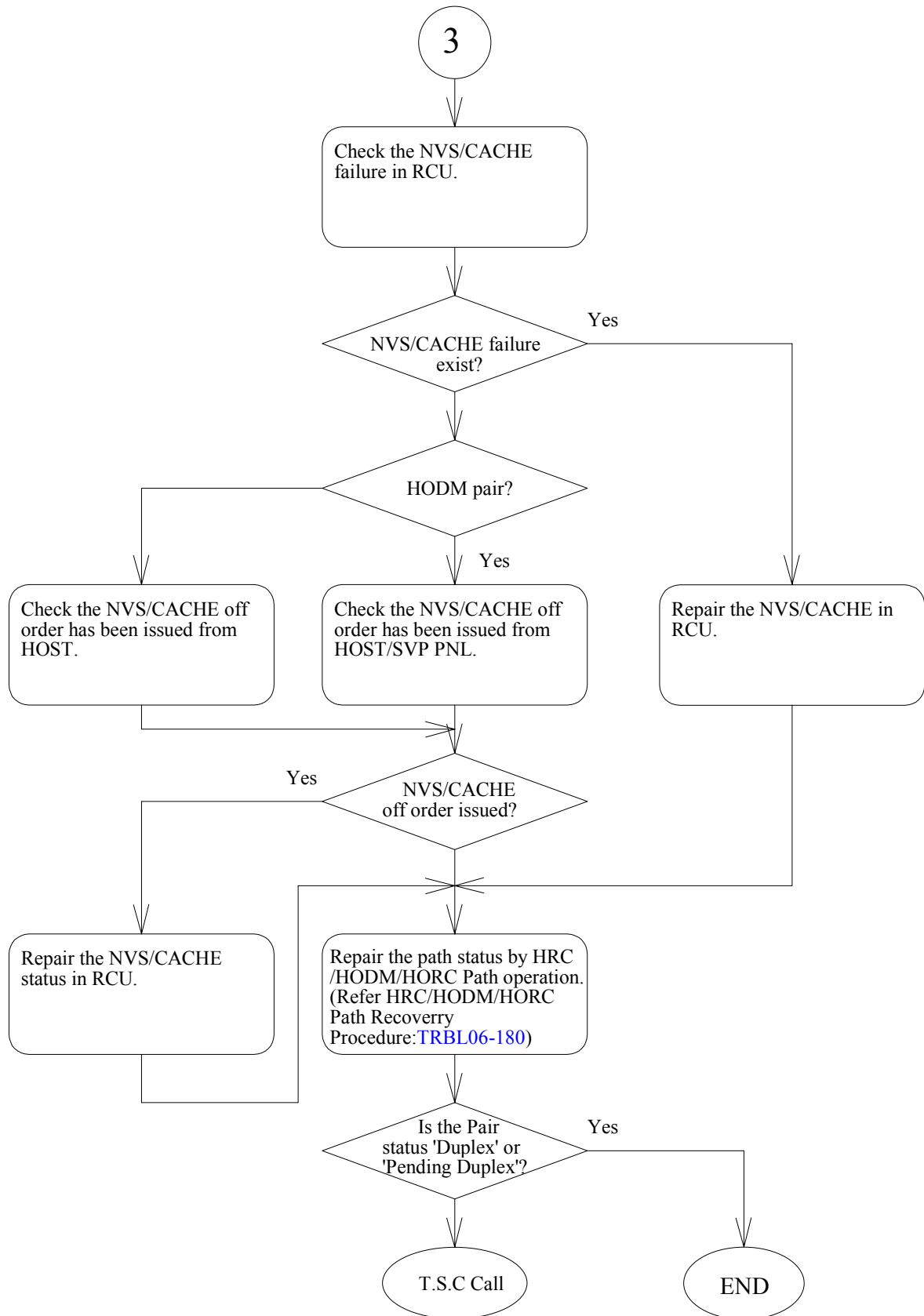
No.	F/M	error code	mean	Recovery Procedure
1	8F	C969	Detect a nonstandard R0 track in R_VOL. (HODM operation)	Change the track format to standard R0 track for the following track. CCHD is as follows.* SSB log byte43:R_VOL# byte72/73:CYL# byte74/75:HD#
2	8F	C96F	(F/M) 8F (error code) C96F (mean) Detect a over run track in R-VOL. (HODM operation) (Recovery Procedure)	Recovery the following (over run) track. CCHD is as follows. SSB log byte43:R_VOL# byte72/73:CYL# byte74/75:HD#
3	8F	C4CE	Detect a nonstandard R0 track in M_VOL. (HRC or PPRC operation)	Change the track format to standard R0 track for the following track. CCHD is as follows.* (Note 1) LDEV in the 'SSB log' window : M_VOL# SSB log byte44/45:CYL# byte46:HD#
4	8F	C883	Detect time-over during retrial for RCU detected error. (HODM operation)	*(Note 2)
5	8F	C884	An SCP reported from RCU. (HODM operation)	*(Note 2)
6	8F	C88E	Detect an I/O error for R-VOL not recoverable with retrial. (HODM operation)	*(Note 2)

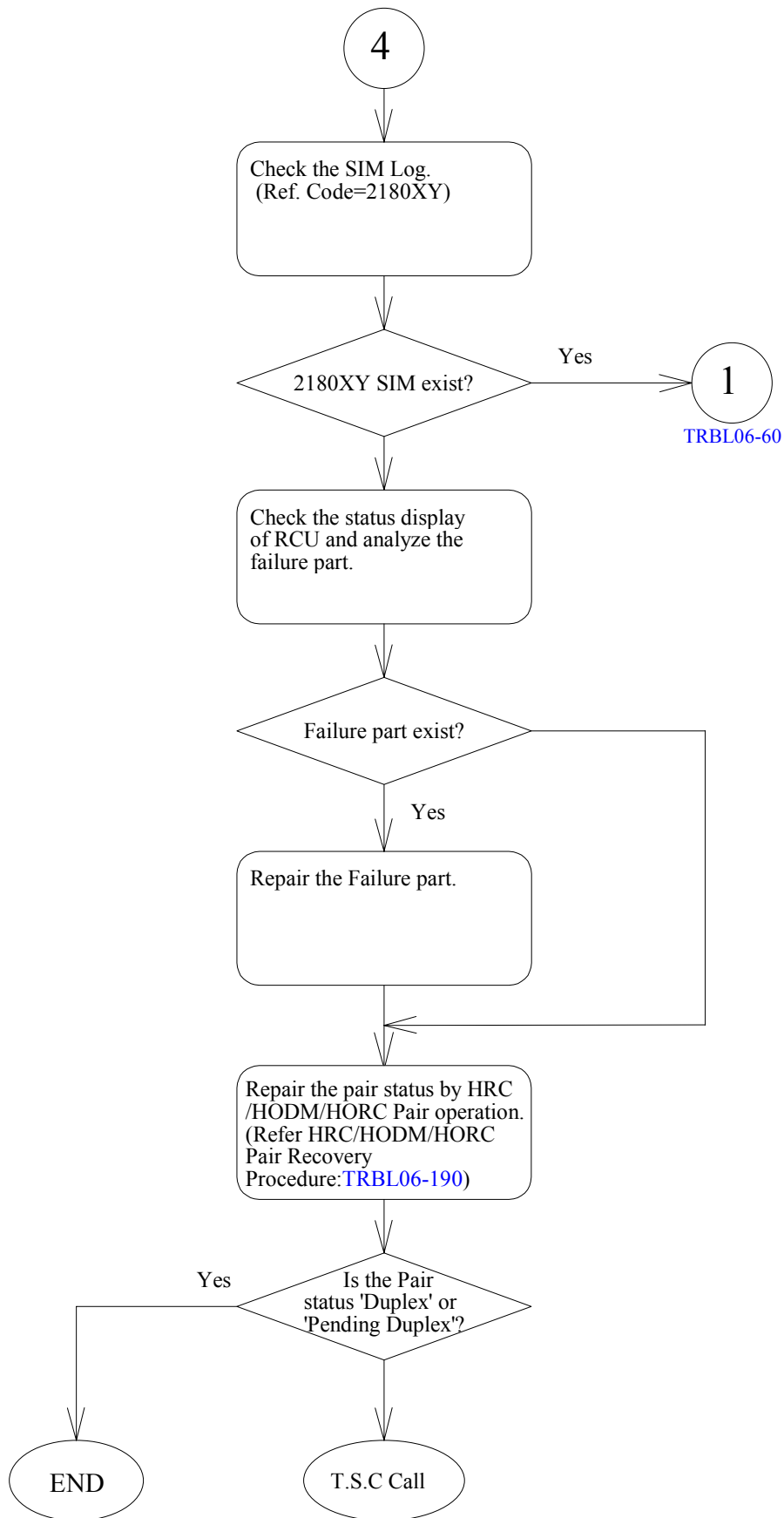
\* use DSF INSPECT NOPRESERVE

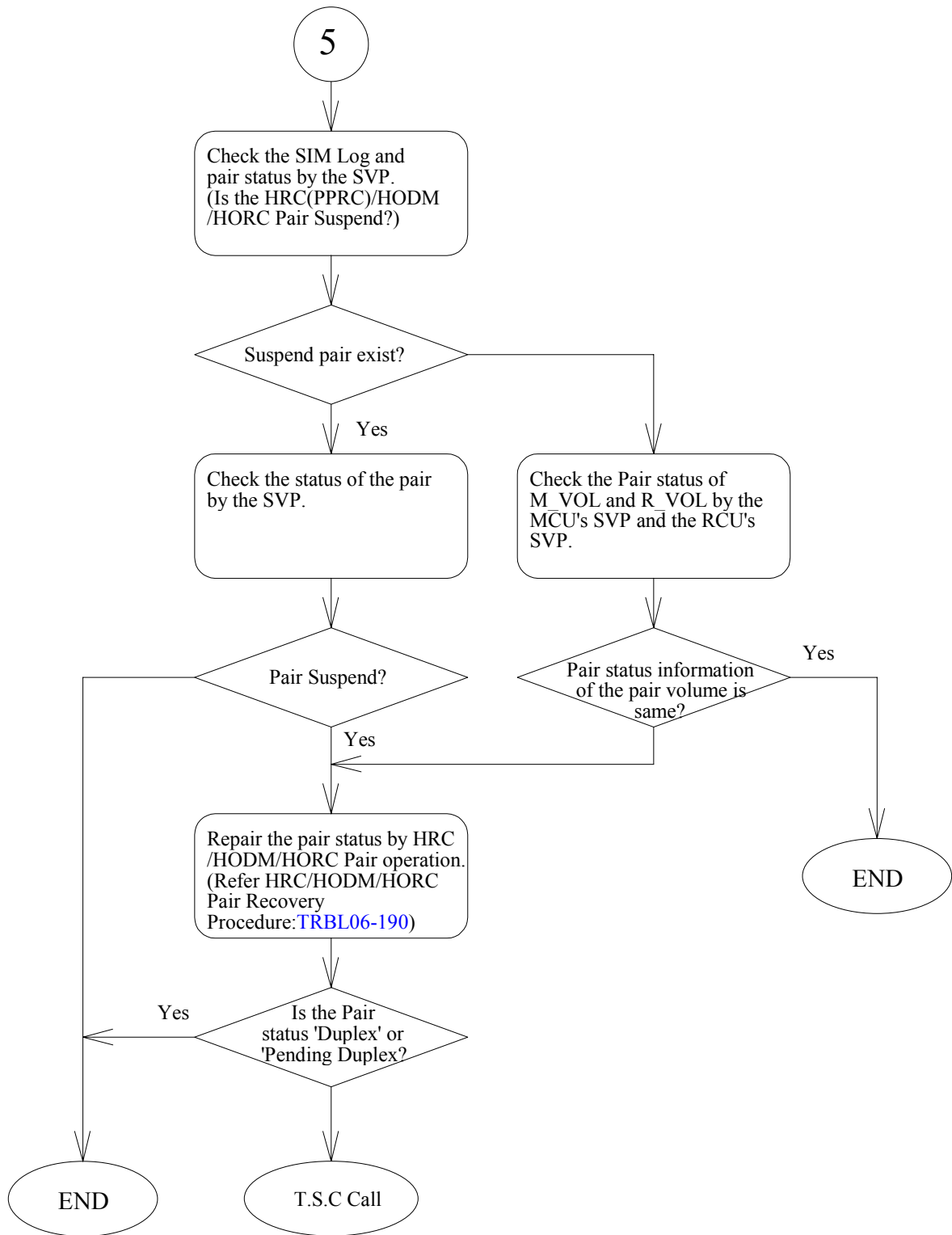
Note 1 If you canceled HODM Pair Operation before this phenomenon has occurred, you must execute format the blocked LDEVs (See [SVP02-700](#)) or DSF (Medial initialization) or DSF (INSTALL) for the Suspended Vol. Because this Volume data is incomplete. If the volume type is RAMAC, you can not change the track format to standard R0 track by DSF.

Note 2 Erase operation after migration copy from IBM RAMAC after migration copy may fail with SSB EC = C883, C884 or C88E due to SCP reported from RAMAC. In this case, reduce the concurrency of erase operation to 1 or 2 and retry the operation.

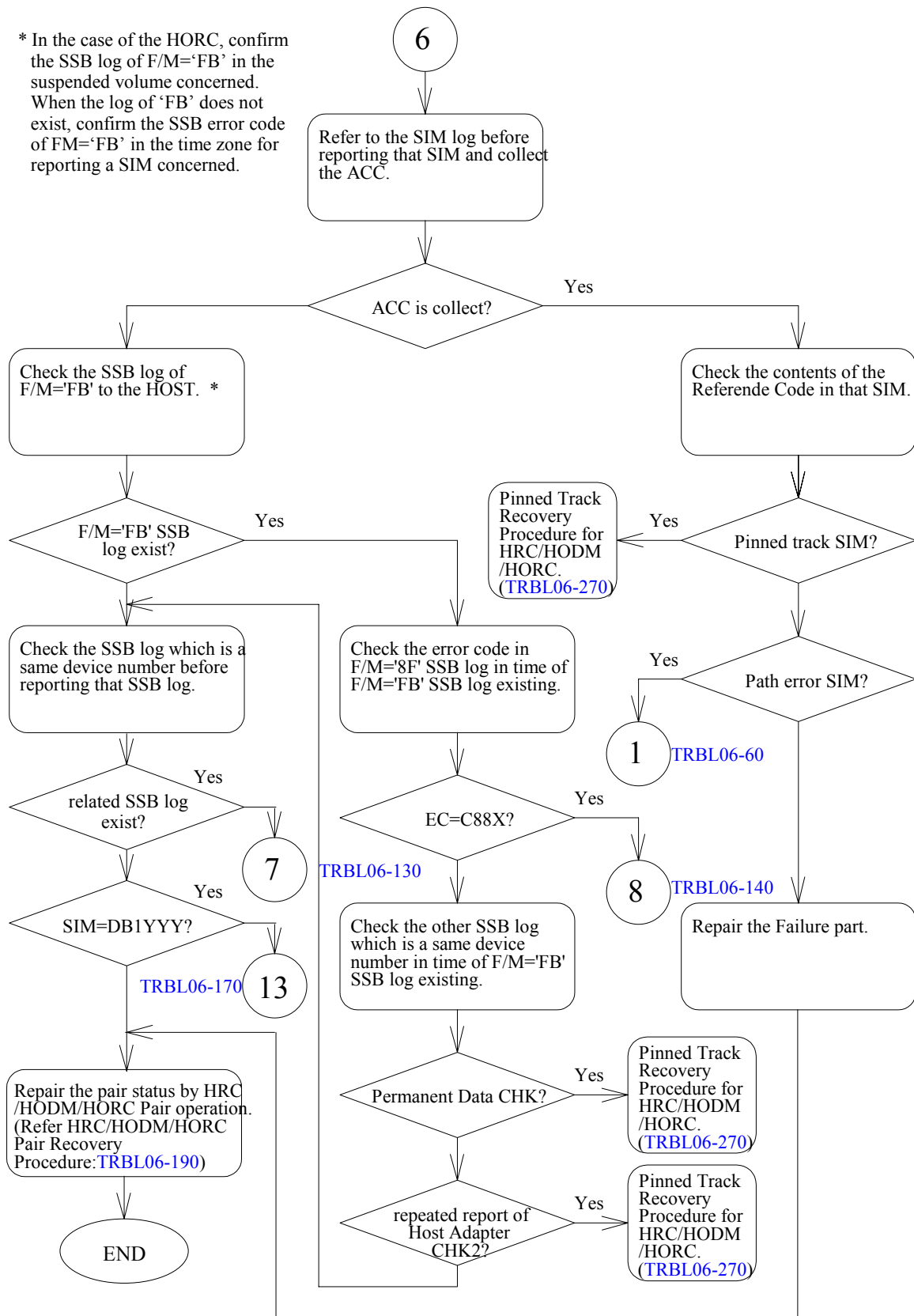


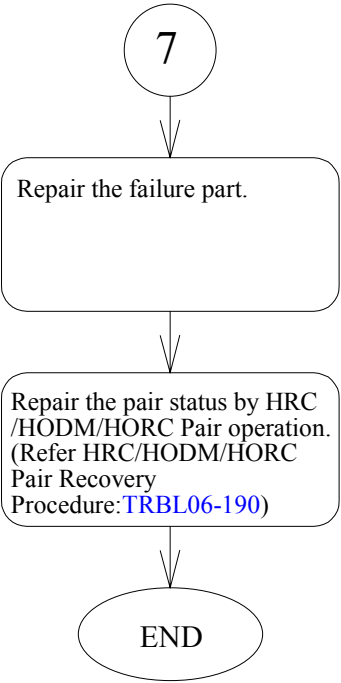






\* In the case of the HORC, confirm the SSB log of F/M='FB' in the suspended volume concerned. When the log of 'FB' does not exist, confirm the SSB error code of FM='FB' in the time zone for reporting a SIM concerned.





8

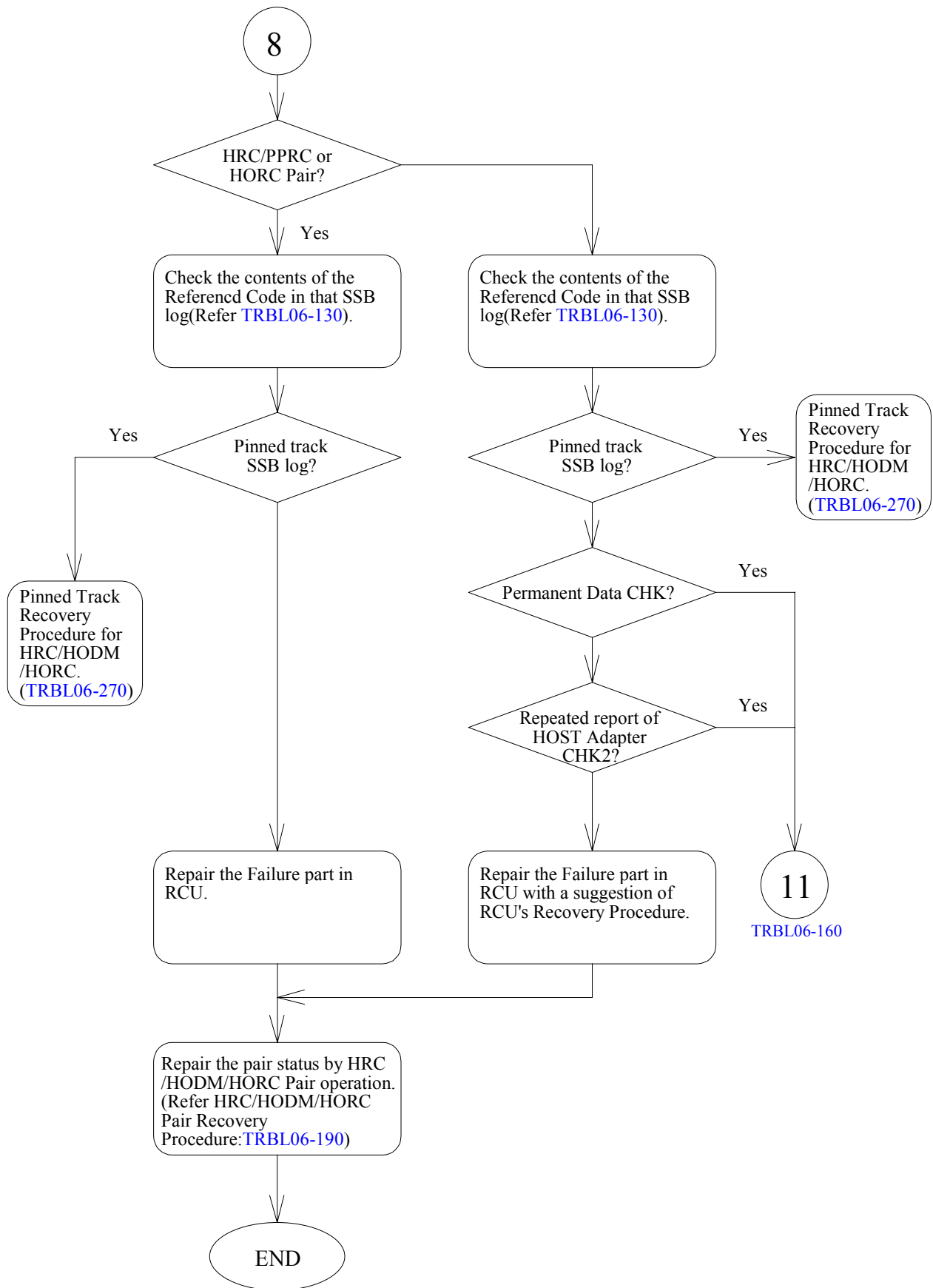
Logical Path Number      Logical Device address of the R-VOL

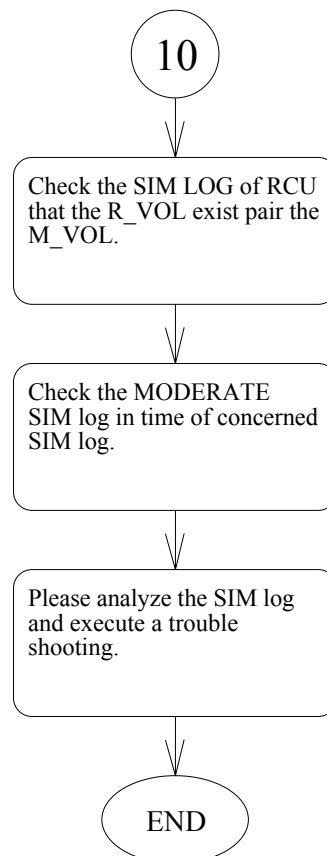
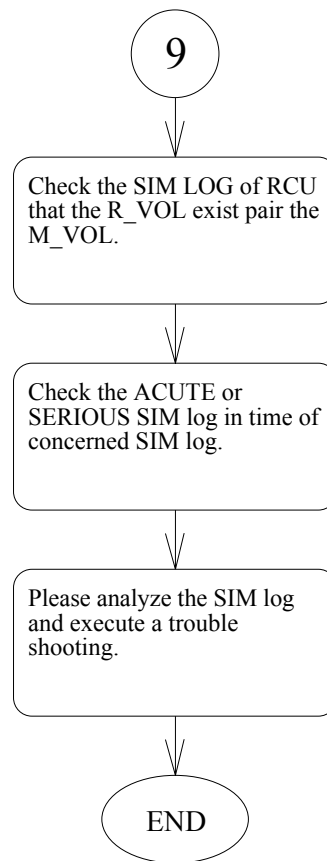
Table F/M='8F', EC=C88X SSB logformat

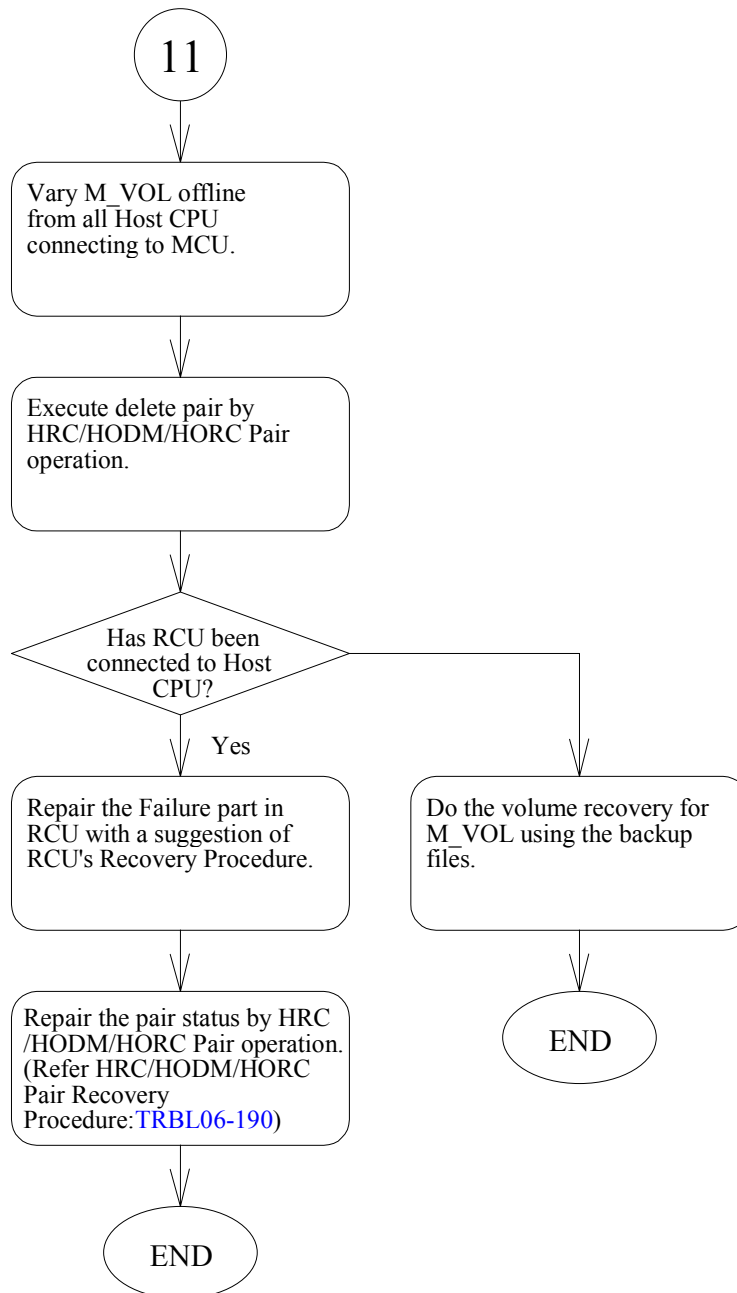
(byte)	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00																
10																
20								8	F	C	8	8	X			
30																
40																
50																
60																
70																

The format of F/M='8F', EC=C88X SSB log is shown above. This SSB log include a SSB log (an oblique plane) which is reported from RCU. Please analyze the SSB log and execute a troubleshooting.

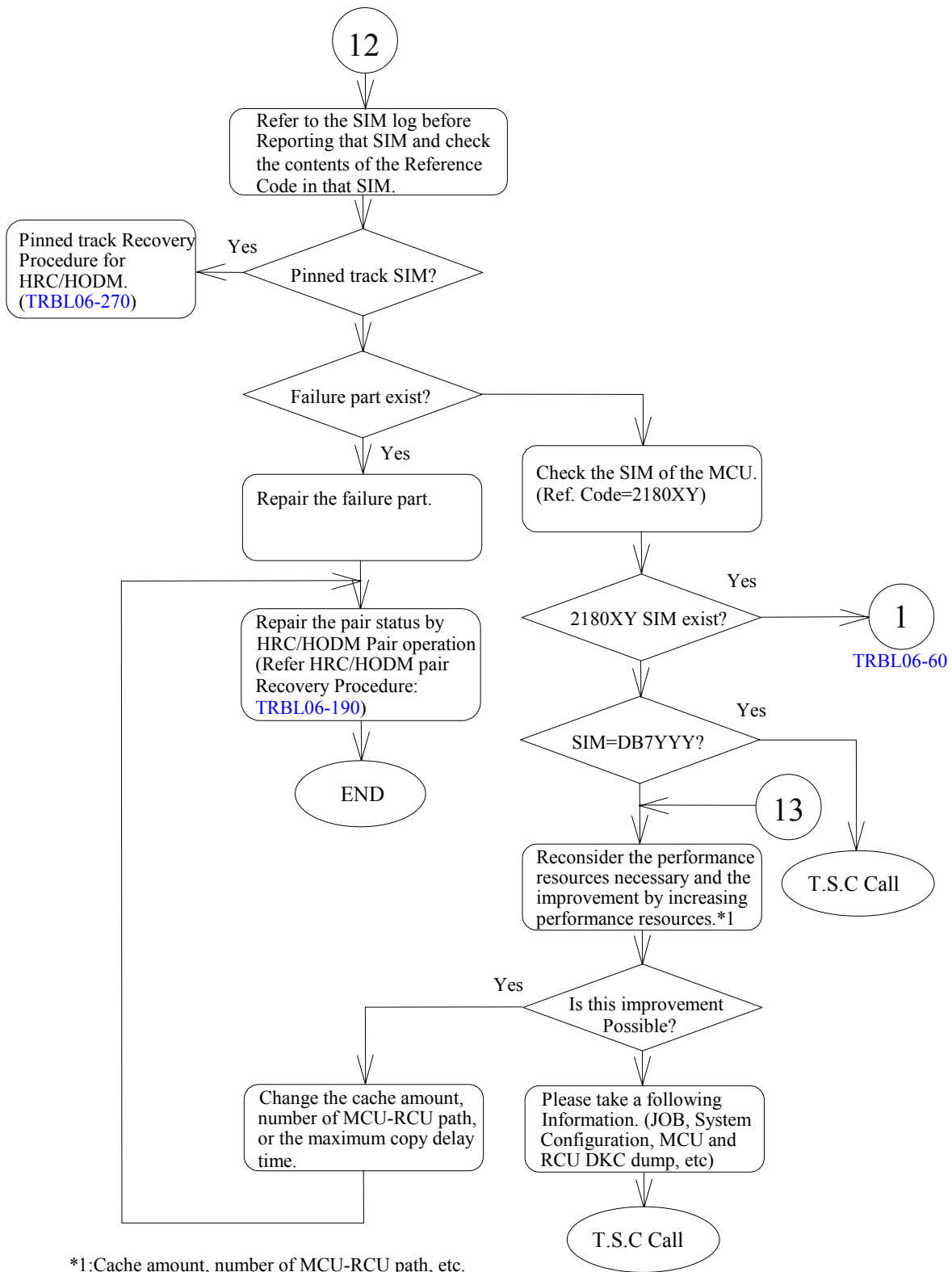
(if byte61 bit 0 = 0)  
"CCHH" which MCU accessed when the Unit Check DSB was reported.



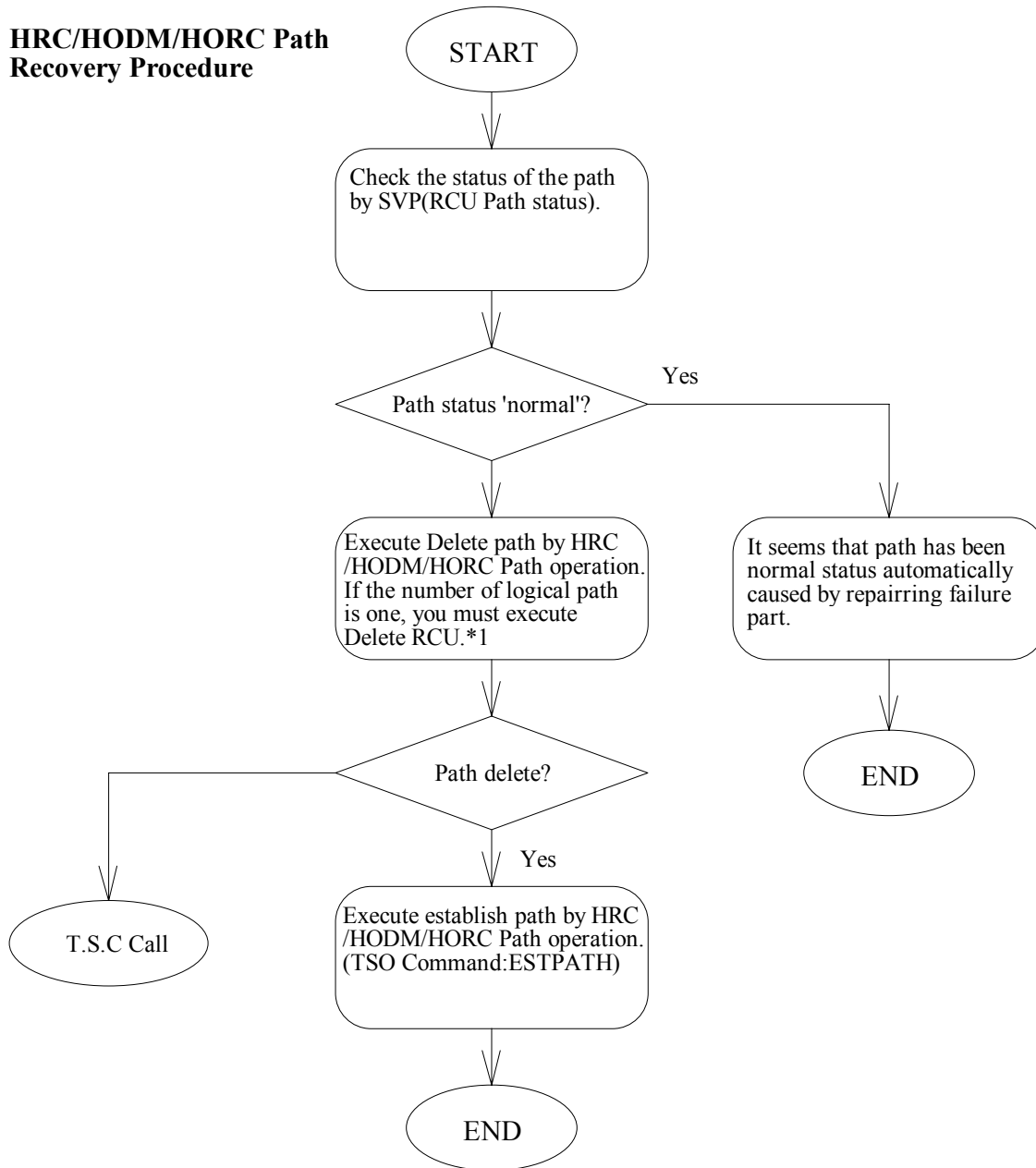




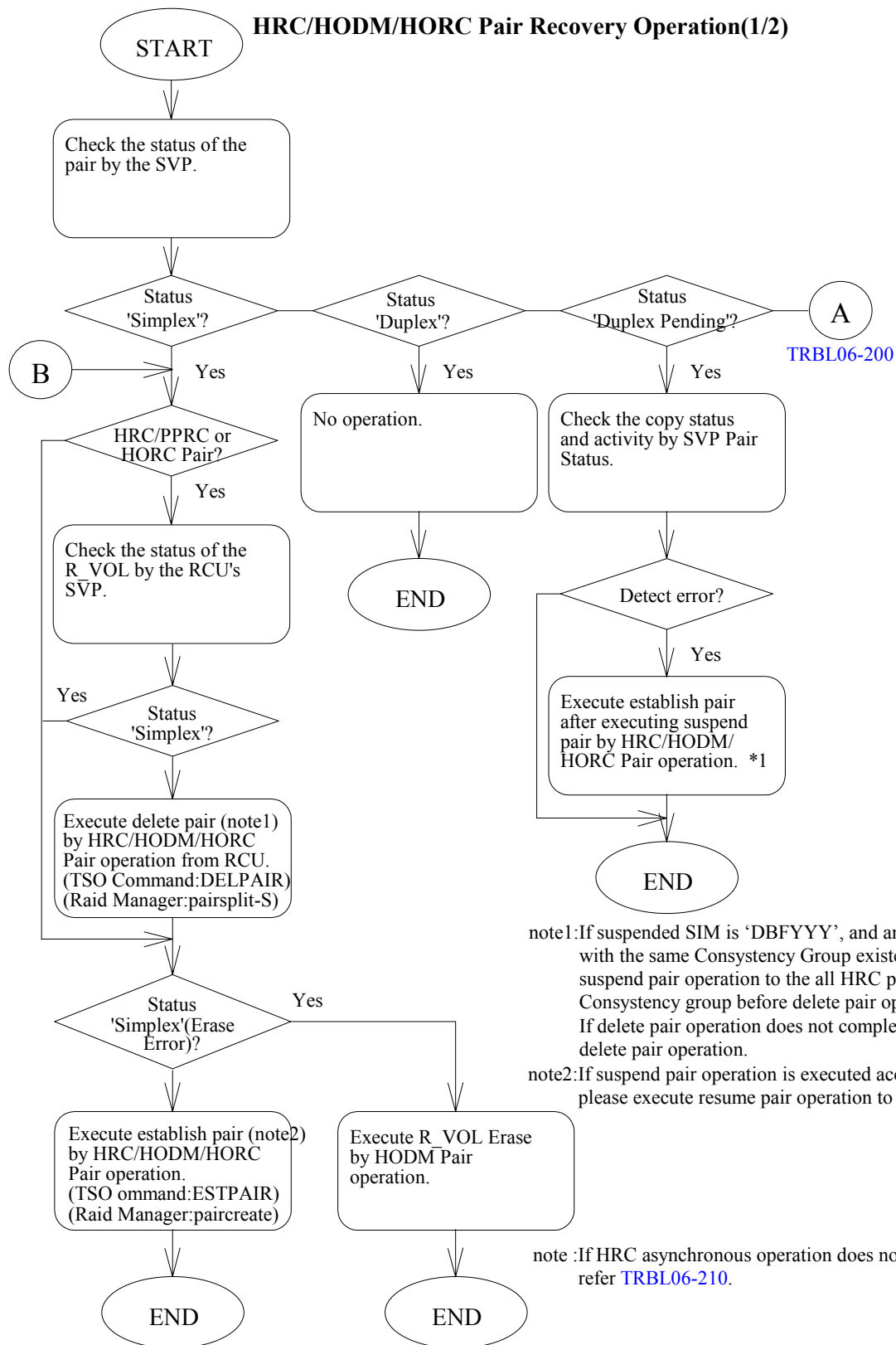




# HRC/HODM/HORC Path Recovery Procedure



\*1 For TSO Command operation, you issue ESTPATH Command, specified the path that established by the last ESTPATH Command issued except failed path. If the number of logical path is one, you must issue DELPATH Command. (Refer IBM PPRCOPY Commands Manual)

**HRC/HODM/HORC Pair Recovery Operation(1/2)**

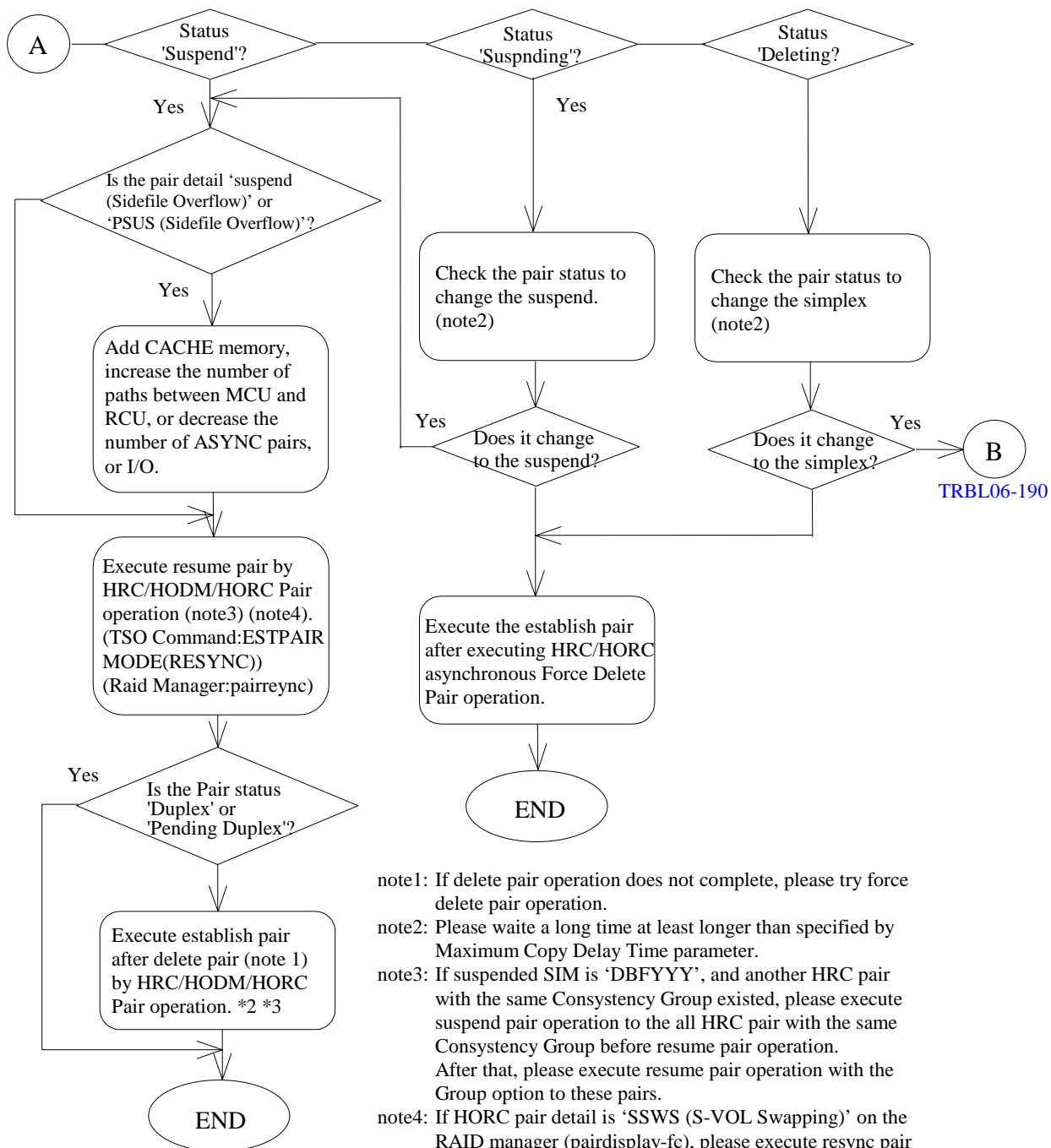
\*1 For TSO Commnad operation, you issue ESTPAIR Command after SUSPEND Command issued.

In the case of the Raid Manager, issue the Pairresync command after the Pairsplit command is issued.

\*2 For TSO Command operation, you issue ESTPAIR Command after DELPAIR Command issued.

In the case of the Raid Manager, issue the Paircreate command after the Pairsplit-S command is issued.

## HRC/HODM/HORC Pair Recovery Operation(2/2)



note1: If delete pair operation does not complete, please try force delete pair operation.

note2: Please wait a long time at least longer than specified by Maximum Copy Delay Time parameter.

note3: If suspended SIM is 'DBFYYY', and another HRC pair with the same Consistency Group existed, please execute suspend pair operation to the all HRC pair with the same Consistency Group before resume pair operation. After that, please execute resume pair operation with the Group option to these pairs.

note4: If HORC pair detail is 'SSWS (S-VOL Swapping)' on the RAID manager (pairdisplay-fc), please execute resync pair operation (pairresync-swaps).

note: If HRC asynchronous operation does not complete, Please refer [TRBL06-210](#).

\*1 For TSO Command operation, you issue ESTPAIR Command after SUSPEND Command issued.

In the case of the Raid Manager, issue the Pairresync command after the Pairsplit command is issued.

\*2 For TSO Command operation, you issue ESTPAIR Command after DELPAIR Command issued.

In the case of the Raid Manager, issue the Paircreate command after the Pairsplit-S command is issued.

\*3 After force-deleting a HORC asynchronous pair whose detailed status was 'SSWS (S-VOL Swapping)', please delete the C/T group from Web Console, which the deleted pair belonged to, before executing paircreate.

## HRC/HORC asynchronous Pair Recovery Operation

HRC/HORC asynchronous pair recovery operation is the same as it of the HRC synchronous pair basically. Please pay attention to the following.

### (1) Extent of the suspend pair volume

When the volume pair which error level is Group is suspended due to the failure, all volume pairs in the same consistency group will be suspended together. In this case, All volume pairs in the same consistency group are in need of Resume Pair operation.

On condition that failure volume pair status is the Duplex pending(volume failure occur during Initial Copy), suspend is only this volume.

### (2) Resume pair Operation

It specifies whether all suspended volume pairs, which belong to the same consistency group and whose M-VOLs are behind this MCU, should be resumed together or not.

### (3) Force Delete Operation

This section describes the error recovery procedures to recover from the following hung-up conditions:

- The volume pairs were suspended due to some kind of failure. However the pair status of the affected volume pairs remained **unchanged from Suspending** for a long time (at least longer than specified by Maximum Copy Delay Time parameter).
- **Delete Pair** or **Suspend Pair** operation completed without error message. However the pair status of the volume pairs to be deleted or suspended remained **unchanged from Deleting or Suspending** for a long time (at least longer than specified by Maximum Copy Delay Time parameter).
- Add Pair operation failed with Web Console messages '6005 8808' or '6005 8809' and the operation could not complete after several times of retries.
- **Suspend Pair, Delete Pair** or **Delete Group** operation failed with Web Console messages '6005 8844', '6005 8855' or '6005 8880' respectively and the operation could not complete after several times of retries.

The recovery procedure is described in the next figure on [TRBL06-230](#).

## (3-1) Operating Delete Pair with Delete Pair by Force option

**Delete pair operation with Delete Pair by Force option** is effective to recover from such hung-up conditions. Being specified with this option, the specified control unit (MCU or RCU) performs the forcible delete process as follows:

- Changes the volume status of all the volumes that are behind the specified control unit and belong to the consistency group to simplex.
- Discard all the record sets that are pending (not sent to the RCU or not settled yet) in the specified control unit.

Note that the specified control unit performs the forcible delete without communicating with the paired control unit. Since both the MCU and RCU manages volume pair status and can have the pending record set within, this operation **must be done at both the MCU and RCU**.

## (3-2) Re-establishing Volume Pair

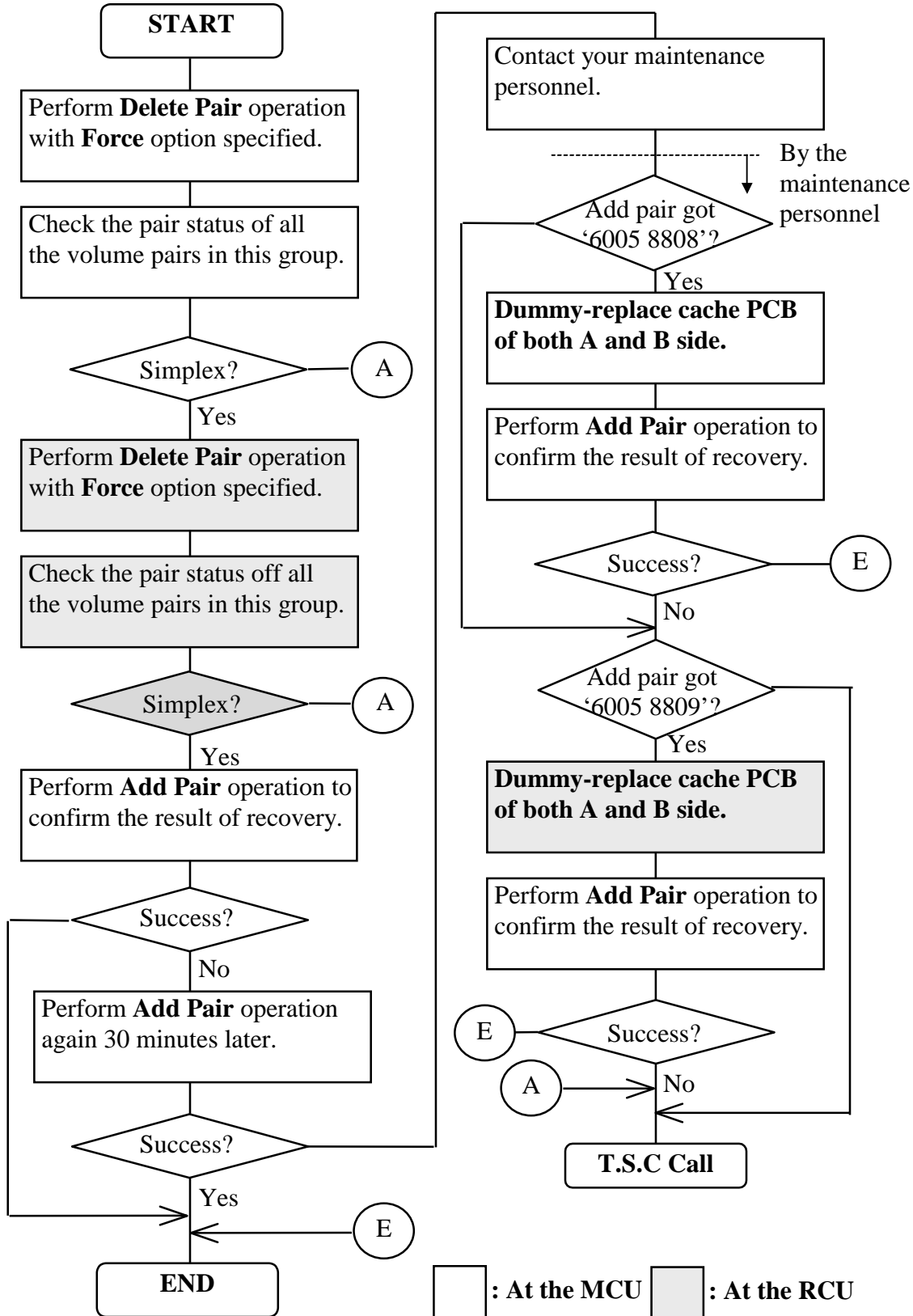
After completing delete pair operation at both the MCU and RCU, establish volume pair(s) again to check that hung-up conditions are recovered. Be sure that **the same consistency group number** must be specified as before. If the different consistency group number is specified, the result is unpredictable.

## (3-3) Performing Dummy-Replacement of Cache PCB

If the delete pair operation can not recover from hung-up conditions (establishing volume pair results in failure with message '6005 8808' or '6005 8809'), some inconsistent condition may remain in the sidefile structure. To recover from this situation, dummy-replacement of cache PCB requires to be performed for **both A and B sides**. The operated control unit discards all the inconsistent sidefiles during replacement procedure.

Only the sidefiles of the deleted consistency group is discarded. Therefore dummy-replacement can be performed while other consistency groups are working at the control unit.

### HRC asynchronous Force Delete Operation



## Recovery Operation of the Suspended HORC Pair

This document which is addition of the HRC/HODM/HORC Pair Recovery Operation ([TRBL06-190](#)) explain the recovery operation for the suspended HORC pair in the extended LU.

After repairing the failure part, execute a resume (pair resync) operation for the suspended pair. HORC resume operation can be executed from SVP, Remote Console, and Raid Manager. A means, an object and a procedure of the resume operation are shown in a following table.

Table HORC Resume Operation Procedure in the extended LU

means	object	procedure
Web Console	Volume	(1) Check the volume number of the suspend pair from the F/M="FB" SSB. (2) Check the LU pair status which comprises the suspended pair volume is a "Suspend", "Duplex (W)", or "Pending Duplex (W)". (3) Execute a resume pair operation to the suspended volume pair. (4) Check the pair status is a "Duplex" or a "Pending Duplex".
	Logical Unit	(1) Check the volume number of the suspend pair from the F/M="FB" SSB. (2) Check the LU pair status which comprises the suspended pair volume is a "Suspend", "Duplex (W)", or "Pending Duplex (W)". (3) Execute a resume pair operation to the suspended LU pair. (4) Check the pair status is a "Duplex" or a "Pending Duplex".
Raid Manager	Logical Unit	(1) Check the volume number of the suspend pair from the F/M="FB" SSB. (2) Check the LU pair status which comprises the suspended pair volume is a "PDUB". (3) A pairresync command issues to the suspended LU pair. (4) Check the pair status is a "Pair" ("Duplex") or a "Copy" ("Pending Duplex").

If pair status does not change "Duplex" or "Pending Duplex" after executing a resume operation, please try delete pair operation (pairsplit-s command for Raid Manager), and execute Add pair operation (paircreate command for Raid Manager) again.



## Procedure when the host hangs

If a host hangs up while it is writing data in HORC P-Vols, data difference between paired P-Vol and S-Vol may occur because of reasons as follows.

1. A write command didn't finish normally for some reason in DKC and it reported check condition to the host, but because of the hang-up, the host didn't retry the command.
2. During a process of a write command in DKC, a reset message was issued from the host and stopped the write command process, but because of the hang-up, the host didn't retry the command.

In this case, since write data of the last write command before hang-up was not written on the disk completely, please restart the job in order to complete the aborted command.

### Special mentions on SIM = 2182-XY

The SIM = 2182 host report is supported for the HRC/HORC activity with the CNT extender (Ultraset) between the MCU and RCU.

The SIM = 2182 indicates that the extender has detected a failure that occurred in a communication line or the ESCON link of a remote site and the failure has been reported to the MCU.

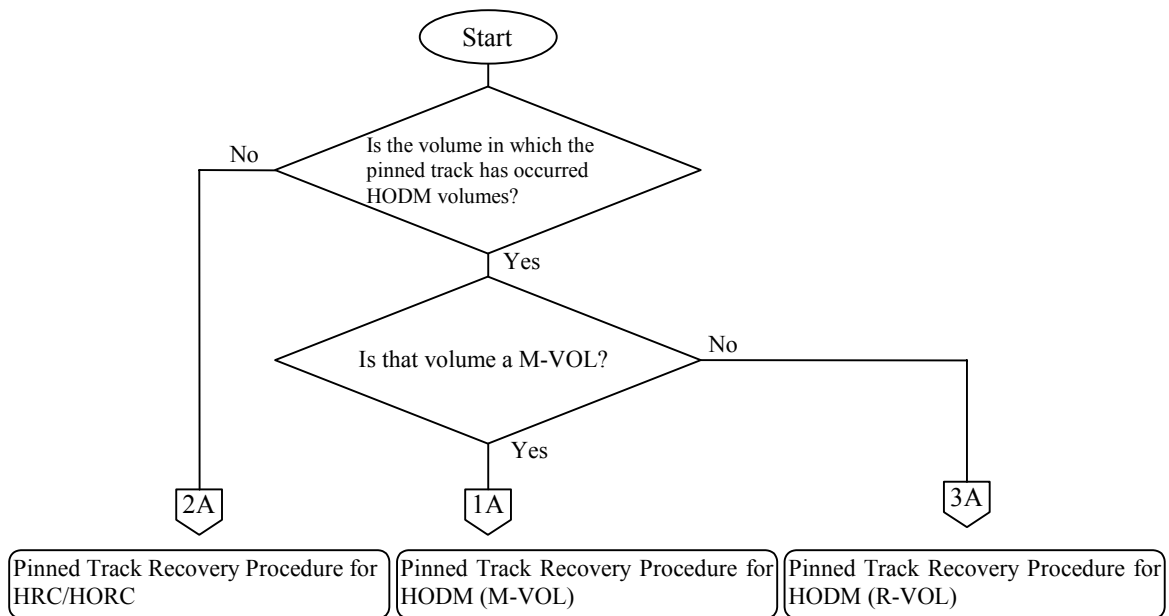
However, there are several special mentions on the SIM = 2182 report.

#### [Special mentions]

- ① When one extender has some alternative communication lines and a failure occurs on one line, the device will retry for another active line. Therefore, no line failure report is sent from the extender when the retrying succeeds.
- ② -The remote copy logical path that has received a line failure notification from the line extender is blocked (Path status: Communication Time Out).  
-When recovering the logical path, please follow the HRC/HORC path recovery procedure ([TRBL06-180](#)).

### 6.3 Pinned Track Recovery Procedure for HRC/HODM/HORC

Pinned track recovery procedure for HRC/HODM/HORC is as follows.

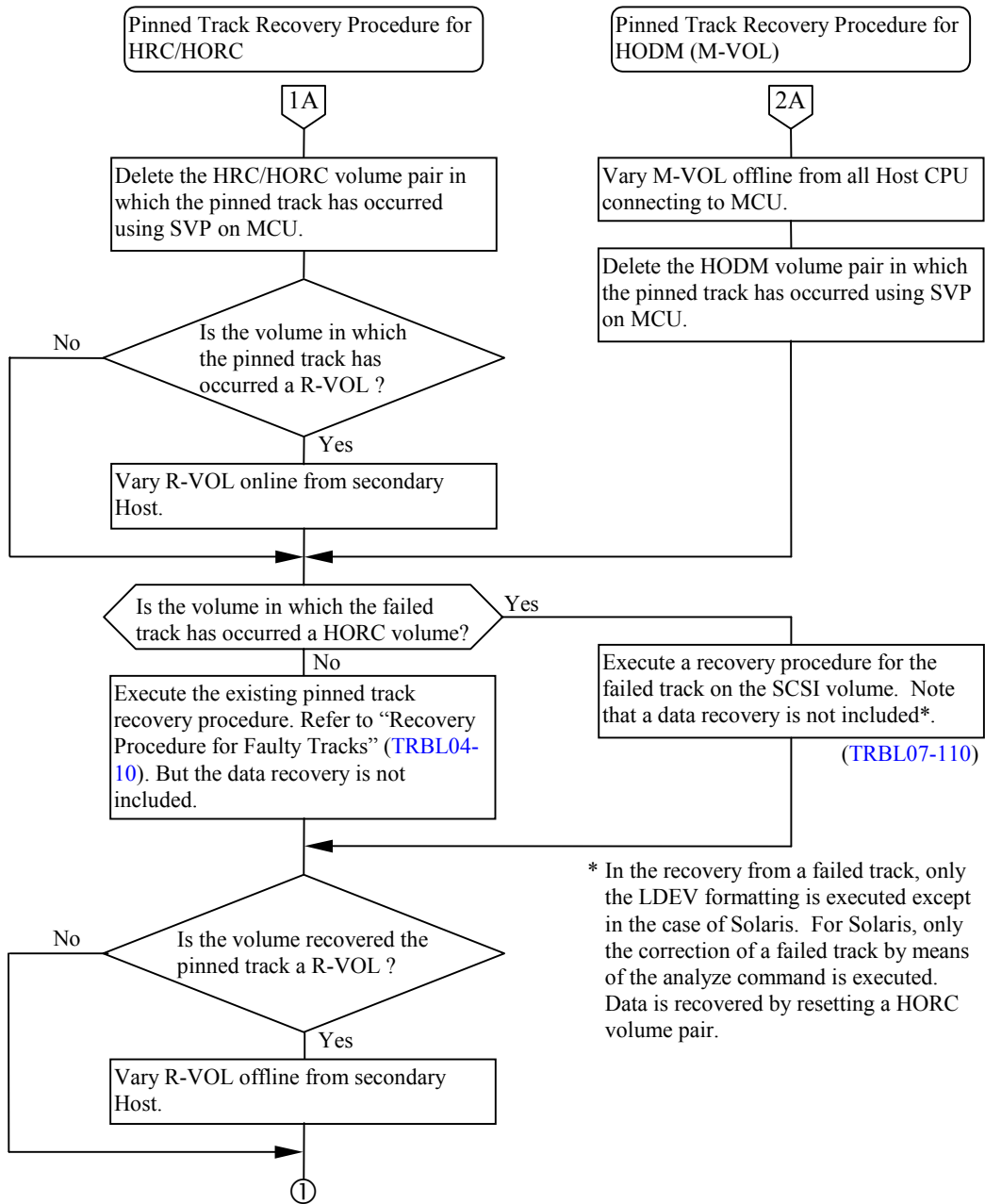


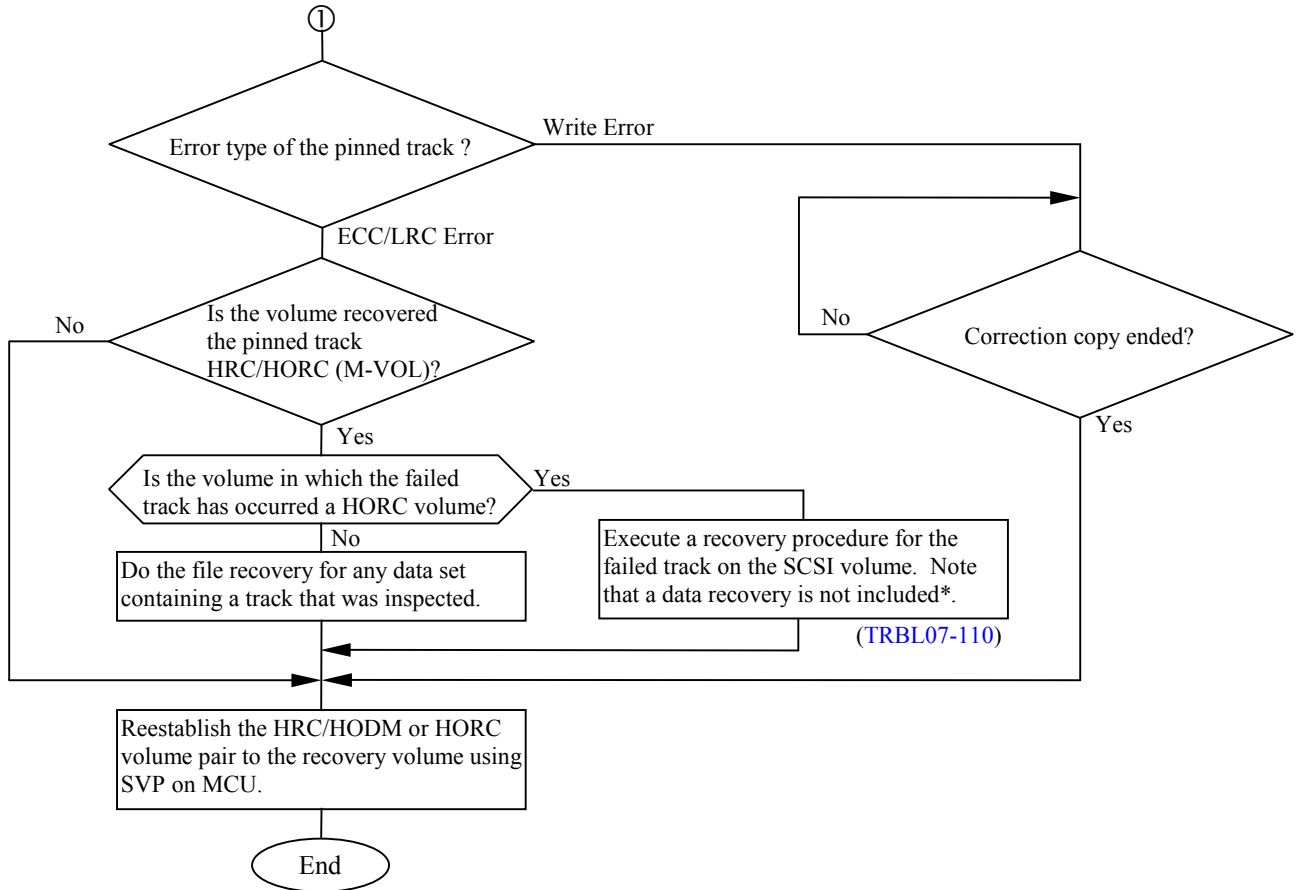
(Note)

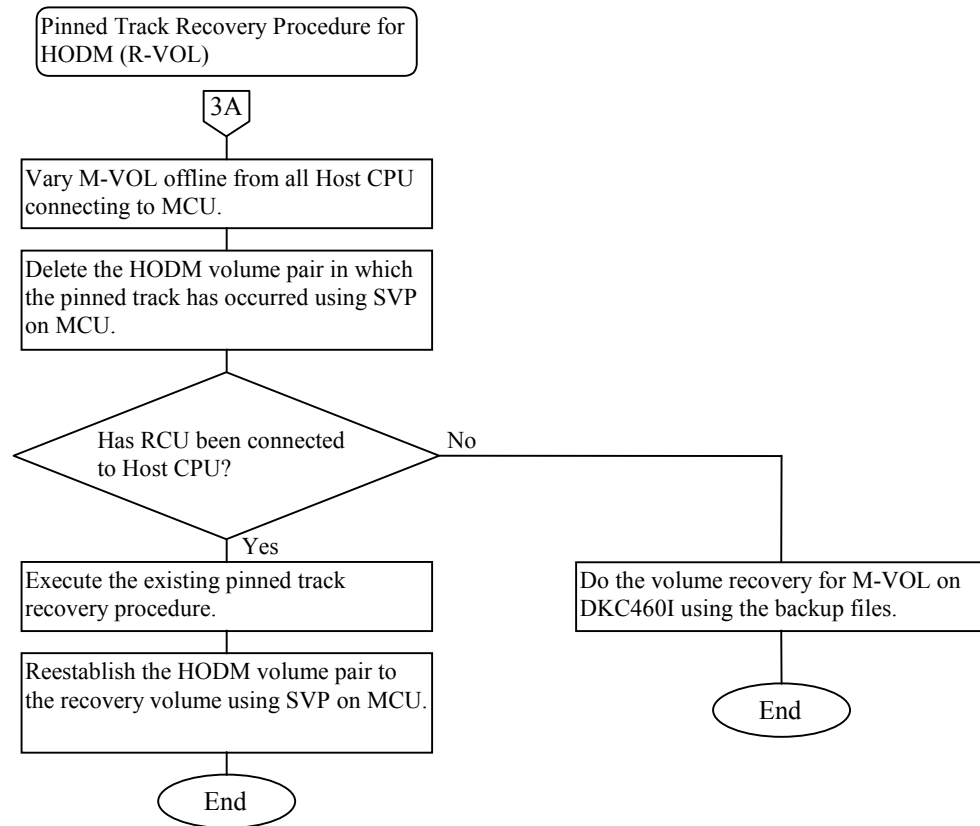
If the pinned track has occurred in both M-VOL and R-VOL, recover the volumes according to the following sequence.

HRC/HORC : ① M-VOL → ② R-VOL

HODM : ① R-VOL → ② M-VOL







## 6.4 Recovery Action of Path Status Error

Path Status	Factor	Recovery Action
"Nothing"	Establishing path or deleting path	Delete the path with "Edit Path" or "Delete RCU", add a new path with "Edit Path" or "Add RCU".
"Initialization Failed"	Incorrect the physical connection between the MCU and RCU	Correct the physical path connection between the MCU, RCU and the path relay equipment.
	Incorrect the Port topology settings. (Fibre connection)	Setup the port topology of the both MCU and RCU correctly.
	Blockade of the MP or Port on the RCU	Repair the MP status or Port status on the RCU.
	Broken the connection cable physically	Replace the broken cable.
	Incorrect the path relay equipment settings or the path relay equipment doesn't work.	Correct the path relay equipment settings or repair it.
"Communication Time Out"	Blockade of the MP or Port on the MCU	Repair the MP status or Port status on the MCU.
"Resource Shortage (MCU)"	MCU resource over	Too many path in the MCU or the RCU. Delete the paths and RCUs not currently in use.
"Resource Shortage (RCU)"	RCU resource over	
"Serial Number Mismatch"	Incorrect the RCU S/N or Controller ID (Fibre connection)	Delete the path with "Delete RCU", add a new path with the correct RCU S/N and Controller ID with "Add RCU" again.
	Incorrect the physical connection between the MCU and RCU	Correct the physical path connection between the MCU, RCU and the path relay equipment.
	Incorrect the Port topology settings. (Fibre connection)	Setup the port topology of the both MCU and RCU correctly.
	Blockade of the MP or Port on the RCU	Repair the MP status or Port status on the RCU.
	Broken the connection cable physically	Replace the broken cable.
	Incorrect the path relay equipment settings or the path relay equipment doesn't work.	Correct the path relay equipment settings or repair it.

(To be continued)

(Continued from the preceding page)

Path Status	Factor	Recovery Action
“Invalid Port”	Specified port is not existence on the MCU.	Delete the path with “Edit Path” or “Delete RCU”, add a new path with correct port on MCU again.
	Incorrect specified port type “Serial/Fibre”.	Delete the path with “Edit Path” or “Delete RCU”, add a new path with correct port type “Serial/Fibre” again.
	Specified MCU port type is not RCP or Initiator.	Change the channel type of the MCU port to RCP or Initiator.
	Incorrect the specified CU of MCU (Serial connection)	Delete the path with “Edit Path” or “Delete RCU”, and add a new path with correct CU again.
	Incorrect the specified link address. (Serial connection)	When MCU and RCU connects directly, set the link address to except "00". When MCU and RCU does not connect directly, set the link address to "00".
	Exists the same path.	Confirm the MCU port, link address or RCU port, and logical address or CU#, and delete the path with “Edit Path”.
“RCU Port Number Mismatch”	Incorrect the specified RCU port.	Delete the path with “Edit Path” or “Delete RCU”, and add a new path with correct RCU port again.
	Incorrect the physical connection between the MCU and RCU	Correct the physical path connection between the MCU, RCU and the path relay equipment.
	Incorrect the Port topology settings. (Fibre connection)	Setup the port topology of the both MCU and RCU correctly.
	Blockade of the MP or Port on the RCU	Repair the MP status or Port status on the RCU.
	Broken the connection cable physically	Replace the broken cable.
	Incorrect the path relay equipment settings or the path relay equipment doesn't work.	Correct the path relay equipment settings or repair it.
“RCU Port type is not 'RCU Target’”	Incorrect specified RCU port.	Delete the path with “Edit Path” or “Delete RCU”, and add a new path with correct RCU port again.
	Specified RCU port type is not RCU Target	Change the channel type of the RCU port to RCU Target.
“Communication Failed”	Blockade of the MP or Port on the RCU	Repair the MP status or Port status on the RCU.
	The path relay equipment doesn't work.	Repair the path relay equipment



## 7 Troubleshooting of Multiplatform

### 7.1 Troubleshooting of error on host Fibre channel interface

This section describes troubleshooting of error on host Fibre channel interface.

#### 7.1.1 Possible error and cause

Table 7-1 Possible error and cause

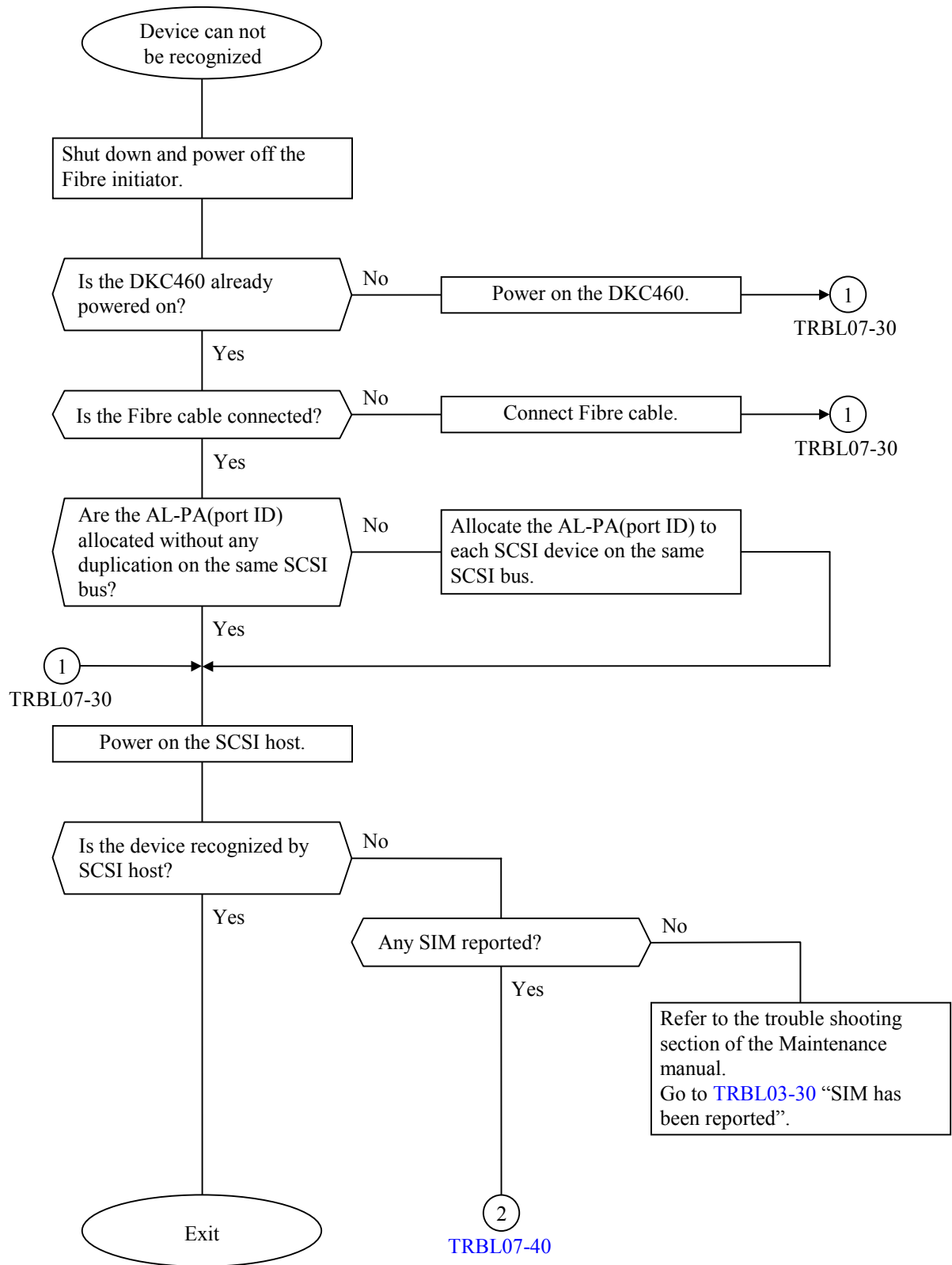
No.	Possible error	Cause
1	DKC465 LDEV is not recognized by Fibre initiator	(1) SCSI installation, i.e. recognition and connecting procedure from SCSI initiator is not executed correctly (2) Problem of Fibre cable or connection (3) Problem of Fibre initiator. Fibre board, device driver version, parameters, etc. (4) SCSI path definition from SVP (5) Other

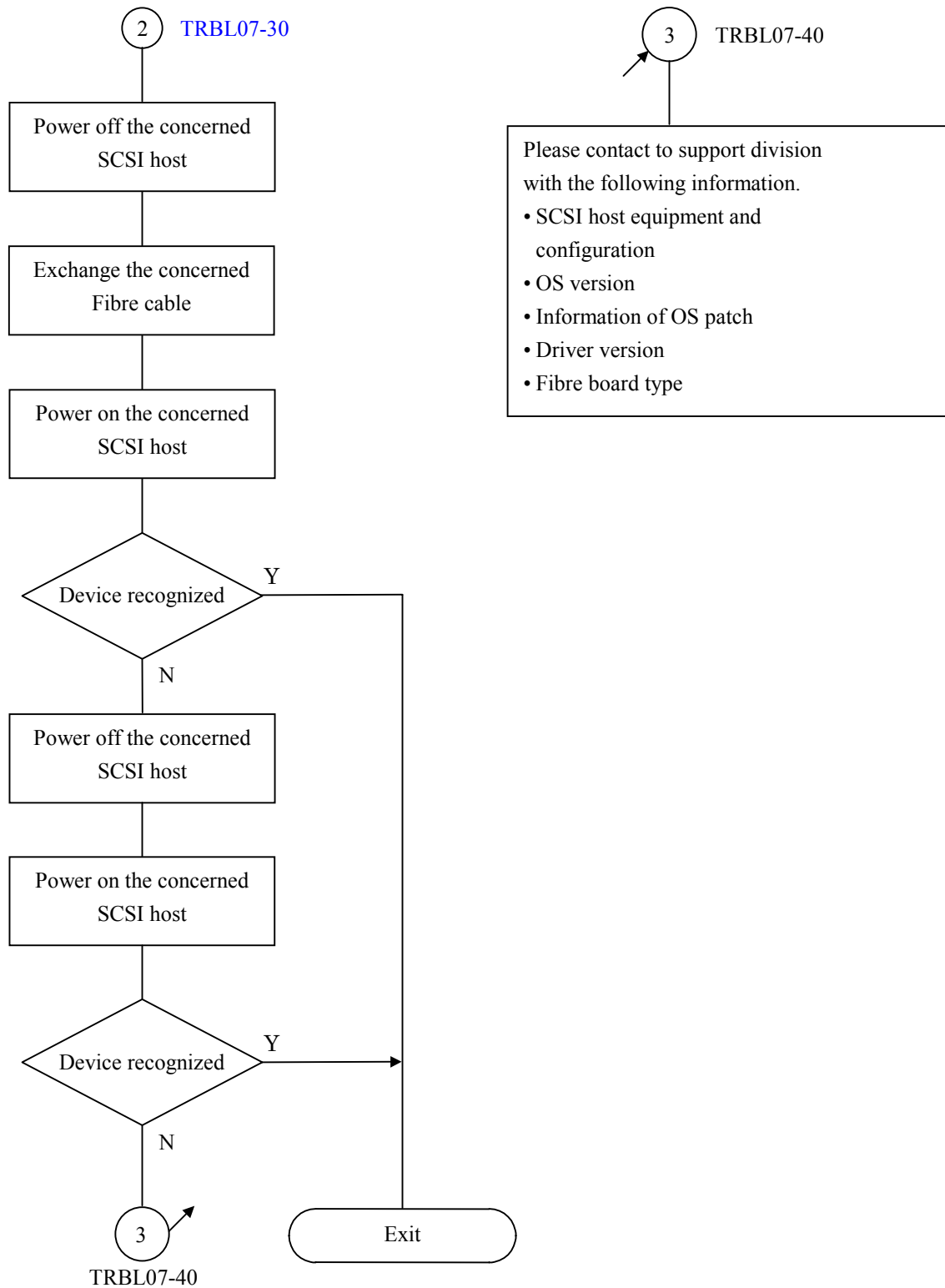
## 7.1.2 Checking item when some errors occur on host Fibre channel path

Check item (correct value)

- (1) Is the DKC ready lamp lit? (The ready lamp should be lit. Check visually.)
- (2) Is the concerned channel port lamp lit? (The concerned channel port lamp should be lit. Check visually.)
- (3) Is the concerned LDEV status normal? (The LDEV status should be normal or correction access. Check on SVP.)
- (4) Is the concerned FCP/FOP status normal? (Check on SVP.)
- (5) Is the concerned CHF status normal? (Check on SVP.)
- (6) Are the CHF locations of Basic/Additional 1/Additional 2 and Cluster 1/Cluster 2 and the I/F connector panel location understood precisely?  
Refer to pages [LOCATION04-70](#), [LOCATION05-40](#) and [INST03-FIB-10 through INST03-FIB-80](#) on DKC465I Maintenance Manual for correct information.
- (7) Is the concerned Fibre cable the one used without any problem?  
Do the concerned Fibre cable work well with other Fibre devices?  
Do another Fibre cable work well if it is replaced with the concerned Fibre cable?
- (8) Is the concerned Fibre cable connected to the I/F connector panel stably? \*1
- (9) Is the concerned Fibre cable connected to the host Fibre board connector? \*1
- (10) Isn't there any duplication of AL-PA(port ID)? (Check SVP path definition, Port ID of the host board, and ID of other devices.)
- (11) Is the SVP SCSI path definition correct?
- (12) Is Fibre installation work from Fibre host done correctly?

\*1 : Check visually or check by shutting down the Fibre host disconnect and reconnect the cable.





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## 7.3 Pinned track recovery of SCSI LDEV

This chapter shows about the explanation and the recovery procedure for faulty tracks in the OPEN-VOL.

### 7.3.1 Faulty Tracks

Hardware error sometimes causes pinned track to occur.

The following reports are to inform pinned track occurrence.

- The read-error report or the time-out error report from Application occurs.
- SIM report of pinned track occurrence.

SIM REF.CODE	Meaning	Comment
EF4X-XX	Unable to write to PDEV on a track.	X-XX : CU-LDEV number
FF4X-XX	Unable to read from cache on a track.	X-XX : CU-LDEV number

To recover pinned tracks, the following information about the pinned track to be recovered are necessary.

- Address in LDEV(LDEV number)
- First and last LBA of stripe including the pinned track  
\* LBA : Logical Block Address
- Error type of the pinned track
- PDEV number including the pinned track

These information can be obtained by “Pinned Data indication” ([SVP02-940](#)) in SVP.

### 7.3.2 Error Types

Pinned track has 2 error types as follows.

Display on SVP		Meaning	Cause	Recovery method
Slot	Reason			
—	Write Error	Unable to write to PDEV.	Drive error	Replacement of PDEV
DATA	ECC/LRC Error	Unable to read from Cache.	Cache error	File recovery
PRTY	ECC/LRC Error	Unable to generate parity	Cache error	—

The following document explains each error type of the pinned track and shows the recovery flow chart for the ECC/LRC Error in the OPEN-VOL. As for the pinned track except the ECC/LRC error condition in the OPEN-VOL, refer to [TRBL04-10](#) section.

### 7.3.2.1 ECC/LRC Error

**Cause:**

- (1) An ECC/LRC type of a pinned track is caused when dirty data stored in both Cache sides A/B can not be accessed correctly. Also, during a dirty data de-stage process the data read from Cache sides A/B is failed, an ECC/LRC Error is set. In this case, the host I/O operation is processed normally. However, if the host I/O access to Cache side A/B is failed, the original data for the track is lost.
- (2) ECC/LRC type of a pinned track is caused when a failed de-staging track occurs due to a drive failure during P/S Off and On without batteries. In this case, the original data for the track is lost.
- (3) ECC/LRC type of a pinned track is caused when a track cannot be correctly reconstructed during a correction copy. In this case as stated above in (2), the original data for the track is lost.
- (4) ECC/LRC type of a pinned track is caused when a parity track cannot be correctly reconstructed during a parity construction process. This means that all data tracks in the stripe do not match up with a parity track.  
Factors involving a parity construction failure are as follows.
  - (a) When all necessary data required for a parity reconstruction process is not gathered correctly due to a failure drive etc. In a detail, when one or more data tracks(old data) within the stripe have failed staging tracks due to a drive failure.
  - (b) When a parity data de-staging failed due to a drive failure.

**Result of host I/O operation:**

When a track with an ECC/LRC Error is accessed, the result of the host I/O operation is as follows;

- (a) If the read in the track is possible, the host I/O operation ends normally.
- (b) If it isn't possible to read a track, "(03)h: Medium Error" or "(0b)h: Abort" to I/O operation is reported to the host. In this case, data is lost.

**The outline of the recovery procedure:**

A track with ECC/LRC Error means that the original data for the track may be lost. Therefore, we recommend to a customer that a recovery of the data for the track with ECC/LRC Error in the OPEN-VOL should be performed from a back-up file etc.

A recovery method for the track with ECC/LRC Error in the OPEN-VOL is as follows;

- (1) Obtains the CU-LDEV number which the pinned track occurred by “Pinned Data Display Function”(SVP02-940) in SVP and the head and the last LBAs of the pinned track.
- (2) Executes the “showrel” tool and examine the relation in fault LDEV and the device which the file system recognizes.
- (3) Checks a volume on the file system which is composed of the device and also checks data in the volume. (Using the command like **sum**)
- (4) Requests the customer the recovery of the data whth “I/O error” or “read error” from a back-up file, etc.
- (5) Confirms pinned track information by “Pinned Data Display Function” (SVP02-940) in SVP. If there is not a display of the pinned track and the system test result is normal, the recovery is ended.  
If a new pinned track occurs, back to the process of hard error recovery.
- (6) If an old pinned track display is left, executes the Pin Track Tool.
- (7) For the “slot:PRTY” display, the Pin Track Tool isn’t necessary.  
It is automatically recovered when the pinned track of the “slot:DATA” display is recovered.

**The cancellation of the faulty tracks**

When data is written to the whole stripe, the whole stripe data is fixed and the fault track is canceled.

### 7.3.2.2 Write Error

**Cause:**

Write Error type of a pinned track is caused when the data de-staging process to the PDEV failed due to a drive failure. When a drive failure occurs, the drive itself and the DKC perform the following recovery procedure. A write Error occurs when the following recovery procedure failed.

(1) Medium failure: Automatic reallocation of alternate sector.

(2) Other failure : Alternate path retry.

Write Error count information per PDEV is stored in DKC. When a Write Error count exceeds the threshold value, the PDEV is blocked. One PDEV blocked per parity group will not stop the DKC operation to the parity group. When a track with Write Error is accessed by the host after the PDEV is blocked, the Write Error status will reset.

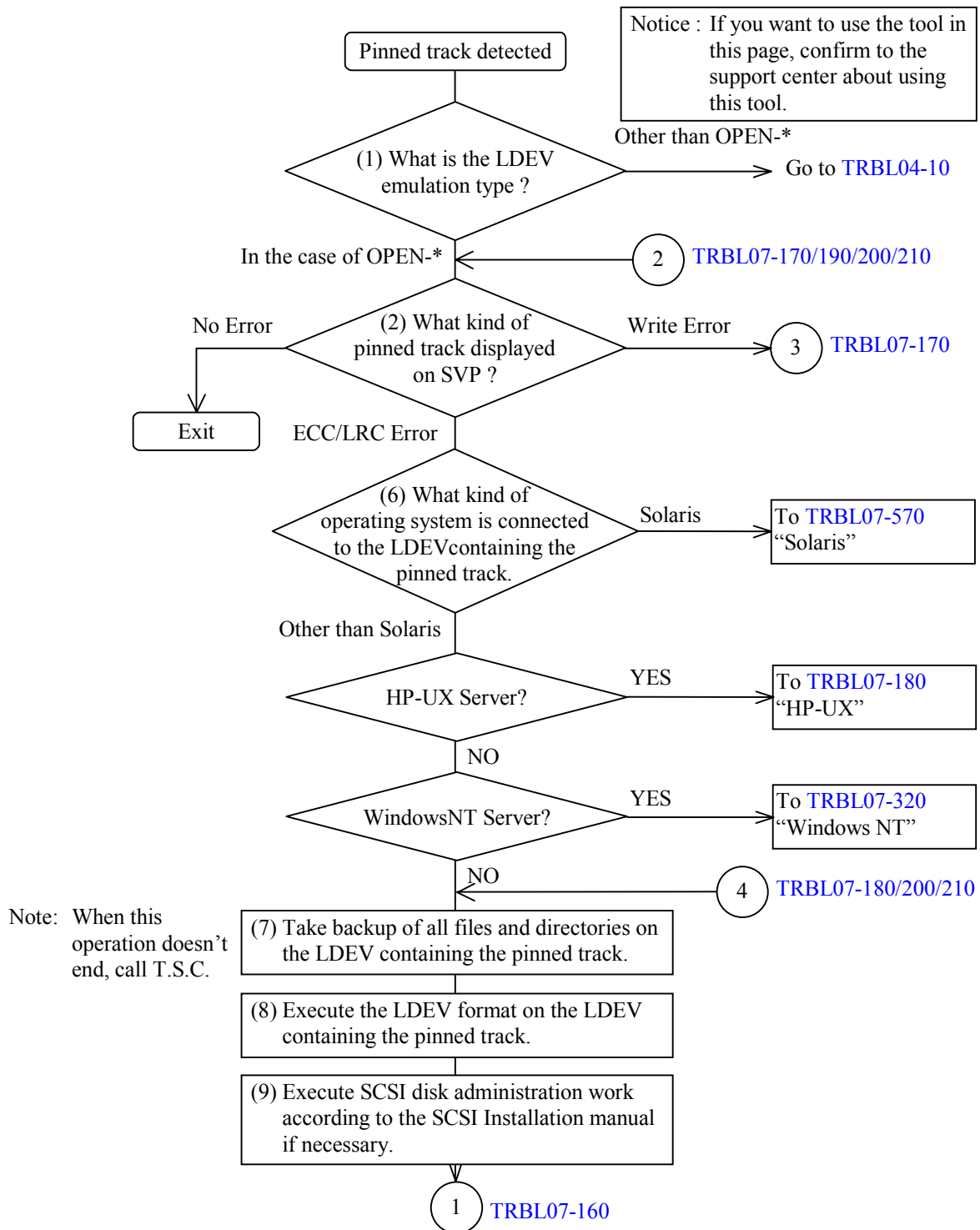
**Result of host I/O operation:**

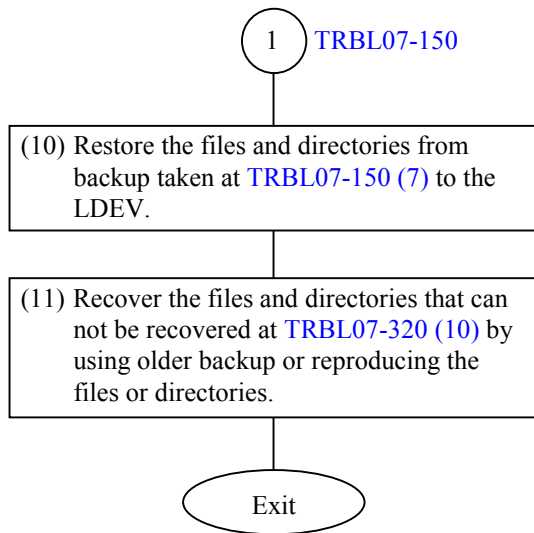
Any access to a track with Write Error will be successful.

**Recovery Method:**

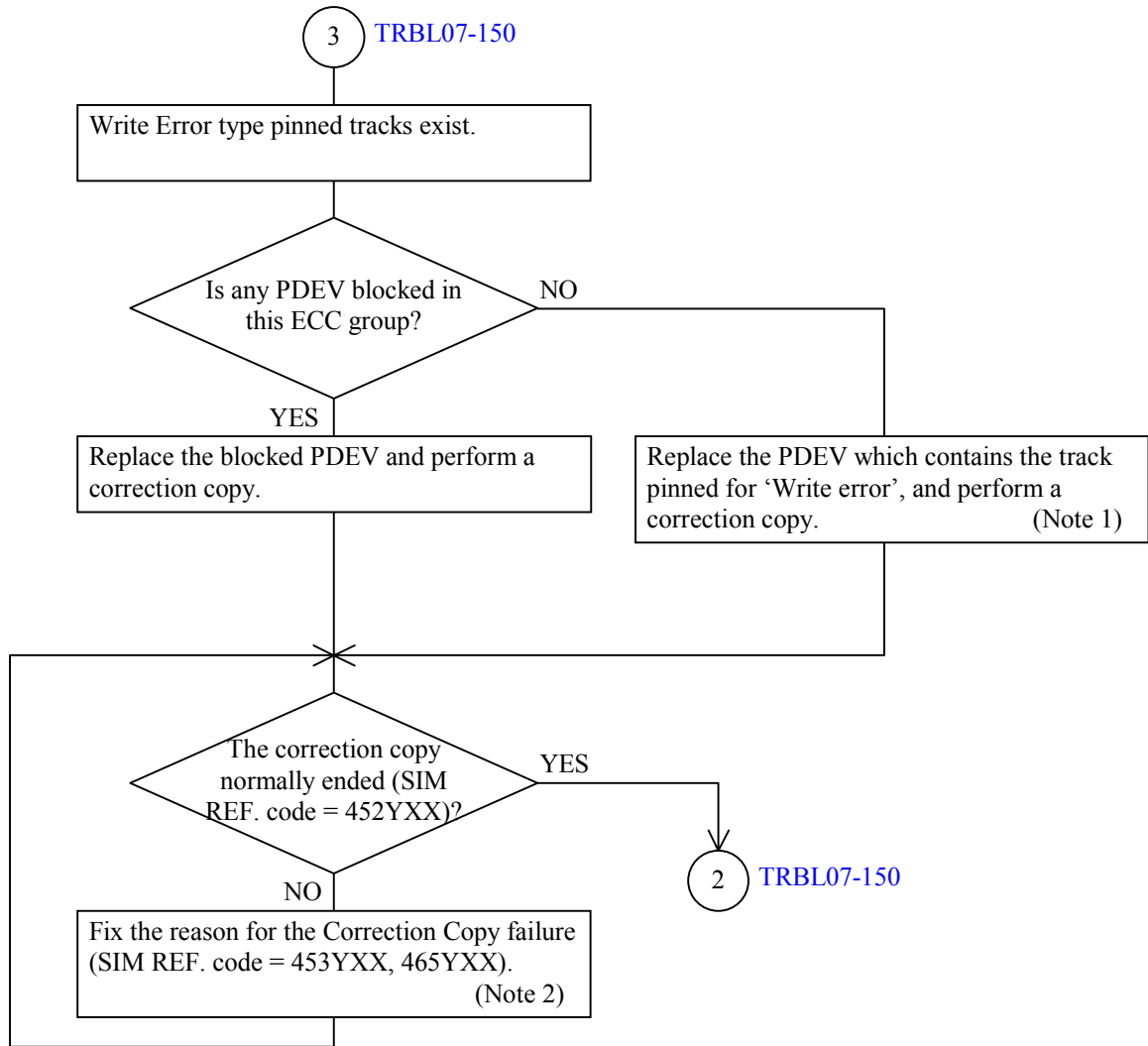
The PDEV containing a track with Write Error is replaced. At this time, if the blocked PDEV already exist within the parity group, first replace the blocked PDEV which already exists. Next, replace the PDEV containing a track with Write Error. Also, if there are many PDEVs containing a track with Write Error, check the ORM Display on the SVP Panel, then replace the PDEV with the Highest Error Rate. A track with Write Error is recovered by a correction copy.

### 7.3.3 Pinned track erasing procedure









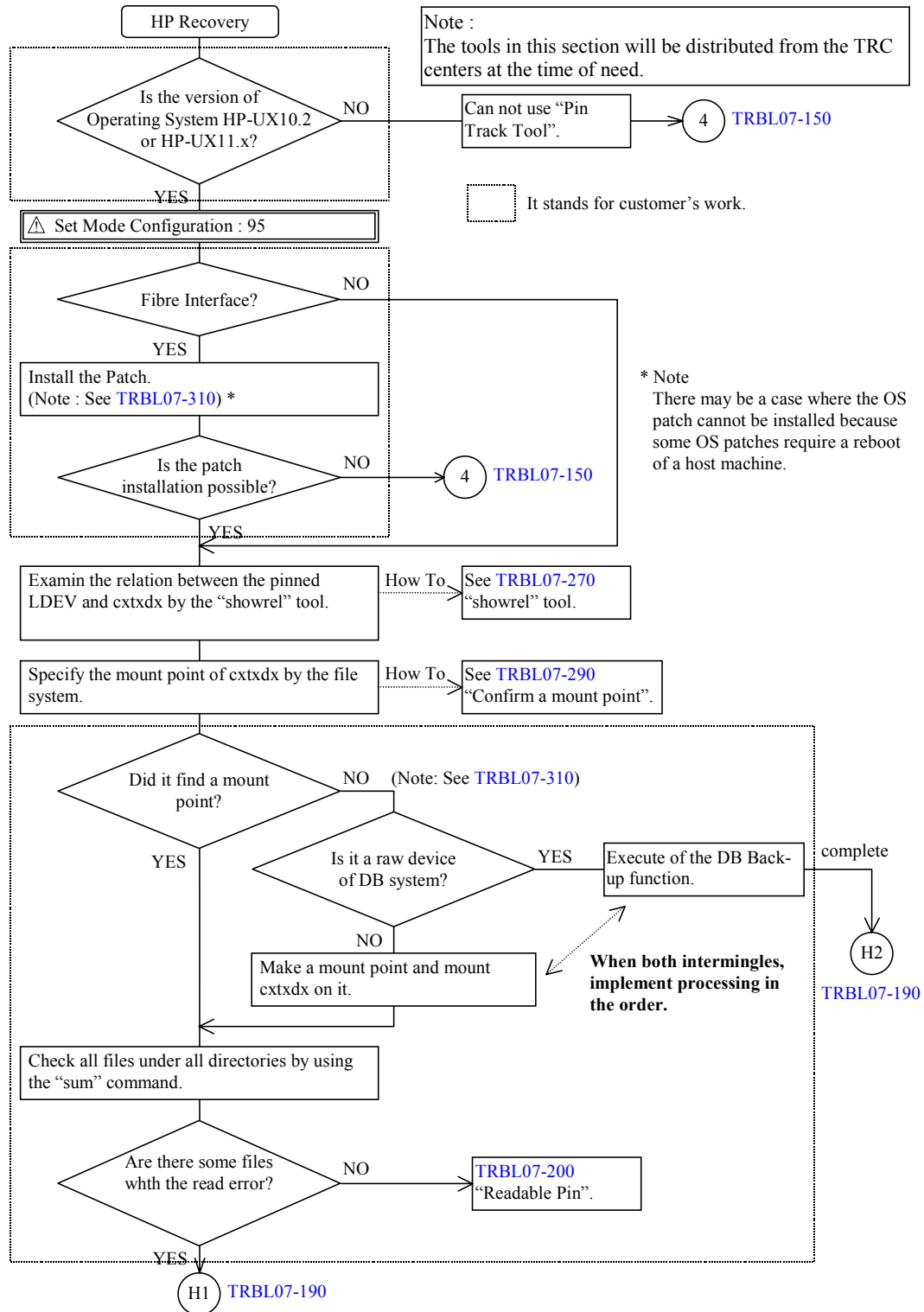
Note 1 If spare drives are available, you can perform Drive copy instead of Correction copy.

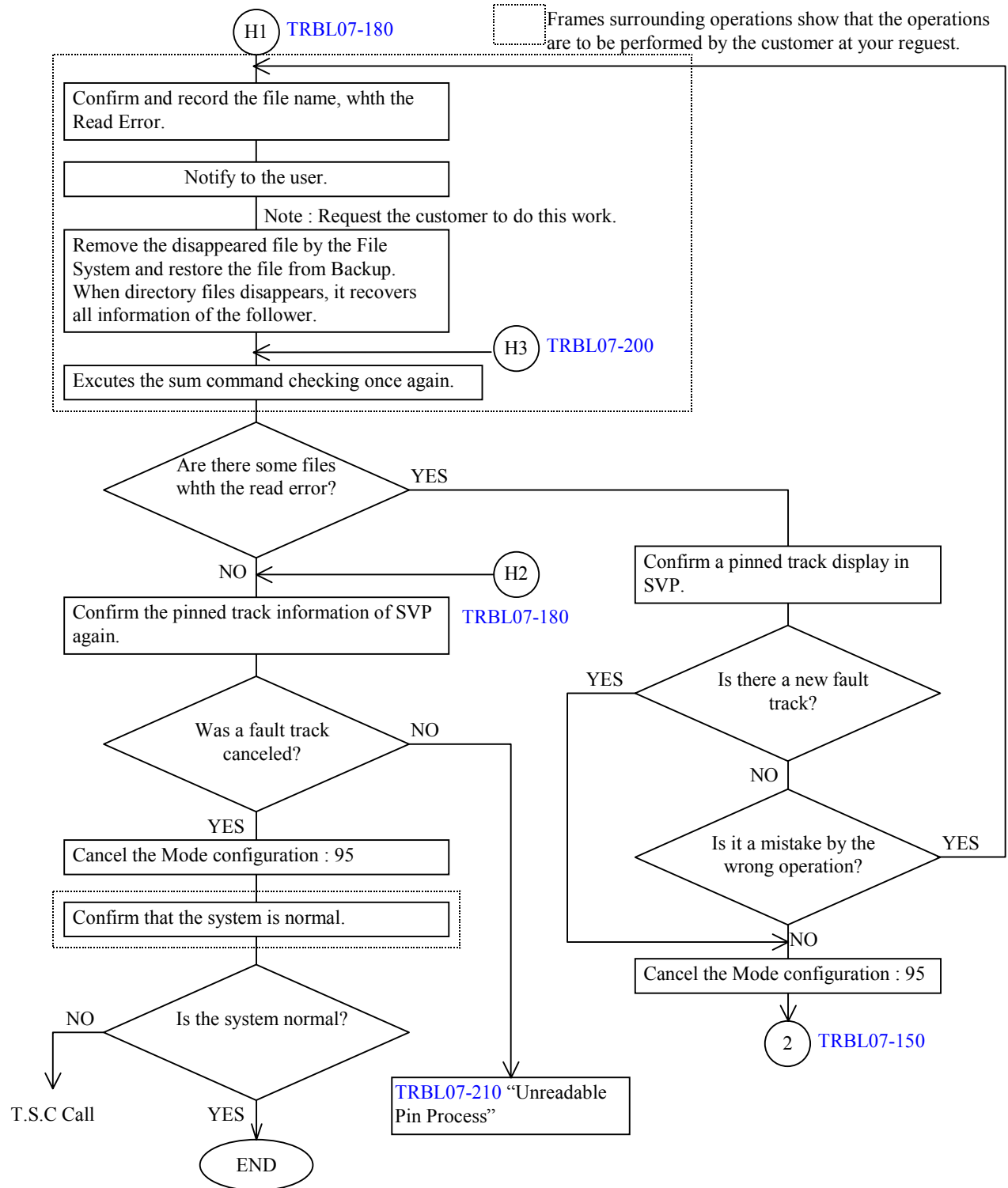
Note 2 If Drive copy abnormally ended, the SIM REF. code is “453YXX” or “465YXX”.

### 7.3.3.1 HP-UX Procedure

This chapter shows the pinned track erasing flow about the HP-UX system.

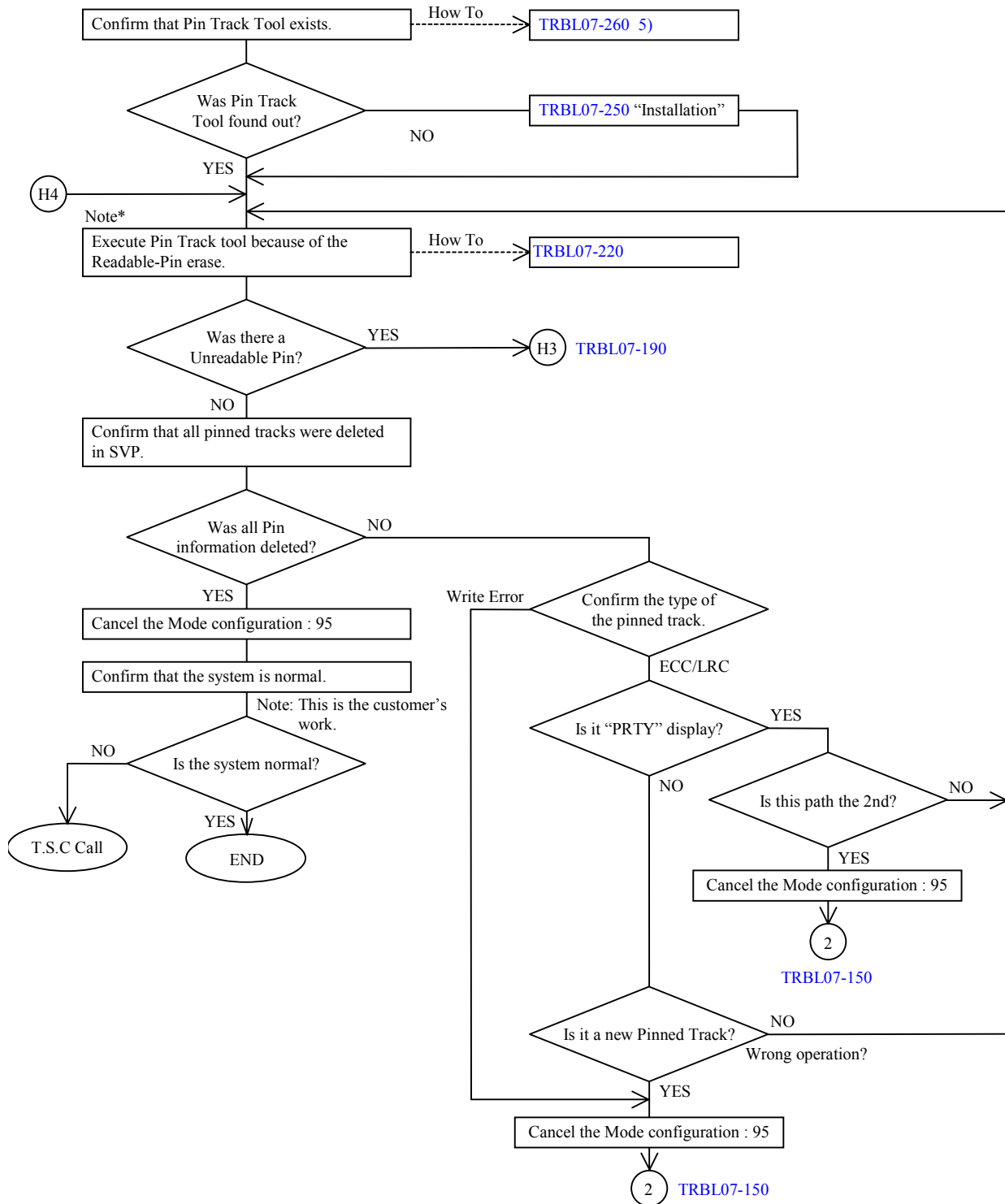
#### Procedure Flow





## Readable Pin Process

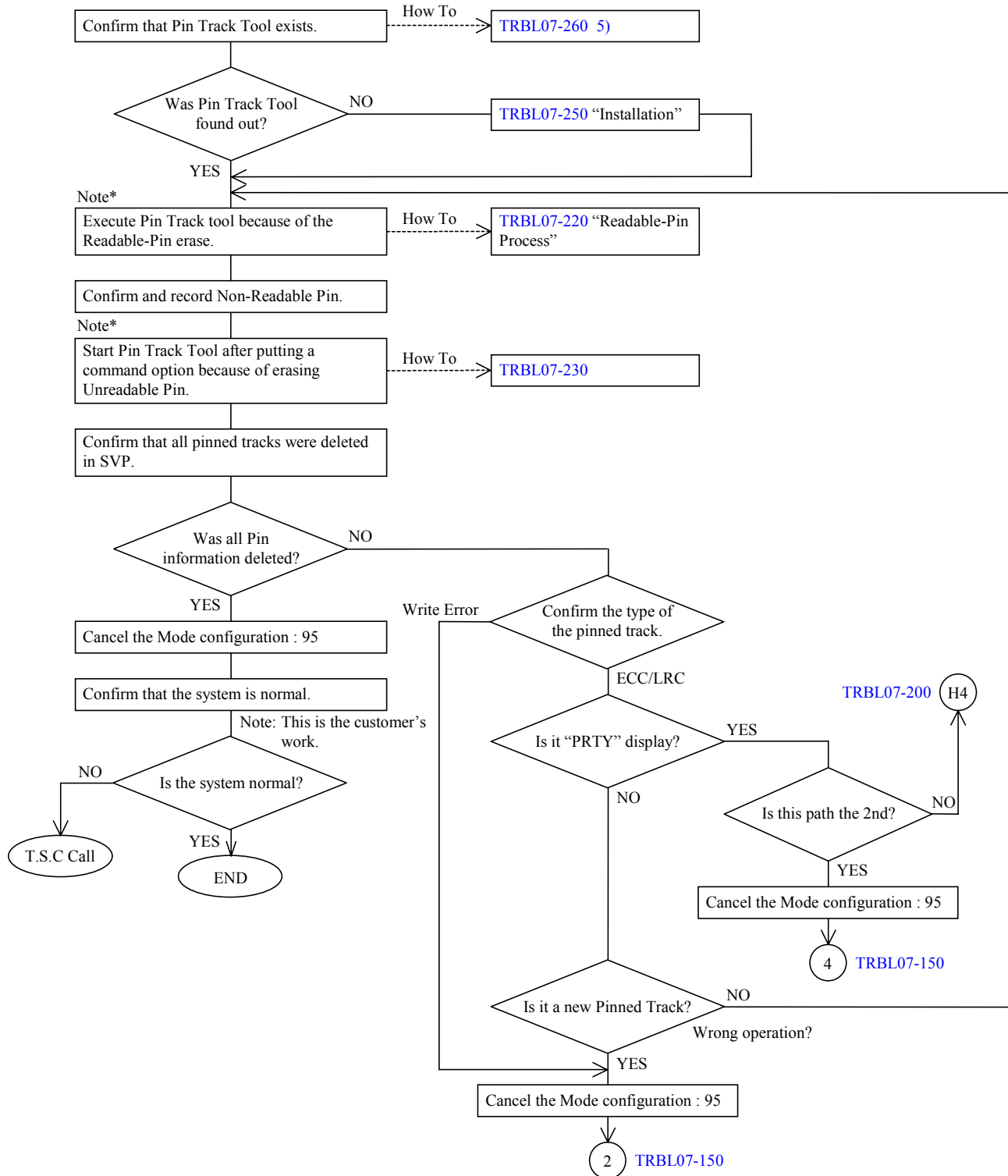
The erasing flow in the readable pinned track by Pin Track Tool is as follows.



**\* Note** - On an SIM reported owing to a use of the Pin Track Tool - When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and a SIM may be reported. Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when the erasure of all the Pins by the Pin Track Tool is confirmed.

## Unreadable Pin Process

The erasing flow in the unreadable pinned track by Pin Track Tool is as follows.



\* **Note** - On an SIM reported owing to a use of the Pin Track Tool -  
When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and a SIM may be reported. Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when the erasure of all the Pins by the Pin Track Tool is confirmed.

## Operation of Readable-Pin Process (HP-UX)

This clause describes how to operate the Pin Track Tool to erase Readable-Pin.

### <Operation>

- 1) Move to the directory of the Pin Track Tool.

```
# cd /usr/raidopen/pinhp
```

- 2) Execute the Pin Track Tool without a command option.

```
# ./pinhp.exe -log (Put pass ".")
```

Note: This option “-log” collects detailed logs.

However, when processing LBA with (60)h length, the log becomes about 400KB.

Be careful of the available capacity of the disk. (refer to [TRBL07-280](#).)

- 3) According to the question, input the appropriate information.

```
# ./pinhp.exe -log
```

```
Input Device Name -> /dev/rdisk/c3t0d0
```

```
Input Start LBA Data -> 180
```

```
Input End LBA Data -> 1df
```

```
Input Next LBA ?(Y/N) -> n
```

```
Input Next Device ?(Y/N) -> n
```

(Input the LBA number which was acquired from SVP.  
Do not input the LBA of the “slot:PRTY” display.)

(When there still is a fault track in the same Device, it inputs "y".)

(When erasing different Device at the same time, it inputs "y".)

- 4) Because an input data list is displayed, check the input information.

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0	00000180	000001DF
Before you try to proceed the readable pin, please check the pin information on SVP. If the pin data has been cleared, please do not try to proceed the pin data again. Do you want to do the process of the readable Pin? Please input[y/n(default n)] : y		

When the input is not collect, input “n” or just hit [Return] and then start the procedure again from (2).

Confirm whether or not a pinned track is deleted from the display of SVP.

When the data is already deleted, input "n" or just hit [return]. When canceled, input "y" and [return].

- 5) When the Pin is judged, Unreadable through the pin type judgment, go to [TRBL07-230](#).

Unreadable Pin:

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0	00000180	000001DF

Note: This tool recognizes an inputted range as the 1 processing unit.

Therefore, the range where Unreadable pin exists is displayed in the inputted range.

- 6) When Pin Track Tool ends, a log file (month -day -hour -minute -second .log) is made on the same directory. (ex:0614200552.log)

As for the log file, the execution result of the Pin Track processing is recorded.

Confirm that processing was normally ended (there is “Pin Track Process completed” in the log file).

## Operation of Unreadable Pin Process (HP-UX)

This clause describes how to operate the Pin Track Tool to erase Unreadable Pin.

<Operation>

- 1) Move to the directory of the Pin Track Tool.

# **cd /usr/raidopen/pinhp**

- 2) Put a command option and execute a pin recovery tool.

# **./pinhp.exe -f -log**

(Put command option "-f")

Note: This option "-log" collects detailed logs.

However, when processing LBA with (60)h length, the log becomes about 400KB.

Be careful of the available capacity of the disk. (refer to [TRBL07-280](#).)

- 3) According to the question, input the appropriate information.

# **./pinhp.exe -f -log**

Input Device Name -> **/dev/rdisk/c3t0d0**

Input Start LBA Data-> **180**

Input End LBA Data -> **1df**

Input Next LBA?(Y/N) -> **n**

Input Next Device?(Y/N) -> **n**

Input the LBA number which was acquired from SVP.  
Do not input the LBA of the "slot:PTY" display.

(when there still is a fault track in the same device, it input "y".)

(When erasing different device at the same time, it inputs "y".)

- 4) Because an input data list is displayed, check the input information.

Device Name	Start LBA	End LBA
<b>/dev/rdisk/c3t0d0</b>	<b>00000180</b>	<b>000001DF</b>

Before you try to proceed the readable pin,  
please check the pin information on SVP.  
If the pin data has been cleared, please do not try to proceed the pin data again.  
Do you want to do the process of the readable Pin?  
Please input[y/n(default n)]: **y**

When the Input is not correct, input "n" or just hit [Return] and then start the procedure again  
From (2).

Confirm whether or not a pinned track is deleted from the display of SVP.

When the data is already deleted, input "n" or just hit [return]. When canceled, input "y" and  
[return].

- 5) When Unreadable Pin is judged, the following message is displayed.

Unreadable Pin:		
Device Name	Start LBA	End LBA
<b>/dev/rdisk/c3t0d0</b>	<b>00000180</b>	<b>000001DF</b>
Do you want to do the process of the unreadable Pin?		
WARNING! if you input 'y', Pin Blocks will be over written by '0'.		
Please input[y/n(default n)]: <b>y</b>		

When erasing by the "0" writing to Unreadable Pin, input "y" and [Return].

Note: This tool recognizes an inputted range as the 1 processing unit.

Therefore, the range where Unreadable pin exists is displayed in the inputted range.

- 6) When Pin Track Tool ends, a log file (month -day -hour -minute -second .log) is made on the same directory. (ex:0614200552.log)  
 As for the log file, the execution result of the Pin Track processing is recorded.  
 Confirm that processing was normally ended (there is "Pin Track Process completed" in the log file).



## Installation of Pin Track Tool (HP-UX)

This clause describes the installation of Pin Track Tool.

### The preliminary preparation

If work logs need to be collected by the Pin Track Tool, confirm that there is the disk capacity for collecting in the work log.

An installation procedure from the tape device (ex: 4mmDDS-DAT) is shown below.

### Installation

- 1) Login to the host as "root".
- 2) Move to the install area by the "cd" command and make a directory "raidopen".
  - # **cd /usr** (ex: Move to the "/usr")
  - # **mkdir raidopen** (ex: Make the directory "raidopen")
- 3) Move to the created directory and copy a file from the tape by the "tar" command.
  - # **cd raidopen** (ex: Move to the "raidopen")

-In case of DDS-DAT-(Devide Name depends on each host)

- # **mt -t /dev/rmt/0m rew** (ex: Rewing a tape)
- # **tar -xvf /dev/rmt/0m** (ex: copy a file from the tape.)
- # **tar -xvf ./pinhpXX.tar** (ex: Copy the tar file for Hp-UX)

-In case of CDROM-(Devide Name depends on each host)

- # **mount -F cdfs -o cdcase /dev/dsk/c2t6d0 SD\_CDROM** (ex: mount the CDROM)
- # **tar -xvf /SD\_CDROM/tool/pintrack/hp\_ux/pinhpXX.tar** (ex: copy a file from the CDROM.)

- 4) After the thawing is complete, confirm a file name.
  - # **cd ./pinhp** (ex: Move to the directory made by the thawing.)
  - # **ls -l** (ex: Display a file list.)

- 5) Refer to the contents of “Ver-Rev.txt” file and confirm each file size of the list.  
**# more /usr/raidopen/pinhp/Ver-Rev.txt** display contents of the file

HITACHI RAID Subsystem PinTrackTool for HP-UX  
 Ver XX-YY-/Z (Revision ID)  
 All right reserved, Copyright (c) 1999,2000, Hitachi Ltd.  
 File size (Bytes) pinhp.exe (Module ID)  
 File size (Bytes) showrelh.exe (Module ID)

Confirm that the contents of “Ver-Rev.txt” and a list of the “ls -l” command are identical.

### File preservation and the way of removing Pin Track Tool.

#### Log-File preservation

- 1) Compress the log file made by the pin recovery.  
**# cd /usr/raidopen/pinhp** (ex: Move to the working directory.)  
**# mkdir ./log** (ex: Make to the directory for Log-file.)  
**# mv \*.log ./log** (ex: Move logfiles to the directory for Log-file.)  
**# tar -cvf pinlog.tar ./log** (ex: Make the tar file from logdir.)  
**# compress pinlog.tar** (ex: Compress the “pinlog.tar” file.)
- 2) Preserve the log file at the tape and rewind it..  
**# tar -cvf /dev/rmt/0m pinlog.tar.Z** (ex: Preserve log file)  
**# mt -t /dev/rmt/0m rew** (ex: rewind the tape)

#### The way of removing Pin Track Tool

The removal of the Pin Track Tool deletes all bottoms of the installed directory.

- # cd /** (ex: Move to the root directory.)  
**# \rm -r /usr/raidopen/pinhp\*** (ex: Deletes all bottoms of the installed directory)

## The acquisition of the device information(HP-UX)

This chapter describes the tool “showrel” to acquire the device information.

- 1) Move to the installed directory.

# **cd /usr/raidopen/pinhp**

- 2) Input commands as follows.

# **./showrelh.exe**

(Put the path “./”)

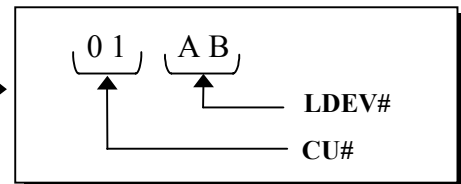
### <Display Example>

The display depends on the specification of the disk subsystem.

For the Hitachi specification, it is displayed as follows.

```
#./showrelh.exe

Device File      ---> Port   Serial#  LDEV#
/dev/rdisk/c0t0d1 ---> CL1M    3ABE    01A6
/dev/rdisk/c0t0d2 ---> CL1M    3ABE    01A7
/dev/rdisk/c0t0d3 ---> CL1M    3ABE    01A8
/dev/rdisk/c0t0d4 ---> CL1M    3ABE    01A9
/dev/rdisk/c0t0d5 ---> CL1M    3ABE    01AA
/dev/rdisk/c0t0d6 ---> CL1M    3ABE    01AB
```



For the OEM specification, it is displayed as follows.

The point of view of CU:LDEV# is the same.

```
#./showrelh.exe

Device File      ---> Port   Serial#  LDEV#
/dev/rdisk/c7t2d1 ---> CL2E    00010028 03C0
/dev/rdisk/c7t2d2 ---> CL2E    00010028 03C1
/dev/rdisk/c7t2d3 ---> CL2E    00010028 03C2
/dev/rdisk/c7t2d4 ---> CL2E    00010028 03C3
/dev/rdisk/c7t2d5 ---> CL2E    00010028 03C4
/dev/rdisk/c7t2d6 ---> CL2E    00010028 03C5
```

“LDEV#” is composed of the CU number and the LDEV number. Confirm CU# and LDEV# with the pinned track displayed in SVP and specify a clearing device file.

Device File name is input information to Pin Track Tool.

**(Example)** Above mentioned “For the Hitachi specification”,

LDEV# = **01A6** → Device File = **/dev/rdisk/c0t0d1**

**Notice:** In the case of HP-UX, If there is LDEV that is non given LUN#, LDEV#159F or LDEV#FFFF are displayed to LUN# non-given a definition by showrelh.exe.

## The way of collecting detailed information(HP-UX)

It is possible to collect the detailed information of the erasing process when putting a command option to Pin Track Tool and starting the process.

```
# ./ pinhp.exe (-f) -log
```

Like the following, it outputs read data and write data in the log file.

<Display Example>

Unreadable LBA is displayed by “\*”.

```

Input Device Name = /dev/rdsd/c1t0d0
Input Start LBA = 00000180
Input End LBA = 000001DF
/dev/rdsd/c1t0d0, Start LBA = 00000180, End LBA = 000001df readable PIN Track read error
Read Data: Top Pin No = 00000180
00000000: ****
00000010: ****
00000020: ****
00000030: ****
00000040: ****
. . .
Read Data: Top Pin No=00000181
. . .
Read Data: Top Pin No=00000182
. . .
Read Data: Top Pin No=00000183
. . .
Read Data: Top Pin No=000001DF
000001A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
Write Data: Top Pin No=00000180
. . .
00000000: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000040: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
. . .
0000BFC0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFD0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFE0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BF00: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
/dev/rdsd/c1t0d0, Start LBA=00000180, End LBA=000001DF Pin Track Process completed!!!

```

Unreadable data!

Execute LBA!

Note: In the processed range, log information is recorded.

➡ This log is executing read processing by the 1 LBA unit.

➡ This log shows to have processed all ranges in the writing at once.

## The way of examining Mount Point which has a Pinned Track(HP-UX)

Note : As for the following work, request a system administrator to operate.

- 1) Input the "vgdisplay" command and to display a Volume Group list.

**# vgdisplay -v**

- 2) Specify "lvol(/dev/vgx/lvolx)" which is composed of Physical Volume(cxtxdx) with the Pinned Track.

### <Display Example>

--- Volume groups ---		
VG Name	/dev/vg11	← Volume Group Name
VG Write Access	read/write	
VG Status	available	
Max LV	255	
Cur LV	1	
Open LV	1	
Max PV	16	
Cur PV	4	
Act PV	4	
Max PE per PV	1016	
VGDA	8	
PE Size (Mbytes)	4	
Total PE	2344	
Alloc PE	2000	
Free PE	344	
Total PVG	0	
Total Spare PVs	0	
Total Spare PVs in use	0	
--- Logical volumes ---		
LV Name	/dev/vg11/lvol1	← "/dev/vg11/lvol1" is made in a Volume Group("/dev/vg11")
LV Status	available/syncd	
LV Size (Mbytes)	8000	
Current LE	2000	
Allocated PE	2000	
Used PV	4	
--- Physical volumes ---		
PV Name	/dev/dsk/c9t1d0	} PV(cxtxdx) which composes volume group "/dev/vg11" is displayed.
PV Status	available	
Total PE	586	
Free PE	0	
PV Name	/dev/dsk/c9t1d1	}
PV Status	available	
Total PE	586	
Free PE	0	
PV Name	/dev/dsk/c9t1d2	}
PV Status	available	
Total PE	586	
Free PE	0	
PV Name	/dev/dsk/c9t1d3	}
PV Status	available	
Total PE	586	
Free PE	344	

- 3) Check if `"/etc/fstab"` is displayed.

```
#cat /etc/fstab
```

- 4) Specify all the mount points for PV which was confirmed in `"vgdisplay"`.

<Display Example>

```
# System /etc/fstab file. Static information about the file systems
# See fstab(4) and sam(1M) for further details on configuring devices.
/dev/vg00/lvol3 /vxfs delaylog 0 1
/dev/vg00/lvol1 /stand hfs defaults 0 1
/dev/vg00/lvol4 /tmp vxfs delaylog 0 2
/dev/vg00/lvol5 /home vxfs delaylog 0 2
/dev/vg00/lvol6 /opt vxfs delaylog 0 2
/dev/vg00/lvol7 /usr vxfs delaylog 0 2
/dev/vg00/lvol8 /var vxfs delaylog 0 2
/dev/vg00/lvol10 /home1 vxfs rw,suid,nolargefiles,delaylog,datainlog 0 2
/dev/vg11/lvol1 /open3 vxfs delaylog 0 4
```

— mount point (in bold)

- 5) Input the `"bdf"` command and confirm the mount point.

```
#bdf
```

<Display Example>

Filesystem	kbytes	used	avail	%used	Mounted on
/dev/vg00/lvol3	86016	26109	56212	32%	/
/dev/vg00/lvol1	67733	31932	29027	52%	<b>/stand</b>
/dev/vg00/lvol8	512000	159876	331072	33%	<b>/var</b>
/dev/vg00/lvol7	614400	428475	174362	71%	<b>/usr</b>
/dev/vg00/lvol4	32768	1131	29663	4%	<b>/tmp</b>
/dev/vg00/lvol6	258048	102174	146171	41%	<b>/opt</b>
/dev/vg00/lvol10	1544192	2858	1445062	0%	<b>/home1</b>
/dev/vg00/lvol5	20480	6078	13595	31%	<b>/home</b>
/dev/vg11/lvol1	8192000	3149893	4726982	40%	<b>/open3</b>

- 6) Determine Mount Point to check by the `"sum"` command.

## The attention item for HP-UX.

This clause explains notes when using Pin Track Tool.

## The notes for use of the Pin Track Tool.

- 1) Specify the start and end LBAs in SVP for the setting range of Pin Track Tool. 1 slot is composed of 96 LBAs ((60)h LBA). The fault can not be cleared is the value is less than 96 LBAs.
- 2) Pin Track Tool is not a tool to recover data. Therefore, when Unreadable Pin occurs, it is necessary to be restored using the back-up data of the customer.
- 3) There is a case that O/S patch is prepared which has an influence to the fault track read operation. For HP Server which has Fibre Interface, the following patch is necessary.
  - PHSS\_18326 Fibre Channel Mass Storage Driver Patch. (HP-UX10.2)
  - PHSS\_18652 Fibre Channel Mass Storage Driver Patch. (HP-UX11.0)
 or XSWG1100 HP-UX General Release Patches, June 2001  
 XSWHWCR1100 HP-UX Hardware Enablement and Critical Patches, June 2001 : (HP-UX11.0)
  - PHCO\_18217 Cumulative SAM/ObAM Patch. (HP-UX10.2)
  - PHKL\_16751 SIG\_IGN/SIGCLD,LVM,JFS,PCI/SCSI cumulative patch. (HP-UX10.2)

The patch information is as of Jun/99.

The patch information of OS may change at frequent intervals. Confirm the latest information. Request the system administrator to install patch.

When the system administrator judges this action may impact on the system, cancel the Mode Configuration:95 and return to [TRBL07-150 ④](#).

- 4) When it isn't possible to use Pin Track Tool, use the LDEV Format to clear.
- 5) Because HP-UX executing retry to read the pinned track where it isn't possible to read many times, It sometimes takes 3 hours maximum about processing 1 slot.

## The erasing process of Pinned Track on the DB(HP-UX, Solaris)

DB has two types of the Pinned Track earasing.

- 1) Raw device-based Data Base type :
  - Without passing O/S, by the physical level, the data base soft wear manages a disk.
  - (It is managed by the LBA unit on the physical level.)
  - The device of local type isn't mounted on File System. Then, it has a powerful back-up function.
- 2) File system-based Data Base type :
  - Mounted an File System.

In case of (2), it is necessary to be restored from the backup data.

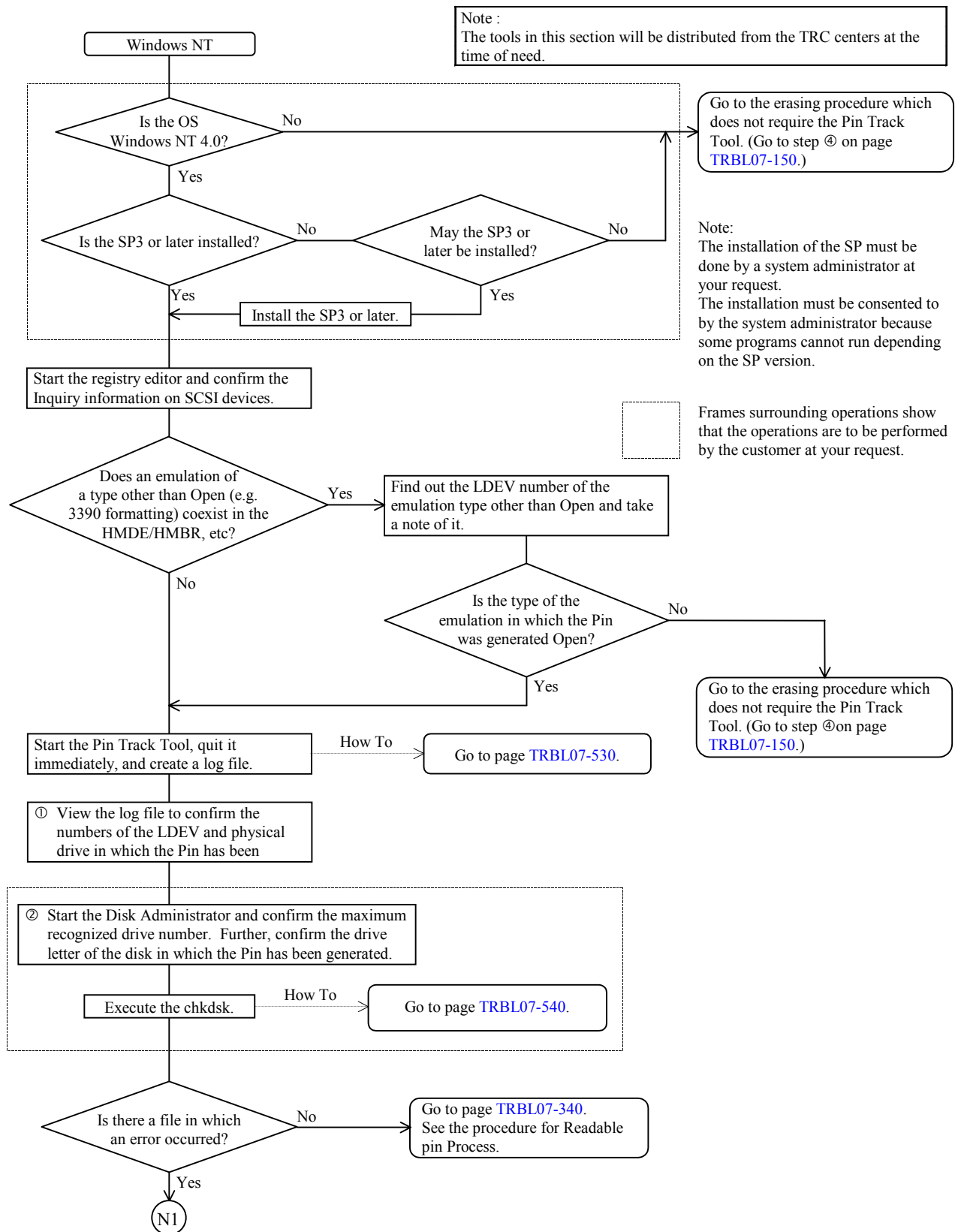
In case of (1), it be restored by the backup-restore feature of the DBMS.

If a pin is left after the data recovers, erase a pin by the Pin Track Tool.

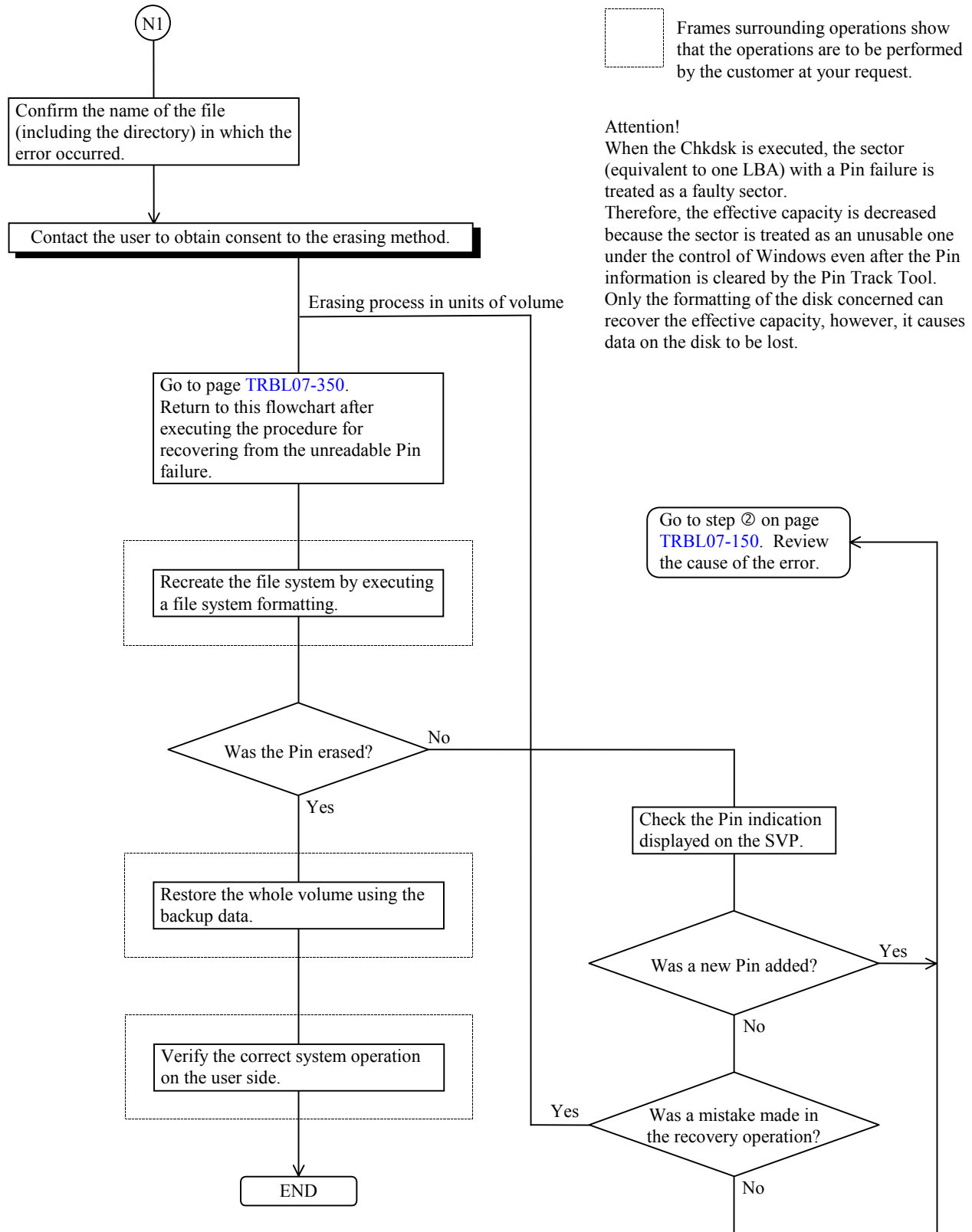
**The most important thing is to execute the recovery function in the DB software.**

### 7.3.3.2 Procedure on Windows NT

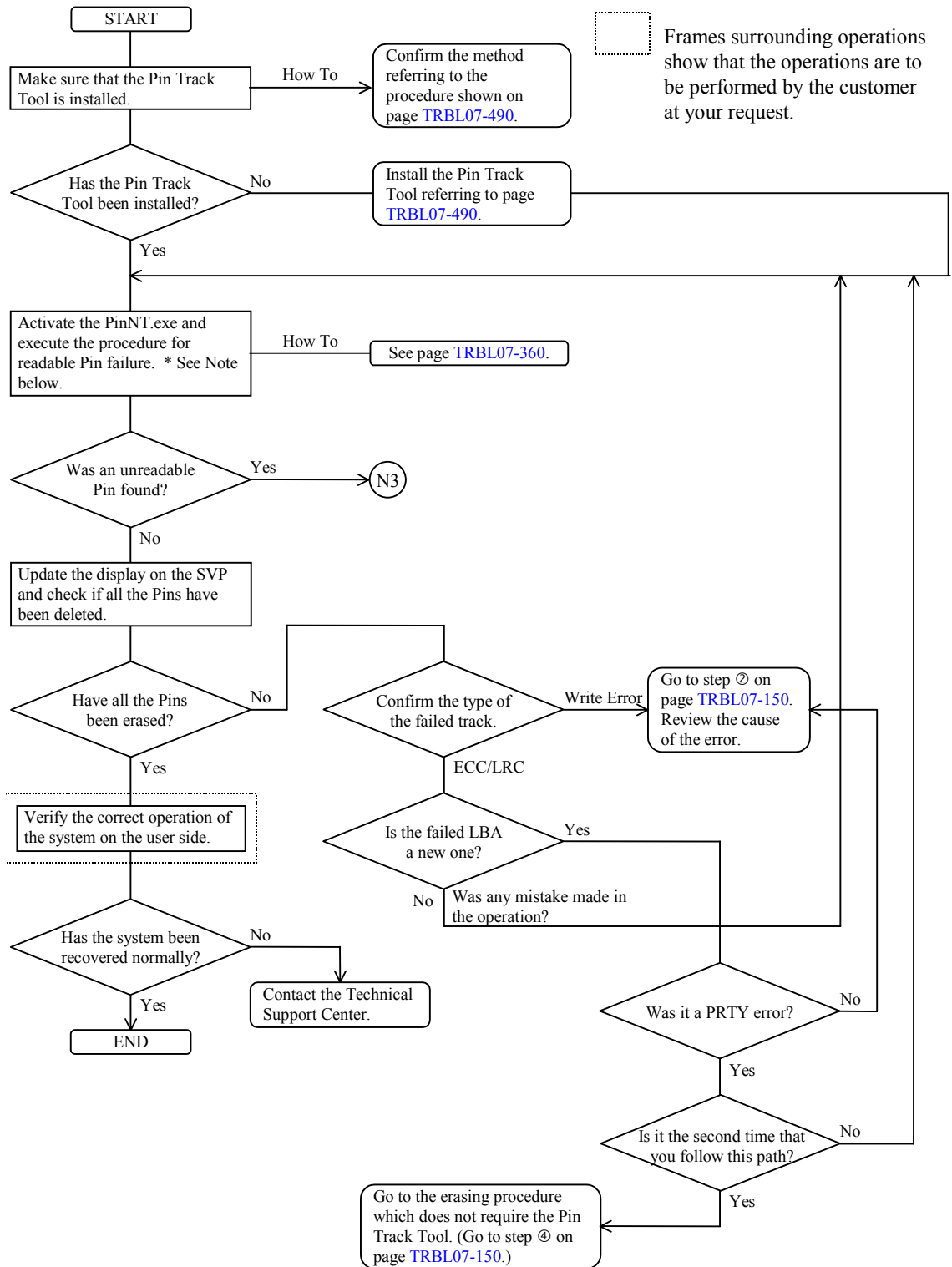
- The following is an erasing procedure to be used when a Pin failure occurs on Windows NT.





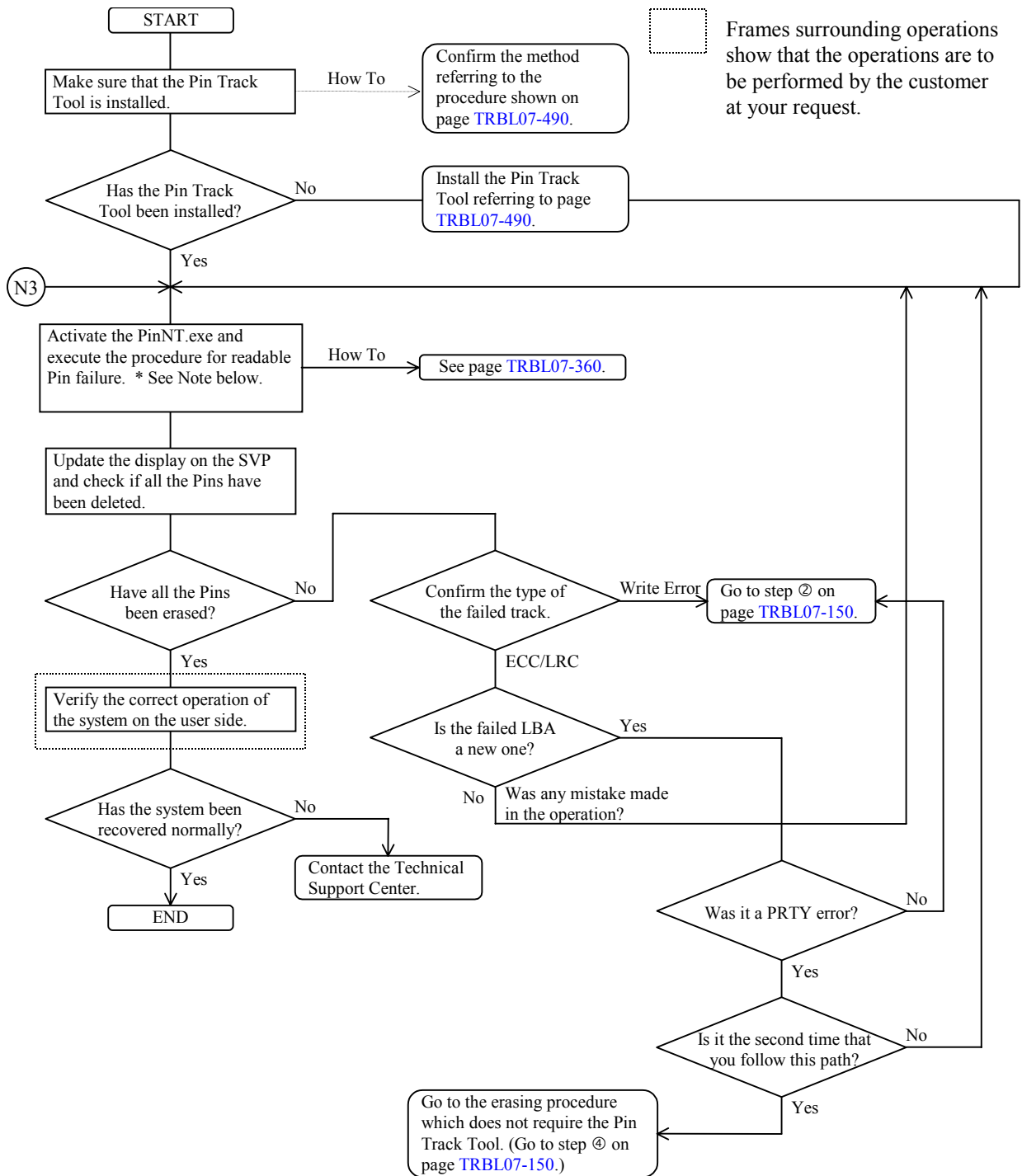


## Readable Pin Process (Windows NT)



**\* Note** - On an SIM reported owing to a use of the Pin Track Tool - When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported. Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when the erasure of all the Pins by the Pin Track Tool is confirmed.

## Unreadable Pin Process (Windows NT)



**\* Note** - On an SIM reported owing to a use of the Pin Track Tool -  
When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported. Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

## Operation of Readable Pin Process (Windows NT)

The following explains how to operate the Pin Track Tool for erasing a readable Pin.

The procedure for erasing a readable Pin is to be firstly applied to all types of Pins.

A Pin which cannot be erased by the readable Pin erasing process will be erased by a process which treats it as an unreadable Pin.

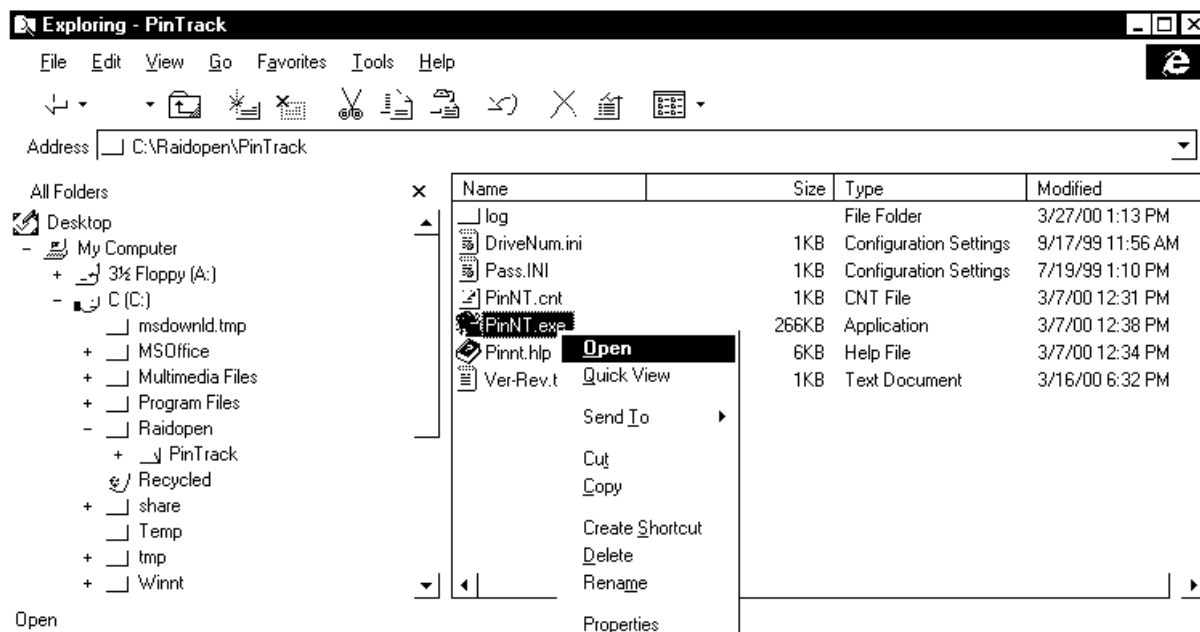
**\* Note** - On an SIM reported owing to a use of the Pin Track Tool -

When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.

Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

(1) Activate the Pin Track Tool.

- Execute the PinNT.exe in the folder in which the tool is installed after activating it by selecting "Open" by clicking it with the right mouse button or double-clicking it with the left mouse button..



- When the PinNT.exe is executed, the following window is displayed.

**Pin Track Tool**

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
<div>◀   ▶</div>					

Drive Name:  Port:  LDEV:

Start-LBA:

End-LBA:

☐ Proceed unreadable Pin

Operation

- (2) Input the information, which has been got from the SVP, on the device from which the Pin is to be erased.
- \* You can enter two or more Pins in order.
  - ① Find a name of a drive to which the LDEV in which the Pin has been generated is allocated.
  - Select a physical drive for which the LDEV number of the device in which the Pin has been generated is displayed using an acquired port number (e.g. 1J for CL1J).

**Pin Track Tool**

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
<div>◀   ▶</div>					

Drive Name:  Port:  LDEV:

Start-LBA:

End-LBA:

☐ Proceed unreadable Pin

Operation

- \* The drive names are not sorted in order of the drive numbers.

Input the Start LBA and End LBA of the drive input in step ① to specify the range where the Pin has been generated.

**Pin Track Tool**

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
<div style="display: flex; justify-content: space-between;"> <span>◀</span> <span>▶</span> </div>					

Drive Name: 
 Port: 
 LDEV:

Start-LBA:

End-LBA:

☐ Proceed unreadable Pin

Operation

- When specifying the LBAs, the allowable range for them is as follows.  
 $[0x60 \geq \text{End LBA} - \text{Start LBA}]$   
 Input the range of the 0x60 shown on the SVP.

## (3) Add the input device to the Pin Track Device List.

- When the "Add" button is clicked after making sure that the selected and input items are correct, the drive is added to the list.

**Pin Track Tool**

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\.\PhysicalDrive9	2F	0017	00000180	000001DF	

◀ | ▶

Drive Name:  Port:  LDEV:

Start-LBA:

End-LBA:

☐ Proceed unreadable Pin

Operation

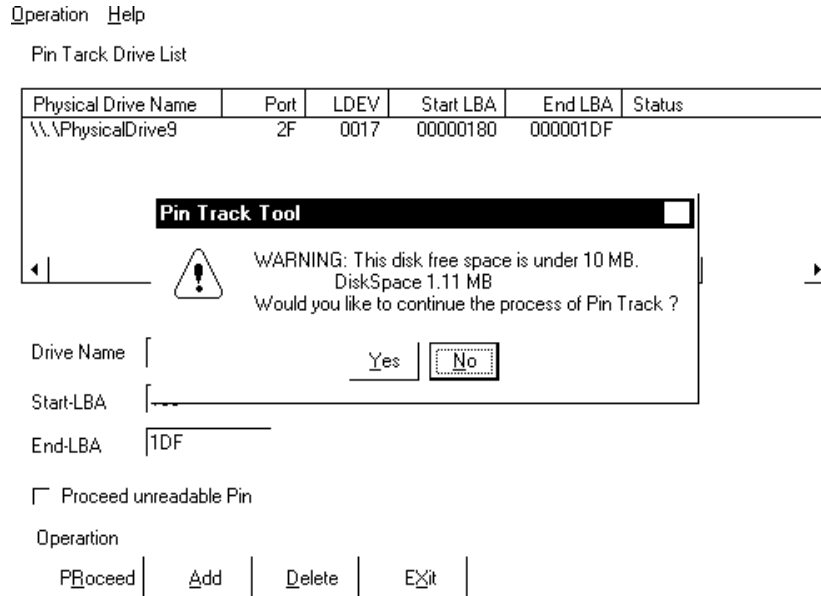
## (4) Erase the readable Pin.

- Check if the input information is correct. When correcting it, select the device and input the LBA over again by selecting the item concerned from the list and clicking the "Delete" button.
  - When you want to add two or more devices, you can do it by repeating the input. When they are added, the Pin erasing process is applied to them in an ascending order of the listing.
- \* Since the readable Pin is to be erased here, do not check off the check box of the Unreadable PIN.

- When no wrong input is found, click the “Proceed” button to erase the readable Pin.

When the “Proceed” button is clicked, the program checks whether a free area for outputting a log is ensured in the current drive in which the Pin Track Tool is installed.

If the free area is less than 10 MB, the following dialog box is displayed to warn it.



The current free area on the disk is displayed in the dialog box. A free area of approximately 400 kB is required to erase a Pin. When the necessary free area is provided, the processing can be continued.

Note: Install the program in a drive in which the enough free area can be ensured.

When “Yes” is selected, the processing is continued. If the disk capacity is less than that required for the log, as large log file as can be accommodated is acquired.

Normally, select “Yes” only when the free area is enough.

When “No” is selected, the routine is returned to the main window. If the option has been checked off, it is cancelled. Ensure a free area in the drive, put the collected log file in order, or install the program in another drive.



When the “Proceed” button is clicked to continue the processing, status of each drive is displayed in the main window and the following dialog box is displayed.

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\.\PhysicalDrive9	2F	0017	00000180	000001DF	readable-pin

**Pin Track Tool**

Would you like to do the process of the readable Pin ?

Drive Name | Yes | **No**

Start-LBA | 180

End-LBA | 1DF

☐ Proceed unreadable Pin

Operation

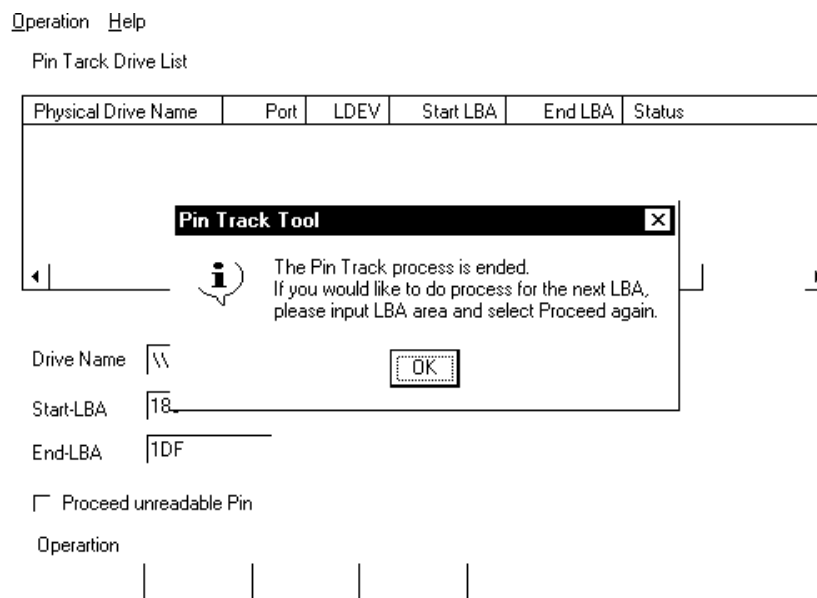
When “readable-pin” is displayed in the “Status” column, it shows that the slot is a readable Pin.

When “Unreadable-pin” is displayed in the “Status” column, it shows that the slot is an unreadable Pin.

- Update the display on the SVP before executing the erasing process and check if the Pin of the input device has been erased.  
When the Pin has already been erased, click the “No” button to return to the main window.  
When the Pin has not been erased, click the “Yes” button to erase the readable Pin.

## (5) The Pin erasing process is executed.

When the Pin erasing process is executed, the following dialog box is displayed.



When the Pin erasing process completes normally, the items are deleted from the list automatically. Confirm the execution result of the Pin erasing process in the log file.

- Open the PinTrack.log file in the folder in which the tool is installed by using a memo pad, etc.

```

2000/03/27 13:22:16 Pin Track Tool started.

\\.\PhysicalDrive0
    No information

\\.\PhysicalDrive1
    Product Serial R400 00030036 0042
    Port Number    1E
    LDEV Number    002A
    Disk Capacity  2461040640 bytes
    Maximum LBA    0049583F

\\.\PhysicalDrive2
\\.\PhysicalDrive3
\\.\PhysicalDrive4

\\.\PhysicalDrive9
    Product Serial R400 00030036 0023
    Port Number    2F
    LDEV Number    0017
    Disk Capacity  2461040640 bytes
    Maximum LBA    0049583F

Read Data: Top Pin No=00000180
00000000:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

0000BFE0:00 00 00 00 00 00 00 00 00 00 00 00 00 3E BC 12 E6
0000BFF0:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Write Data: Top Pin No=00000180
00000000:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

0000BFE0:00 00 00 00 00 00 00 00 00 00 00 00 00 3E BC 12 E6
0000BFF0:00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

2000/03/27 13:23:13
\\.\PhysicalDrive9,Start LBA=00000180,End LBA=000001DF,The Pin Track process is completed.
2000/03/27 13:23:48 Pin Track Tool is exited.

```

The log file is backed up to the log folder under the directory in which the tool is installed with a name given as “PinTrack-year-month-date-hours-minutes-seconds.log”.

- When “The Pin Track process is completed.” is displayed in the log file, it means that the Pin has been erased. View the display on the SVP to confirm that the Pin has been erased.
- (6) When you erase another readable Pin successively, repeat the procedure from step (2).
- When an unreadable Pin exists in the device added to the list  
When the “Proceed” button is pressed in the case where an unreadable Pin exists in the list, the following is displayed.

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\.\PhysicalDrive9	2F	0017	00000180	000001DF	unreadable-pin

**Pin Track Tool**

? Would you like to do the process of the readable Pin ?

Drive Name | Yes | No

Start-LBA | 180

End-LBA | 1DF

☐ Proceed unreadable Pin

Operation


“unreadable” is displayed in the “Status” column showing that the slot is an unreadable Pin. In this case, the Pin is not erased even if the procedure for erasing a readable Pin is executed and the device is not deleted from the list as follows.

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\\\PhysicalDrive9	2F	0017	00000180	000001DF	read error

**Pin Track Tool** [X]

 The Pin Track process is ended.  
If you would like to do process for the next LBA,  
please input LBA area and select Proceed again.

Drive Name [\\\\] [OK]

Start-LBA [18]

End-LBA [1DF]

☐ Proceed unreadable Pin

Operation

[ ] [ ] [ ] [ ]

When the status is changed to “read error” and the device remains in the list, go to Subsection [TRBL07-450](#), “Procedure for erasing unreadable Pin”.

## Operation of Unreadable Pin Process (Windows NT)

The following explains how to operate the Pin Track Tool for erasing an unreadable Pin. Since the procedure for erasing a readable Pin is to be applied to all the Pins first, follow the procedure below after executing the procedure given in Subsection [TRBL07-360](#), “Procedure for erasing readable Pin”.

\* **Note** - On an SIM reported owing to a use of the Pin Track tool -  
 When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.  
 Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

- (1) When the log file (PinTrack.log) is open, close it.
- (2) Reconfirm the device, which was not deleted from the list when the readable Pin erasing process was executed, and the display on the SVP.
- (3) Specify the unreadable Pin erasing process.  
 After confirming that the input information is correct, check off the check box of the Unreadable Pin. When it is checked off, the password dialog box is displayed.

The screenshot shows the 'Pin Track Drive List' window. At the top, there are menu items 'Operation' and 'Help'. Below them is the title 'Pin Track Drive List'. A table lists drive information:

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\.\PhysicalDrive9	2F	0017	00000180	000001DF	read error

Below the table, there is a 'Password' dialog box with a text input field and 'OK' and 'Cancel' buttons. Under the dialog box, there are input fields for 'Drive Name' (\\.\PhysicalDrive9), 'Start-LBA' (180), and 'End-LBA' (1DF). Below these is a checked checkbox labeled 'Proceed unreadable Pin'. At the bottom, there is an 'Operation' section with buttons: 'Proceed', 'Add', 'Delete', and 'Exit'.

The password is to be obtained from the Technical Support Center. Without the password, the unreadable Pin cannot be erased.

Input the password and click the “OK” button.

When the correct password is input, the check box is checked off.

If the box is not checked off although the correct password has been input, copy the Pass.INI file in the folder in which the tool is installed from the media for installation again.

## (3) Erase the unreadable Pin.

- Check off the check box. When no wrong input is found, click the “Proceed” button to erase the unreadable Pin.

**Pin Track Tool**

Operation Help

Pin Track Drive List

Physical Drive Name	Port	LDEV	Start LBA	End LBA	Status
\\.\PhysicalDrive9	2F	0017	00000180	000001DF	read error

Drive Name: \\.\PhysicalDrive9 Port: 2F LDEV: 0017

Start-LBA: 180

End-LBA: 1DF

☒ Proceed unreadable Pin

Operation

Proceed Add Delete Exit

When the “Proceed” button is clicked, the program checks whether a free area for outputting a log is ensured in the current drive in which the Pin Track Tool is installed. If the free area is less than 10 MB, the following dialog box is displayed to warn it.

**Pin Track Tool**

 WARNING: This disk free space is under 10 MB.  
DiskSpace 1.11 MB  
Would you like to continue the process of Pin Track ?

Yes No

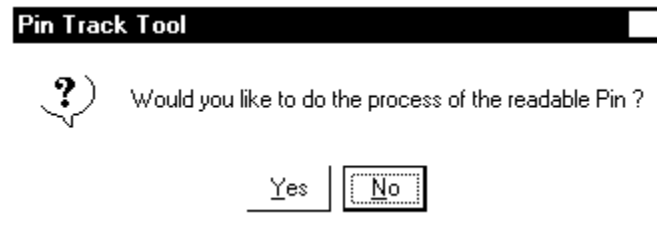
The current free area on the disk is displayed in the dialog box. A free area of approximately 400 kB is required to erase a Pin. When the necessary free area is provided, the processing can be continued.

Note: Install the program in a drive in which the enough free area can be ensured.

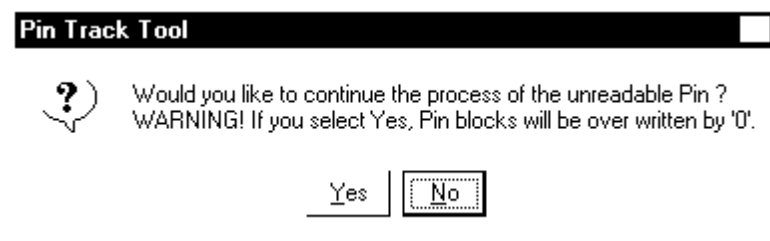
When “Yes” is selected, the processing is continued. If the disk capacity is less than that required for the log, as large log file as can be accommodated is acquired. Normally, select “Yes” only when the free area is enough.

When “No” is selected, the routine is returned to the main window. If the option has been checked off, it is cancelled. Ensure a free area in the drive, put the collected log file in order, or install the program in another drive.

When the “Proceed” button is clicked to continue the processing, the following dialog box is displayed.

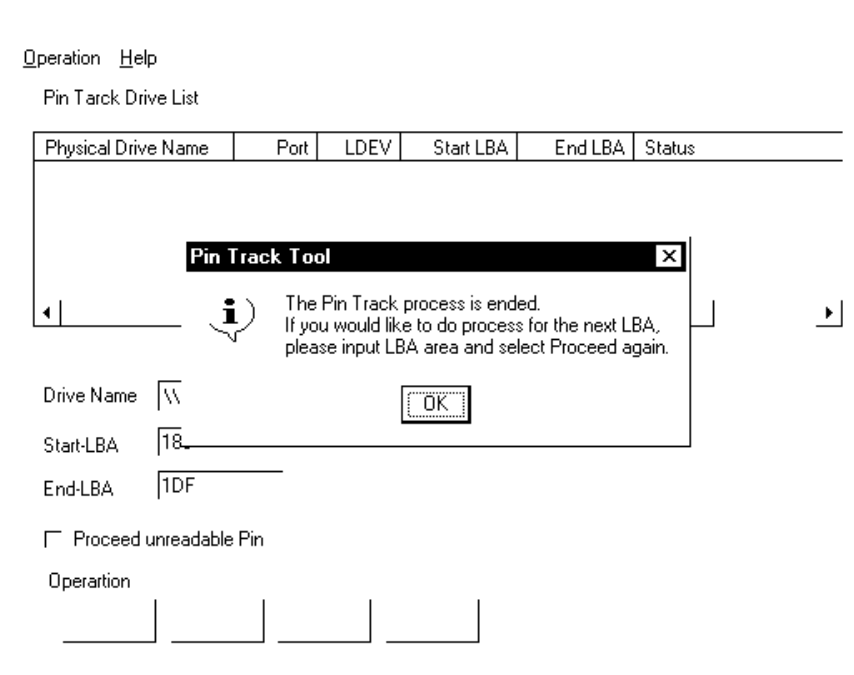


Try to erase the readable Pin first. When the readable Pin Track process cannot be executed when the “Yes” is clicked here, the following dialog box is displayed.



A dialog box for confirming whether to execute the unreadable Pin Track process is displayed. Execution of the unreadable Pin Track process must be decided carefully because it overwrites the Pin blocks with “0” data.

- Update the display on the SVP before executing the unreadable Pin Track process and check if the Pin of the input device has been erased. When the Pin has already been erased, click the “No” button to return to the main window.
- When the Pin has not been erased, click the “Yes” button to erase the unreadable Pin. When the “Yes” button is clicked, the unreadable Pin Track process is executed and the following window is displayed.



- (4) Check the log of the Pin Track Tool.
  - Open the PinTrack.log file in the folder in which the tool is installed by using a memo pad, etc. The log file is backed up to the log folder under the directory in which the tool is installed with a name given as “PinTrack-year-month-date-hours-minutes-seconds.log”.
  - When “Pin Track process is completed” is displayed in the log file, it means that the Pin has been erased.  
View the display on the SVP to confirm that the Pin has been erased.
- (5) When you proceed another Pin successively, repeat the “Procedure for erasing readable Pin” in [TRBL07-360](#).



## Installation of Pin Track Tool (Windows NT)

\* Perform the installation only when it is required.

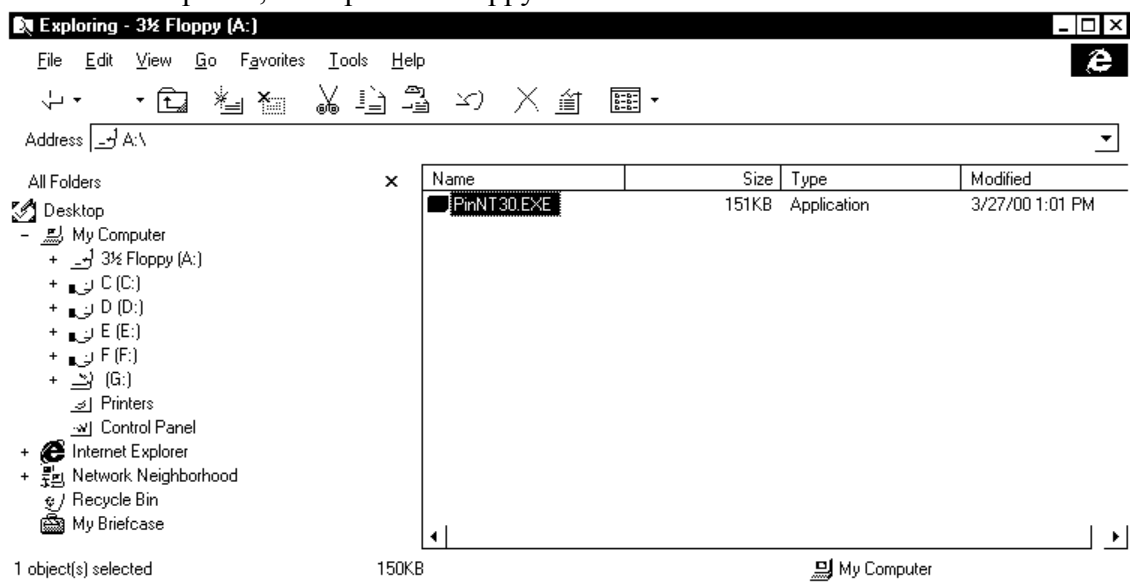
### <Preparation>

The Pin Track Tool is provided being contained in one 3.5-inch floppy disk or CD-ROM. A free area of more than 10 MB is required on the disk on which the program is to be installed as the area for collecting a log. The size of the log file is approximately 400 kB per one erasing process. The log is collected in the log folder under the folder in which the tool is to be installed each time the Pin erasing process completes.

Since the log is collected after the processing, prepare an empty floppy disk.

### <Copying from floppy disk to local disk>

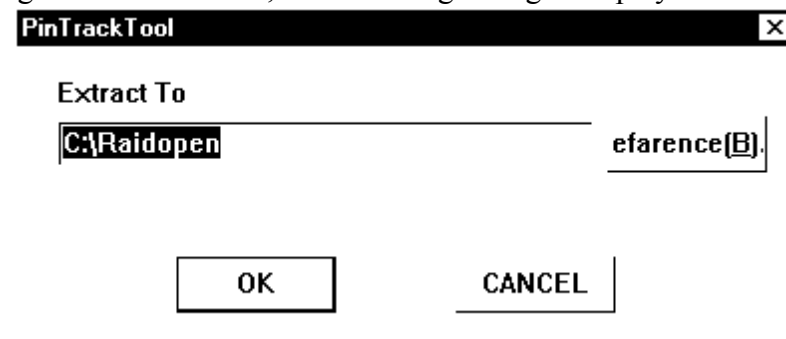
- (1) Logon to the Windows NT system as administrator permission.
- (2) Execute the Explorer, and open the Floppy disk or CD-ROM



For CD-ROM, the path is different.

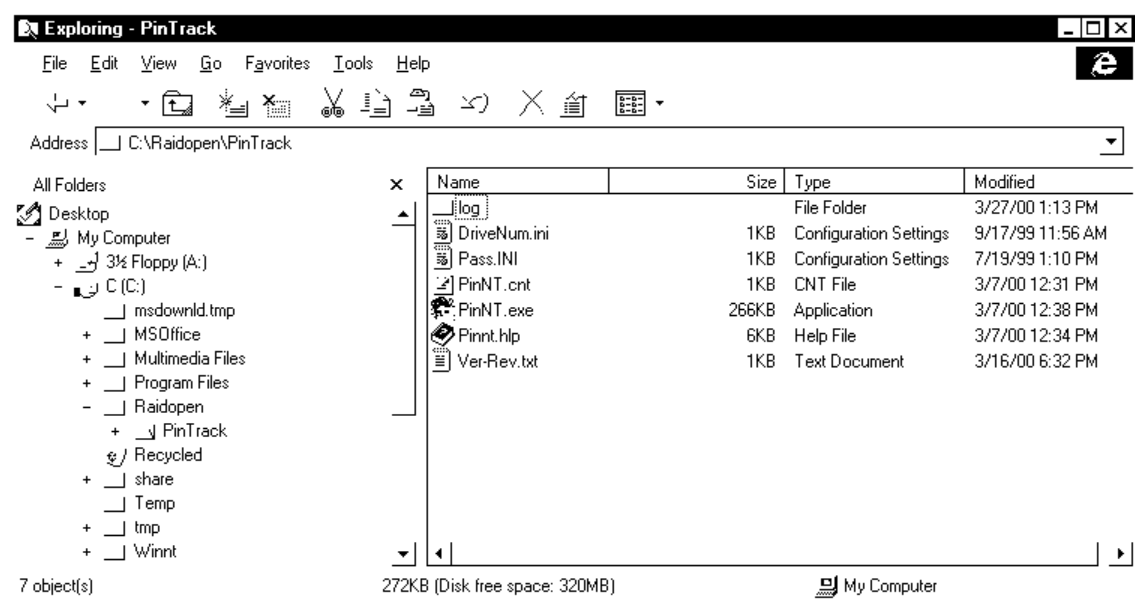
(Example : X:\TOOL\PINTRACK\WIN\_NT\)

- (3) When executing the PinNTxx.exe., the following dialog is displayed.



- (4) If you specify an extracting folder and click the [OK] button, Pin Track Tool are extracted by itself.

- Check the file name and file size displaying on the following windows.



The execution file name is PinNT.exe. When the PinNT.exe is executed, a log file, PinTrack.log, is created in the folder in which the tool has been installed. When the program is quit, the log file is copied to the log folder with a name given as “PinTrack-year-month-date-hours-minutes-seconds.log”.

- Log file  
In the log file, only the head LBA of Read/Write data of specified device is recorded. It can also record Read/Write data of all the LBAs.
- \* When logs of all the LBAs are recorded, a log file of approximately 400 kB is created for each erasing process in the list. Therefore, be careful of the free capacity on the disk on which the tool is installed.

When a log of only the head LBA is recorded, open the DriveNum.ini file in the folder in which the tool is installed and replace “LogMode=1” with “LogMode=0”.

Data in the log file is recorded in the format shown below.

```

2000/03/27 13:22:16 Pin Track Tool started.
\\.\PhysicalDrive0
    No information
\\.\PhysicalDrive1
    Product Serial R400 00030036 0042
    Port Number    1E
    LDEV Number    002A
    Disk Capacity  2461040640 bytes
    Maximum LBA    0049583F
2000/03/27 13:23:13
\\.\PhysicalDrive1,Start LBA=00000180, LBA=000001DF,An error occurred when reading.
Read Data: Top Pin No=00000180
00000000:** ** ** ** **
00000010:** ** ** ** **
00000020:** ** ** **^
0000BFE0:** ** ** **^
0000BFF0:** ** ** **^
Read Data: Top Pin No=00000181
Read Data: Top Pin No=00000182
Read Data: Top Pin No=000001DF
0000BFE0:** ** ** **^
0000BFF0:** ** ** **^
Write Data: Top Pin No=00000180
00000000:00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010:00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020:00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFE0:00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFF0:00 00 00 00 00 00 00 00 00 00 00 00 00 00
2000/03/27 13:24:13
\\.\PhysicalDrive9,Start LBA=00000180,End LBA=000001DF,The Pin Track process is completed.
2000/03/27 13:24:48 Pin Track Tool is exited.

```

#### [Displayed items]

Time when the program was started : Date and time are indicated.

SCSI device information : Port No., LDEV No., disk capacity, maximum LBA, etc. are displayed.

Read log : Readable Pins in a specified LBA range are displayed in the lump. Unreadable Pins are displayed for each LBA. Each unreadable Pin is recorded with an asterisk (\*).

Write log : Pins in a specified LBA range are displayed in the lump.

Entered information : Selected devices, Start LBA, and End LBA are displayed.

Error information : When a Pin is unreadable, "read error" is displayed. Besides, when an error occurs in the program, details of it are displayed.

Time when the program is quit : Date and time are displayed.

---

### Log file saving and de-installation

\* Perform the de-installation only when it is required.

#### <Saving of log file>

- (1) Save the log file which executed Pin Track Tool.  
Execute Explorer to open the folder in which the tool is installed.
- (2) Insert the empty floppy disk prepared for the log collection and copy the whole log directory to the floppy disk.
- (3) Delete the files under the log directory if they are not necessary.

#### <De-installation of Pin Track Tool>

To uninstall the Pin Track Tool, delete all the files concerned by deleting the whole PinTrack folder in which the tool is installed.

## Acquisition of disk information

You can confirm information of the disk connected to Windows NT by viewing the log file.

- (1) Execute the PinNT.exe.
- (2) When the window is displayed, open the PinTrack.log file.
- (3) A list of the connected devices is recorded in the file.

### <Log file>

- For disks other than the disk subsystem, “No Information” is displayed.
- Since the disk information is acquired using the OS function, the recording order in the log may vary.

### <Variation of device information>

The display in the log file varies depending on the disk subsystem configuration.

<b>For the HITACHI Specification</b>	
\\.\PhysicalDrive14	
Product Serial	HITACHI R4003ABE0108
Port Number	2N
LDEV Number	0108
Disk Capacity	2461040640 bytes
Maximum LBA	0049583F

The “LDEV Number” is the intact “Product Serial” expressed in hexadecimal.

<b>For the OEM Specification</b>	
\\.\PhysicalDrive13	
Product Serial	R400 00015038 0012
Port Number	1J
LDEV Number	000C
Disk Capacity	1874903040 bytes
Maximum LBA	0037E05F

The “LDEV Number” is the “Product Serial” converted from hexadecimal to decimal.

### Identification of files affected by Pin failure

When a drive letter possible to have a Pin failure is found, identify files affected by the failure using the chkdsk command.

- (1) Execute Explorer, click the drive concerned by the right mouse button, and display the property.
- (2) Open the tool tab and click "Check" in the "Error Check".
- (3) Check off all the check disk options and click the "Start" button.
- (4) When the dialog box is displayed, follow the instruction given in it.
- (5) Perform the above operations for all the drives concerned.

### Verification of files and recovery of them using backup

When a file is repaired or partially deleted by the chkdsk command, verify whether the file is normal.

When the file was partially deleted or broken, delete the file and restore it using the backup.

After the file is recovered normally, check the Pin status. If a Pin remains, erase it by executing the readable Pin Track process first.

### 7.3.3.3 Procedure on Solaris

The following is an erasing procedure to be used when a Pin failure occurs on Solaris.

= Notices =

In Solaris, disk device is shown as cXtYdZsN, which denotes controller, SCSI target ID, logical unit number, and slice (partition). One LDEV can be logically divided into eight portions, s0 to s7, and the each portion can be used as a disk drive. For a slice, a capacity can be set in units of cylinder, and the user accesses each slice treating it as a disk drive having LBAs starting from LBA 0.

In Solaris, note that some restrictions on the Pin erasing are induced by handling the disk drive as cXtYdZsN.

- Ensure a free capacity on the disk on which the Pin Track Tool is to be installed.  
The showrel tool, which identifies device files of Solaris according to the LDEV and LBA range shown on the SVP, creates a temporary file on the disk in order to acquire device information. If the free capacity on the disk is insufficient, the information cannot be displayed accurately.
- The Pin erasing process cannot be applied to all the LBAs.  
In Solaris, the last two cylinders are not allocated to the file system because they are reserved as alternate cylinders. For example, in OPEN-3, the two of the total 3338 cylinders (a range of the LBA from 0x494D00 to 0x49583F out of LBAs maximum LBA number in which is 0x495840) are not allocated to the data cylinders. Therefore, if a Pin including such a range is generated, the Pin shown on the SVP cannot be erased because I/Os cannot be issued. The remedy for it is limited to an elimination of unreadable LBAs in the file system.
- The Pin Track Tool cannot be used against a Pin including the inaccessible cylinder.  
In the case where cylinders are divided to be allocated to slices and there exist cylinders which are not allocated to any of the slices, if a Pin including such a range is generated, the Pin shown on the SVP cannot be erased because I/Os cannot be issued. The remedy for it is limited to an elimination of unreadable LBAs in the file system.

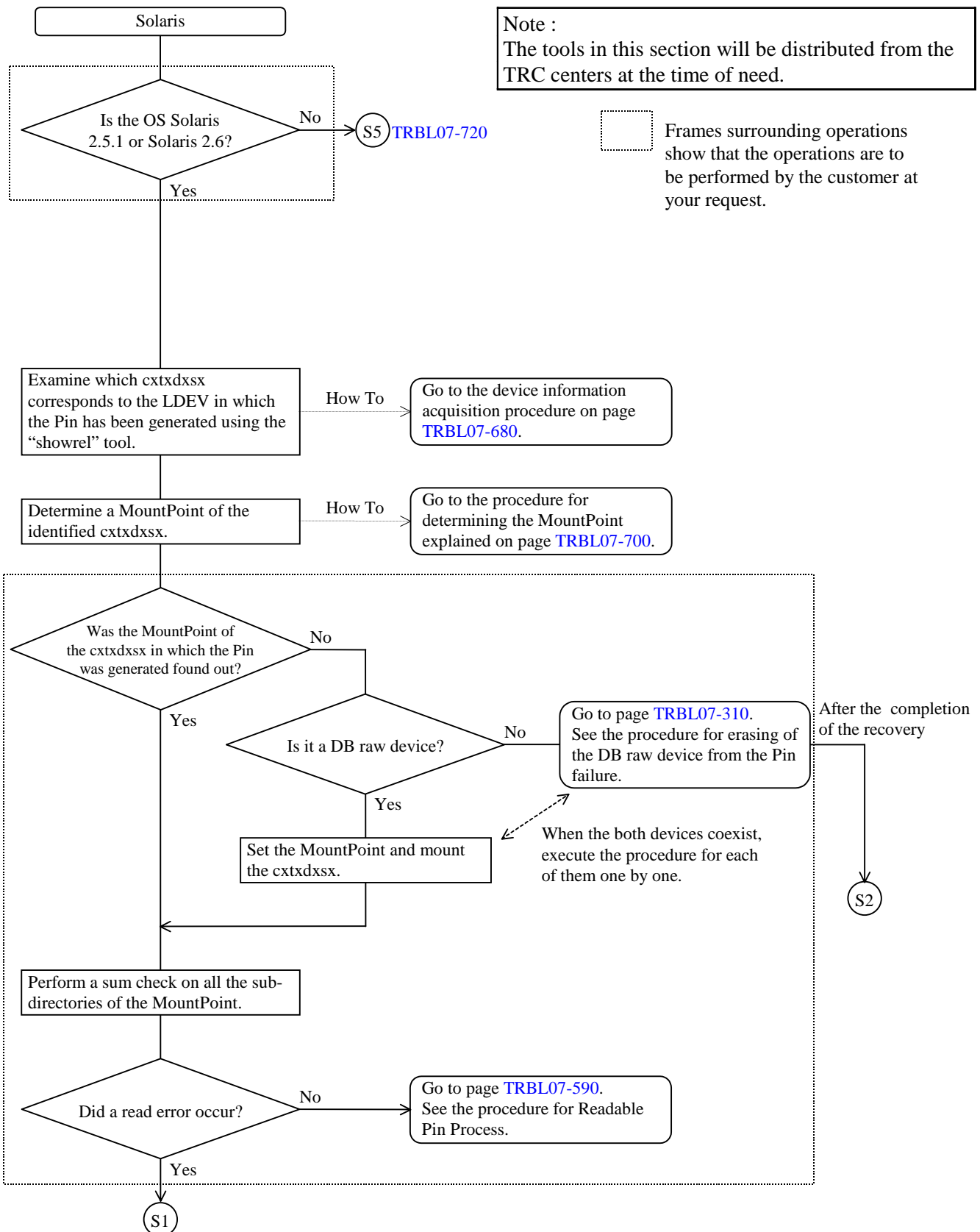
- When an unreadable Pin is generated in the head LBA, the Pin cannot be erased.  
The management information including the device geometry is recorded in the range of the LBA0x0 to LBA0x5F corresponding to the slot#15. If a Pin including this range is generated, the device cannot be recognized to be a disk by the OS. Format the LDEV following the maintenance manual.
- Specify the LBA to be input in the Pin Track Tool correctly.  
When the input LBA is wrong, Solaris judges the specified range to be an unreadable Pin. Even if you try to apply the unreadable Pin Track process to it specifying the range, a write error will be caused and it is not erased. Make sure that the input LBA is correct before executing the process.
- After executing the Pin Track process, verify that the processing has completed normally referring to a log.  
To verify the execution result of the Pin Track process, refer to the log file. Read/write errors are not displayed on the screen. Furthermore, check if the input information was correct when a write error occurred as described above.

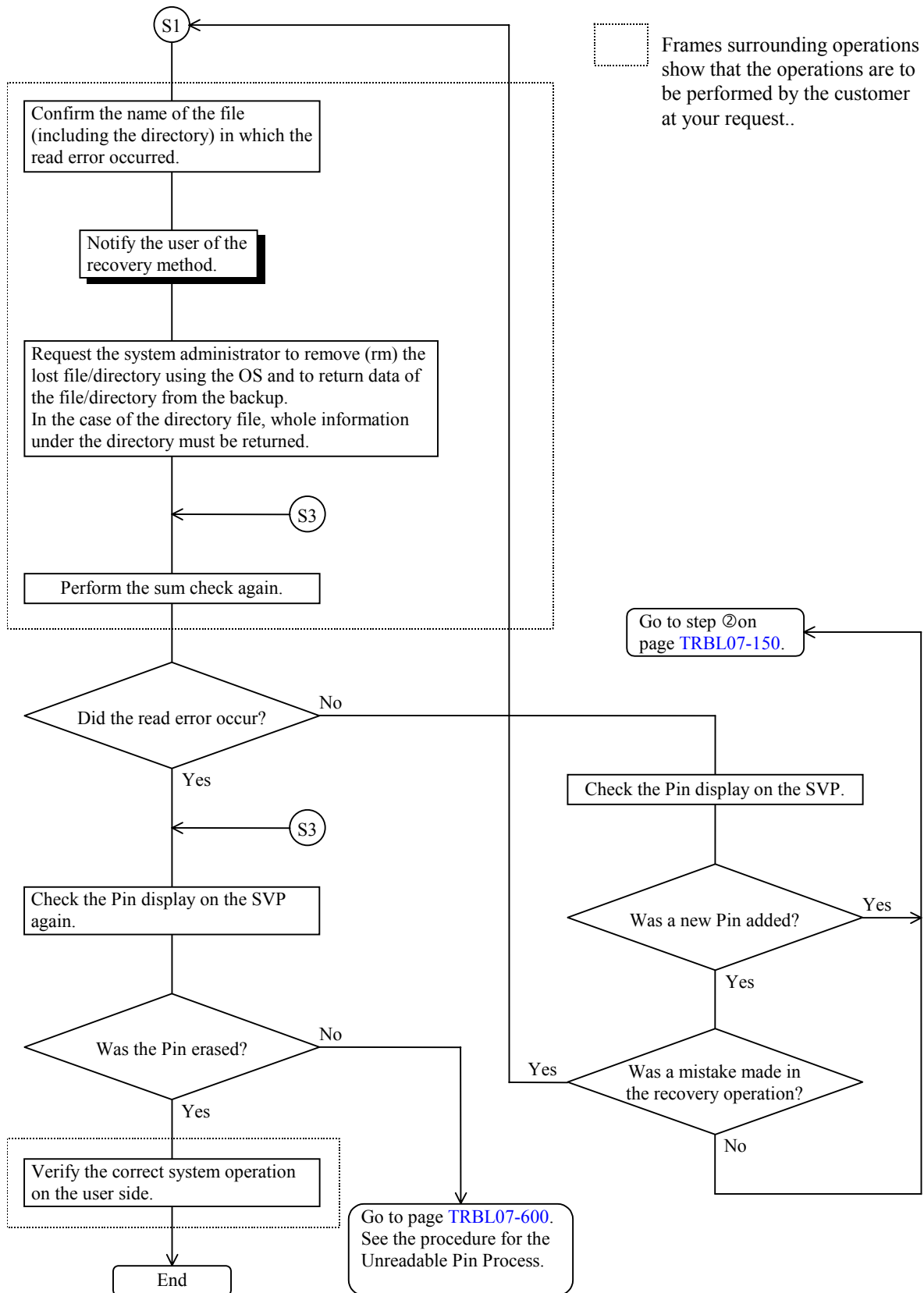
When you have to eliminate the unreadable LBA in the file system on the above conditions, operate according to the flowchart on the following pages.

When you can not use the Pin Track Tool, perform at the [TRBL07-720](#).

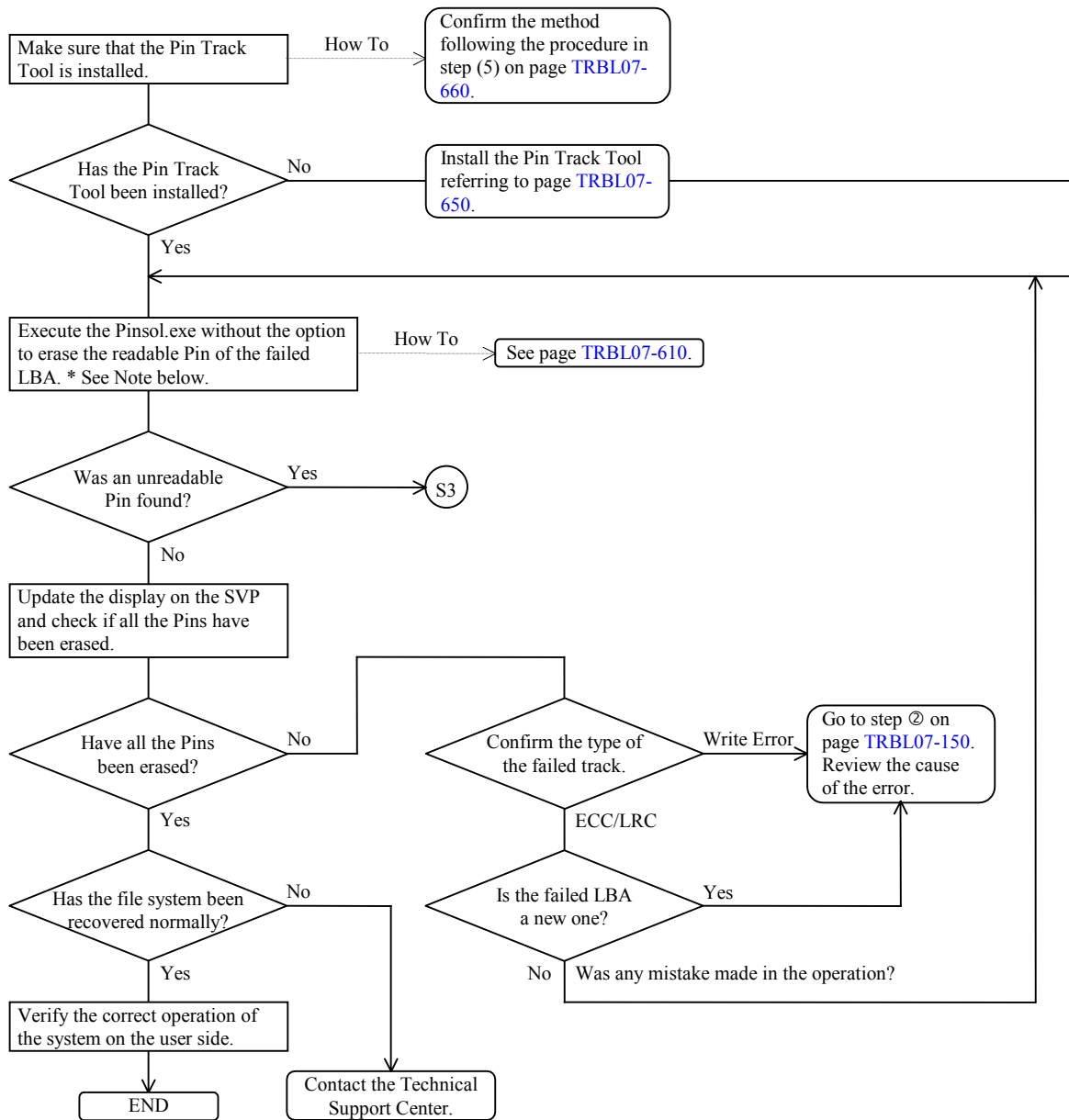


○ The following is an erasing procedure to be used when a Pin failure occurs on Solaris.



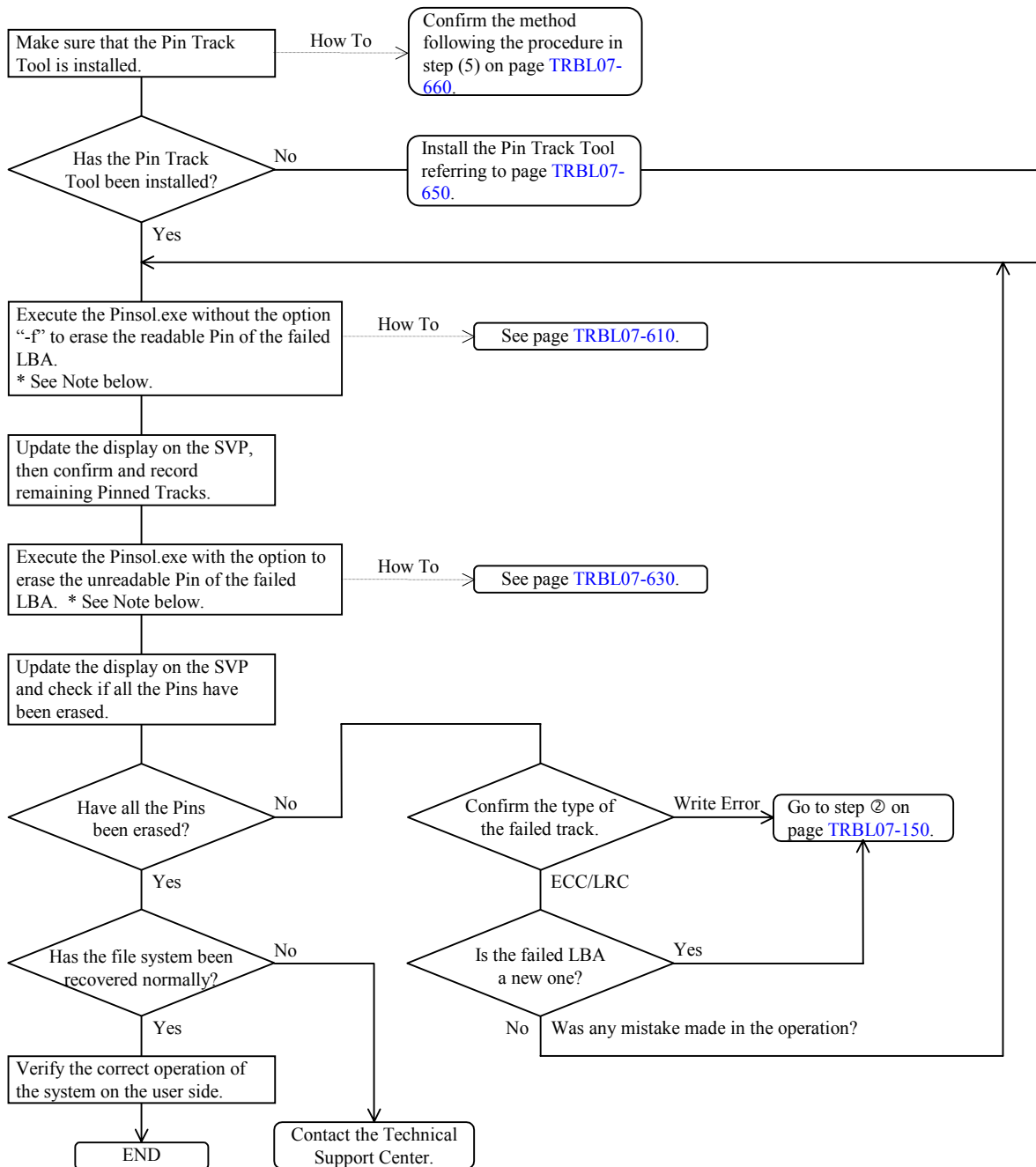


## Readable Pin Process (Solaris)



**\* Note** - On an SIM reported owing to a use of the Pin Track Tool -  
 When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.  
 Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when the erasure of all the Pins by the Pin Track Tool is confirmed.

## Unreadable Pin Process (Solaris)



\* Note - On an SIM reported owing to a use of the Pin Track Tool -  
 When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.  
 Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

## Operation of Readable Pin Process (Solaris)

The following explains how to operate the Pin Track Tool for erasing a readable Pin.

**\* Note** - On an SIM reported owing to a use of the Pin Track Tool -  
 When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.  
 Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

- (1) Move to the directory of the installed Pin Track Tool.  

```
#cd /usr/raidopen/pinsol
```
- (2) Execute the pinsol.exe without the option.  

```
# ./pinsol.exe -log
```

Execute with a path “./”.
- (3) Input the following information in response to questions.  

```
# ./pinsol.exe -log
```

Input Device Name -> /dev/rdisk/c3t0d0s2	Input the cxytdzsn.
Input Start LBA Data-> 180	Input the Start LBA.
Input End LBA Data-> 1df	Input the End LBA.
Input Next LBA ?(Y/N) ->n	When two or more LBAs exist in the same device, input “y”.
Input Next Device ?(Y/N) ->n (Input of “n” is recommended.)	
When erasing Pins of two or more devices simultaneously, input “y”.	

To input the LBA, convert the LBA of the LDEV shown by the SVP into the slice of Solaris the device file of which corresponds to the LBA, and input the slice. For the conversion of indication between the LBA and slice, refer to Section 6.2, “Acquisition of Device Information”.

The showrel tool may display the two or more corresponding LBAs depending on the slice structure. In this case, the same LBA range is possible to be designated as different slices. Execute the Pin erasing process for the LBAs in the list shown by the showrel one by one in a descending order, and check the display on the SVP each time.

**Note:** When the input LBA does not exist in the device file of the specified slice, it is judged to be an unreadable Pin and causes a write error if the operation is continued leaving it as it is. Make sure that the input information is correct before starting the processing and that the Pin has been erased normally referring to the log after the processing terminates.

- (4) Since the input data and a message for confirming whether to erase the Pin, check if the input data is correct.

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0s2	00000180	000001DF

Before you try to proceed the readable pin,  
please check the pin information on SVP.

If the pin data have been created, please do not try to proceed the pin track again.

Do you want to do the process of the readable Pin?

Please input[y/n(default n)]:y

If the input data is incorrect, input “n” or simply press the “Return” key and perform the data input over again from step (2).

Update the display on the SVP and check if the Pin concerned has been erased following the message.

When the Pin has already been erased, terminate the processing by inputting “n” or simply press the “Return” key.

When the Pin has not been erased, input “y” and press the “Return” key.

- (5) When the Pin is judged unreadable through the Pin type judgment, go to [TRBL07-630](#).

Unreadable Pin:

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0s2	00000180	000001DF

- (6) When the Pin Track Tool is quit, a log file is created on the same directory.

The log file name is given as “mm-dd-hh-m‘m’-ss.log” (m: month; d: date; h: hours; m’: minutes; s: seconds).

Example:

0614200552.log means a log file created at 5minutes and 52 seconds after 20 o’clock on June 14.

In the log file, the execution result of the Pin Track process is recorded. Make sure that the process has completed normally by checking if “pin track process complete” is displayed.

## Operation of Unreadable Pin Process (Solaris)

The following explains how to operate the pintool for erasing an unreadable Pin.

**\* Note** - On an SIM reported owing to a use of the Pin Track Tool -  
 When two or more Pins have been generated in the LBAs adjacent in the same LU, a generation of a new temporary Pin caused by the parity calculation performed in the Pin Track process may occur and an SIM may be reported.  
 Since this Pin is erased at the same time when the erasing process of the Pin concerned completes, complete the SIM when it is confirmed that all the Pins have been erased by the Pin Track Tool.

- (1) Move to the directory of the installed Pin Track Tool.  

```
# cd /usr/raidopen/pinsol
```
- (2) Execute the pintool with the option.  

```
# ./pinsol.exe -f -log
```

When “-f” is added, the unreadable LBA is overwritten with “0” data.
- (3) Input the following information in response to questions.  

```
# ./pinsol.exe -f -log
```

Input Device Name -> /dev/rdisk/c3t0d0s2	Input the cxtydzn.
Input Start LBA Data-> 180	Input the Start LBA.
Input End LBA Data-> 1df	Input the End LBA.
Input Next LBA ?(Y/N) ->n	When two or more LBAs exist in the same device, input “y”.
Input Next Device ?(Y/N) ->n (Input of “n” is recommended.)	When erasing Pins of two or more devices simultaneously, input “y”.

To input the LBA, convert the LBA of the LDEV shown by the SVP into the slice of Solaris the device file of which corresponds to the LBA, and input the slice. For the conversion of indication between the LBA and slice, refer to Section [TRBL07-680](#), “Acquisition of Device Information”. The showrel tool may display the two or more corresponding LBAs depending on the slice structure. In this case, the same LBA range is possible to be designated as different slices. Execute the Pin erasing process for the LBAs in the list shown by the showrel one by one in a descending order, and check the display on the SVP each time.

Note: When the input LBA does not exist in the device file of the specified slice, it is judged to be an unreadable Pin and causes a write error if the operation is continued leaving it as it is. Make sure that the input information is correct before starting the processing and that the Pin has been erased normally referring to the log after the processing terminates.

- (4) Since the input data and a message for confirming whether to erase the Pin, check if the input data is correct.

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0s2	00000180	000001DF

Before you try to proceed the readable pin,  
please check the pin information on SVP.

If the pin data have been created, please do not try to proceed the pin track again.

Do you want to do the process of the readable Pin?

Please input[y/n(default n)]:y

If the input data is incorrect, input “n” or simply press the “Return” key and perform the data input over again from step (2).

Update the display on the SVP and check if the Pin concerned has been erased following the message.

When the Pin has already been erased, terminate the processing by inputting “n” or simply press the “Return” key.

When the Pin has not been erased, input “y” and press the “Return” key.

- (5) The following message is displayed and the Pin is judged unreadable through the Pin type judgment.

Unreadable Pin:

Device Name	Start LBA	End LBA
/dev/rdisk/c3t0d0s2	00000180	000001DF

Do you want to do the process of the unreadable Pin?

WARNING! if you input ‘y’, Pin Blocks will be over written by ‘0’.

Please input[y/n(default n)]:y

In the Pin Track Process, the window may be changed into monochrome, and the following message may be showed on the window. In the case, please do not pay attention to them.

After 3 minutes or so, the window will back to originally status.

Example:

This is a message which it is displayed on the monochromic window .

WARNING: /sbus@if, 0/fc0@1, 0/sd@0, 0(sd15)

Error for Command: read

Error Level: Retryable (or Fatal)

Requested Block: 766560

Error Block: 766560

Vender: HP

Serial Number: 0450F4290000

Sense key: Media Error

ASC: 0x11 (unrecovered read error), ASCQ: 0x0, FRU: 0x0

- (6) When the Pin Track Tool is quit, a log file is created on the same directory.

The log file name is given as “mm-dd-hh-m‘m’-ss.log” (m: month; d: date; h: hours; m’: minutes; s: seconds).

Example:

0614200552.log means a log file created at 5minutes and 52 seconds after 20 o’clock on June 14.

In the log file, the execution result of the Pin erasing process is recorded. Make sure that the processing has completed normally by checking if “pin track process complete” is displayed.



## Installation of Pin Track Tool (Solaris)

Perform the installation only when it is required.

### <Preparation>

Since the Pin Track Tool is provided being contained in a 4-mm DDS DAT or 3.5-inch floppy disks, a drive for installing it is necessary in the host device which controls the Pin erasing operation or on the network which includes the device concerned.

Acquire a name for the device beforehand.

Besides, ensure a free area on the disk necessary for a log collection because a log of approximately 400 bytes per failed track is output when the log is collected using the pintool.

Note: If the free capacity on the disk is insufficient, the whole information cannot be collected.

The showrel tool for LBA-slice conversion ensures a memory and creates a temporary file in order to acquire device information. Make sure that the memory capacity (larger than 128MB/CPU) and the disk free capacity (larger than 10MB) sufficient for the server are ensured.

### <Copying from media to disk>

- (1) Login to the host as “root”.
- (2) Move to the install directory by the “cd” command and make a directory “raidopen”.
 

```
# cd /usr                (Move to the “/usr”)
# mkdir raidopen         (Make the directory “raidopen”)
# cd raidopen            (Move to the “raidopen”)
```
- (3) Move to the created directory and copy the files from the distribution medium.
 

<In the case of DDS DAT> (device name depends on each host.)

```
# mt -t /dev/rmt/0 rew    (Rewind a tape.)
# tar -xvf /dev/rmt/0      (Copy a file from the tape.)
# tar -xvf pinsolXX.tar    (Copy the tar file for Solalis.)
```

<In case of the Floppy disk or CD-ROM>

```
# volcheck                recognize a floppy disk or CD-ROM
                           confirm the label, and copy the files from the distribution medium.
# tar -xvf /floppy/no_name/PINSOLxx.TAR  (Floppy)
# tar -xvf /cdrom/zzzz/tool/pintrack/solaris/pinsolXX.tar  (CD-ROM)
```

Note: (The volume label (no\_name) and directory name (ZZZZ) is depend on the system.)

If the copy from the floppy disk to the disk ends, it takes out a floppy disk.

```
# eject                    Eject the medium.
```

If necessary, it pushes an eject button and it takes out a floppy disk.

- (4) After the thawing is complete, confirm the file name.  
 # cd ./pinsol (Move to the directory made by the thawing.)  
 # ls -l (Display a file list.)
- (5) Refer to the contents of the “Ver-Rev.txt” file and confirm each file size of the list.  
 # more /usr/raidopen/pisol/Ver-Rev.txt display contents of the file

HITACHI RAID Subsystem PinTrackTool for Solaris  
 Ver XX-YY-/Z (Revision ID)  
 All right reserved, Copyright (c) 1999,2000, Hitachi Ltd.  
 File Size (Bytes) pinsol.exe (Module ID)  
 File Size (Bytes) showrels.exe (Module ID)

Refer to the contents of “Ver-Rev.txt” file and confirm each file size of the list.

#### Saving of log file and de-installation of Pin erasing tool

Perform the de-installation only when it is required.
---

##### <Saving of log file>

In order to save the log file created through the Pin erasure, compress the file.

# cd /usr/raidopen/pinsol	Move to the execution directory.
# mkdir ./log	Create a directory for the log file.
# mv *.log ./log	Move the log file to the logdir.
# tar -cvf pinlog.tar ./log	Create the tar file.
# compress pinlog.tar	Compress the tar file.

Save the log file to the media.

# tar -cvf /dev/rmt/0 pinlog.tar.Z	(In the case of DDS DAT)
# tar -cvf /vol/dev/rdiskette0/unlabeled pinlog.tar.Z	(In the case of floppy disk (tar))
# cp pinlog pinlog.tar.Z /vol/dev/rdiskette0/raidopen	(In the case of floppy disk(DOS))

##### <De-installation of Pin Track Tool>

To uninstall the Pin Track Tool, delete all the files concerned by deleting the whole directory in which the tool is installed.

# cd /	Move to the root directory.
# rm -r /usr/raidopen/pinsol*	Delete files created under the /usr/raidopen.
	When “\” is added, the alias is invalidated.

## Procedure for collecting detail log

You can acquire detailed information on the erasing process from the Pin failure by adding the option to the Pin Track Tool.

Method of use: `./pinsd.exe(-f) -log`

By executing the above, you can acquire data which was read and written in the process for erasing from the Pin failure.

Example:

```
Input Device Name = /dev/rdisk/c1t0d0s2
Input Start LBA = 00000180
Input End LBA = 000001DF
/dev/rdisk/c1t0d0s2, Start LBA=00000180, End LBA=000001df readable Pin Track read error
Read Data: Top Pin No=00000180
00000000: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000040: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
Read Data: Top Pin No=00000181
...
Read Data: Top Pin No=00000182
...
Read Data: Top Pin No=00000183
...
Read Data: Top Pin No=000001DF
000001A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000001F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
Write Data: Top Pin No=00000180
...
00000000: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000010: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000020: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00000040: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
...
0000BFC0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFD0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFE0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000BFF0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
/dev/rdisk/c1t0d0s2, Start LBA=00000180, End LBA=000001DF Pin Track Process is complete!!
```

The log size is approximately 400 kB per one Pin erasure.

When the -log option is not added, data for each LBA is recorded for each of reading and writing.

## Acquisition of device information (how to use showrel tool) on Solaris

When the program has been installed, confirm the failed track information.

- (1) Move to the installed directory.

```
# cd /usr/raidopen/pinsol
```

Moving of the directory

- (2) Execute the following program

# ./showrels.exe	Add the ./ path and execute.
Input LDEV -> 000A	Input the device name shown on the SVP.
Input Start LBA -> 00044000	Input the start LBA shown on the SVP.
Input End LBA -> 0004405F	Input the End LBA shown on the SVP.

Input example and output result.

# ./showrels.exe
Input LDEV -> 0123
Input Start LBA -> 0000ABE0
Input End LBA -> 0000AC3F
 DeviceName=c0t1d0s6 Port=1A LDEV=0123
Start=00000FA0 End=00000FFF

The LDEV number is indicated with the CU:LDEV number of four figures. Confirm the CU number and LDEV number of the Pin displayed on the SVP and identify the slice of the device file. The device file name identified here is used as the input information in the process for the Pin failure.

[Note]

- The showrel tool issues commands peculiar to the disk subsystem to all the disk devices in order to acquire device information. Therefore, when the command is issued to a disk other than the disk subsystem such as a built-in disk, an error (Illegal Request) may be reported to the system. It is not a problem, though.
- When an unreadable Pin is generated in the slot#15 including the head LBA in which the disk management information is recorded, the device information cannot be acquired because the disk becomes unable to respond to the OS.

The following may be displayed depending on the setting of the slice.

When there is no information of the input LDEV in the device management table.  
LDEV=010C This is not a target disk.

Check if the input LDEV number is correct.

When an LDEV which does not belong to any slice is input  
DeviceName=c2t3d0s6 Port=1D LDEV=0189 The target LBA is missing or invalid.

In this case, the Pin shown on the SVP cannot be erased. However, it does not have any effect on the file system.

When a Pin extends over multiple slices  
DeviceName=c0t1d0s5 Port=1A LDEV=000A  
Start=00001357 End=00001387  
DeviceName=c0t1d0s1 Port=1A LDEV=000A  
Start=00000000 End=0000002F

When it is displayed that a Pin extends over multiple slices, specify the LBA displayed for each slice when executing the pinsol.exe. The Pin cannot be erased unless all the range concerned are processed by the pinsol.exe.

When an LDEV is allocated to multiple ports  
DeviceName=c1t1d0s6 Port=1A LDEV=0123  
Start=00000FA0 End=00000FFF  
DeviceName=c0t3d0s6 Port=1C LDEV=0123  
Start=00000FA0 End=00000FFF

When an LDEV is displayed at the different ports with the same LBA range of the slice, a single LDEV is allocated to multiple ports. In this case, specify any one of the devices to execute the pinsol.exe.

When multiple LBAs are displayed for an LDEV  
DeviceName=c0t3d0s2 Port=1A LDEV=0123  
Start=00000FA0 End=00000FFF  
DeviceName=c0t3d0s6 Port=1A LDEV=0123  
Start=00000FA0 End=00000FFF

When two slices are displayed with the same LBA range for an LDEV, a slice to which the same cylinder is allocated may exist. Normally one of the slices is 2. In this case, specify a device described ahead in order to execute the pinsol.exe. If the Pin is not erased when the SVP display is checked after the Pin erasing process has terminated normally, execute the pinsol.exe for another device.

## Procedure for determining MountPoint (on Solaris)

The following operation must be performed by the system administrator at your request checking result at each step.

### **Display by the showrel**

When the showrel explained in TRBL07-680 is used, the LDEV number shown on the SVP and to which device file the range between the Start LBA and End LBA corresponds are displayed. Refer to [TRBL07-680](#), “How to use showrel tool”.

### **Display by prtvtoc**

Input “#prtvtoc <raw-device-name>” to display the slice list and find out all the slices in which Pins have been generated.

```
* /dev/rdisk/c0t1d0s5 partition map
*
* Dimension:
* 512 bytes/sector
* 80 sectors/track
* 9 tracks/cylinder
* 720 sectors/cylinders
* 2500 cylinders
* 1151 accessible cylinders
*
* Flags:
* 1: unmountable
* 10: read-only
*
*
* Partition  Tag  First Sector Last  Count  Sector Mount Directory
* 0          2    00      0    76320  76319
* 1          3    01  76320  132480 208799
* 2          5    00      0   828720 828719
* 5          6    00  208800  131760 340559 /opt
* 6          4    00  340560  447120 787679 /usr
* 7          8    00  787680   41040 828719 /export/home
```

You can confirm the current MountPoint by viewing the display of the Mount Directory.

## Determination using /etc/vfstab

Find out the MountPoint where the /dev/dsk/cxytdzsn is mounted using the #cat/etc/vfstab.

```
# cat /etc/vfstab
#device      device      mount      FS      fsck      mount      mount
#to mount    to fsck      point      type     pass     at boot    options
fd      -      /dev/fd fd      -      no      -
/proc    -      /proc  proc    -      no      -
/dev/dsk/c0t3d0s1  -      -      swap    -      no      -
/dev/dsk/c0t3d0s0  /dev/rdisk/c0t3d0s0 /      ufs      1      no      -
/dev/dsk/c0t3d0s6  /dev/rdisk/c0t3d0s6 /usr     ufs      1      no      -
/dev/dsk/c0t3d0s3  /dev/rdisk/c0t3d0s3 /var     ufs      1      no      -
/dev/dsk/c0t3d0s7  /dev/rdisk/c0t3d0s7 /export/home ufs      2      yes     -
/dev/dsk/c0t1d0s7  /dev/rdisk/c0t1d0s7 /export/home1 ufs      3      yes     -
swap     -      /tmp   tmpfs    -      yes     -
#
```

You can make sure whether the object device is mounted automatically or not.

## Determination using df

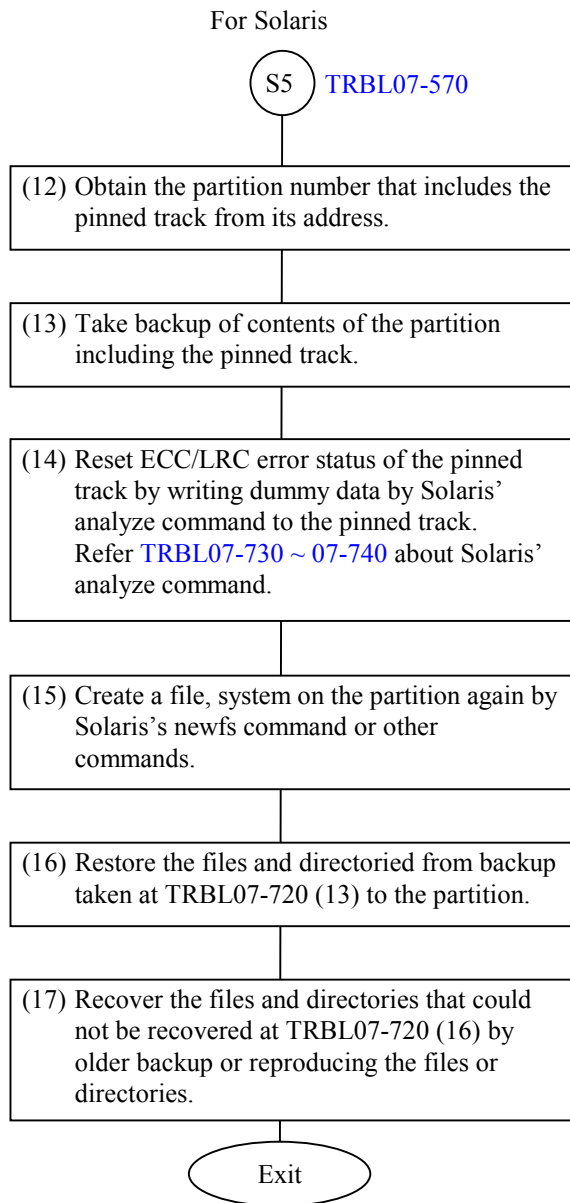
Reconfirm the “Ivol name” and “mount point” recognized by the filesystem using the #df -k.

```
# df -k
Filesystem      kbytes    used    avail    capacity  Mounted on
/dev/dsk/c0t3d0s0  76767    17735    58956     24%      /
/dev/dsk/c0t3d0s6  738902   552048   185931     75%      /usr
/proc              0          0          0         0%      /proc
fd                  0          0          0         0%      /dev/fd
/dev/dsk/c0t3d0s3   30807    17911    12866     59%      /var
/dev/dsk/c0t3d0s7   53535     7923    45559     15%      /export/home
/dev/dsk/c0t1d0s7  489702   189635   299578     39%      /export/home1
swap              121856     212    121644      1%      /tmp
/dev/dsk/c1t0d0s0  7095037  2163405  4860682     31%      /open9-0
/dev/dsk/c1t1d0s0  7095757  2049632  4975168     30%      /open9-1
#
```

You can make sure whether the object device is being mounted currently or not.

Decide the MountPoint to be checked by the “sum” command following the procedure above.

## &lt;Erasing procedure which does not require the Pin Track Tool&gt;





## Reset ECC/LRC error status of pinned track by Solaris analyze command (Solaris)

ECC/LRC error status of a pinned track can be resetted by writing dummy data by the analyze command in case of the LDEV containing the pinned track is connected to Solaris.

The procedure is described below. Input commands are shown by boldface characters.

- 1) Login to the Solaris as superuser

Example

```
host console login: root↵
password: sorry↵
```

- 2) Writing dummy data to the pinned track by the analyze command

The analyze command is a subcommand of the format command

Example

```
# format↵
Searching for disks...done

AVAILABLE DISK SELECTIONS:
(recognized SCSI disks are listed here.)
Specify disk (enter its number):n↵           ...device number of the LDEV
                                                including the pinned track.

selecting n
[disk formatted]

FORMAT MENU:
(format command menu is listed here.)
format> analyze↵
(analyze subcommand menu is listed here.)
analyze> setup↵
Analyze entire disk[no]? no↵
Enter starting block number[0, 0/0/0]: starting LBA of the pinned track
Enter ending block number[5806479, 3336/14/115]: ending LBA of the track
Loop continuously[no] no↵
Repair defective blocks[yes] ↵
Stop after first error[no] ↵
Use random bit patterns[no] ↵
```

```

Enter number of blocks per transfer[126, 0/1/10]: 1↵
Verify media after formatting [yes]? no↵
Enable extended messages[no]? yes↵
Restore defect list[yes]? ↵
Restore disk label[yes]? ↵

analyze> write↵
Ready to analyze (will corrupt data). This takes a long time,
but is interruptable with CTRL-C. Continue? y↵

    PASS 0 - pattern = c6dec6de
    cylinder number/head number/block counts

Total of 0 defective blocks repaired.
analyze> quit↵

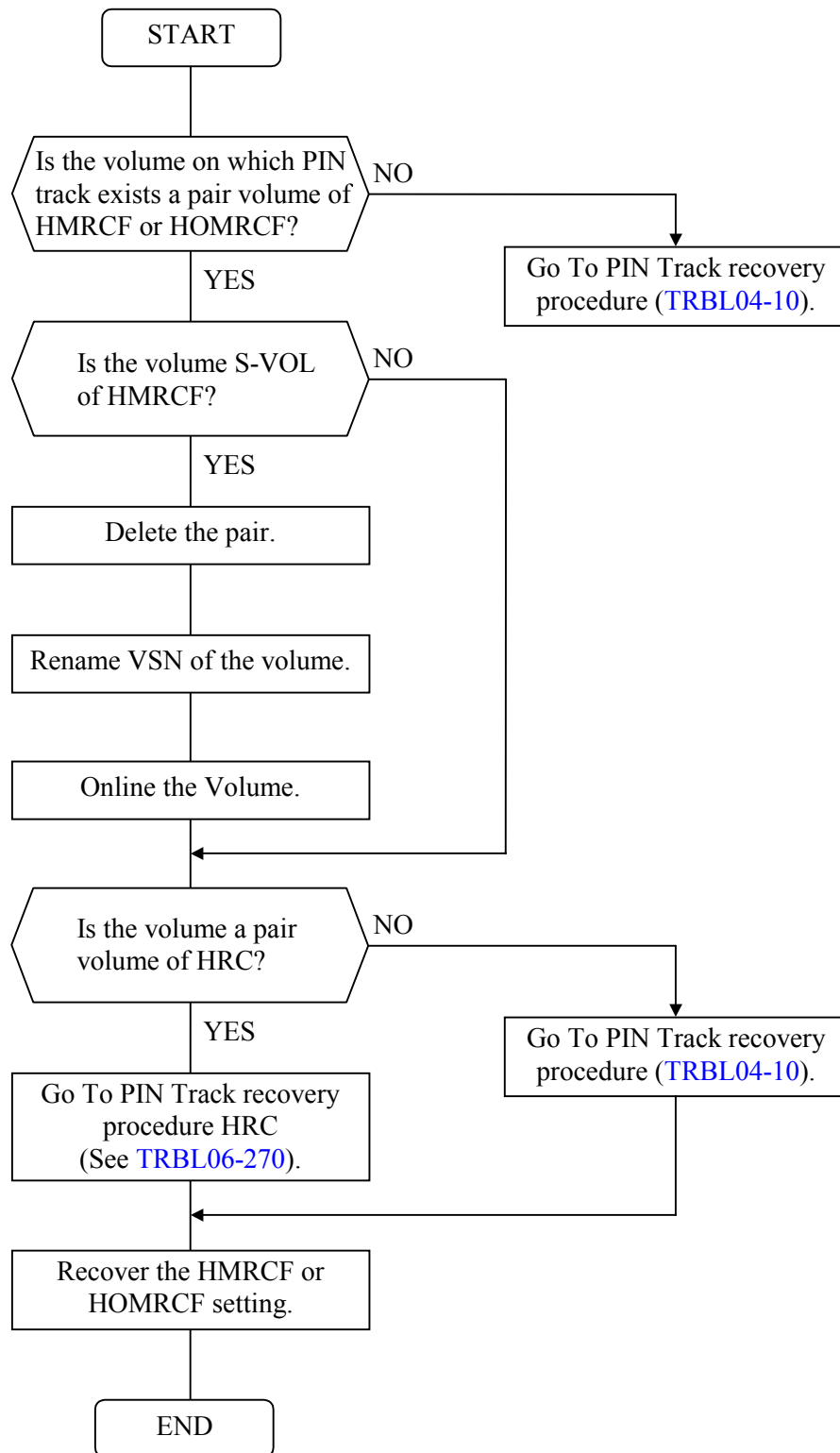
```

(Note)

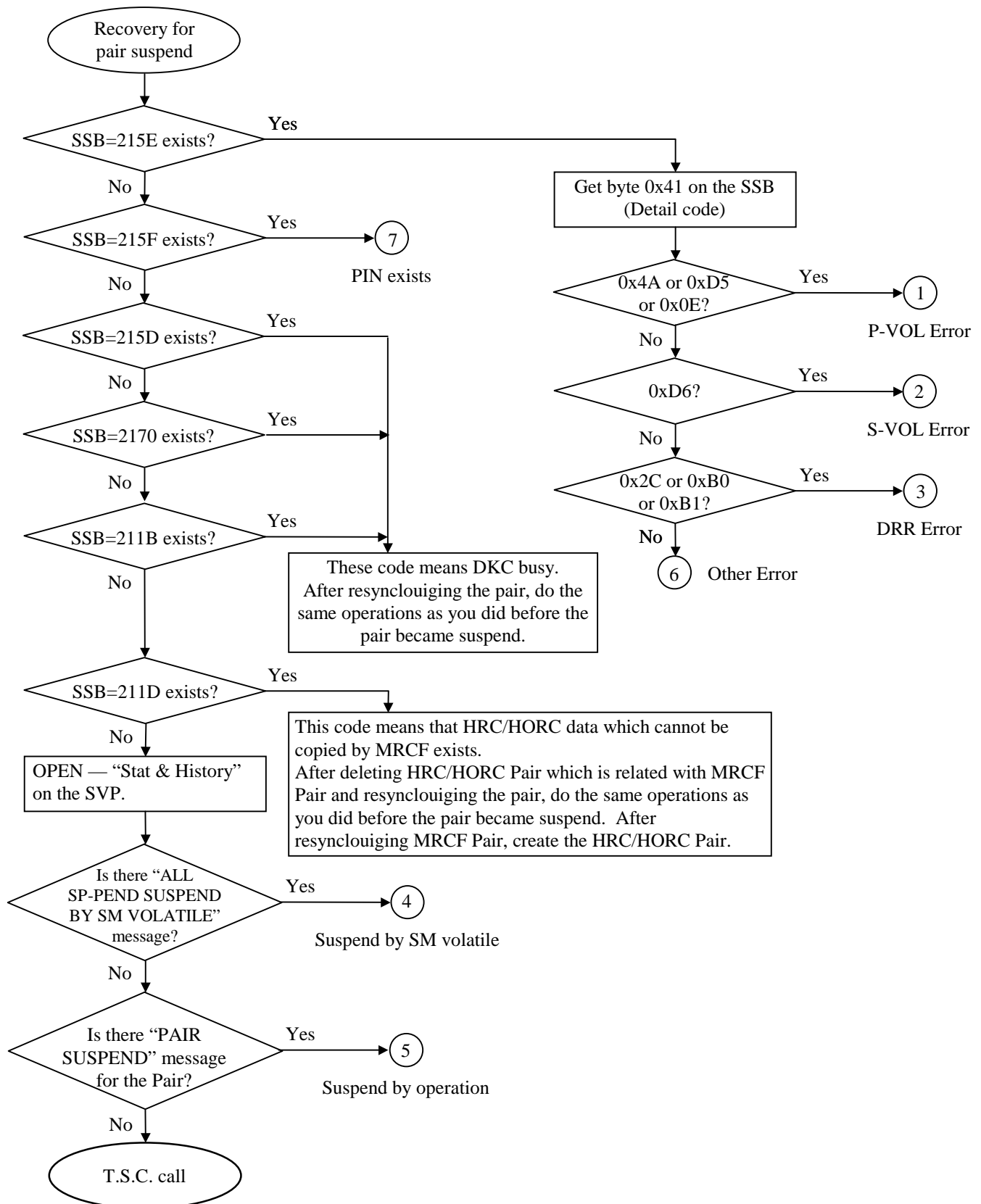
- 1) The above procedure and messages may depend on Solaris versions.
- 2) The device number cxtxdx used for Solaris is different from the DKC460 LDEV number.  
The device number cxtxdx should be obtained by DKC460 SCSI path configuration.
  - (a) Isolate the LDEV number of the LDEV containing the pinned track by SVP.
  - (b) Obtain the SCSI port number (CL1A through CL2R) and SCSI target ID and LUN that constructing the SCSI path from Solaris to the LDEV.
  - (c) Login to Solaris as superuser and execute the format command.  
Determine the device number cxtxdx by SCSI port number and SCSI target ID and LUN and the SCSI board installed into SUN to which the DKC460 SCSI port is connected.
- 3) Whole track range must be specified by the start and end LBAs.  
1 track has 96 blocks for OPEN-3 and OPEN-9.  
It happens that a pinned track is not correctly resetted if whole track range is not specified.
- 4) Data written on a pinned track must be recovered by a backup file because the analyze command writes dummy data on the pinned track.
- 5) The files or directories written with dummy data can not be determined because of structure of the UNIX file systems.  
The whole files and directories on the partition containing a pinned track must be recovered from backup file.

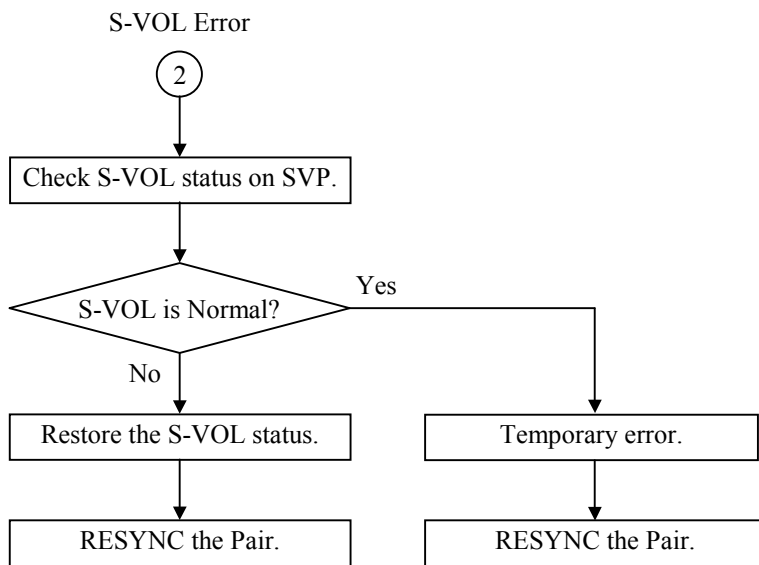
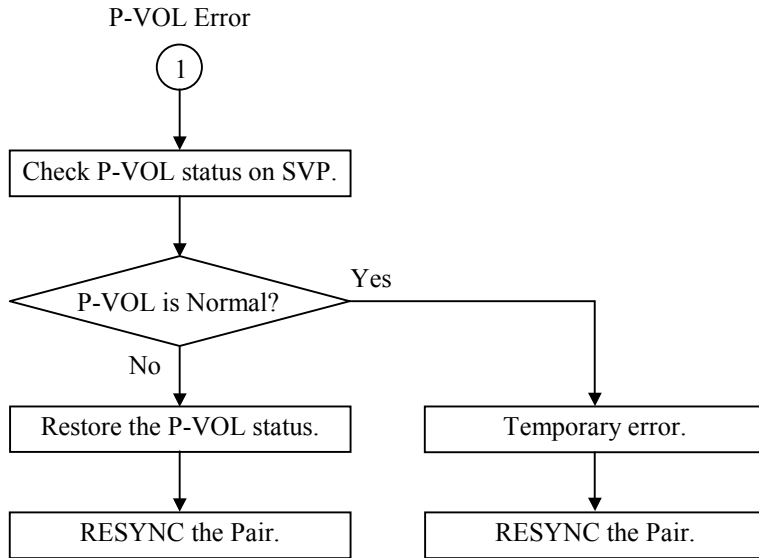
## 9 HMRCF & HOMRCF Error Recovery

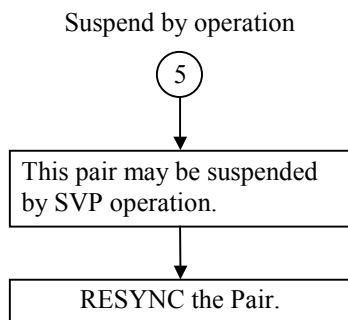
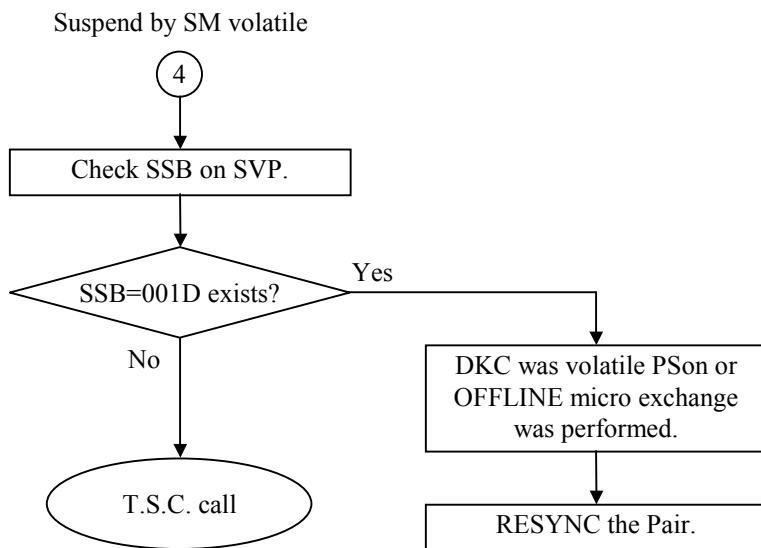
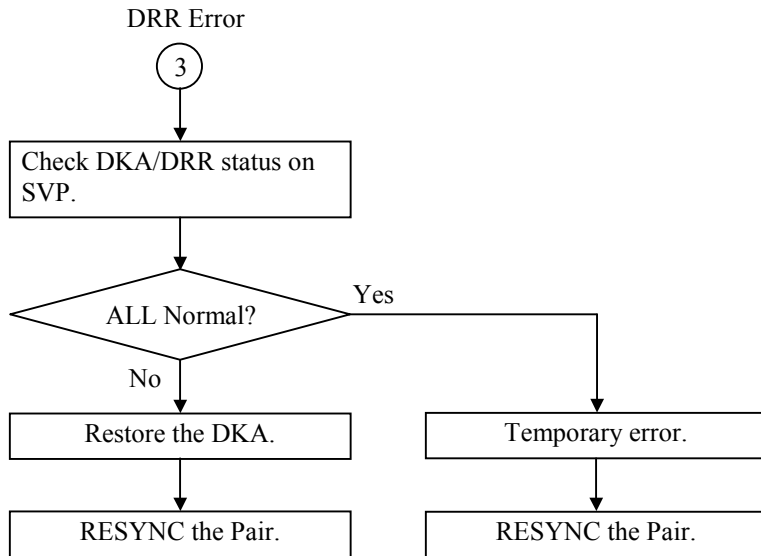
### 9.1 PIN Track recovery procedure for HMRCF

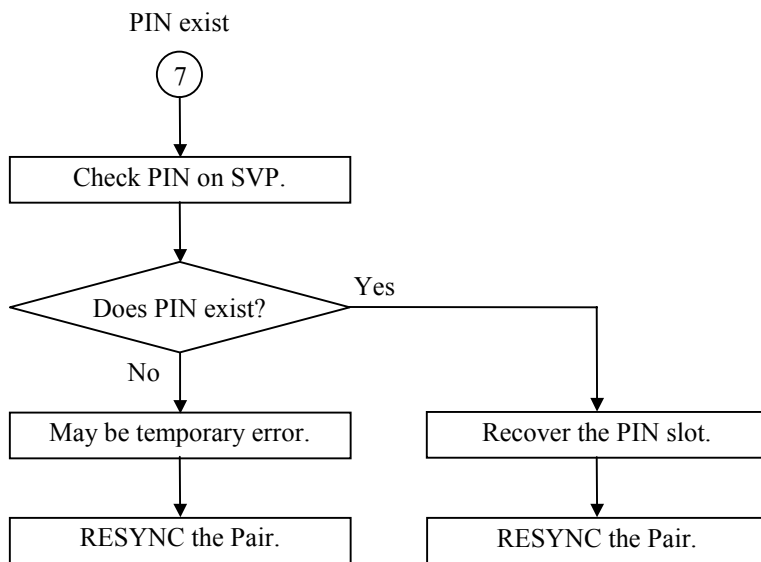
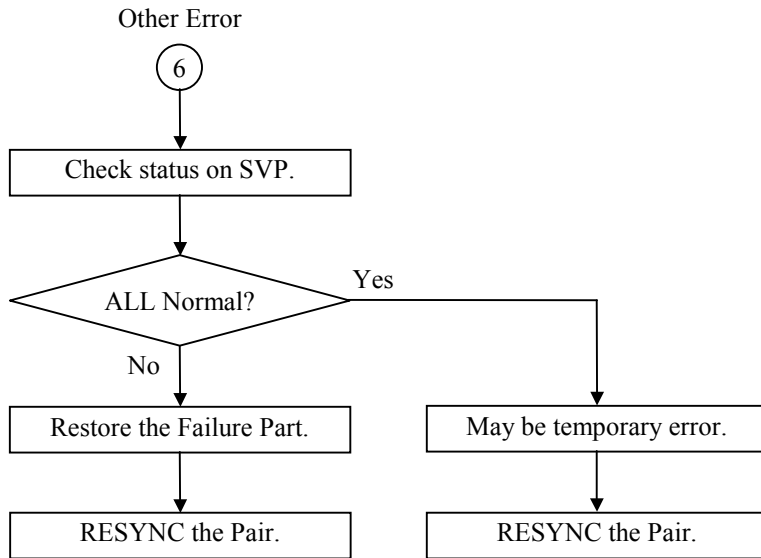


## 9.2 Recovery Procedure for Suspend Pair



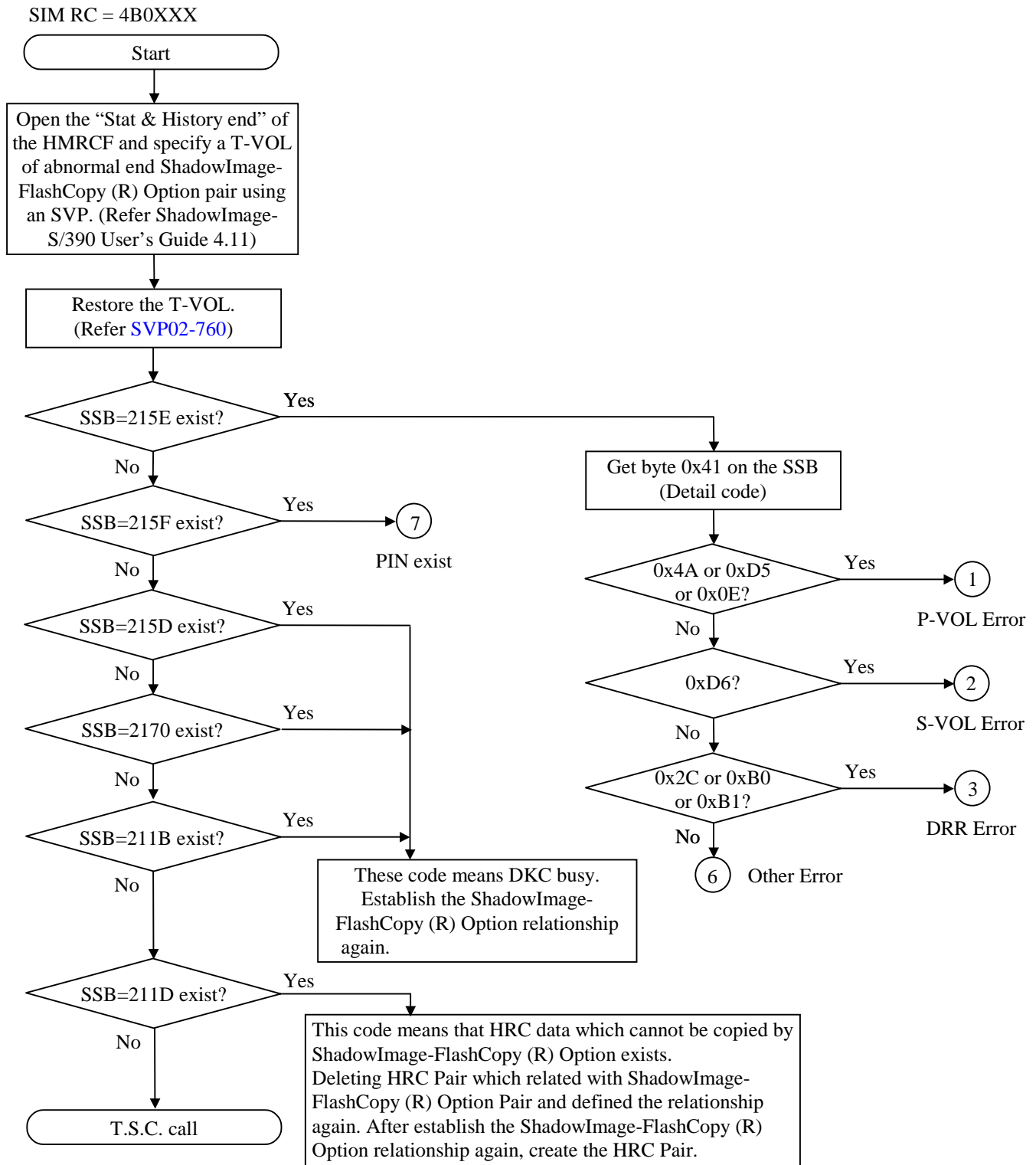




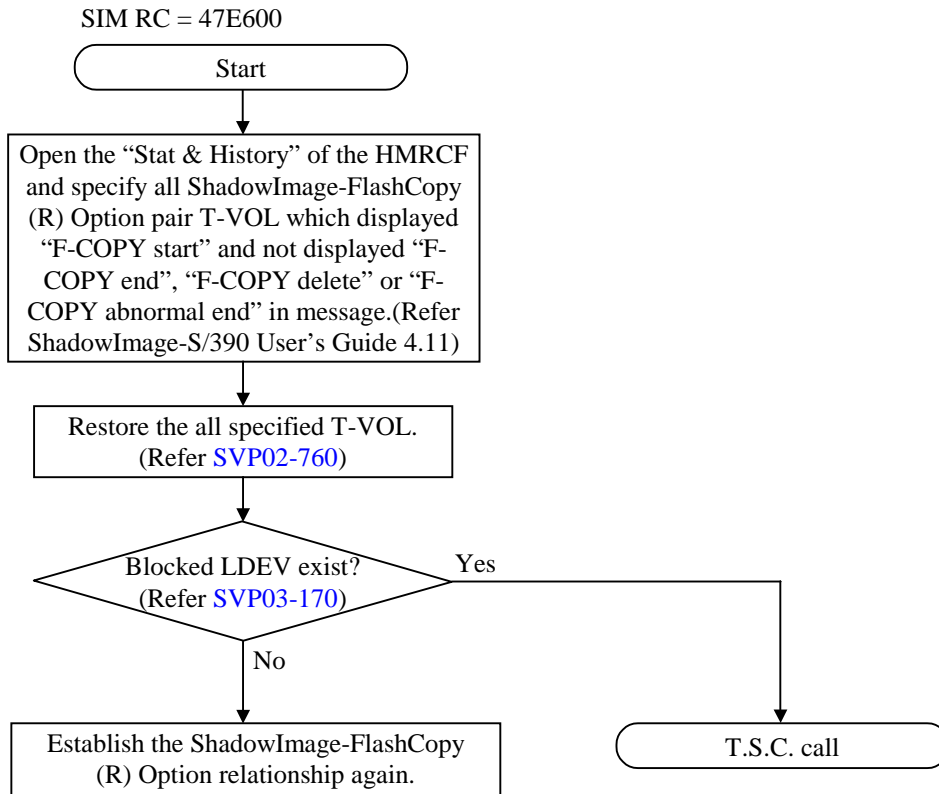


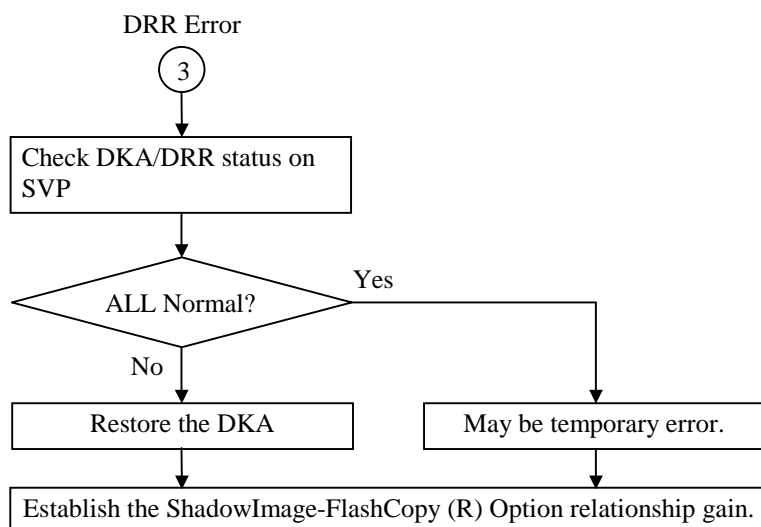
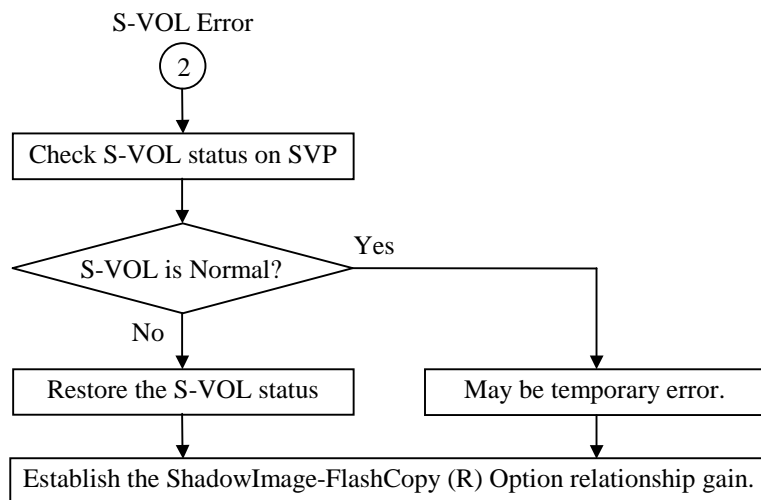
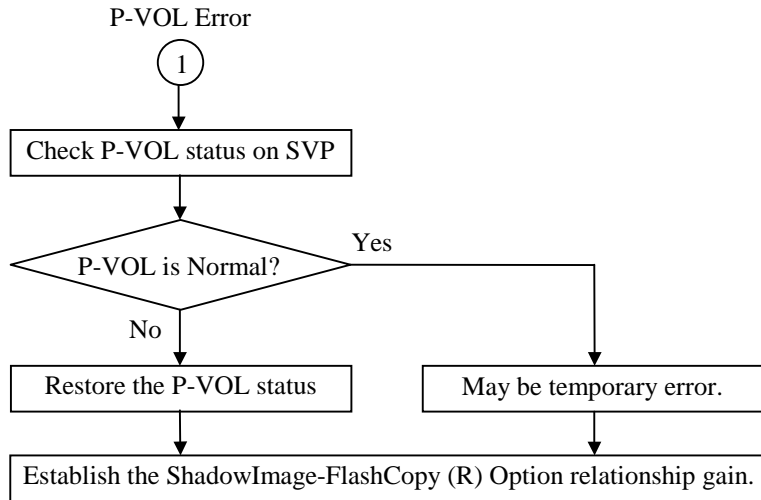
### 9.3 Procedure for recovery from ShadowImage-FlashCopy (R) Option failure (SIM=4B0XXX, 47E600)

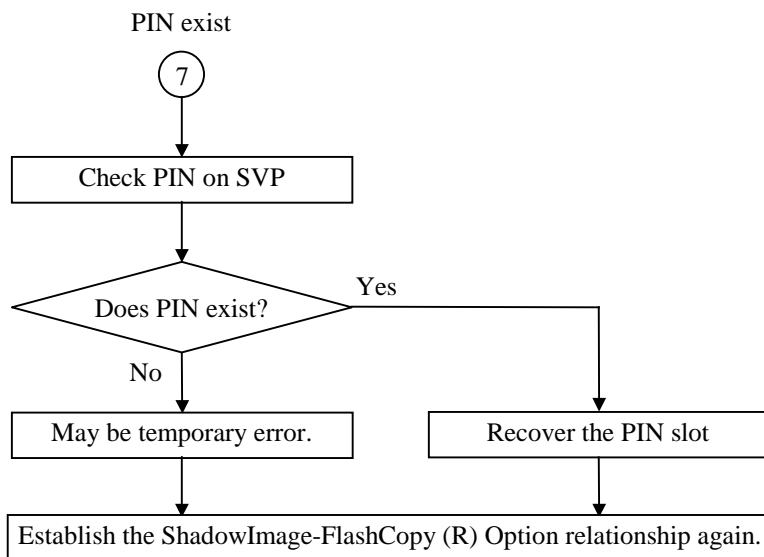
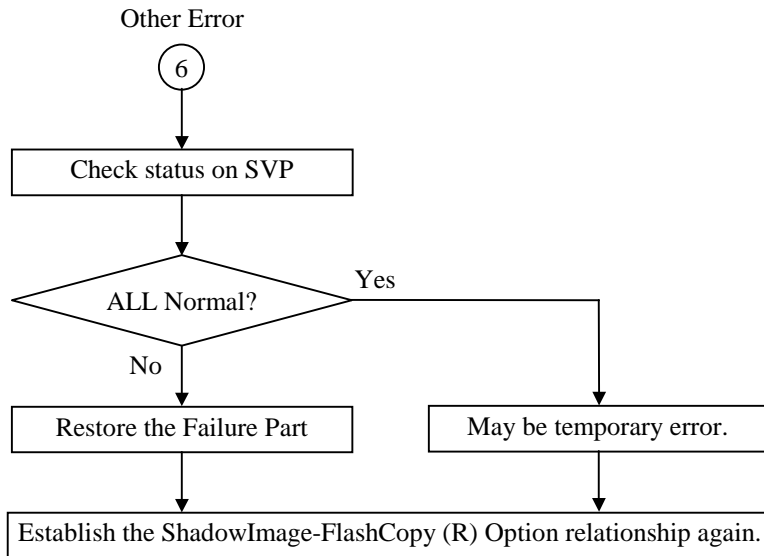
The procedure for recovery from an ShadowImage-FlashCopy (R) Option failure is explained below.





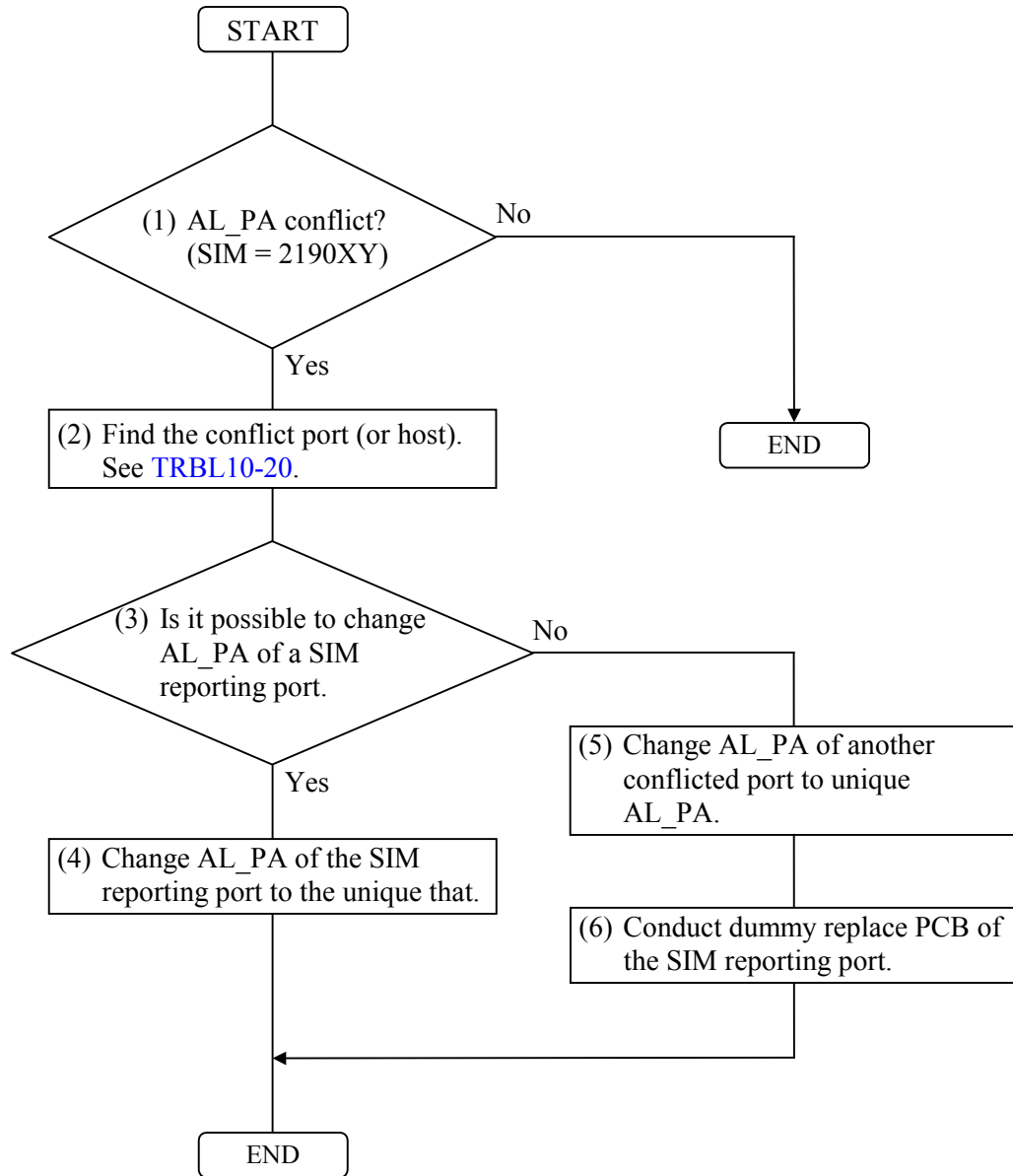






## 10 Recovery procedure of AL\_PA conflict (SIM = 2190XY)

The following flow chart is a recovery procedure in the case where AL\_PA of the nodes (CHT port, Host's Fibre channel port) which exist on the same loop overlaps. Before performing the following recovery procedure, the AL\_PA of a SIM reporting port is automatically changed into an other AL\_PA from previously given AL\_PA. But AL\_PA doesn't affect the values on SVP.



\* Whether AL\_PA of (3) is changed or not depends on the condition under operation.

**Content - SIM**

Log Number: 13  
 Date/Time: 2000/08/01 14:21:42  
 Reference Code: 219043  
 Error Section: FCP ERROR  
 Error Detail: AL\_PA VALUE CONFLICT  
 Error Location: CHA PCB# = 4, PORT# = 3  
 Alert Level: Service  
 Status: Initial

SIM Data:

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20	00000000	00008fe0	11100012	20437e0c												
30	6900f016	00002190	01000000	f1000000												

Action Code Possible Failure Parts Location  
 80000000 SEE MANUAL (TROUBLESHOOT SECTION)

**Refer**

Types

☐ SIM  
☒ SSB  
☐ Detail  
☐ LCP

OK  
 Cancel

**Content - SSB**

Log Number: 14  
 Date/Time: 2000/08/01 14:21:42  
 Error Code: b463  
 Micro Processor: CHP42-2V  
 CU-LDEV:  
 PDEV:  
 F/M 9f

CTRL Data:

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	6408010e	152a9f52	63b40000	00822800												
10	00000000	00000e00	00000d00	00000004												
20	00000000	0000009f	b4630000	00000000												
30	00000000	4004ff9f	00000000	00000000												
40	0000b6a0	00000001	000000ef	00000001												
50	00000000	00000000	00000000	00000000												
60	00000000	00000000	00000000	00000000												
70	00000000	00000000	00000000	00000000												

SSB Data:

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20	00000000	0000009f	b4630000	00000000												
30	00000000	4004ff9f	00000000	00000000												
40	0000b6a0	00000001	000000ef	00000001												
50	00000000	00000000	00000000	00000000												
60	00000000	00000000	00000000	00000000												
70	00000000	00000000	00000000	00000000												

Internal Data:

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
40	0000b6a0	00000001	000000ef	00000001												
50	00000000	00000000	00000000	00000000												
60	00000000	00000000	00000000	00000000												
70	00000000	00000000	00000000	00000000												

Action Code Possible Failure Parts Location

Value of AL\_PA after conflict

Value of AL\_PA before conflict

1. Please refer to the SSB corresponded to the SIM (=2190XY) to specify values of AL\_PA before/after conflict.
2. When the value of AL\_PA before conflict is determined, please find RAID's port or host HBA's port whose AL\_PA conflicts with this port.

## 11 HIHSM Error Recovery

When it lapses into the state where a system does not expect, volume move processing of HIHSM sometimes carries out an unusual end. In this case, the state of S-VOL and T-VOL which was during movement is not changing from the state before movement. So, there is no necessity of daring carry out recovery to resume VOL move processing. Please resume processing after checking states, such as a hard part, with reference to SSB/SIM.

Please carry out recovery procedure with reference to the following flowchart.

Table 11.1-1 HIHSM SIM REF.CODE

SIM REF. CODE	meaning	comment
47FYXX	HIHSM VOL Move unusual end	XX: T-VOL No. Y: CU No.

## 11.1 HIHSM Error Recovery Flowchart

