

SSBLOG SECTION

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SSBLOG05-90	• Byte27 = 81 & Byte34:b4-7 = 0 (Shared Memory Failure)
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SSBLOG05-110	(1) Byte34, Bit4 to Bit7 = 0: Shared Memory Failure
SSBLOG05-120	• Byte27 = 81 Byte40-7F(MICRO ERROR, MPA INTERNAL ERROR)(Byte28[bit2-3]=x'00')
SSBLOG05-160	• Byte27 = 81 Byte40-7F(SM ERROR)(Byte28[bit2-3]=x'01')
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SSBLOG05-291	• Byte27 = 88 & Byte34:b4-7=A & Byte28 = 01 (LCP Failure: CHK-2/ADP I/F Error)
SSBLOG05-293	• Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 02 (LCP Failure: Link Receive Error)
SSBLOG05-295	• Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0C (LCP Failure: Long Write Data)
SSBLOG05-297	• Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0E (LCP Failure: Timeout)
SSBLOG05-299	• Byte27 = 88 & Byte34:b4-7 = A & Byte28 ≠ 01/02/0C/0E (LCP Failure: Others)

SSBLOG05-300	• Byte27 = 89 & Byte0D Bit2 = 0 (ESCON/FICON CHA CHK2)
SSBLOG05-310	• Byte27 = 89 & Byte0D Bit2 = 0 (FIBRE CHA CHK2)
SSBLOG05-320	Byte27 = 89 (CHA CHK2) Byte28-31 Detail
SSBLOG05-320	(1) Byte28 = 00
SSBLOG05-330	(2) Byte28 = 01
SSBLOG05-340	(3) Byte28 = 02
SSBLOG05-350	(4) Byte28 = 1F
SSBLOG05-360	(5) Byte28 = 20
SSBLOG05-370	(6) Byte28 = 3F
SSBLOG05-380	(7) Byte28 = 60 ~ 7F
SSBLOG05-390	(8) Byte28 = 80 ~ 87
SSBLOG05-400	(9) Byte28 = 90 ~ 97
SSBLOG05-410	Byte27 = 89 (CHA CHK2) Byte40-7F Detail
SSBLOG05-410	(1) Byte28 = 00/1F and Byte32 = 58/59
SSBLOG05-450	(2) Byte28 = 00/01/02/1F/20/3F and Byte32 ≠ 58/59 and Byte42 = 58/59
SSBLOG05-460	(3) Byte28 = 00/01/02/1F/20/3F and Byte32 ≠ 58/59 and Byte42 ≠ 58/59
SSBLOG05-470	(4) Byte28 = 00/1F and Byte32 = 58/59
SSBLOG05-510	(5) Byte28 = 60 or 62 or 68 or 6a
SSBLOG05-550	(6) Byte28 = 61 or 69
SSBLOG05-590	(7) Byte28 = 63 or 6b
SSBLOG05-630	(8) Byte28 = 64 ~ 67 or 6c ~ 6f
SSBLOG05-670	(9) Byte28 = 81
SSBLOG05-710	(10) Byte28 = 80 or 82 or 84 or 86
SSBLOG05-750	(11) Byte28 = 90 or 93 or 95 or 97
SSBLOG05-790	(12) Byte28 = 91
SSBLOG05-830	(13) Byte28 = 92 or 94 or 96
SSBLOG05-861	• Byte27 = 8A (FCA CHK2)
SSBLOG05-861A	Byte27 = 8A (FCA CHK2) Byte28-31 Detail
SSBLOG05-862	Byte27 = 8A (FCA CHK2) Byte40-7F Detail
SSBLOG05-862	(1) Byte28 = 10
SSBLOG05-863	(2) Byte28 = 11
SSBLOG05-864	(3) Byte28 = 20
SSBLOG05-865	(4) Byte28 = 21
SSBLOG05-866	(5) Byte28 = 30
SSBLOG05-867	(6) Byte28 = 40

SSBLOG05-870	• Byte27 = 8B (DRR CHK2)
SSBLOG05-880	Byte27 = 8B (DRR CHK2) Byte28-31 Detail
SSBLOG05-880	(1) Byte28 = 10 or 13 or 21 or 23
SSBLOG05-890	(2) Byte28 = 11 or 20 or 22 or 40
SSBLOG05-900	(3) Byte28 = 21
SSBLOG05-910	(4) Byte28 = 30
SSBLOG05-920	(5) Byte28 = 31
SSBLOG05-930	(6) Byte28 = 32
SSBLOG05-940	(7) Byte28 = 36
SSBLOG05-950	(8) Byte28 = 37
SSBLOG05-960	(9) Byte28 = 38
SSBLOG05-970	(10) Byte28 = A0
SSBLOG05-980	Byte27 = 8B (DRR CHK2) Byte40-7F Detail
SSBLOG05-980	(1) Byte28 ≠ 30, 31, 36, 37
SSBLOG05-1020	(2) Byte28 = 30
SSBLOG05-1060	(3) Byte28 = 36
SSBLOG05-1100	(4) Byte28 = 37
SSBLOG05-1140	• Byte27 = 8C (SVP Failure:SVP (PC))
SSBLOG05-1160	Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail
SSBLOG05-1160	(1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure / Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error / SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over / SVP change refusal / Ip address setting error / Subnet mask setting error / Virus detection error / SVP environment error / Situation observation error / Dummy PS/Off starting (normal)
SSBLOG05-1180	(2) SSVP failure
SSBLOG05-1190	(3) CUDG detected error
SSBLOG05-1250	(4) BOOT detected error

SSBLOG05-1260	• Byte27 = 8D (Power Failure)
SSBLOG05-1280	• Byte27 = 8D (DKU/HDU Power Failure) Hard information
SSBLOG05-1310	• Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A)
SSBLOG05-1370	• Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B)
SSBLOG05-1440	• Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B)
SSBLOG05-1510	• Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B)
SSBLOG05-1570	• Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B)
SSBLOG05-1630	• Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B)
SSBLOG05-1760	• Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B)
SSBLOG05-1820	• Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B)
SSBLOG05-1880	• Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B)
SSBLOG05-1940	• Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B)
SSBLOG05-2010	• Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B)
SSBLOG05-2080	• Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B)
SSBLOG05-2140	• Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B)
SSBLOG05-2210	• Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B)
SSBLOG05-2300	• Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B)
SSBLOG05-2370	• Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B)
SSBLOG05-2440	• Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B)
SSBLOG05-2500	• Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B)
SSBLOG05-2570	• Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B)
SSBLOG05-2621	• Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)
SSBLOG05-2630	• Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset)
SSBLOG05-2633	• Byte27 = 8E & Byte34:bit4-7=3 (Wait SENSE Timeout)
SSBLOG05-2635	• Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset)
SSBLOG05-2637	• Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset)
SSBLOG05-2639	• Byte27 = 8E & Byte34:bit4-7=7 (LCP Internal Reset)
SSBLOG05-2640	• Byte27 = 8E & Byte34:bit4-7=8 (Force Reset)
SSBLOG05-2650	• Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset)
SSBLOG05-2670	• Byte27 = 8F (Logical Inconsistency)
SSBLOG05-2690	• Byte27 = 9F (Trace data/Log data)
SSBLOG05-2701	• Byte27 = E0 & Byte08 = CE (HUR Service SIM)
SSBLOG05-2710	• Byte27 = EC (Environment Monitor Detected DKC Failure)
SSBLOG05-2740	• Byte27 = EE (Environment Monitor Detected DKU Failure)
SSBLOG05-2761	• Byte27 = F0 (Operation Terminated)
SSBLOG05-2763	• Byte27 = F1 (Micro-program Detected Cache Failure)
SSBLOG05-2770	• Byte27 = F2 (CACHE CHK2)
SSBLOG05-2780	Byte27 = F2 (CACHE CHK2) Byte28-31 Detail
SSBLOG05-2820A	• Byte27 = F5 (Remote Copy Paths Removed/Restored)
SSBLOG05-2821	• Byte27 = F6 (CFW Access not Authorized)
SSBLOG05-2823	• Byte27 = FA (NVS Terminated)
SSBLOG05-2825	• Byte27 = FB (HRC/HODM Pair suspend)

SSBLOG05-2830	• Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support)
SSBLOG05-2850	Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information
SSBLOG05-2850	(1) Byte28 = 01 : DXBF Correctable error
SSBLOG05-2860	(2) Byte28 = 02 : ECMA Warning
SSBLOG05-2870	(3) Byte28 = 04 : SMA Warning
SSBLOG05-2880	(4) Byte28 = 06/07/08/09 : Three SM DISABLE lines inconsistent
SSBLOG05-2890	(5) Byte28 = 10 : SMA slave error
SSBLOG05-2900	(6) Byte28 = 11 : MPA slave error
SSBLOG05-2910	(7) Byte28 = 12 : SMA SCINT ERROR
SSBLOG05-2920	(8) Byte28 = 20 : FSW HBC BUS test
SSBLOG05-2930	(9) Byte28 = 40 : Abnormal DC Voltage CTL Warning
SSBLOG05-2940	(10) Byte28 = 41 : Abnormal CEMODE Warning
SSBLOG05-2950	(11) Byte28 = 80 : CHUB correctable error
SSBLOG05-2960	(12) Byte28 = 81 : DHUB DDR warning
SSBLOG05-2970	(13) Byte28 = 82 : FCA warning
SSBLOG05-2980	(14) Byte28 = 83 : DCMA warning
SSBLOG05-2990	(15) Byte28 = 84 : CARB memory warning
SSBLOG05-3000	(16) Byte28 = 85 : PCI warning
SSBLOG05-3010	(17) Byte28 = B0 : CARB warning
SSBLOG05-3020	Byte 40-7F Common detail information : Type1
SSBLOG05-3060	Byte 40-7F Common detail information : Type2
SSBLOG05-3100	Byte 40-7F Common detail information : Type3
SSBLOG05-3140	Byte 40-7F Common detail information : Type4
SSBLOG05-3180	Byte 40-7F Common detail information : Type5
SSBLOG05-3220	Byte 40-7F Common detail information : Type6
SSBLOG05-3260	Byte 40-7F Common detail information : Type7
SSBLOG05-3300	Byte 40-7F Common detail information : Type8
SSBLOG05-3340	Byte 40-7F Common detail information : Type9
SSBLOG05-3380	Byte 40-7F Common detail information : Type10
SSBLOG05-3420	Byte 40-7F Common detail information : Type11
SSBLOG05-3460	Byte 40-7F Common detail information : Type12
SSBLOG05-3500	Byte 40-7F Common detail information : Type13
SSBLOG05-3540	Byte 40-7F Common detail information : Type14
SSBLOG05-3580	Byte 40-7F Common detail information : Type15
SSBLOG05-3620	Byte 40-7F Common detail information : Type16
SSBLOG05-3660	Byte 40-7F Common detail information : Type17

SSBLOG05-3700	• Byte0D : Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception)
SSBLOG05-3720	• Byte0D : Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception: Incomplete Domain)
SSBLOG05-3740	• Byte0D : Bit1 = 1 & Byte36-37 = 4XXX (Data exception)
SSBLOG05-3770	• Byte36 = 6F (Subsystem Information)
SSBLOG05-3790	• Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 0 (FPC Detected Failure : DSP)
SSBLOG05-3810	• Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 1 (FPC Detected Failure : FBP)
SSBLOG05-3840	• Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 0 (Drive Detected Failure : DSP)
SSBLOG05-3860	• Byte36 : Bit0-3 = E & Byte2C : Bit0-7 ≠ 5X/6X (Drive Detected Failure : DSP)
SSBLOG05-3880	• Byte36 = E0 & Byte2C = 5X/6X & Byte20 : Bit3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited)
SSBLOG05-3900	• Byte36-37 = E210 & Byte 2C = 5X/6X & Byte20 : Bit1 = 1 (Intervention requested : LDEV not ready)
SSBLOG05-3920	• Byte26 : Bit4-7 = F & Byte3C = F1 (DKC SIM)
SSBLOG05-3950	• Byte26 : Bit4-7 = F & Byte3C = F2 (CACHE SIM)
SSBLOG05-3980	• Byte26 : Bit4-7 = F & Byte3C = FE (Device SIM)
SSBLOG05-4000	• Byte26 : Bit4-7 = F & Byte3C = FF (Media SIM)

1. Introduction

The internal SSB (SSB Log) information is presented as either 24-byte compatibility SSB format or ECKD 32-byte SSB format. The 24-byte SSB is used for DKC and the 32-byte SSB is used for DKU.

The internal SSB is divided into the following sections:

- Internal SSB common information
This information is used for SSB management, and same for all formats.
- External SSB information presented to host.
This is 32 bytes sense information presented to host as SSB.
- Format-dependent information
This is auxiliary information for error analysis.

See Fig. 1-1 and Fig. 1-2.

Note: The byte offset in SSB logs is described in hexadecimal number.

This section is corresponding to SSB log on SVP.

If you want to know about SSB which was reported to host, you should see SSB section.

	SSB Pointer (4 bytes)		← Out of description in the manual	
	Reserved (28 bytes)		← Out of description in the manual	
00 05	Time stamp		Error handling support	Internal SSB common information
06	Format Message			
07	MP number			
08	System error code		Error detection source	
09	Reserved			
0A	JCB ID			
0B	REQ ID		Error handling support	
0C	SSB type			
0D	Valid flag			
0E	SSB type		Error detection source	
10	LDEV number			
11	CDEV number			
12	RDEV number		Error handling support	
13	JCB number			
14	Internal SSB log number			
15	Related failure log number		Error detection source	
16	Related SIM log number			
17	Related SSB log number			
18	Reserved area		← Error detection source	
19	SCB (SSB control block)			
20 26	Byte 0 ~ 6 of 24 byte formatted SSB reported to host			
27	Format	Message		
28 37	Byte 8 ~ 23 of 24 byte formatted SSB reported to host		Error detection source	
38 3F	Byte 24 ~ 31 of 24 byte formatted SSB reported to host			
40 7F	Free area			

Same as a SSB reported to host

Format-dependent information of internal SSB

Fig. 1-1 Outline of 24-byte Format Internal SSB

	SSB Pointer (4 bytes)	← Out of description in the manual		
	Reserved (28 bytes)	← Out of description in the manual		
00 05	Time stamp	Error handling support	Internal SSB common information	
06	Format Message			
07	MP number			
08	System error code			
09		Error detection source		
0A	Reserved			
0B	JCB ID			
0C	REQ ID			
0D	SSB type	Error handling support		
0E	Valid flag			
0F	SSB type			
10	LDEV number			
11		Error detection source		
12	CDEV number			
13	RDEV number			
14	JCB number			
15		Error detection source		
16	Internal SSB log number			
17				
18	Related failure log number			
19		Error detection source		
1A	Related SIM log number			
1B				
1C	Related SSB log number			
1D		Error detection source		
1E	Reserved area			
1F	SCB (SSB control block)			
20 25	Byte 0 ~ 6 of 32 byte formatted SSB reported to host	Command control	Same as a SSB reported to host	
26	Format			
27 35	Byte 7 ~ 23 of 32 byte formatted SSB reported to host	Error detection source		
36	Exception Class			
37	Exception Code	Command control		
38 3F	Byte 24 ~ 31 32 byte formatted of SSB reported to host			
40 7F	Free area	Error detection source	Format-dependent information of internal SSB	

Fig. 1-2 Outline of 32-byte Format Internal SSB

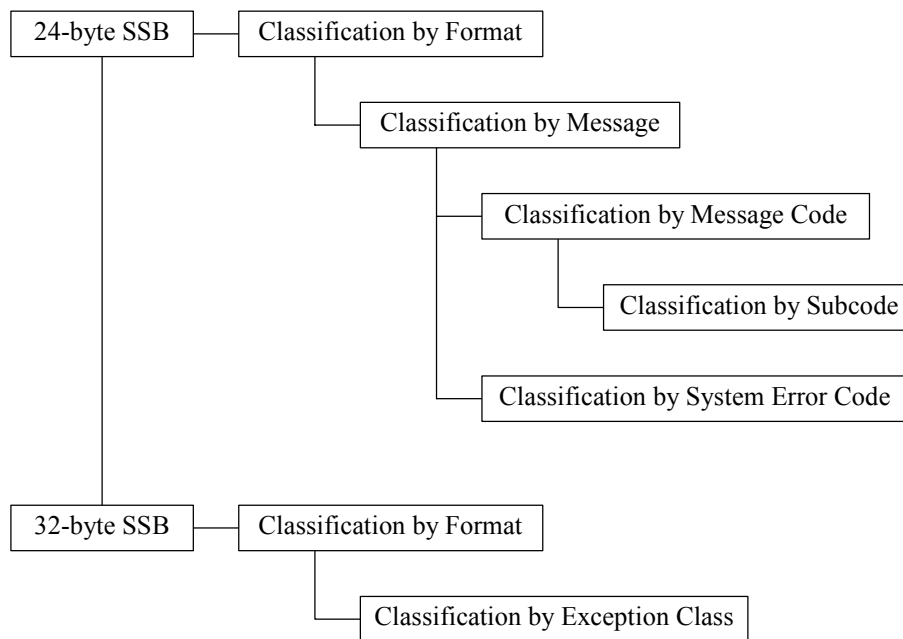
SSB Pointer (4 bytes)		← Out of description in the manual
Reserved (28 bytes)		← Out of description in the manual
00 05	Time stamp	Error handling support
06	Format Message	
07	MP number	
08 09	System error code	
0A	Reserved	Error detection source
0B	JCB ID	
0C	REQ ID	
0D	SSB type	
0E	Valid flag	Error handling support
0F	SSB type	
10 11	LDEV number	
12	CDEV number	
13	RDEV number	Error detection source
14 15	JCB number	
16 17	Internal SSB log number	
18 19	Related failure log number	
1A 1B	Related SIM log number	Error detection source
1C	Related SSB log number	
1D	Reserved area	
1E	Reserved area	
1F	SCB (SSB control block)	← Error detection source
20 3F	Byte 0 to 31 of SCSI host reported format SSB	Command control
40 7F	Byte 32 to 95 of SCSI host reported format SSB	Error detection source

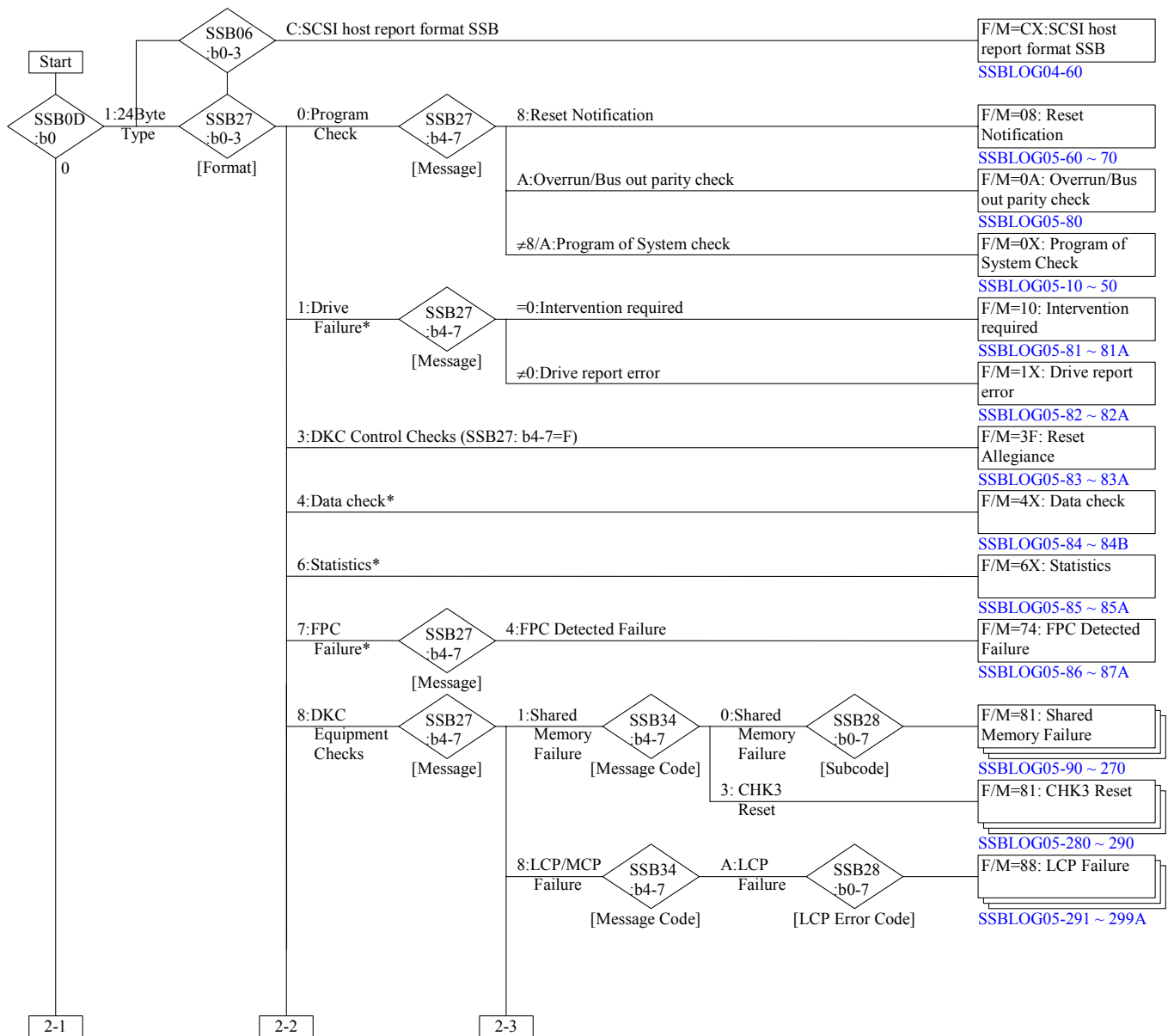
Internal SSB common information

Fig. 1-3 Overview of SCSI Host Report Format Internal SSB

2. Classification of Internal SSB

The internal SSB is classified as the following figure, and Fig. 2-1 for detail.





*: For DKU86I (IBM 3380 emulation mode) only.

Fig. 2-1 Search method for SSB tables (1/5)

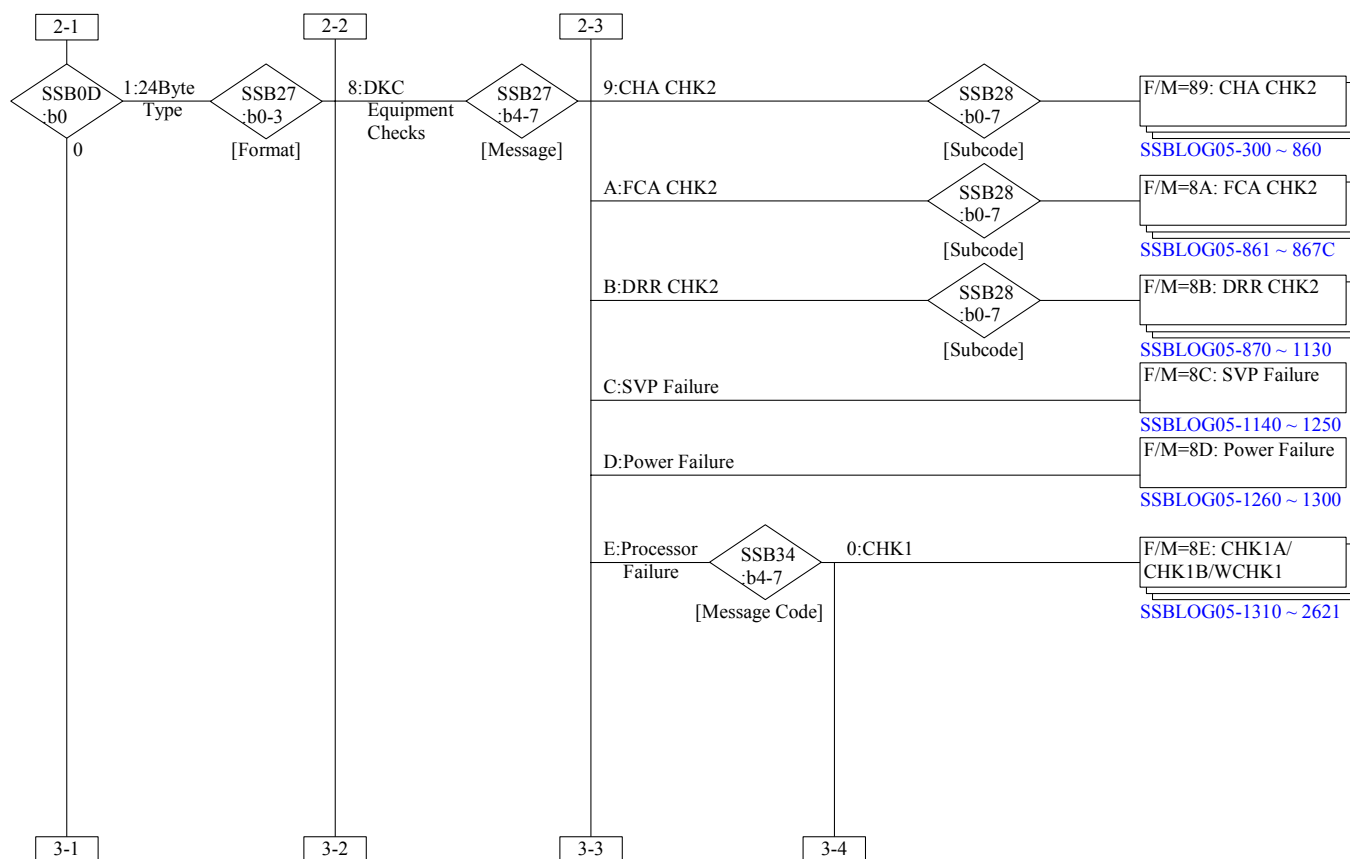


Fig. 2-1 Search method for SSB tables (2/5)

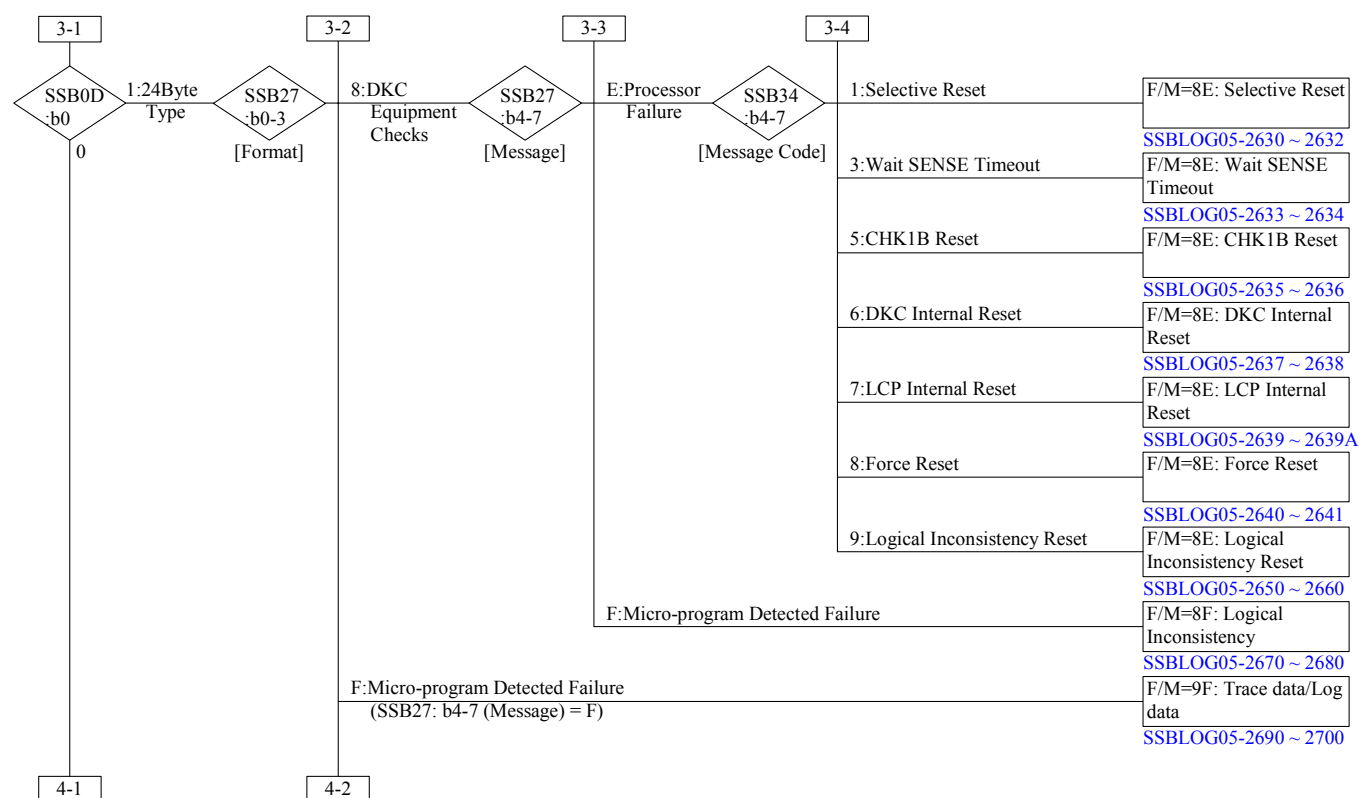


Fig. 2-1 Search method for SSB tables (3/5)

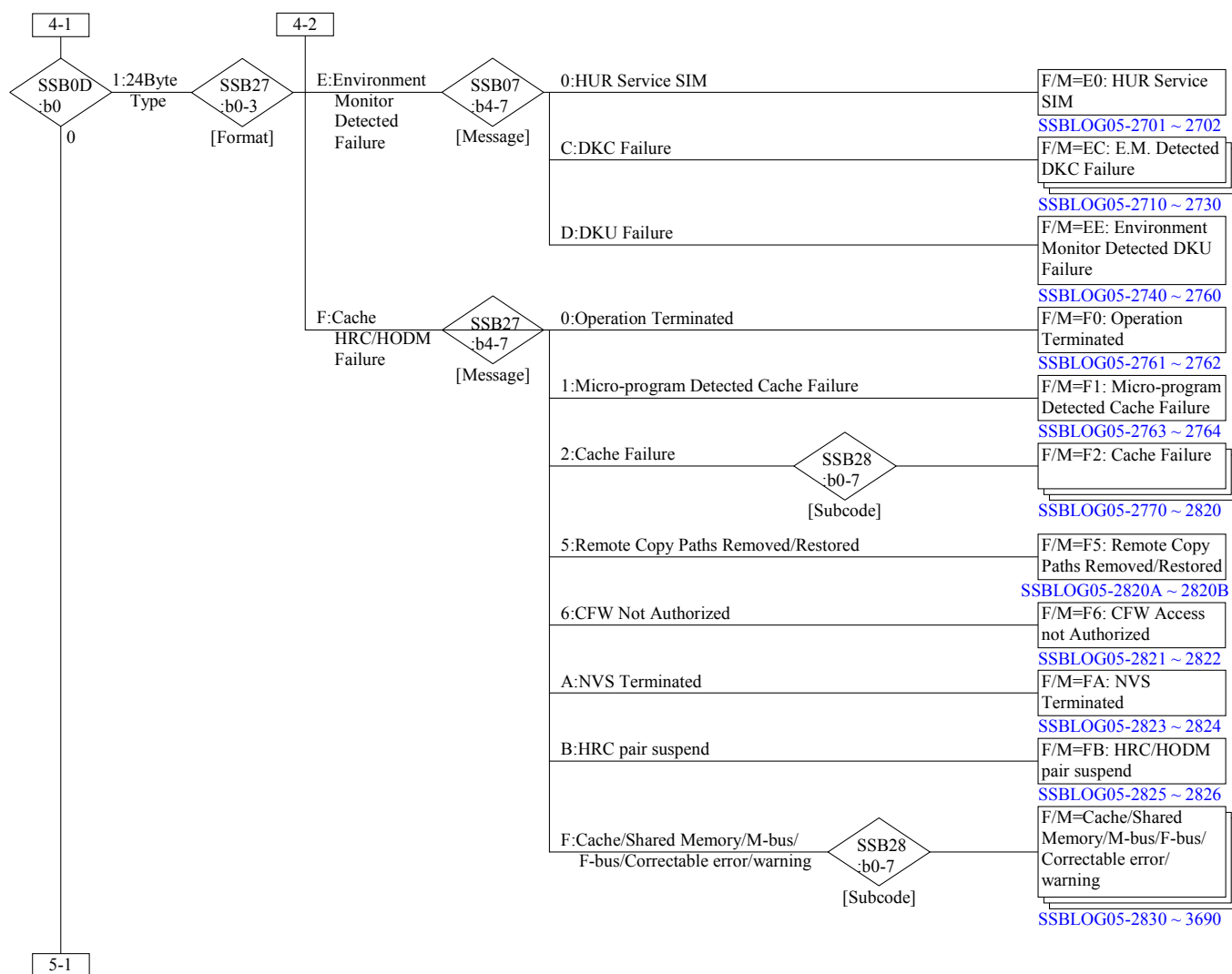


Fig. 2-1 Search method for SSB tables (4/5)

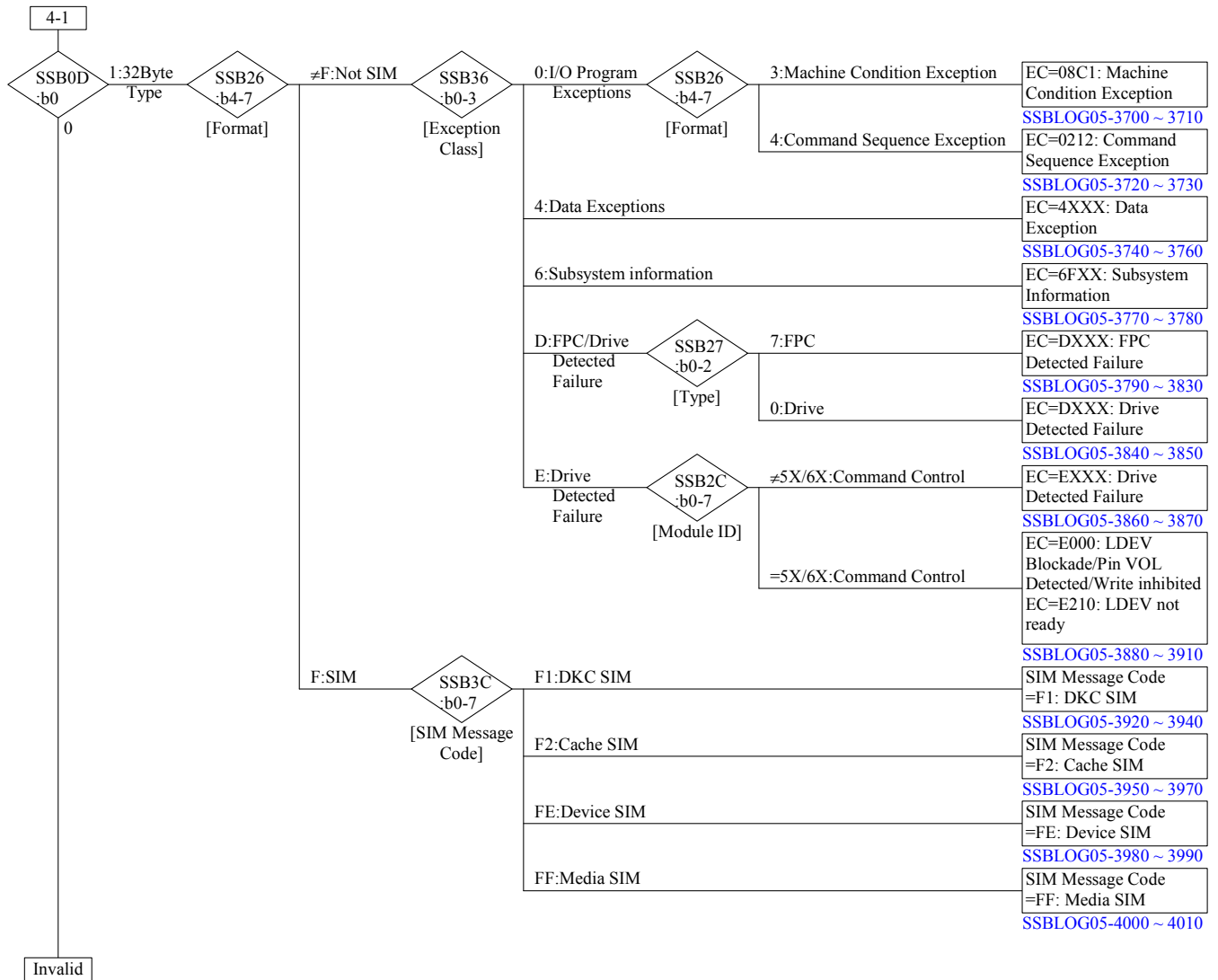


Fig. 2-1 Search method for SSB tables (5/5)

3. Common information description

	0	1	2	3	4	5	6	7						
00	Time Stamp													
01														
02														
03														
04														
05														
06	Format Message													
07	Processor ID													
08	System Error Code													
09														
0A	Reserved													
0B	JCB ID													
0C	REQ ID													
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host RSP	SVP RSP	Force Log						
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved						
0F	SEMI-SYNC	RCU SIM	Reserved											
10	LDEV number													
11														
12	CDEV#													
13	RDEV#													
14	JCB number													
15														
16	Internal SSB log number													
17														
18	Related failure log number													
19														
1A	Related SIM log number													
1B														
1C	Related SSB log number													
1D														
1E	Reserved													
1F	SCB (SSB Control Block)													
20														
	Host SSB Information													
3F														
40														
	Internal SSB Format-Dependent Information													
7F														

Common Information (1/2)

Byte	Bit	Meaning	Setting	Remarks
00 - 05		Time stamp The time error occurred	Error handling support	
06		Format Message	Error handling support	
07		Processor number (Internal ID) x'00' - x'7F' : CHA0x00 – 0x7F x'80' - x'BF' : DKA0x80 – 0xBF	Error handling support	
08 - 09		System error code	Error detection source	
	0 - 7	Module ID		
	8 - 15	Routine ID		
0A		Reserved	Error handling support	
0B		JCB ID	Error detection source	
0C		REQ ID	Error detection source	
0D		SSB type	Error handling support	
	0	24Byte type flag 0: Invalid 1: 24Byte type SSB		
	1	32Byte type flag 0: Invalid 1: 32Byte type SSB		
	2	Flag for SCSI host processor detected the error 0: SSB detected by CHA and DKA 1: SSB detected by CHS		
	3	Remote copy flag 0: Invalid 1: Remote copy valid		
	4	Separately stored log flag 0: General log 1: Separately stored log		
	5	Host report flag 0: Pending 1: Reported		
	6	SVP report flag 0: Pending 1: Reported		
	7	Forcible log flag 0: Normal 1: Forcibly ejected log		

Common Information (2/2)

Byte	Bit	Meaning	Setting	Remarks
0E		Valid flag	Error handling support	
	0	LDEV number valid or invalid 0: Invalid 1: Valid		
	1	PDEV number valid or invalid 0: Invalid 1: Valid		
	2	SSB number valid or invalid 0: Invalid 1: Valid		
	3	Related failure log number valid or invalid 0: Invalid 1: Valid		
	4	Related SIM log number valid or invalid 0: Invalid 1: Valid		
	5	Related SSB log number valid or invalid 0: Invalid 1: Valid		
	6	PK information valid or invalid 0: Invalid 1: Valid	Error handling source	
	7	Reserved	Error handling support	
0F		SSB type	Error handling support	
	0	HRC type SSB flag 0: Sync HRC SSB 1: Semi Sync HRC SSB		
	1	RCU SIM flag 0: Invalid 1: RUC SIM flag		
	2 - 7	Reserved		
10 - 11		LDEV number	Error handling support	
12		CDEV number	Error handling support	
13		RDEV number	Error handling support	
14 - 15		JCB number		
16 - 17		Internal SSB log number		
18 - 19		Related Failure log number		
1A - 1B		Related SIM log number		
1C - 1D		Related SSB log number		
1E		Reserved area		
1F		SSB control block Use when assemble BASIC SSB of Host SSB. See "SSB Key Code" table.		

SSB Key Code (1/4)

Code	Basic SSB	Meaning	Remarks
00	000000	Dummy SSB	
01	800000	Command reject (A program error or invalid link between BAD and ALT)	
02	400000	Intervention required (An addressed device is offline or not connect)	
03	200000	Bus out check (Channel bus out parity error)	
04	100000	Equipment check (Equipment check in subsystem)	
05	080000	Data check (Uncorrectable: retry failed or retry inhibited)	
06	040000	Overrun (Command retry failed when service overrun occurred)	
07	004000	Invalid track format (Track capacity over or track format error)	
08	002000	End of cylinder (A multi track operation outside LOCATE domain)	
09	000800	No record found	
0A	000400	File protected (A seek or multi track operation violates the file mask or extent)	
0B	100200	Equipment check and write inhibited (A write command on the write inhibited path by a DIAG CTL command)	
0C	080040	Data check and correctable (The retry succeeded for data check in PCI fetch mode)	
0D	000020	First log mode error	Not used
0E	800400	Command rejected and file protected (A multi track operation outside primary track)	
0F	800200	Command reject and write inhibited	Not used

SSB Key Code (2/4)

Code	Basic SSB	Meaning	Remarks
10	108000	Equipment check and permanent error (A retry failed for the equipment check)	
11	088000	Data check and permanent error (A retry failed for the data check)	
12	009010	SIM (DKC SIM or Cache SIM)	
13	000000	Reserved	
14	000104	Imprecise ending (This error is for a previously completed CCW)	
15	800104	Command reject and imprecise ending	
16	400104	Intervention required and imprecise ending	
17	200104	Bus out check and imprecise ending	
18	100104	Equipment check and imprecise ending	
19	080104	Data check and imprecise ending	
1A	040104	Overflow and imprecise ending	
1B	004104	Invalid track format and imprecise ending	
1C	002104	End of cylinder and imprecise ending	
1D	000904	No record found and imprecise ending	
1E	000504	File protected and imprecise ending	
1F	100304	Equipment check and write inhibited and imprecise ending	

SSB Key Code (3/4)

Code	Basic SSB	Meaning	Remarks
20	080144	Equipment check and correctable and imprecise ending	
21	000124	First log mode error and imprecise ending	Not used
22	800504	Command reject and file protected and imprecise ending	
23	800304	Command reject and write inhibited and imprecise ending	
24	108104	Equipment check and permanent error and imprecise ending	
25	088104	Data check and permanent error and imprecise ending	
26	009114	SIM and imprecise ending	Not used
27	000000	Reserved	
28	808000	Command reject and permanent error	
29	00C000	Permanent error and invalid track format	
2A	008800	Permanent error and request inhibit write	
2B	088200	Data check and permanent error and write protected	
2C	000000	Reserved	
2D	000000	Reserved	
2E	000000	Reserved	
2F	000000	Reserved	

SSB Key Code (4/4)

Code	Basic SSB	Meaning	Remarks
30	000000	Reserved	
31	000000	Reserved	
32	010000	Incomplete domain (Received commands < LOCATE command parameter)	
33	804000	Command reject and invalid track format (A TLF mismatch in CKD conversion mode)	
34	000000	Reserved	
35	100010	Equipment check and environmental data present (Status change pending)	
36	800010	Command reject and environmental data present	
37	000000	Reserved	
38	000000	Reserved	
39	000000	Reserved	
3A	000000	Reserved	
3B	000000	Reserved	
3C	000000	Reserved	
3D	000000	Reserved	
3E	000000	Reserved	
3F	000000	Reserved	

4. Host SSB information and internal SSB format-depended information description

4.1 24-byte type SSB

	0	1	2	3	4	5	6	7	
00 05	Internal SSB common Information								
20	Command reject	Intervention REQ	Bus out check	Equipment check	Data check	Overrun	Unused	Incomplete domain	Basic SSB
21	Permanent error	Invalid track FMT	End of cylinder	Operator message	No Record found	File protect	Write inhibit	Imprecise ending	
22	Write INH request	Correctable	1st log mode ERR	Environmental DAT	Unused	Imprecise ending	Not used	Not used	
23	Count								
24	Device address								
25	Cylinder address (low)								
26	Cylinder address (high)				Head address				
27	Format (See next page)				Message (See next page)				
28 37	Host SSB Format-depended Information								
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC mode (1)	Duplex pair (0)	S-volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	*
3B	24Byte SSB (1)	DEV ADR valid	TRK ADR valid	Not used	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Not used								
3D	Cylinder address								
3E									
3F	Head address								
40 7F	Internal SSB Format-depended Information								

*: Configuration information

**: Byte 20-27 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB section.

Format and Message (1/2)

Format	Message	Meaning	
0	0	No message	
	1	Invalid command	
	2	Invalid sequence	
	3	Data short	
	4	Invalid data	
	5	Violate file mask	
	6	Channel disconnect retry	
	7	Invalid retry command	
	8	System reset	
	9	Not used	
	A	Bus of parity check/Overrun	
	B	Invalid link between bad track and alternate track	
	C	Not used	
	D	Not used	
	E	Invalid command to secondary volume	
	F	Invalid status	
1	0	Intervention required	For DKU86I (IBM 3380 emulation mode) only
	1	Not used	
	2	Drive not ready	
	3	Not used	
	4	Drive report error	
	5 ~ F	Not used	
3	0 ~ E	Not used	
	F	Reset allegiance	
4	0	Data check in HA field	For DKU86I (IBM 3380 emulation mode) only
	1	Data check in C field	
	2	Data check in K field	
	3	Data check in D field	
	4	Not used	
	5	PA error	
	6 ~ F	Not used	
6	0 ~ 7	Statistics	
	8 ~ F	Not used	
7	0 ~ 3	Not used	
	4	FPC report error	
	5 ~ 8	Not used	
	9	SCSI bus parity error	
	A ~ F	Not used	

Format and Message (2/2)

Format	Message	Meaning
8	0	Not used
	1	Shared memory failure
	2 ~ 7	Not used
	8	LCP failure
	9	Host adaptor CHK2
	A	Disk adaptor CHK2
	B	DRR failure
	C	SVP failure
	D	Power failure
	E	Processor failure
	F	Micro-program detected error (Logical Inconsistency)
9	0 ~ E	Not used
	F	Micro-program detected error (Trace data/Log data)
E	0 ~ B	Not used
	C	Environmental monitor detected DKC failure
	D	Not used
	E	Environmental monitor detected DKU failure
	F	Not used
F	0	Operation Terminated
	1	Microprogram detected cache failure
	2	Cache failure
	3	Not used
	4	Not used
	5	Remote Copy Paths Removed/Restored
	6	CFW not authorized
	7 ~ 9	Not used
	A	NVS terminated
	B	HRC/HODM Pair suspend
	C ~ E	Not used
	F	Cache/Shared memory/M-bus/F-bus/J-bus warning

4.2 32-byte type SSB

	0	1	2	3	4	5	6	7	
00 05	Internal SSB common Information								
20	Command reject	Intervention REQ	Not used	Equipment check	Data check	Not used	Not used	Incomplete domain	Basic SSB
21	Permanent error	Invalid TRK FMT	End of cylinder	Operator message	Not used	File protect	Write inhibit	precise ending	
22	EC=0/1/2/3/F:Environmental data present EC=4/6/D/E :Storage control type (x'06':Cache DKC)								
23	Count / Command overrun threshold is exceeded.								
24	Device address								
25	Device type (x'24':DKU871-3)								
26	SSB2/SSB5 valid	SSB4 valid	SSB29-31 valid	Not used	Format				
27 37	Host SSB Format-depended Information								
38	Log and message control								
39	Program action code								
3A	Dual frame	EDCC mode (1)	Duplex pair (0)	S-Volume ERR (0)	Async operation	Serial channel	Report output	Permanent path ERR	*
3B	32Byte SSB (0)	Not used	Not used	Not used	3380 TRK COMPT (0)	Not used	Storage path number		
3C	Message code								
3D	Cylinder address								
3E									
3F	Head address								
40 7F	Internal SSB Format-depended Information								

*: Configuration information

**: Byte 20-26 and byte 38-3F are not certain until DKC reports SSB to Host. So if you want to know the detail, please refer to SSB section.

Exception Class (Byte36:bit0-3) and Format (Byte26:bit4-7)

Exception Class	Format	Meaning
0	0 ~ 2	Not used
	3	Machine condition exception
	4	I/O Program exception (incomplete Domain)
	5 ~ F	Not used
2	0 ~ E	Not used
	F	DKC SIM
3	0 ~ E	Not used
	F	DKC SIM
4	0	Not used
	1	Data exception (PCI, Permanent)*
	2 ~ E	Not used
	F	Media SIM
5	0 ~ E	Not used
	F	Device SIM/Media SIM
6	0	Not used
	1	Statistics*
	2 ~ F	Not used
C	0 ~ E	Not used
	F	DKC SIM
D	0	Drive detected failure*
	1	FPC detected failure*
	0 ~ E	Not used
	F	DKC SIM
E	0	Drive detected failure/LDEV blockade/PIN VOL detected/ LDEV not ready/Write inhibited*
	1 ~ E	Not used
	F	Device SIM
F	0 ~ E	Not used
	F	DKC SIM/Cache SIM

*: For DKU87I (IBM 3390 emulation mode) only.

4.3 SCSI host report format SSB

	0	1	2	3	4	5	6	7
00 1F	Internal SSB common information (*1)							
20	Information byte valid	Error code (0x70)						
21	Segment No (0)							
22	File mark (0)	ECM (0)	ILI (0)	Not in use	Sense key			
23 26	Information byte							
27	Additional sense data length (0x58)							
28 2B	Command specific information							
2C	Sense code							
2D	Field Replaceable Unit (0)							
2E 31	Sense key specific information	Sense key specific information						
32 7F	Additional sense data valid	Additional sense data						

*1: 0xCX (X=sense key) is set to byte 7 (log type low-order byte). SSB on SVP is indicated as F/M=CX.

Details of SCSI host report format SSB information

Byte	Bit	Description	Setting	Remarks
20	0	Information byte valid/invalid 0: Invalid 1: Valid	CHS command control	
	1 ~ 7	Error code (0x7): Indicates an I/O process error.		
21		Segment No. (0)		
22	0	File mark (0)		
	1	ECM (0)		
	2	ILI (0)		
	3	Not in use		
	4 ~ 7	Sense key: For more details, see "sense keys list".		
23 ~ 26		Information bytes: LBA related to sense key if information byte is valid.		
27		Additional sense data length (0x58): Indicates sense data length after byte 8 or latter.		
28 ~ 2B		Command specific information		
2C ~ 2D		Sense codes: For more details, see "sense codes list".		
2E		Field Replaceable Unit (0)		
2F ~ 31	0	Sense key specific information valid/invalid 0: Invalid 1: Valid		
	1 ~ 17	Sense key specific information		
32 ~ 7E	0	Additional sense data valid/invalid 0: Invalid 1: Valid	Error detection source	
	1 ~ END	Additional sense data		

Sense Keys List

Sense key	Description
0	NO SENSE
1	RECOVERED ERROR
2	NOT READY
3	MEDIUM ERROR
4	HARDWARE ERROR
5	ILLEGAL REQUEST
6	UNIT ATTENTION
7	DATA PROTECT
8	BLANK CHECK (Not in use)
9	VENDOR UNIQUE
A	COPY ABORTED
B	ABORTED COMMAND
C	EQUAL (Not in use)
D	VOLUME OVERFLOW
E	MISCOMPARE
F	(RESERVED)

5. Format-depended information description

Byte27 = 0X (X ≠ 8/A: System/Program Check) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'X':X ≠ 8/A)			
28	Command code (for F/M ≠ F)/Reason code (for F/M=F) (Note 1)							
29	Detail failure information 1 (Note 2)							
2A								
2B								
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID (Note 4)							
31	Routine ID (Note 4)							
32	PCB number							
33	Detail failure information 2 (Note 3)							
34	SSID of self subsystem							
35								
36	Sympton code (x'0F0X')							
37	X=Message code (x'2F00' for dummy SENSE)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 0X (X ≠ 8/A: System/Program Check) (2/3)

	0	1	2	3	4	5	6	7
40	Not used							
55								
56	Module ID (detected the error)							
57	Routine ID (detected the error)							
58	Not used							
7F								

(Note 1) F/M = 0F:Byte08 Reason code

Code	Reason
00	No message
01	Trying to make cache available and cache status is pending.
02	Not used
03	Trying to make cache force unavailable and cache status is not pending with destage complete.
04	Trying to activate CFW and CFW status is pending.
05	Trying to activate caching (device) and cache status is pending.
06	Trying to destage (track) and NVS is failed.
07	Trying to make NVS unavailable and NVS is initializing.
08	Trying to deactivate caching , DFW or DISCARD (device) , and DFW status is failed.
09	Trying to force deactivate DFW (device) and status is not as follows. • EDFW status is pending or failed. • ENVS is failed and the data exists in NVS, but not in cache.
0A	Trying to make NVS available and DFW status (device) is pending or failed.
0B	Trying to make NVS available and NVS status is pending.
0C	When NVS is failed, trying to stop caching or DFW (device) and the data exists in NVS, but not in cache.
0D	Command requires NVS and NVS is unavailable.
0E	Command requires cache and cache is unavailable.
0F	Not used
10 ~ 28	Not used
29	PFS command (Set Special Interrupt Condition) is issued on an interface on which path group is not established.
2A	Trying to make path group include parallel channel and serial channel.
2B	Message buffer is full.
2C ~ 34	Not used
35	A Set Guaranteed Path subcommand is issued on a fenced path.
36 ~ 3E	Not used
3F	A Make NVS Avail for Subsystem subcommand is issued, but NVS is disable. ('Disable NVS Capability' is set to disable in VPD.)
40	Not used
41	A normal command is issued to a device reserved by maintenance.
42 ~ 7F	Not used
80	A special command is issued on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
81	An attention report on an interface that was disabled by the PFS command with Set Special Intercept Condition order.
82 ~ FF	Not used

Byte27 = 0X (X ≠ 8/A: System/Program Check) (3/3)

(Note 2)

(1) Issued command information when F/M=02

B\b	0	1	2	3	4	5	6	7
29	SCH ID	SCH KEY	Locate, LC EXT	SK, SK CYL	RD IPL	RECAL	SUSP MPR	RD C
2A	DIAG CTL	DIAG CTL (LOC DOM)	DEF EXT	SET FM	SET SUB MODE	SP C	WR CKD	WR R0
2B	SET INTF ID	DIAG CTL (PRE REM)	0	Other command	RD C chaining	All byte match	LC domain	Erase execute
2C	Pseud through	0	0	Allow WR KD	Allow WR D	Allow WR CKD	Allow WR R0	Allow WR HA

(2) Short byte flag when F/M=03

x'0040 0000'

(3) Mask information when F/M=05

Byte 29 : Mask for write control

x'00' : Allows all write operation except WR
HA, WR R0.

x'40' : Inhibits all write operation.

x'80' : Allows update write operation.

x'C0' : Allows all write operation.

Byte 2A : Mask for seek control

x'00' : Allows all seek commands and RECAL.

x'08' : Allows SK CYL and SK HD only.

x'10' : Allows SK HD only.

x'18' : Inhibits all seek command and multi
operation.

Byte 2B : Mask for access

x'00' : Allows normal access.

x'02' : Allows device support.

x'04' : Allows diagnostic access.

x'06' : Allows device support inhibit data
check correction and retry.

Byte 2C : Not used

(4) Record address when other F/M

Byte 29 and 2A : Cylinder address

Byte 2B : Head address

Byte 2C : Record number

(Note 3) Bit 4-7 of byte 32 : Type of byte 33

x'08' : Byte 33 is cylinder address (high).

x'09' : Byte 33 is cylinder address (low).

x'0A' : Byte 33 is head address (high).

x'0B' : Byte 33 is head address (low).

x'0C' : Byte 33 is record number.

Byte 33 : One byte of CCHHR.

(Note4) The details information of every Module ID & Routine ID

- Module ID & Routine ID=0xC0CA ; The Specified R-VOL of HRC is still Online from the HOST.
(Add Pair operation from the Remote Console was rejected)
Check the SSB.LOG on SVP of the specified RCU. SSB containing EC=C0CA on the specified R-VOL number should be logged.
 - Module ID & Routine ID=0xC0A6 ; The M-VOL of HODM is Online from the HOST.
Check the SSB.LOG on SVP of the MCU. SSB containing EC=C0A6 on the specified M-VOL should be logged.
- (1) Online host's specific method
- Please confirm HOST ONLINE from Main Frame Path Information of the Maintenance screen.
Please refer to "3.15 Path of LCP/HTP" of SVP section for 'Main Frame Path Information'.
'Main Frame Path Information' displays the host that online is done in the unit of CU.
Please confirm the ONLINE of the Volume from the displayed HOST.

Blank Sheet

Byte27 = 08 (Reset Notification) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'8')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (Note 1)							
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				Not used (x'0')			
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Sympton code (x'2FFF')							
37								
38	Log & message control (x'00')							
39	Program action code (x'16')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 08 (Reset Notification) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

(Note 1) Hardware Level

Bit0 : Serial Channel or Parallel Channel	
Bit0=1(Serial Channel)	
Bit 1	: Not used
Bit 2~3	: Storage Path Number
Bit 4~7	: 0xa : Fixed
Bit 8	: Dual Frame
	0 : Dual Frame
	1 : Modular Power
Bit 9~11	: Cache size
	000 : none Cache
	001 : 256MB
	010 : 512MB
	011 : 768MB
	100 : 1024MB
	101 : 1280MB
	110 : 1536MB
	111 : over 1536MB
Bit 12~13	: Cluster hardware level
Bit 14~15	: Cache/NVS hardware level

Byte27 = 0A (Bus out parity check/Overrun on LCP interface (Byte3A:b4 = 1)) (1/1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'0')				Message (x'A')			
28	Command code							
29	Cylinder address							
2A								
2B	Head address							
2C	Record number							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number				BBF OVR	PND OVR	STOP	OVER
33	Not used							
34	SSID of self subsystem							
35								
36	Sympton code (x'0F0A')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40	Not used							
-7F								

Byte27 = 10 (Intervention required) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'0')			
28	Ready	Enable	SSB PEND	Not used ('00000')				
29	Not used (x'00')							
2A	M.M. RSV	PIN VOL	Not used ('000000')					
2B	Host type	DKC type	Not used ('00')			DKU type		
2C	Module ID (x'50')							
2D	Routine ID (x'1C')							
2E	Command code							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0')			
33	Not used							
34	SSID of self subsystem							
35								
36	Symptom code (x'9F10')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 10 (Intervention required) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte27 = 1X (Drive report error) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'1')				Message (x'2/4')			
28	Additional sense code							
29	Additional sense code qualifier							
2A	SCSI Command (*2)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used (x'0')				DKA#			
2F	CDEV#				RDEV#			
30	Type code ('000')			0	Sense key (*1)			
31	Not used (x'00')							
32	Not used (x'00')							
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'9FIX' X: Message)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 1X (Drive report error) (2/2)

	0	1	2	3	4	5	6	7
40	Status byte							
41	Message							
42	CDB							
43								
44								
45								
4B	SPC status register							
4C	SPC nexus status register							
4D	SPC data block register							
4E	SPC data byte register							
4F								
50								
51								
52	SPC SCSI control signal status register							
53	SCSI extended sense data							
54								
55								
56								
73	Not used							
74								
75								
76								
7F								

*1: Sense key

x'0' : No sense
 x'1' : Recoverd error
 x'2' : Not ready
 x'3' : Medium error
 x'4' : Hardware error
 x'5' : Illegal request
 x'6' : Unit attention
 x'7' : Data protect
 x'8' : Blank check (Not used)
 x'9' : Vender unique (Not used)
 x'A' : Copy aborted (Not used)
 x'B' : Aborted command
 x'C' : Equal (Not used)
 x'D' : Volume overflow (Not used)
 x'E' : Miscompare
 x'F' : (Reserved)

*2: SCSI command

x'00' : Test unit ready
 x'03' : Request sense
 x'04' : Format unit
 x'07' : Reassign blocks
 x'12' : Inquiry
 x'15' : Mode serect
 x'1A' : Mode sense
 x'1B' : Start/stop unit
 x'1C' : Recieve diagnostic result
 x'1D' : Send diagnostic
 x'28' : Read (extend)
 x'2A' : Write (extend)
 x'2E' : Write and verify
 x'3B' : Write buffer

Byte27 = 3F (Reset Allegiance) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'3')				Message (x'F')			
28	Not used							
29	Logical path number							
2A	DCN status (*1)							
2B								
2C	Command code							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Module ID							
31	Routine ID							
32	PCB number							
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'3F3F')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 3F (Reset Allegiance) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1:								
	0	1	2	3	4	5	6	7
28	Device busy	ECI	Command chaining	Path reserve	Stack status	Wait SENSE	Long busy	Processor connect
29	Single path mode	Guarant path mode	MCB chaining	0	Block switch	0	0	0

Byte27 = 4X (Data check) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'4')				Message (x'X')			
28	Cylinder address (*1)							
29								
2A	Head address (*1)							
2B								
2C	Record number (*1)							
2D	Sector number (*1)							
2E	Controller ID							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	PCB number				Not used (x'0')			
33	Not used							
34	Command code							
35	SSID (low) of self subsystem							
36	Symptom code (x'4XYY') (X: *2) (YY: *3)							
37								
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Not used (x'01')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 4X (Data check) (2/3)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	LDEV number, slot number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT flag (*4)							
52	Slot failure information (*5)							
53	Obtained information flat (*6)							
54	Head sub block number for HIT/MISS decision + Remain sub block count							
55	(when byte36 is x'40/41/42/43')/PACC in CBUF (when byte36 is x'45')							
56	Not used (when byte36 is x'40/41/42/43')/PAHF in CBUF (when byte36 is x'45')							
57	Not used (when byte36 is x'40/41/42/43')/Current CC in JCB (when byte36 is x'45')							
58	Not used (when byte36 is x'40/41/42/43')/Current HH in JCB (when byte36 is x'45')							
59								
5A								
5B								
5C								
	Reserved							
7F								

*1: Not determined for occurrence in HA or RO

*2: Contents of message

x'0' : Data check in HA field
 x'1' : Data check in C field
 x'2' : Data check in K field
 x'3' : Data check in D field
 x'4' : Missing SYNC byte in HA field
 x'5' : Missing SYNC byte in C field (PA error)
 x'6' : Missing SYNC byte in K field
 x'7' : Missing SYNC byte in D field
 x'8' : Not used
 x'9' : Missing AM during retry
 x'A' to x'F' : Not used

*3: Contents of correction flag

Bits 0 and 1 : ECC correction bit
 10 : Correctable (Recovered)
 11 : Uncorrectable
 Bit 2 : Offset active
 Bits 3 to 7 : Not used

Byte27 = 4X (Data check) (3/3)

*4: EXIT flag

- x'08' : CHL I/F CHK2 time over
- x'0D' : EOF
- x'0E' : DSB = 0E requested
- x'0F' : Logical error
- x'1E' : Make sense only
- x'1F' : Logical error (make sense only)
- x'30' : Selective reset requested
- x'40' : 'Staging' requested
- x'41' : 'Waiting free segment' requested
- x'42' : 'Time over of waiting for staging' requested
- x'43' : 'Staging for PA unmatched' requested
- x'45' : 'Destaging' requested
- x'46' : 'High priority destaging' requested
- x'47' : 'Pseud-through process' requested
- x'4B' : 'DSB = 4E retry' requested (data transfer)
- x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
- x'4F' : STG requested (Read or STG in progress)

*5: Slot failure

- x'80' : Sector write error
- x'40' : Sector read error
- x'20' : ECC/LRC error
- x'10' : Incomplete data
- x'08' : No decision

*6: Obtained information flag

- bit0-1 : Block type
 - 00 : Data block
 - 01 : HA/RO block
 - 10 : Bit map block
- bit2-3 : Side
 - 00 : Cache write side
 - 01 : NVS write side
 - 10 : Cache read side
- bit4 : Segment
 - 0 : No segment
 - 1 : Segment exist
- bit5-7 : Reserved

Byte27 = 6X (Statistics) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Controller ID							
24	Device address							
25	Command overrun threshold exceeded							
26	Data overrun threshold exceeded							
27	Format (x'6')				Message (x'X')			
28	Read/search byte count							
29								
2A								
2B								
2C	Seek count							
2D								
2E	Not used (x'00')							
2F	Manufacture code ('000000')						Factory code ('00')	
30	Not used (x'00')							
31	DKC serial number							
32								
33								
34	SSID of self SSID							
35								
36	Symptom code (x'6FXY' X: 0: Statistics/RRBL command, 2: Channel data overrun							
37	Y: CHL# (parallel CHL)/LCP# (serial CHL))							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00000000')							
3D								
3E								
3F								

Byte27 = 6X (Statistics) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte27 = 74 & Byte40 : Bit7 = 0 (FPC Detected Failure : DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06': Cache DKC)							
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ :RDEV number							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 74 & Byte40 : Bit7 = 0 (FPC Detected Failure : DSP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	Not used							
42	CDB							
43								
44								
45								
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD BLK number							
4F	Data transfer end flag							
50	I/O status							
51								
52	PDEV status							
53								
54	LOOP status							
55								
56								
57								
58	Frame Manager status							
59								
5A								
5B								
5C	TL status							
5D								
5E								
5F								
60	IO issued status 1							
61								
62	IO issued status 2							
63								
64	Not used							
65								
66								
67								
68	JOB require info							
69								
6A								
6B								
6C	Data transfer status							
6D								
6E								
6F								
70	Not used							
71								
72								
73								
7F								

Byte27 = 74 & Byte40 : Bit7 = 1 (FPC Detected Failure : FBP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'7')				Message (x'4')			
28	Not used							
29	Not used							
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used (x'0')				DKA number			
2F	CDEV number				RDEV number			
30	Type code ('111')			0	Not used (x'0')			
31	Not used (x'00')							
32	Not used (x'00')							
33	Not used (x'00')							
34	SSID of self subsystem							
35								
36	Symptom code (x'DF74')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 74 & Byte40 : Bit7 = 1 (FPC Detected Failure : FBP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	Not used							
42	CDB							
43								
44								
45								
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD BLK number							
4F	Data transfer end flag							
50	I/O status							
51								
52	PDEV status							
53								
54	LOOP status							
55								
56								
57								
58	Frame Manager status							
59								
5A								
5B								
5C	TL status							
5D								
5E								
5F								
60	IO issued status 1							
61								
62	IO issued status 2							
63								
64	Not used							
65								
66								
67								
68	JOB require info							
69								
6A								
6B								
6C	Data transfer status							
6D								
6E								
6F								
70	Not used							
71								
72								
73								
7F								

Byte27 = 81 & Byte34:b4-7 = 0 (Shared Memory Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'1')			
28	Subcode			(See following pages)				
29	Hardware information			(See following pages)				
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Not used (x'00')							
33	Not used (x'00')							
34	PCB number			Message code (x'0')				
35	SSID of self subsystem							
36	Symptom code (x'FF81' : LDEV type = 3390/x'EF81' : LDEV type = 3380)*							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 81 & Byte34:b4-7 = 0 (Shared Memory Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Hardware information (See following pages)							
7F								

Byte27 = 81 (Shared Memory Failure) Byte28-31 Detail

(1) Byte34, Bit4 to Bit7 = 0: Shared Memory Failure

	0	1	2	3	4	5	6	7
28	Subcode							
	Micro error	MPA internal error	‘00’:Micro error or MPA internal error ‘01’: shared memory error ‘10’:SHSN error ‘11’: others error		Access path ‘0000’:A0 path error ‘0010’:B0 path error ‘0100’:C0 path error ‘0110’:D0 path error		‘0001’:A1 path error ‘0011’:B1 path error ‘0101’:C1 path error ‘0111’:D1 path error	
29	MPA: CHK3ADRS: 4400 0088 (b37 - 30)							
	PCI address							
2A	MPA: CHK3ADRS: 4400 0088 (b47 - 40)							
	PCI address							
2B	MPA: CHK3ADRS: 4400 0088 (b57 - 50)							
	PCI address							
2C	MPA: CHK3ADRS: 4400 0088 (b67 - 60)							
	PCI address							
2D	MPA: CHK3ADRS: 4400 0088 (b77 - 70)							
	PCI address							
2E	MPA: MPACHK3STS: 4200 0108 (b07 - 00)							
	Reserved	Reserved	XR CMP CHK3	XR SMRX CHK3	XR SMTX CHK3	XR RCV CHK3	XR FIFO CHK3	PCIIF error or Micro error
2F	MPA: MPACHK3STS: 4200 0108 (b17 - 10)							
	A0 CTLERR CHK3	A1 CTLERR CHK3	B0 CTLERR CHK3	B1 CTLERR CHK3	C0 CTLERR CHK3	C1 CTLERR CHK3	D0 CTLERR CHK3	D1 CTLERR CHK3
30	MPA: MPACHK3STS: 4200 0108 (b27 - 20)							
	A0 DTERR CHK3	A1 DTERR CHK3	B0 DTERR CHK3	B1 DTERR CHK3	C0 DTERR CHK3	C1 DTERR CHK3	D0 DTERR CHK3	D1 DTERR CHK3
31	MPA: MPACHK3STS: 4200 0108 (b37 - 30)							
	A0 path Timeout	A1 path timeout	B0 path timeout	B1 path timeout	C0 path timeout	C1 path timeout	D0 path timeout	D1 path timeout

Byte27 = 81 Byte40-7F(MICRO ERROR, MPA INTERNAL ERROR)(Byte28[bit2-3]=x'00') (1/4)

	0	1	2	3	4	5	6	7
40	MPA: HIN: 4200 0050 (b27 - 20)							
	SMDC Majority decision result	SMDC input signal status			SMDD Majority decision result	SMDD input signal status		
41	MPA: HIN: 4200 0050 (b37 - 30)							
	SMDA Majority decision result	SMDA input signal status			SMDB Majority decision result	SMDB input signal status		
42	MPA: HOUT: 4200 0054 (b27 - 20)							
	SMDC Output result	SMDC individual signal output			SMDD Output result	SMDD individual signal output		
43	MPA: HOUT: 4200 0054 (b37 - 30)							
	SMDA Output result	SMDA individual signal output			SMDB Output result	SMDB individual signal output		
44	MPA: REG7 ERR STS1: 4200 073C (b07 - 00)							
	Reserved	Reserved	2RD CMPEP	RD ELRCER B	RD ELRCER A	RD NLRCEP B	RD NLRCEP A	Reserved
45	MPA: REG7 ERR STS2: 4200 074C (b07 - 00)							
	WR ELRCER B	WR ELRCER A	WR NLRCEP B	WR NLRCEP A	ERDTBEPER			
46	MPA: REG7 ERR STS2: 4200 074C (b17 - 10)							
	LRCAPER	STAPER	LRCBPER	STBPER	RXBPER			
47	MPA: REG7 ERR STS2: 4200 074C (b27 - 20)							
	RXBPER				RXAPER			
48	MPA: REG7 ERR STS3: 4200 0770 (b37 - 30)							
	Reserved	Reserved	Reserved	Reserved	A CRCCMPER	A CRCPER	B CRCCMPER	B CRCPER
49	MPA: REG1 ADR4 BD: 4200 03A0 (b07 - 00)							
	XPT							
4A	MPA: RLKERR ADR: 4200 0420 (b30 - 37)							
	Reserved	Reserved	Reserved	Reserved	Reserved	QUEPOS	LCMP	LMODE
4B	MPA: ATERR0 / ATERR1: 4200 045C (b23 - 20) / 4200 0460 (b23 - 20)							
	Reserved	Reserved	Reserved	ATMODE0 Micro error	Reserved	Reserved	Reserved	ATMODE1 Micro error
4C	MPA: ATERR1 ADR(8B): 4200 0460 (b37 - 30)							
	ATMASK1 register parity error				ATCHG register parity error			
4D	MPA: SQ0ER: 4200 063C (b37 - 30)							
	FM BNKER	CMDEP_ATM	CMDEP_LCK	SPLT MSTER	SPLT XRER	LWMSTER	LWXRER	SEQO CSTER
4E	MPA: SQ1ER: 4200 02B0 (b37 - 30)							
	HOT D	HOT C	HOT B	HOT A	ILLACS	DECODE	ENCODE	CST
4F	MPA: CHK3 SLV: 4200 010C (b27 - 20)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	LtoL CHK3	DSTAGE CHK3

Byte27 = 81 Byte40-7F(MICRO ERROR, MPA INTERNAL ERROR)(Byte28[bit2-3]=x'00') (2/4)

	0	1	2	3	4	5	6	7
50	MPA: CHK3 SLV: 4200 010C (b37 - 30)							
	XMST0 CHK3	XMST1 CHK3	XMST2 CHK3	XMST3 CHK3	SCAN A status bit	SCAN B status bit	SCAN C status bit	SCAN D status bit
51	MPA: CHK3 PKTMON: 4200 02D4 (b17 - 10)							
	Reserved	SMAIO	FACE	TMODE	3rd_CMD1	3rd_CMD0	2nd_CMD1	2nd_CMD0
52	MPA: CHK3 PKTMON: 4200 02D4 (b27 - 20)							
	1st_CMD1	1st_CMD0	MST	BC address parity error	ATM micro access error	ATM address parity error	ATMCMP	RLK micro access error
53	MPA: CHK3 PKTMON: 4200 02D4 (b37 - 30)							
	RLK address parity error	RLKCMP	WTCMP	WT micro access error	WT address parity error	RD micro access error	RD address parity error	RDCMP
54	MPA: XR TIMEOUT: 4200 06A8 (b00) / 4200 0594 (b30) / 4200 05AC (b30) / 4200 05C4 (b30) / 4200 05DC (b30) / 4200 05F4 (b30)							
	Reserved	Reserved	SEQ0 timer timeout	SEQ1 timer timeout	SM side A SEQ2 timer timeout	SM side B SEQ2 timer timeout	SM side C SEQ2 timer timeout	SM side D SEQ2 timer timeout
55	MPA: A0TXER: 4210 j204 (b27 - 20) j: 4 ~ 7 (MP# 0 ~ 3)							
	Reserved	Reserved	Reserved	Reserved	CNFLCT_ER	TXTOUT	ENTON_ERR	ILLMD
56	MPA: A1TXER: 4211 j204 (b27 - 20) j: 4 ~ 7 (MP# 0 ~ 3)							
	Reserved	Reserved	Reserved	Reserved	CNFLCT_ER	TXTOUT	ENTON_ERR	ILLMD
57	MPA: MPASTS: 4200 0100 (b37 - 30)							
	BCINT or SCANINT	Master CHK3 status bit	Slave CHK3 status bit	FM IF CHK1B status bit	Reserved	Reserved	XR IF CHK1B status bit	MPINT
58	MPA: BMDLOG: 4200 0164 (b27 - 20)							
	Reserved	SM copy mode enable	SM disable side C	SM disable side D	C0 path disable	C1 path disable	D0 path disable	D1 path disable
59	MPA: BMDLOG: 4200 0164 (b37 - 30)							
	Reserved	SM copy mode enable	SM disable side A	SM disable side B	A0 path disable	A1 path disable	B0 path disable	B1 path disable
5A	PERR VALID / MACS ADR							
	Parity error	Reserved	Reserved	Reserved	Micro Access Address			
5B	MACS ADR							
	Micro Access Address							
5C	MACS ADR							
	Micro Access Address							
5D	MACS ADR							
	Micro Access Address							
5E	MACS ADR							
	Micro Access Address							
5F	MACS DAT							
	Micro Access Address							

Byte27 = 81 Byte40-7F(MICRO ERROR, MPA INTERNAL ERROR)(Byte28[bit2-3]=x'00') (3/4)

	0	1	2	3	4	5	6	7
60	MACS DAT							
	Micro Access Data							
61	MACS DAT							
	Micro Access Data							
62	MACS DAT							
	Micro Access Data							
63	MACS DAT							
	Micro Access Data							
64	MACS DAT							
	Micro Access Data							
65	MACS DAT							
	Micro Access Data							
66	MACS DAT							
	Micro Access Data							
67	MPA: REG1 ADR0 AC: 4200 0368 (b27 - 20)							
	BUF							
68	MPA: REG1 ADR0 AC: 42000368 (b37 - 30)							
	BUF							
69	MPA: REG1 ADR1 AC: 4200 036C (b07 - 00)							
	BUF							
6A	MPA: REG1 ADR1 AC: 4200 036C (b17 - 10)							
	BUF							
6B	MPA: REG1 ADR1 AC: 4200 036C (b27 - 20)							
	BUF							
6C	RXA EDT							
	Side A / Side C Error Data							
6D	RXA EDT							
	Side A / Side C Error Data							
6E	RXA EDT							
	Side A / Side C Error Data							
6F	RXA EDT							
	Side A / Side C Error Data							

Byte27 = 81 Byte40-7F(MICRO ERROR, MPA INTERNAL ERROR)(Byte28[bit2-3]=x'00') (4/4)

	0	1	2	3	4	5	6	7
70	MPA: PATH TIMEOUT: 4210 j20C (b30) / 4211 j20C (b30) / 4212 j20C (b30) / 4213 j20C (b30) / 4214 j20C (b30) / 4215 j20C (b30) / 4216 j20C (b30) / 4217 j20C (b30) j: 4 ~ 7 (MP# 0 ~ 3)							
	SHSN data received wait time out							
71	MPA: REGXRX: 4200 0828 (b17 - 10)							
	BEH PER	BEL PER	BET PER	CMPADR_PER				
72	MPA: REGXRX: 4200 0828 (b27 - 20)							
	ADR_PER							
73	MPA: REGXRX: 4200 0828 (b37 - 30)							
	MMOL_PER			MMO_PER				
74	MPA: REG0TX: 4200 0888 (b37 - 30)							
	MMOL_PER				MMO_PER			
75	MPA: RXB EDT							
	Side B / Side D Error Data							
76	MPA: RXB EDT							
	Side B / Side D Error Data							
77	MPA: RXB EDT							
	Side B / Side D Error Data							
78	MPA: RXB EDT							
	Side B / Side D Error Data							
79	MPA: ACSMODE: 4200 0150 (b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Side A	Side B	Side C	Side D
7A	MPA: ACSMODE: 4200 0150 (b17 - 10)							
	SM1R	SM1W	2RLCK	2WLCK	2RNLCK	2WNLCK	1R1W	1R2W
7B	MPA: ACSMODE: 4200 0150 (b27 - 20)							
	Reserved	Reserved	Reserved	Reserved	Access pattern 1: SM read	SM read	SM write	Atomic
7C	MPA: ACSMODE: 4200 0150 (b37 - 30)							
	Resource lock	SMAIO read	SMAIO write	Broadcast read	Broadcast write	SCAN read	SCAN write	Reserved
7D	MPA: ACSLOG: 4200 0154 (b07 - 00)							
	1 st RD	1 st WT	1 st LK0	1 st LK1	1 st GNT A0	1 st GNT A1	1 st GNT B0	1 st GNT B1
7E	MPA: ACSLOG: 4200 0154 (b17 - 10)							
	1 st GNT C0	1 st GNT C1	1 st GNT D0	1 st GNT D1	2 nd RD	2 nd WT	2 nd LK0	2 nd LK1
7F	MPA: ACSLOG: 4200 0154 (b27 - 20)							
	2 nd GNT A0	2 nd GNT A1	2 nd GNT B0	2 nd GNT B1	2 nd GNT C0	2 nd GNT C1	2 nd GNT D0	2 nd GNT D1

Byte27 = 81 Byte40-7F(SM ERROR)(Byte28[bit2-3]=x'01') (1/4)

	0	1	2	3	4	5	6	7
40	SMA A/C SIDE: SMA STS: 3000 001C (b17 - 10) / 3800 001C (b17 - 10)							
	Reserved	Reserved	CACHE PK slot signal bit ‘01’: A side ‘10’: B side	b2 - 3 parity bit	Reserved	SM disconnect status bit	SM power down status bit	
41	SMA A/C SIDE: SMA STS: 3000 001C (b27 - 20) / 3800 001C (b27 - 20)							
	disconnect request bit	SM self- refresh status bit	SM voltage status bit	LSI voltage status bit	System timer enable status bit	SMA all lock status bit	SM ready status bit	SMA LSI blockade status bit
42	SMA A/C SIDE: SMA STS: 3000 001C (b37 - 30) / 3800 001C (b37 - 30)							
	SM PK version bit				SMA LSI version bit			
43	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b07 - 00) / 3800 0120 (b07 - 00)							
	WDB error	MWB error	MRB error	RDB error	RWB error	MARB error	MVSEQ error	MSEQ error
45	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b17 - 10) / 3800 0120 (b17 - 10)							
	RDSEQ error	RTNSEQ error	REFCTL error	LQARB error	LQ error	IWDB error	IWB error	IRDB error
46	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b27 - 20) / 3800 0120 (b27 - 20)							
	IRB error	IARB error	IMVSEQ error	ISEQ error	TIMER error	SCSEQ error	BCREG error	SLOT error
47	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b37 - 30) / 3800 0120 (b37 - 30)							
	PD address error	CLK error	LK WAIT TO	LK PRT error	LADRP error	PID error	FIFO error	DLL CTL error
48	SMA A/C SIDE: DIMM SIG ERR DIMM: 3000 1090 (b37 - 30) / 3800 1090 (b37 - 30)							
	IMVSEQ	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LQARB	SCANSEQ
49	SMA A/C SIDE: UNCOR ERR DIMM: 3000 1094 (b37 - 30) / 3800 1094 (b37 - 30)							
	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LOCKCUE SEQ	LQARB	SCANSEQ
4A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	SMA B/D SIDE: SMA STS: 3200 001C (b17 - 10) / 3A00 001C (b17 - 10)							
	Reserved	Reserved	CACHE PK slot signal bit ‘01’: A side ‘10’: B side	b2 - 3 parity bit	Reserved	SM disconnect status bit	SM power down status bit	
4D	SMA B/D SIDE: SMA STS: 3200 001C (b27 - 20) / 3A00 001C (b27 - 20)							
	disconnect request bit	SM self- refresh status bit	SM voltage status bit	LSI voltage status bit	System timer enable status bit	SMA all lock status bit	SM ready status bit	SMA LSI blockade status bit
4E	SMA B/D SIDE: SMA STS: 3200 001C (b37 - 30) / 3A00 001C (b37 - 30)							
	SM PK version bit				SMA LSI version bit			
4F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 81 Byte40-7F(SM ERROR)(Byte28[bit2-3]=x'01') (2/4)

	0	1	2	3	4	5	6	7
50	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b07 - 00) / 3A00 0120 (bit07 - 00)							
	WDB error	MWB error	MRB error	RDB error	RWB error	MARB error	MVSEQ error	MSEQ error
51	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b17 - 10) / 3A00 0120 (b17 - 10)							
	RDSEQ error	RTNSEQ error	REFCTL error	LQARB error	LQ error	IWDB error	IWB error	IRDB error
52	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b27 - 20) / 3A00 0120 (b27 - 20)							
	IRB error	IARB error	IMVSEQ error	ISEQ error	TIMER error	SCSEQ error	BCREG error	SLOT error
53	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b37 - 30) / 3A00 0120 (b37 - 30)							
	PD address error	CLK error	LK WAIT TO	LK PRT error	LADRP error	PID error	FIFO Error	DLL CTL error
54	SMA B/D SIDE: DIMM SIG ERR DIMM: 3200 1090 (b37 - 30) / 3A00 1090 (b37 - 30)							
	IMVSEQ	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LQARB	SCANSEQ
55	SMA B/D SIDE: UNCOR ERR DIMM: 3200 1094 (b37-30) / 3A00 1094 (b37 - 30)							
	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LOCKCUE SEQ	LQARB	SCANSEQ
56	MPA: XR TIMEOUT: 4200 06A8 (b00) / 4200 0594 (b30) / 4200 05AC (b30) / 4200 05C4 (b30) / 4200 05DC (b30) / 4200 05F4 (b30)							
	Reserved	Reserved	SEQ0 timer timeout	SEQ1 timer timeout	SM side A SEQ2 timer timeout	SM side B SEQ2 timer timeout	SM side C SEQ2 timer timeout	SM side D SEQ2 timer timeout
57	MPA: MPASTS: 4200 0100 (b37 - 30)							
	BCINT or SCANINT	Master CHK3 status bit	Slave CHK3 status bit	FM IF CHK1B status bit	Reserved	Reserved	XR IF CHK1B status bit	MPINT
58	MPA: BMDLOG: 4200 0164 (b27 - 20)							
	Reserved	SM copy mode enable	SM disable side C	SM disable side D	C0 path disable	C1 path disable	D0 path disable	D1 path disable
59	MPA: BMDLOG: 4200 0164 (b37 - 30)							
	Reserved	SM copy mode enable	SM disable side A	SM disable Side B	A0 path disable	A1 path disable	B0 path disable	B1 path disable
5A	PERR VALID / MACS ADR							
	Parity error	Reserved	Reserved	Reserved	Micro Access Address			
5B	MACS ADR							
	Micro Access Address							
5C	MACS ADR							
	Micro Access Address							
5D	MACS ADR							
	Micro Access Address							
5E	MACS ADR							
	Micro Access Address							
5F	MACS DAT							
	Micro Access Data							

Byte27 = 81 Byte40-7F(SM ERROR)(Byte28[bit2-3]=x'01') (3/4)

	0	1	2	3	4	5	6	7
60	MACS DAT							
	Micro Access Data							
61	MACS DAT							
	Micro Access Data							
62	MACS DAT							
	Micro Access Data							
63	MACS DAT							
	Micro Access Data							
64	MACS DAT							
	Micro Access Data							
65	MACS DAT							
	Micro Access Data							
66	MACS DAT							
	Micro Access Data							
67	MPA: REG1 ADR0 AC: 4200 0368 (b27 - 20)							
	BUF							
68	MPA: REG1 ADR0 AC: 4200 0368 (b37 - 30)							
	BUF							
69	MPA: REG1 ADR1 AC: 4200 036C (b07 - 00)							
	BUF							
6A	MPA: REG1 ADR1 AC: 4200 036C (b17 - 10)							
	BUF							
6B	MPA: REG1 ADR1 AC: 4200 036C (b27 - 20)							
	BUF							
6C	RXA EDT							
	Side A / Side C Error Data							
6D	RXA EDT							
	Side A / Side C Error Data							
6E	RXA EDT							
	Side A / Side C Error Data							
6F	RXA EDT							
	Side A / Side C Error Data							

Byte27 = 81 Byte40-7F(SM ERROR)(Byte28[bit2-3]=x'01') (4/4)

	0	1	2	3	4	5	6	7
70	MPA: PATH TIMEOUT: 4210 j20C (b30) / 4211 j20C (b30) / 4212 j20C (b30) / 4213 j20C (b30) / 4214 j20C (b30) / 4215 j20C (b30) / 4216 j20C (b30) / 4217 j20C (b30) j: 4 ~ 7 (MP# 0 ~ 3)							
	SHSN data received wait time out							
71	MPA: REGXRX: 4200 0828 (b17 - 10)							
	BEH PER	BEL PER	BET PER		CMPADR_PER			
72	MPA: REGXRX: 4200 0828 (b27 - 20)							
	ADR_PER							
73	MPA: REGXRX: 4200 0828 (b37 - 30)							
	ADR_PER							
74	MPA: REG0TX: 4200 0888 (b37 - 30)							
	MMOL_PER				MMO_PER			
75	MPA: RXB EDT							
	Side B / Side D Error Data							
76	MPA: RXB EDT							
	Side B / Side D Error Data							
77	MPA: RXB EDT							
	Side B / Side D Error Data							
78	MPA: RXB EDT							
	Side B / Side D Error Data							
79	MPA: ACSMODE: 4200 0150 (b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Side A	Side B	Side C	Side D
7A	MPA: ACSMODE: 4200 0150 (b17 - 10)							
	SM1R	SM1W	2RLCK	2WLCK	2RNLCK	2WNLCK	1R1W	1R2W
7B	MPA: ACSMODE: 4200 0150 (b27 - 20)							
	Reserved	Reserved	Reserved	Reserved	Access pattern 1:SM read	SM read	SM write	Atomic
7C	MPA: ACSMODE: 4200 0150 (b37 - 30)							
	Resource lock	SMAIO read	SMAIO write	Broadcast read	Broadcast write	SCAN read	SCAN write	Reserved
7D	MPA: ACSLOG: 4200 0154 (b07 - 00)							
	1 st RD	1 st WT	1 st LK0	1 st LK1	1 st GNT A0	1 st GNT A1	1 st GNT B0	1 st GNT B1
7E	MPA: ACSLOG: 4200 0154 (b17 - 10)							
	1 st GNT C0	1 st GNT C1	1 st GNT D0	1 st GNT D1	2 nd RD	2 nd WT	2 nd LK0	2 nd LK1
7F	MPA: ACSLOG: 4200 0154 (b27 - 20)							
	2 nd GNT A0	2 nd GNT A1	2 nd GNT B0	2 nd GNT B1	2 nd GNT C0	2 nd GNT C1	2 nd GNT D0	2 nd GNT D1

Byte27 = 81 Byte40-7F(SHSN ERROR)(Byte28[bit2-3]=x'10') (1/4)

	0	1	2	3	4	5	6	7
40	SMA: SMA STS: 3i00 001C (b17 - 10) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	Reserved	Reserved	CACHE PK slot signal bit ‘01’ : A side ‘10’ : B side	‘01’ : C side ‘11’ : D side	b2 - 3 parity bit	Reserved	SM disconnect status bit	SM power down status bit
41	SMA: SMA STS: 3i00 001C (b27 - 20) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	disconnect request bit	SM self- refresh status bit	SM voltage status bit	LSI voltage status bit	System timer enable status bit	SMA all lock status bit	SM ready status bit	SMA LSI blockade status bit
42	SMA: SMA STS: 3i00 001C (b37 - 30) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	SM PK version bit				SMA LSI version bit			
43	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	SMA: PATH0 RXB ERR: 3i00 0104 (b07 - 00) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	RXB CDM parity error	RXB address parity error	RXB write data parity error	RX TO error	SYNC 0 UND	SYNC 1 UND	SYNC 0 OVR	SYNC 1 OVR
45	SMA: PATH0 RXB ERR: 3i00 0104 (b17 -10) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	LRC error	LRC CMP error	LOCK CMD error	R/W CMD error	ENT ON error	ENT OFF error	RPTR error	NEXT RPTR error
46	SMA: PATH0 RXB ERR: 3i00 0104 (b27 - 20) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	RX CNT error	PCNT 0 error	RX SEQ error	Reserved	REQ TO error	Reserved	Reserved	Reserved
47	SMA: PATH0 RXB ERR: 3i00 0104 (b37 - 30) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	CMD PGEN error	ADR PGEN error	WDT PGEN error	Reserved	RXB parity error	Reserved	Reserved	Reserved
48	SMA: PATH0 RXB ERR: 3i00 0108 (b07 - 00) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	IR PID parity error	IRB parity error	Reserved	Reserved	PR PID parity error	PRB parity error	PRCC parity error	Reserved
49	SMA: PATH0 RXB ERR: 3i00 0108 (b17 - 10) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	PCNT 1 error	PRB CNT error	TX SEQ error	PID error	PRB TO error	EINT error	Reserved	Reserved
4A	SMA: PATH0 RXB ERR: 3i00 0108 (b27 - 20) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	MEM BKUP BSY	PATH BKUP BSY	DBL ALLK error	ALLK MPID error	DIMM INV error	SM AREA error	Reserved	MPA OUT error
4B	SMA: PATH0 RXB ERR: 3i00 0108 (b37 - 30) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	SMA: PATH0 RXB ERR: 3i00 0114 (b07 - 00) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	RXB CDM parity error	RXB address parity error	RXB write data parity error	RX TO error	SYNC 0 UND	SYNC 1 UND	SYNC 0 OVR	SYNC 1 OVR
4D	SMA: PATH0 RXB ERR: 3i00 0114 (b17 - 10) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	LRC error	LRC CMP error	LOCK CMD error	R/W CMD error	ENT ON error	ENT OFF error	RPTR error	NEXT RPTR error
4E	SMA: PATH0 RXB ERR: 3i00 0114 (b27 - 20) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	RX CNT error	PCNT 0 error	RX SEQ error	Reserved	REQ TO error	Reserved	Reserved	Reserved
4F	SMA: PATH0 RXB ERR: 3i00 0114 (b37 - 30) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	CMD PGEN error	ADR PGEN error	WDT PGEN error	Reserved	RXB Parity error	Reserved	Reserved	Reserved

Byte27 = 81 Byte40-7F(SHSN ERROR)(Byte28[bit2-3]=x'10') (2/4)

	0	1	2	3	4	5	6	7
50	SMA: PATH0 RXB ERR: 3i00 0118 (b07 - 00) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	IR PID parity error	IRB parity error	Reserved	Reserved	PR PID parity error	PRB parity error	PRCC parity error	Reserved
51	SMA: PATH0 RXB ERR: 3i00 0118 (b17 - 10) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	PCNT 1 error	PRB CNT error	TX SEQ error	PID error	PRB TO error	EINT error	Reserved	Reserved
52	SMA: PATH0 RXB ERR: 3i00 0118 (b27 - 20) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	MEM BKUP BSY	PATH BKUP BSY	DBL ALLK error	ALLK MPID error	DIMM INV error	SM AREA error	Reserved	MPA OUT error
53	SMA: PATH0 RXB ERR: 3i00 0118 (b37 - 30) i: 0 (side A) / 2 (side B) / 8 (side C) / A (side D)							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
54	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
55	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
56	MPA: XR TIMEOUT: 4200 06A8 (b00) / 4200 0594 (b30) / 4200 05AC (b30) / 4200 05C4 (b30) / 4200 05DC (b30) / 4200 05F4 (b30)							
	Reserved	Reserved	SEQ0 timer timeout	SEQ1 timer timeout	SM side A SEQ2 timer timeout	SM side B SEQ2 timer timeout	SM side C SEQ2 timer timeout	SM side D SEQ2 timer timeout
57	MPA: MPASTS: 4200 0100 (b37 - 30)							
	BCINT or SCANINT	Master CHK3 status bit	Slave CHK3 status bit	FM IF CHK1B status bit	Reserved	Reserved	XR IF CHK1B status bit	MPINT
58	MPA: BMDLOG: 4200 0164 (b27 - 20)							
	Reserved	SM copy mode enable	SM disable side C	SM disable side D	C0 path disable	C1 path disable	D0 path disable	D1 path disable
59	MPA: BMDLOG: 4200 0164 (b37 - 30)							
	Reserved	SM copy mode enable	SM disable side A	SM disable side B	A0 path disable	A1 path disable	B0 path disable	B1 path disable
5A	PERR VALID / MACS ADR							
	Parity error	Reserved	Reserved	Reserved	Micro Access Address			
5B	MACS ADR							
	Micro Access Address							
5C	MACS ADR							
	Micro Access Address							
5D	MACS ADR							
	Micro Access Address							
5E	MACS ADR							
	Micro Access Address							
5F	MACS DAT							
	Micro Access Data							

Byte27 = 81 Byte40-7F(SHSN ERROR)(Byte28[bit2-3]=x'10') (3/4)

	0	1	2	3	4	5	6	7
60	MACS DAT							
	Micro Access Data							
61	MACS DAT							
	Micro Access Data							
62	MACS DAT							
	Micro Access Data							
63	MACS DAT							
	Micro Access Data							
64	MACS DAT							
	Micro Access Data							
65	MACS DAT							
	Micro Access Data							
66	MACS DAT							
	Micro Access Data							
67	MPA: REG1 ADR0 AC: 4200 0368 (b27 - 20)							
	BUF							
68	MPA: REG1 ADR0 AC: 42000368 (b37 - 30)							
	BUF							
69	MPA: REG1 ADR1 AC: 4200 036C (b07 - 00)							
	BUF							
6A	MPA: REG1 ADR1 AC: 4200 036C (b17 - 10)							
	BUF							
6B	MPA: REG1 ADR1 AC: 4200 036C (b27 - 20)							
	BUF							
6C	RXA EDT							
	Side A / Side C Error Data							
6D	RXA EDT							
	Side A / Side C Error Data							
6E	RXA EDT							
	Side A / Side C Error Data							
6F	RXA EDT							
	Side A / Side C Error Data							

Byte27 = 81 Byte40-7F(SHSN ERROR)(Byte28[bit2-3]=x'10') (4/4)

	0	1	2	3	4	5	6	7
70	MPA: PATH TIMEOUT: 4210 j20C (b30) / 4211 j20C (b30) / 4212 j20C (b30) / 4213 j20C (b30) / 4214 j20C (b30) / 4215 j20C (b30) / 4216 j20C (b30) / 4217 j20C (b30) j: 4 ~ 7 (MP# 0 ~ 3)							
	SHSN data received wait time out							
71	MPA: REGXRX: 4200 0828 (b17 - 10)							
	BEH PER	BEL PER	BET PER		CMPADR_PER			
72	MPA: REGXRX: 4200 0828 (b27 - 20)							
	ADR_PER							
73	MPA: REGXRX: 4200 0828 (b37 - 30)							
	ADR_PER							
74	MPA: REG0TX: 4200 0888 (b37 - 30)							
	MMOL_PER				MMO_PER			
75	MPA: RXB EDT							
	Side B / Side D Error Data							
76	MPA: RXB EDT							
	Side B / Side D Error Data							
77	MPA: RXB EDT							
	Side B / Side D Error Data							
78	MPA: RXB EDT							
	Side B / Side D Error Data							
79	MPA: ACSMODE: 4200 0150 (b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Side A	Side B	Side C	Side D
7A	MPA: ACSMODE: 4200 0150 (b17 - 10)							
	SM1R	SM1W	2RLCK	2WLCK	2RNLCK	2WNLCK	1R1W	1R2W
7B	MPA: ACSMODE: 4200 0150 (b27 - 20)							
	Reserved	Reserved	Reserved	Reserved	Access pattern 1:SM read	SM read	SM write	Atomic
7C	MPA: ACSMODE: 4200 0150 (b37 - 30)							
	Resource lock	SMAIO read	SMAIO write	Broadcast read	Broadcast write	SCAN read	SCAN write	Reserved
7D	MPA: ACSLOG: 4200 0154 (b07 - 00)							
	1 st RD	1 st WT	1 st LK0	1 st LK1	1 st GNT A0	1 st GNT A1	1 st GNT B0	1 st GNT B1
7E	MPA: ACSLOG: 4200 0154 (b17 - 10)							
	1 st GNT C0	1 st GNT C1	1 st GNT D0	1 st GNT D1	2 nd RD	2 nd WT	2 nd LK0	2 nd LK1
7F	MPA: ACSLOG: 4200 0154 (b27 - 20)							
	2 nd GNT A0	2 nd GNT A1	2 nd GNT B0	2 nd GNT B1	2 nd GNT C0	2 nd GNT C1	2 nd GNT D0	2 nd GNT D1

Byte27 = 81 Byte40-7F(OTHERS ERROR)(Byte28[bit2-3]=x'11') (1/4)

	0	1	2	3	4	5	6	7
40	SMA A/C SIDE: SMA STS: 3000 001C (b17 - 10) / 3800 001C (b17 - 10)							
	Reserved	Reserved	CACHE PK slot signal bit '01' : A side '10' : B side	'01' : C side '11' : D side	b2 - 3 parity bit	Reserved	SM disconnect status bit	SM power down status bit
41	SMA A/C SIDE: SMA STS: 3000 001C (b27 - 20) / 3800 001C (b27 - 20)							
	disconnect request bit	SM self- refresh status bit	SM voltage status bit	LSI voltage status bit	System timer enable status bit	SMA all lock status bit	SM ready status bit	SMA LSI blockade status bit
42	SMA A/C SIDE: SMA STS: 3000 001C (b37 - 30) / 3800 001C (b37 - 30)							
	SM PK version bit				SMA LSI version bit			
43	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
44	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b07 - 00) / 3800 0120 (b07 - 00)							
	WDB error	MWB error	MRB error	RDB error	RWB error	MARB error	MVSEQ error	MSEQ error
45	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b07 - 00) / 3800 0120 (b17 - 10)							
	RDSEQ error	RTNSEQ error	REFCTL error	LQARB error	LQ error	IWDB error	IWB error	IRDB error
46	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b27 - 20) / 3800 0120 (b27 - 20)							
	IRB error	IARB error	IMVSEQ error	ISEQ error	TIMER error	SCSEQ error	BCREG error	SLOT error
47	SMA A/C SIDE: BOARD ERR STS: 3000 0120 (b37 - 30) / 3800 0120 (b37 - 30)							
	PD address error	CLK error	LK WAIT TO	LK PRT error	LADRP error	PID error	FIFO error	DLL CTL error
48	SMA A/C SIDE: DIMM SIG ERR DIMM: 3000 1090 (b37 - 30) / 3800 1090 (b37 - 30)							
	IMVSEQ	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LQARB	SCANSEQ
49	SMA A/C SIDE: UNCOR ERR DIMM: 3000 1094 (b37 - 30) / 3800 1094 (b37 - 30)							
	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LOCKCUE SEQ	LQARB	SCANSEQ
4A	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4B	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
4C	SMA B/D SIDE: SMA STS: 3200 001C (b17 - 10) / 3A00 001C (b17 - 10)							
	Reserved	Reserved	CACHE PK slot signal bit '01' : A side '10' : B side	'01' : C side '11' : D side	b2 - 3 parity bit	Reserved	SM disconnect status bit	SM power down status bit
4D	SMA B/D SIDE: SMA STS: 3200 001C (b27 - 20) / 3A00 001C (b27 - 20)							
	disconnect request bit	SM self- refresh status bit	SM voltage status bit	LSI voltage status bit	System timer enable status bit	SMA all lock status bit	SM ready status bit	SMA LSI blockade status bit
4E	SMA B/D SIDE: SMA STS: 3200 001C (b37 - 30) / 3A00 001C (b37 - 30)							
	SM PK version bit				SMA LSI version bit			
4F	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved

Byte27 = 81 Byte40-7F(OTHERS ERROR)(Byte28[bit2-3]=x'11') (2/4)

	0	1	2	3	4	5	6	7
50	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b07 - 00) / 3A00 0120 (bit07 - 00)							
	WDB error	MWB error	MRB error	RDB error	RWB error	MARB error	MVSEQ error	MSEQ error
51	SMA B/D SIDE: BOARD ERR STS: 3200 0120 (b17 - 10) / 3A00 0120 (b17 - 10)							
	RDSEQ error	RTNSEQ error	REFCTL error	LQARB error	LQ error	IWDB error	IWB error	IRDB error
52	SMA B/D SIDE:BOARD ERR STS: 3200 0120 (b27 - 20) / 3A00 0120 (b27 - 20)							
	IRB error	IARB error	IMVSEQ error	ISEQ error	TIMER error	SCSEQ error	BCREG error	SLOT error
53	SMA B/D SIDE:BOARD ERR STS: 3200 0120 (b37 - 30) / 3A00 0120 (b37 - 30)							
	PD address error	CLK error	LK WAIT TO	LK PRT error	LADRP error	PID error	FIFO error	DLL CTL error
54	SMA B/D SIDE:DIMM SIG ERR DIMM: 3200 1090(b37-30) / 3A00 1090 (b37-30)							
	IMVSEQ	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LQARB	SCANSEQ
55	SMA B/D SIDE:UNCOR ERR DIMM: 3200 1094(b37-30) / 3A00 1094 (b37-30)							
	TIMERSEQ	CCHKMST1 SEQ	CCHKMST2 SEQ	CCHKSLV1 SEQ	CCHKSLV2 SEQ	LOCKCUE SEQ	LQARB	SCANSEQ
56	Reserved							
	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
57	MPA: MPASTS: 4200 0100 (b37 - 30)							
	BCINT or SCANINT	Master CHK3 status bit	Slave CHK3 status bit	FM IF CHK1B status bit	Reserved	Reserved	XR IF CHK1B status bit	MPINT
58	MPA: BMDLOG: 4200 0164 (b27 - 20)							
	Reserved	SM copy mode enable	SM disable side C	SM disable side D	C0 path disable	C1 path disable	D0 path disable	D1 path disable
59	MPA: BMDLOG: 4200 0164 (b37 - 30)							
	Reserved	SM copy mode enable	SM disable side A	SM disable side B	A0 path disable	A1 path disable	B0 path disable	B1 path disable
5A	PERR VALID / MACS ADR							
	Parity error	Reserved	Reserved	Reserved	Micro Access Address			
5B	MACS ADR							
	Micro Access Address							
5C	MACS ADR							
	Micro Access Address							
5D	MACS ADR							
	Micro Access Address							
5E	MACS ADR							
	Micro Access Address							
5F	MACS DAT							
	Micro Access Data							

Byte27 = 81 Byte40-7F(OTHERS ERROR)(Byte28[bit2-3]=x'11') (3/4)

	0	1	2	3	4	5	6	7
60	MACS DAT							
	Micro Access Data							
61	MACS DAT							
	Micro Access Data							
62	MACS DAT							
	Micro Access Data							
63	MACS DAT							
	Micro Access Data							
64	MACS DAT							
	Micro Access Data							
65	MACS DAT							
	Micro Access Data							
66	MACS DAT							
	Micro Access Data							
67	MPA: REG1 ADR0 AC: 4200 0368 (b27 - 20)							
	BUF							
68	MPA: REG1 ADR0 AC: 42000368 (b37 - 30)							
	BUF							
69	MPA: REG1 ADR1 AC: 4200 036C (b07 - 00)							
	BUF							
6A	MPA: REG1 ADR1 AC: 4200 036C (b17 - 10)							
	BUF							
6B	MPA: REG1 ADR1 AC: 4200 036C (b27 - 20)							
	BUF							
6C	RXA EDT							
	Side A / Side C Error Data							
6D	RXA EDT							
	Side A / Side C Error Data							
6E	RXA EDT							
	Side A / Side C Error Data							
6F	RXA EDT							
	Side A / Side C Error Data							

Byte27 = 81 Byte40-7F(OTHERS ERROR)(Byte28[bit2-3]=x'11') (4/4)

	0	1	2	3	4	5	6	7
70	MPA: PATH TIMEOUT: 4210 j20C (b30) / 4211 j20C (b30) / 4212 j20C (b30) / 4213 j20C (b30) / 4214 j20C (b30) / 4215 j20C (b30) / 4216 j20C (b30) / 4217 j20C (b30) j: 4 ~ 7 (MP# 0 ~ 3)							
	SHSN data received wait time out							
71	MPA: REGXRX: 4200 0828 (b17 - 10)							
	BEH PER	BEL PER	BET PER		CMPADR_PER			
72	MPA: REGXRX: 4200 0828 (b27 - 20)							
	ADR_PER							
73	MPA: REGXRX: 4200 0828 (b37 - 30)							
	ADR_PER							
74	MPA: REG0TX: 4200 0888 (b37 - 30)							
	MMOL_PER				MMO_PER			
75	MPA: RXB EDT							
	Side B / Side D Error Data							
76	MPA: RXB EDT							
	Side B / Side D Error Data							
77	MPA: RXB EDT							
	Side B / Side D Error Data							
78	MPA: RXB EDT							
	Side B / Side D Error Data							
79	MPA: ACSMODE: 4200 0150 (b07 - 00)							
	Reserved	Reserved	Reserved	Reserved	Side A	Side B	Side C	Side D
7A	MPA: ACSMODE: 4200 0150 (b17 - 10)							
	SM1R	SM1W	2RLCK	2WLCK	2RNLCK	2WNLCK	1R1W	1R2W
7B	MPA: ACSMODE: 4200 0150 (b27 - 20)							
	Reserved	Reserved	Reserved	Reserved	Access pattern 1:SM read	SM read	SM write	Atomic
7C	MPA: ACSMODE: 4200 0150 (b37 - 30)							
	Resource lock	SMAIO read	SMAIO write	Broadcast read	Broadcast write	SCAN read	SCAN write	Reserved
7D	MPA: ACSLOG: 4200 0154 (b07 - 00)							
	1 st RD	1 st WT	1 st LK0	1 st LK1	1 st GNT A0	1 st GNT A1	1 st GNT B0	1 st GNT B1
7E	MPA: ACSLOG: 4200 0154 (b17 - 10)							
	1 st GNT C0	1 st GNT C1	1 st GNT D0	1 st GNT D1	2 nd RD	2 nd WT	2 nd LK0	2 nd LK1
7F	MPA: ACSLOG: 4200 0154 (b27 - 20)							
	2 nd GNT A0	2 nd GNT A1	2 nd GNT B0	2 nd GNT B1	2 nd GNT C0	2 nd GNT C1	2 nd GNT D0	2 nd GNT D1

Byte27 = 81 & Byte34:bit4-7 = 3 (CHK3 Reset) (1/2)

	0	1	2	3	4	5	6	7						
00	Time stamp													
01														
02														
03	Format Message													
04	Processor ID													
05	System error code													
06														
07	Reserved													
08	JCB ID													
09	REQ ID													
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log						
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	Reserved						
0C							VLD							
0D	SEMI-SYNC	RCU SIM	Reserved											
0E	LDEV#													
0F														
10	CDEV#													
11	RDEV#													
12	JCB#													
13														
14	Internal SSB log number													
15														
16	Related failure log number													
17														
18	Related SIM log number													
19														
1A	Related SSB log number													
1B														
1C	(Reserved)													
1D	SCB													
1E														
1F	Basic SSB													
20														
21														
22	Count													
23														
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device number										
25	Cylinder address (low)													
26	Cylinder address (high)								Head address					
27	Format (x'8')								Message (x'1')					
28	Related internal SSB log number													
29														
2A	Not used (x'00')													
2B	Not used (x'00')													
2C	Old PSW (Interrupted address)													
2D														
2E														
2F														
30	Internal SSB log number (RESET)													
31														
32	Related RESET detail log number													
33														
34	PCB number				Message code (x'3')									
35	SSID (low) of self subsystem													
36	Symptom code (x'FF81' : LDEV type = DKU87I / x'EF81' : LDEV type = DKU86I)													
37														
38	Log and message control (x'00')													
39	Program action code (x'00')													
3A	Configuration information													
3B														
3C	Not used													
3D	Cylinder address													
3E														
3F	Head address													

Byte27 = 81 & Byte34:bit4-7 = 3 (CHK3 Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information (rstkindc)							
43	Initialize interface information (inidcodec)							
44	Reset type code (rstkindc)							
45	Reset type code (rstkindoldc)							
46	1st reset type code (firstkindc)							
47	Reset SSB type code (ssbkindc)							
48	Interrupt task code (inttaskc)							
49	Self CHK1A difference code (genchk1ac)							
4A	Multi reset type code (rstmultics)							
4B								
4C								
4D	Interrupted address							
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number (from LCT (At the time of DKC x'00'))							
55								
56	Current LPN number							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B								
5C	sys status							
5D	REQ ID (from LCT)							
5E								
5F	JCB ID (from LCT)							
60								
61	wchklmpbitmap (from LCT)							
62								
63								
64	jcb id							
65								
66	req id							
67								
68	Job type code							
69								
6A	job status							
6B								
6C	Executing processor number							
6D								
6E	Initiation cause code							
6F								
70	Initiation detail code							
71								
72	Command code							
73								
74	Channel sequence							
75								
76	Reserved (x'00')							
77								
78	DCN (wldcn11)							
79								
7A	DCN (wldcn21)							
7B								
7C	DKCMAIN program counter							
7D								
7E	SLV-DCN (slvdcnr)							
7F								

Byte27 = 88 & Byte34:b4-7=A & Byte28 = 01 (LCP Failure: CHK-2/ADP I/F Error) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'8')			
28	LCP error message"(x'01':LCP CHK-2/ADP interface error)							
29	LCP error code							
2A								
2B	XR4B (H)							
	BBF OVERRUN	PND OVERRUN	0	0	0	0	0	0
2C	XR4B (L)							
	LRCA ERR	LRCB ERR	BBFP PER	BBIP PER	BBOP PER	PND CK2	CNT PER	REQ CNT OVERRUN
2D	XR3E (H)							
	RBFA APER	RBFB APER	RBFC APER	RBF WRDER	MBFA APER	MBFB APER	MBFC APER	MBF WRDER
2E	XR3E (L)							
	FRM RCTPE	CMP DPER	XFR CHRPER	XFR SEQPER	M-CNT PER	H-CNT PER	0	0
2F	XR55 (H)							
	BUSPE PA	LRCER PA	REQ ER A	ADTDT ER A	0	0	0	0
30	XR55 (L)							
	BUSPE PB	LRCER PB	REQ ER B	ADTDT ER B	0	0	0	0
31								
	Control flag	0	0	STOP (XR49:bit15)	CK2 DTX (XR4A:bit0)	CK2 LIN (XR4A:bit1)	IFERA (XR51:bit12)	IFERB (XR51:bit13)

Byte27 = 88 & Byte34:b4-7=A & Byte28 = 01 (LCP Failure: CHK-2/ADP I/F Error) (2/2)

	0	1	2	3	4	5	6	7
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'A')			
35	SSID (Low) of self subsystem							
36	Symptom code (x'FF88': LDEV type = DKU87I/x'EF88': LDEV type = DKU86I)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	LCP error message (x'01')							
41	Key code (x'04')							
42	LCP error code							
43								
44	x'00'							
45	Command code							
46	x'00'							
47	x'00'							
48	XR4B							
49								
4A	XR3E							
4B								
4C	XR55							
4D								
4E	Control flag							
4F	x'00'							
50								
51	Not used							
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 02 (LCP Failure: Link Receive Error) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'8')			
28	LCP error message”(x'02':Link receive error)							
29	LCP error code							
2A								
2B	XR36 (H)							
	L-SIG ERR0	L-SIG ERR1	CONN ERR0	CONN ERR1	CONN ERR2	0	0	0
2C	XR37 (H)							
	CONN ERR3	L-SIG ERR	CODE VIOLA	SEQ ERR	CRC ERR	ABORT ERR	PROT ERR0	PROT ERR1
2D	XR37 (L)							
	PROT ERR2	PROT ERR3	PROT ERR4	PROT ERR5	PROT ERR6	PROT ERR7	PROT ERR8	PROT ERR9
2E	XR33 (H)							
	C-SOF ERR	P-SOF ERR	D-EOF ERR	P-EOF ERR	L-SIG ERR	CODE VIOLA	SEQ ERR	CRC ERR
2F	XR33 (L)							
	0	0	0	R-CNT 0	R-CNT 1	R-CNT 2	R-CNT 3	R-CNT 4
30								
	0	0	STOP (XR49:bit15)	ABORT (XR32:bit11)	ER RCV1 (XR4A:bit3)	ER RCV2 (XR4A:bit4)	ER RCV3 (XR4A:bit5)	OTHER (XR51:bit6)
31	XR2A (L)							
	Field Information							

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 02 (LCP Failure: Link Receive Error) (2/2)

	0	1	2	3	4	5	6	7
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'A')			
35	SSID (Low) of self subsystem							
36	Symptom code (x'FF88': LDEV type = DKU87I/x'EF88': LDEV type = DKU86I)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40	LCP error message (x'02')							
41	Key code (x'04')							
42	LCP error code							
43								
44	x'00'							
45	Command code							
46	x'00'							
47	x'00'							
48	XR36							
49								
4A	XR37							
4B								
4C	XR33							
4D								
4E	Control flag							
4F	x'00'							
50								
	Not used							
7F								

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0C (LCP Failure: Long Write Data) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'8')			
28	LCP error message (x'0C':Long Write Data)							
29	LCP error code							
2A								
2B	Request count							
2C								
2D	Received data count							
2E								
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'A')			
35	SSID (low) of self subsystem							
36	Symptom Code (x'FF88': LDEV type = DKU87I/x'EF88': LDEV type = DKU86I)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0C (LCP Failure: Long Write Data) (2/2)

	0	1	2	3	4	5	6	7
40	LCP error message (x'0C')							
41	Key code (x'04')							
42	LCP error code							
43								
44	x'00'							
45	Command code							
46	x'00'							
47	x'00'							
48	Requested count							
49								
4A	Received data count							
4B								
4C	x'00'							
4D	x'00'							
4E	x'00'							
4F	x'00'							
50								
51	Not used							
52								
53								
54								
7F								

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0E (LCP Failure: Timeout) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'8')			
28	LCP error message (x'0E':Timeout)							
29	LCP error code							
2A								
2B	Reason code (*1)							
2C								
2D	XR49 (H)							
	ESYCN	END	RDYOK	RDYRCV	0	CMDFRM	ACPRSP	DATA XF
2E	XR49 (L)							
	XFREND	DEVEND	CNTFLG	EOF	CMPEQ	CMPGT	0	STOP
2F	XR51 (H)							
	CAL PA	CAL PB	RDY	P BSY	CALL	SELTD	CHK1	DCK1
30	XR51 (L)							
	BFBSY	ATTN	0	0	IFERA	IFERB	PA BSY	PB BSY
31	XR48 (H)							
	RUN BSY	RUN TRS	RUN ADP	DAT REQ MOD	XFSTOOP	CDFLG	CMPA	CMPB
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'A')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF88': LDEV type = DKU87I/x'EF88': LDEV type = DKU86I)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							

Byte27 = 88 & Byte34:b4-7 = A & Byte28 = 0E (LCP Failure: Timeout) (2/2)

	0	1	2	3	4	5	6	7
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40	LCP error message (x'0E')							
41	Key code (x'04')							
42	LCP error code							
43								
44	x'00'							
45	Command code							
46	x'00'							
47	x'00'							
48	Reason code (*1)							
49								
4A	XR49 (H)							
	ESYCN	END	RDYOK	RDYRCV	0	CMDFRM	ACPRSP	DATA XF
4B	XR49 (L)							
	XFREND	DEVEND	CNTFLG	EOF	CMPEQ	CMPGT	0	STOP
4C	XR51 (H)							
	CAL PA	CAL PB	RDY	P BSY	CALL	SELTD	CHK1	DCK1
4D	XR51 (L)							
	BFBSY	ATTN	0	0	IFERA	IFERB	PA BSY	PB BSY
4E	XR48 (H)							
	RUN BSY	RUN TRS	RUN ADP	DAT REQ MOD	XFSTOP	CDFLG	CMPA	CMPB
4F	XR48 (L)							
	DATA XF	RD / WR (0) (1)	1st CCW	RD/WR XFR	MEFLG	BBF SWEEP	BBIP INC	BBIP INC
50								
7F	Not used							

*1: Reason code

x'FF01' : (Read) Timeout in waiting for ACP CMD RSP
 x'FF02' : (Read) Timeout in waiting for frame header
 x'FF03' : (Read) Timeout in waiting for data frame
 x'FF04' : (Read) Timeout in waiting for end of data transfer
 x'FF05' : (Read) Timeout in waiting for data frame
 x'FF06' : (Read) Timeout in waiting for end of data transfer
 x'FF07' : (Read) Timeout in waiting for command frame
 x'FF08' : (Read) Timeout in waiting for DATA REQ
 x'FF09' : (Read) Timeout in waiting for status
 x'FFF1' : (Write) Timeout in waiting for ACP CMD RSP
 x'FFF2' : (Write) Timeout in waiting for first data
 x'FFF3' : (Write) Timeout in waiting for frame header
 x'FFF4' : (Write) Timeout in waiting for frame header
 x'FFF5' : (Write) Timeout in waiting for frame header
 x'FFF6' : (Write) Timeout in waiting for command frame
 x'FFF7' : (Write) Timeout in waiting for frame header
 x'FFF8' : (Write) Timeout in waiting for end of data transfer
 x'FFF9' : (Write) Timeout in waiting for status

Byte27 = 88 & Byte34:b4-7 = A & Byte28 ≠ 01/02/0C/0E (LCP Failure: Others) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'8')			
28	LCP error message (*1)							
29	LCP error code							
2A								
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
31	Not used (x'00')							
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'A')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF88': LDEV type = DKU87I/x'EF88': LDEV type = DKU86I)							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 88 & Byte34:b4-7 = A & Byte28 ≠ 01/02/0C/0E (LCP Failure: Others) (2/2)

	0	1	2	3	4	5	6	7
40	LCP error message (*1)							
41	Key code (x'04')							
42	LCP error code							
43								
44	x'00'							
45	Command code							
46	x'00'							
47	x'00'							
48	x'00'							
49	x'00'							
4A	x'00'							
4B	x'00'							
4C	x'00'							
4D	x'00'							
4E	x'00'							
4F	x'00'							
50	Not used							
51								
52								
53								
7F								

- *1: LCP error message
 x'00' : No message
 x'08' : Transmission error
 x'09' : Protocol error
 x'0D' : Retry abort

Byte27 = 89 & Byte0D Bit2 = 0 (ESCON/FICON CHA CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'9')			
28	Subcode							
29	Detail Information (See following pages)							
31								
32	Module ID							
33	Routine ID							
34	PCB number (*1)				Message code (x'0')			
35	SSID of self subsystem (low)							
36	Symptom code (x'FF89' : LDEV type = 6587 / x'EF89' : LDEV type = 6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Detail Information (See following pages)							
7F								

Byte27 = 89 & Byte0D Bit2 = 0 (FIBRE CHA CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Inf valid	Err code						
21	Segment Number							
22	Filemark	ECM	ILI	Reserved	Sense Key			
23	Information bytes							
24								
25								
26								
27	Format (x'8')				Message (x'9')			
28	Subcode							
29	Detail Information (See following pages)							
31								
32	Reserved							
33								
34	Module ID 1							
35	Routine ID1							
36	Module ID 2							
37	Routine ID2							
38	Master Path Number							
39	SCSI Initiator ID							
3A	SCSI Target ID							
3B	Logical Unit Number							
3C	SCSI Command Code							
3D	Processor Number							
3E	LDEV#							
3F								
40	Detail Information (See following pages)							
7F								

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(1) Byte28 = 00

	0	1	2	3	4	5	6	7
28	Subcode (x'00' : ESCON I/F CHA - XFR CHK2)							
29	CHA::XFS0							
	TRANSFER END	(Not used)	(Not used)	PFC STATUS	CHK2	CHK2A detected	CHK2B detected	CHK2C detected
2A	CHA::XFS0							
	CHK2D detected	TRUN CATION	HALT I/O	SEARCH DASD = CPU	SEARCH DASD > CPU	SEARCH DASD < CPU	(Not used)	ABORT detected
2B	CHA::XFS1							
	(Not used)	(Not used)	(Not used)	(Not used)	MICRO ABORT detected	HARD ABORT detected	Force CHK1 detected	Force CHK2 detected
2C	CHA::XFS1							
	NEXT SEG ADR REG STATUS	(Not used)	(Not used)	(Not used)	(Not used)	NEXT SEG ADR WAIT	PFC ZERO	(Not used)
2D	CHA::PDC							
	(Not used)	(Not used)	(Not used)	ENLPLTB 0: NORMAL 1: LIST XFR	(Not used)	(Not used)	(Not used)	(Not used)
2E	CHA::XFC0							
	ENBL XFR	EXEC XFR	(Not used)	(Not used)	(Not used)	CBP PNT INC STOP	CACHE I/O MODE	SSD MODE
2F	CHA::XFC0							
	Data Transfer Path: 0: ESCON => CACHE 2: ESCON => CBUF 3: CBUF => ESCON 4: CACHE => ESCON 6: CACHE => CBUF 7: CBUF => ESCON 8: CACHE SEARCH 9: CBUF SEARCH B: CACHE COPY				U-MODE (504B SB FORMAT)	(Not used)	(Not used)	WR FBA MODE
30	CHA::XFC1							
	(Not used)	ABORT EXEC	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(2) Byte28 = 01

	0	1	2	3	4	5	6	7
28	Subcode (x'01' : ESCON I/F CHA – Abnormal Cache Slot Status 1, (*1))							
29	Slot Status							
2A								
2B								
2C								
2D	WRITE in Progress	Make GAP	Execute ERASE	Update HA/R0	Update BM	Not used		
2E	SubBlock Number for Ready Response to Transfer							
2F	Start SubBlock Number for Incompletet Recode							
30	End SubBlock Number for Incompletet Recode							
31	Reserved							

*1: Expect for cache ECC/LRC error has been detected in read side.

*2: ① Module ID = 55 : After synchronous destage, cache slot with dirty attribute has been detected.
 ② Module ID ≠ 55 : In internal search or orientation process, incomplete subblock has been detected.

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(3) Byte28 = 02

	0	1	2	3	4	5	6	7
28	Subcode (x'02' : ESCON I/F CHA – Abnormal Cache Slot Status 1, (*1))							
29	Slot Status							
2A								
2B								
2C								
2D	WRITE in Progress	Make GAP	Execute ERASE	Update HA/R0	Update BM	Not used		
2E	SubBlock Number for Ready Response to Transfer							
2F	Start SubBlock Number for Incompletet Recode							
30	End SubBlock Number for Incompletet Recode							
31	Reserved							

*1: Expect for cache ECC/LRC error has been detected in read side.

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(4) Byte28 = 1F

	0	1	2	3	4	5	6	7
28	Subcode (x'1F': ESCON I/F CHA - XFR TIME OUT)							
29	CHA::XFS0							
	TRANSFER END	(Not used)	(Not used)	PFC STATUS	CHK2	CHK2A detected	CHK2B detected	CHK2C detected
2A	CHA::XFS0							
	CHK2D detected	TRUN- CATION	HALT I/O	SEARCH DASD = CPU	SEARCH DASD > CPU	SEARCH DASD < CPU	(Not used)	ABORT detected
2B	CHA::XFS1							
	(Not used)	(Not used)	(Not used)	(Not used)	MICRO ABORT detected	HARD ABORT detected	Force CHK1 detected	Force CHK2 detected
2C	CHA::XFS1							
	NEXT SEG ADR REG STATUS	(Not used)	(Not used)	(Not used)	(Not used)	NEXT SEG ADR WAIT	PFC ZERO	(Not used)
2D	CHA::PDC							
	(Not used)	(Not used)	(Not used)	ENLPLTB 0: NORMAL 1: LIST XFR	(Not used)	(Not used)	(Not used)	(Not used)
2E	CHA::XFC0							
	ENBL XFR	EXEC XFR	(Not used)	(Not used)	(Not used)	CBP PNT INC STOP	CACHE I/O MODE	SSD MODE
2F	CHA::XFC0							
	Data Transfer Path: 0: ESCON => CACHE 2: ESCON => CBUF 3: CBUF => ESCON 4: CACHE => ESCON 6: CACHE => CBUF 7: CBUF => ESCON 8: CACHE SEARCH 9: CBUF SEARCH B: CACHE COPY				U-MODE (504B SB FORMAT)	(Not used)	(Not used)	WR FBA MODE
30	CHA::XFC1							
	(Not used)	ABORT EXEC	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(5) Byte28 = 20

	0	1	2	3	4	5	6	7
28	Subcode (x'20' : FICON I/F CHA - XFR CHK2)							
29	CHA::XFS0							
	TRANSFER END	(Not used)	(Not used)	PFC STATUS	CHK2	CHK2A detected	CHK2B detected	CHK2C detected
2A	CHA::XFS0							
	CHK2D detected	TRUN- CATION	HALT I/O	SEARCH DASD = CPU	SEARCH DASD > CPU	SEARCH DASD < CPU	(Not used)	ABORT detected
2B	CHA::XFS1							
	(Not used)	(Not used)	(Not used)	(Not used)	MICRO ABORT detected	HARD ABORT detected	Force CHK1 detected	Force CHK2 detected
2C	CHA::XFS1							
	NEXT SEG ADR REG STATUS	(Not used)	(Not used)	(Not used)	(Not used)	NEXT SEG ADR WAIT	PFC ZERO	(Not used)
2D	CHA::DCTL							
	M128 FLG	RUN	READ FLG	ASYNC FLG	CNP FLG	CD FLG	COF	MSW FLG
2E	CHA::XFC0							
	ENBL XFR	EXEC XFR	(Not used)	(Not used)	(Not used)	CBP PNT INC STOP	CACHE I/O MODE	SSD MODE
2F	CHA::XFC0							
	Data Transfer Path: 0: ESCON => CACHE 2: ESCON => CBUF 3: CBUF => ESCON 4: CACHE => ESCON 6: CACHE => CBUF 7: CBUF => ESCON 8: CACHE SEARCH 9: CBUF SEARCH B: CACHE COPY				U-MODE (504B SB FORMAT)	(Not used)	(Not used)	WR FBA MODE
30	CHA::XFC1							
	(Not used)	ABORT EXEC	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(6) Byte28 = 3F

	0	1	2	3	4	5	6	7
28	Subcode (x'3F' : FICON I/F CHA - TIME OUT)							
29	CHA::XFS0							
	TRANSFER END	(Not used)	(Not used)	PFC STATUS	CHK2	CHK2A detected	CHK2B detected	CHK2C detected
2A	CHA::XFS0							
	CHK2D detected	TRUN- CATION	HALT I/O	SEARCH DASD = CPU	SEARCH DASD > CPU	SEARCH DASD < CPU	(Not used)	ABORT detected
2B	CHA::XFS1							
	(Not used)	(Not used)	(Not used)	(Not used)	MICRO ABORT detected	HARD ABORT detected	Force CHK1 detected	Force CHK2 detected
2C	CHA::XFS1							
	NEXT SEG ADR REG STATUS	(Not used)	(Not used)	(Not used)	(Not used)	NEXT SEG ADR WAIT	PFC ZERO	(Not used)
2D	CHA::DCTL							
	M128 FLG	RUN	READ FLG	ASYNC FLG	CNP FLG	CD FLG	COF	MSW FLG
2E	CHA::XFC0							
	ENBL XFR	EXEC XFR	(Not used)	(Not used)	(Not used)	CBP PNT INC STOP	CACHE I/O MODE	SSD MODE
2F	CHA::XFC0							
	Data Transfer Path: 0: ESCON => CACHE 2: ESCON => CBUF 3: CBUF => ESCON 4: CACHE => ESCON 6: CACHE => CBUF 7: CBUF => ESCON 8: CACHE SEARCH 9: CBUF SEARCH B: CACHE COPY				U-MODE (504B SB FORMAT)	(Not used)	(Not used)	WR FBA MODE
30	CHA::XFC1							
	(Not used)	ABORT EXEC	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(7) Byte28 = 60 ~ 7F

	0	1	2	3	4	5	6	7
28	Subcode (x'60' ~ x'6F' : NAS I/F CHA - DMA CHK2 (*1))							
29	NDTM::XFC							
	XFR Mode: '00': XFR Initialize '10': XFR Abort '11': XFR Start	CRC Mode: '00': No CRC '10': CRC Calculate (Cont.) '11': CRC Calculate (New)		(Not used)	(Not used)	INT DSBL 0: Enable 1: Disable	(Not used)	
2A	NDTM::XFC							
	Data Tarnsfer Path: 0: PCI => CACHE 1: PCI => CACHE(2SLV) 2: DBF => CACHE 3: DBF => CACHE(2SLV) 6: CACHE FORMAT 7: CACHE FORMAT(2SLV) 8: CACHE => PCI 9: CACHE => DBF C: CtoC COPY D: CinC COPY			PCI MODE 0: Mastr 1: Target	PBF MODE 0: No Fetch 1: Fetch	(Not used)	(Not used)	
2B	NDTM::XFC							
	CHSN DATA FORMAT '00': Sub-Block '10': I/O, no SLRC '11': I/O, with LRC	(Not used)	(Not used)	(Not used)	SLRC CHK 0: Enable 1: Disable	SP Mode 0: Normal 1: SP ACS	DIAG Mode 0: Normal 1: DIAG ACS	
2C	NDTM::XFC							
	REQID		PBF CHAIN Chain Number= 1 ~ 32 (32: When Set as '0')					
2D	XMPA::PnFINTS							
	(Not used)	(Not used)	Other XFR END DMA#0	Other XFR END DMA#1	Other XFR END DMA#2	Other XFR END DMA#3	Other XFR END DMA#4	Other XFR END DMA#5
2E	XMPA::PnFINTS							
	(Not used)	(Not used)	Own XFR END DMA#0	Own XFR END DMA#1	Own XFR END DMA#2	Own XFR END DMA#3	Own XFR END DMA#4	Own XFR END DMA#5
2F	XMPA::PnFINTS							
	(Not used)	(Not used)	Other CHK2A INT DMA#0	Other CHK2A INT DMA#1	Other CHK2A INT DMA#2	Other CHK2A INT DMA#3	Other CHK2A INT DMA#4	Other CHK2A INT DMA#5
30	XMPA::PnFINTS							
	(Not used)	(Not used)	Own CHK2A INT DMA#0	Own CHK2A INT DMA#1	Own CHK2A INT DMA#2	Own CHK2A INT DMA#3	Own CHK2A INT DMA#4	Own CHK2A INT DMA#5
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

*1: SubCode Bit 4: DMA# ('0':DMA#0/2, '1':DMA#1/3)

SubCode Bit 5: CHSN CHK2 detected

SubCode Bit 6: DMA CHK2 detected

SubCode Bit 7: PCI CHK2 detected

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(8) Byte28 = 80 ~ 87

	0	1	2	3	4	5	6	7
28	Subcode (x'80' ~ x'87' : FIBRE I/F CHA - DMA CHK2 (*1))							
29	FDTA::PnXFS							
	TRANSFER END	(Not used)	(Not used)	(Not used)	DMA CHK2B detected	DMA CHK2B detected	(Not used)	MP STOP0 detected
2A	FDTA::PnXFS							
	MP STOP1 detected	PCI CHK2A detected	PCI CHK2B0 detected	PCI CHK2B1 detected	PCI CHK2B2 detected	PCI OVER RUN detected	(Not used)	HSN CHK2 detected
2B	FDTA::PnXFS							
	(Not used)	P0 CHK2A detected	P0 CHK2B detected	P0 CHK2C detected	(Not used)	P0 CHK2A detected	P0 CHK2B detected	P0 CHK2C detected
2C	FDTA::PnXFS							
	(Not used)	(Not used)	(Not used)	COMMON CHK2C	(Not used)	(Not used)	(Not used)	ABORT detected
2D	FDTA::PnXFC							
	XFR BIT '00': XFR Initialize '10': XFR ABORT '11': XFR START		(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	INT SEL 0:CHP SEL 1:FOPSEL
2E	FDTA::PnXFC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XFR SEL '000': PCI DEVICE XFR '011': 16B ~ 128B CACHE ACCESS		
2F	FDTA::PnXFC							
	(Not used)	(Not used)	DATA FORMAT '00': 528B FORMAT '10': 16B ~ 128B FORMAT		(Not used)	16 XFR CNT '000': 16B ACCESS '011': 64B ACCESS		
30	FDTA::PnXFC							
	(Not used)	(Not used)	XBF CHAIN Chain Number= 1 ~ 32 (32: When Set as '0')					
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

*1: SubCode Bit 5: CHSN CHK2 detected

SubCode Bit 6: DMA CHK2 detected

SubCode Bit 7: PCI CHK2 detected

Byte27 = 89 (CHA CHK2) Byte28-31 Detail

(9) Byte28 = 90 ~ 97

	0	1	2	3	4	5	6	7
28	Subcode (x'90' ~ x'97' : FIBRE I/F CHA - PMA CHK2 (*1))							
29	FDTA::PnXFS							
	TRANSFER END	(Not used)	(Not used)	(Not used)	PMA CHK2B detected	PMA CHK2B detected	(Not used)	MP STOP0 detected
2A	FDTA::PnXFS							
	MP STOP1 detected	PCI CHK2A detected	PCI CHK2B0 detected	PCI CHK2B1 detected	PCI CHK2B2 detected	PCI OVER RUN detected	(Not used)	HSN CHK2 detected
2B	FDTA::PnXFS							
	(Not used)	P0 CHK2A detected	P0 CHK2B detected	P0 CHK2C detected	(Not used)	P0 CHK2A detected	P0 CHK2B detected	P0 CHK2C detected
2C	FDTA::PnXFS							
	(Not used)	(Not used)	(Not used)	COMMON CHK2C	(Not used)	(Not used)	(Not used)	ABORT detected
2D	FDTA::PnXFC							
	XFR BIT '00': XFR Initialize '10': XFR ABORT '11': XFR START		(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	INT SEL 0:CHP SEL 1:FOPSEL
2E	FDTA::PnXFC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	XFR SEL '010': DEBUG MODE CACHE ACCESS '011': 16B ~ 128B CACHE ACCESS		
2F	FDTA::PnXFC							
	(Not used)	(Not used)	DATA FORMAT '10': NOT FLRC FORMAT '11': FLRC FORMAT		(Not used)	16 XFR CNT '000': 16B ACCESS '011': 64B ACCESS		
30	FDTA::PnXFC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

*1: SubCode Bit 5: CHSN CHK2 detected
 SubCode Bit 6: DMA CHK2 detected
 SubCode Bit 7: PCI CHK2 detected

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/1F and Byte32 = 58/59 (1/4)

	0	1	2	3	4	5	6	7
40	CHA::CK2AST0							
	RDBF PLRC ERR	SLRC ERR	LA ERR	CBUF DT PARITY ERR	SEARCH ERR	SEARCH DT ERR	WRBF INPUT DT PARITY ERR	BYTE ALIGNMENT ERR
41	CHA::CK2AST0							
	LCM OUTPUT DT PARITY ERR	(Not used)	LA PARITY ERR	RDBF OUTPUT DT PARITY ERR	HSN PACKET LEN PARITY ERR	(Not used)	(Not used)	(Not used)
42	CHA::CK2AST2							
	PLIST XFR SEQ PARITY ERR	XR SET XFR SEQ PARITY ERR	PLIST OUT PARITY ERR	PLIST IN PARITY ERR	CBP CNT PARITY ERR	CBUF CKDDL PARITY ERR	TBP PARITY ERR	XFR MODE ERR
43	CHA::CK2AST2							
	(Not used)	LCM CKDDL PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	CHA::CK2AST1							
	FBADL PARITY ERR	SGOP PARITY ERR	PFC PARITY ERR	WRBF CNT PARITY ERR	RDBF CNT PARITY ERR	PACKET SEQ PARITY ERR	UNMATCH ERR0	UNMATCH ERR1
45	CHA::CK2AST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
46	CHA::CK2AST1							
	SOP PARITY ERR	BOP PARITY ERR	CKDFBA CNG CKDDL PARITY ERR	CKDFBA CNG SEQ PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
47	CHA::CK2AST1							
	SLAVE1 NEXTSEG ADR PARITY ERR	SLAVE1 HSN ADR PARITY ERR	SLAVE2 NEXTSEG ADR PARITY ERR	SLAVE2 HSN ADR PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
48	CHA::CHK2ASTS							
	WRBF RD ADR PNT PARITY ERR	RDBF WT ADR PNT PARITY ERR	SC CNT PARITY ERR	LOS CNT PARITY ERR	HARD TIMER PARITY ERR	WRBF PLRC ERR	HSN ADR PARITY ERR	WRBF OUT DT PARITY ERR
49	CHA::CHK2ASTS							
	P0 Recive PARITY ERR	P1 Recive PARITY ERR	X0 Recive PARITY ERR	X1 Recive PARITY ERR	ADRM PARITY ERR	ADRS PARITY ERR	ADRP PARITY ERR	CMD PARITY ERR
4A	CHA::CHK2ASTS							
	XFR CNT PARITY ERR	ILPRM0	ILPRM1	ILPRM2	ILPRM3	ILPRM4	ILPRM5	ILPRM6
4B	CHA::CHK2ASTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	CHA::ADP CK21							
	SR0 PARITY ERR	CMWAD PARITY ERR	CMWD PARITY ERR	TSEQ PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
4D	CHA::ADP CK22							
	LCM-A DATA PARITY ERR	LCM-B DATA PARITY ERR	LCM-C DATA PARITY ERR	LCM-D DATA PARITY ERR	LCM-E DATA PARITY ERR	LCM-F DATA PARITY ERR	LCM-G DATA PARITY ERR	LCM-H DATA PARITY ERR
4E	CHA::CHK2DSTS							
	P0 ERR	P1 ERR	CARB ERRINT	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/1F and Byte32 = 58/59 (2/4)

	0	1	2	3	4	5	6	7
50	CHA CHK2CSTS							
	TIME OUT0	TIME OUT1	TIME OUT2	HSN RD PLRC ERR	STATUS0 PLRC ERR	STATUS1 PLRC ERR	HSN RD PLRC-INV ERR	STATUS0 PLRC ERR
51	CHA CHK2CSTS							
	STATUS1 PLRC ERR	HSN RD END-CODE ERR	STATUS0 END-CODE ERR	STATUS1 END-CODE ERR	(Not used)	(Not used)	STATUS0 PKID ERR	STATUS1 PKID ERR
52	CHA CHK2CSTS							
	STATUS0 BYTE0/1 ERR	STATUS1 BYTE0/1 ERR	ACK ERR	ERRINT	LOS	P0 Send PARITY ERR	P1 Send PARITY ERR	X0 Send PARITY ERR
53	CHA CHK2CSTS							
	X1 Send PARITY ERR	STATUS0 CARB PARITY ERR	STATUS0 CMA PARITY ERR	STATUS1 CARB PARITY ERR	STATUS1 CMA PARITY ERR	(Not used)	(Not used)	(Not used)
54	CHA::ADP SELP							
	LCM-A SELECT	LCM-B SELECT	LCM-C SELECT	LCM-D SELECT	LCM-E SELECT	LCM-F SELECT	LCM-G SELECT	LCM-H SELECT
55	CHA::ADP EALT							
	LCP-A CHK1 detected	LCP-B CHK1 detected	LCP-C CHK1 detected	LCP-D CHK1 detected	LCP-E CHK1 detected	LCP-F CHK1 detected	LCP-G CHK1 detected	LCP-H CHK1 detected
56	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
57	CHA::CBP							
	CBUF Data Transfer Start Address							
58	CHA::CKDDL							
	Transfer Data Length (CKD)							
59	CHA::CKDDL							
	Transfer Data Length (CKD)							
5A	CHA::FBADL							
	Transfer Data Length (FBA)							
5B	CHA::FBADL							
	Transfer Data Length (FBA)							
5C	CHA::PDS							
	PLIST MEM FULL	(Not used)	(Not used)	(Not used)	PLHLD NE0	PLHLD EQ0	(Not used)	(Not used)
5D	CHA::PLIN							
	PLIST Setting Parameter Memory Page Pointer (BYTE0)							
5E	CHA::PLOUT							
	Parameter Decoder Processing PLIST Page Pointer (BYTE0)							
5F	CHA::PFC							
	Packet Frow Counter (Packet Number Able to Transfer)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/1F and Byte32 = 58/59 (3/4)

	0	1	2	3	4	5	6	7
60	CHA::MCSA00							
	Slave 1 CACHE Transfer Address (BYTE0)							
61	CHA::MCSA00							
	Slave 1 CACHE Transfer Address (BYTE0)							
62	CHA::MCSA01							
	Slave 1 CACHE Transfer Address (BYTE0)							
63	CHA::MCSA01							
	Slave 1 CACHE Transfer Address (BYTE0)							
64	CHA::SCSA00							
	Slave 2 CACHE Transfer Address (BYTE0)							
65	CHA::SCSA00							
	Slave 2 CACHE Transfer Address (BYTE0)							
66	CHA::SCSA01							
	Slave 2 CACHE Transfer Address (BYTE0)							
67	CHA::SCSA01							
	Slave 2 CACHE Transfer Address (BYTE0)							
68	CHA::CSPGR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	CHA::CSPGR							
	Slave 1 REG Access Flag	Slave 1 CARB Access Flag	(Not used)	(Not used)	Slave 2 REG Access Flag	Slave 2 CARB Access Flag	(Not used)	(Not used)
6A	CHA::PCMD1							
	CACHE Transfer Command (BYTE0)							
6B	CHA::PCMD1							
	CACHE Transfer Command (BYTE1)							
6C	CHA::PCMD2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	CHA::PCMD2							
	CACHE Transfer Command (BYTE2)							
6E	CHA::LPEN							
	C Path#00 ENABLE	C Path #01 ENABLE	C Path #02 ENABLE	C Path #03 ENABLE	C Path #10 ENABLE	C Path #11 ENABLE	C Path #12 ENABLE	C Path #13 ENABLE
6F	CHA::IMPZREF0							
	IMP Setting Value for LSI UP Side High-Level				IMP Setting Value for LSI UP Side Low-Level			

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(1) Byte28 = 00/1F and Byte32 = 58/59 (4/4)

	0	1	2	3	4	5	6	7
70	CHA::LA0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	CHA::LA0							
	LA Expected Value (BYTE0)							
72	CHA::LA1							
	LA Expected Value (BYTE1)							
73	CHA::LA1							
	LA Expected Value (BYTE2)							
74	CHA::LA2							
	LA Expected Value (BYTE3)							
75	CHA::LA2							
	LA Expected Value (BYTE4)							
76	CHA::SOP							
	Subblock Offset Pointer							
77	CHA::SOP							
	Subblock Offset Pointer							
78	CHA::BOP							
	Block Offset Pointer							
79	CHA::BOP							
	Block Offset Pointer							
7A	CHA::POP							
	Packet Offset Pointer							
7B	CHA::POP							
	Packet Offset Pointer							
7C	CHA::SGOP							
	Segment Offset Pointer							
7D	CHA::SGOP							
	Segment Offset Pointer							
7E	Micro Information							
	Error Devided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(2) Byte28 = 00/01/02/1F/20/3F and Byte32 $\neq$ 58/59 and Byte42 = 58/59

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV / SLOT#							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Micro Information							
4D								
4E								
4F								
50	Internal Search Parameter (Cylinder number)							
51								
52	Internal Search Parameter (Head number)							
53	Internal Search Parameter (Reacord number)							
54	Micro Information							
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E	Curent Command Code							
5F	Old Command Code							
60	Micro Information							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C	Record number in CBUF							
7D	Key Length in CBUF							
7E	Data Length in CBUF							
7F								

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(3) Byte28 = 00/01/02/1F/20/3F and Byte32 ≠ 58/59 and Byte42 ≠ 58/59

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	Micro Information							
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E	Curent Command Code							
5F	Old Command Code							
60	Micro Information							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 00/1F and Byte32 = 58/59 (1/4)

	0	1	2	3	4	5	6	7
40	CHA::CK2AST0							
	RDBF PLRC ERR	SLRC ERR	LA ERR	CBUF DT PARITY ERR	SEARCH ERR	SEARCH DT ERR	WRBF INPUT DT PARITY ERR	BYTE ALIGNME NT ERR
41	CHA::CK2AST0							
	LCM OUTPUT DT PARITY ERR	(Not used)	LA PARITY ERR	RDBF OUTPUT DT PARITY ERR	HSN PACKET LEN PARITY ERR	(Not used)	(Not used)	(Not used)
42	CHA::CK2AST2							
	PLIST XFR SEQ PARITY ERR	XR SET XFR SEQ PARITY ERR	PLIST OUT PARITY ERR	PLIST IN PARITY ERR	CBP CNT PARITY ERR	CBUF CKDDL PARITY ERR	TBP PARITY ERR	XFR MODE ERR
43	CHA::CK2AST2							
	(Not used)	LCM CKDDL PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	CHA::CK2AST1							
	FBADL PARITY ERR	SGOP PARITY ERR	PFC PARITY ERR	WRBF CNT PARITY ERR	RDBF CNT PARITY ERR	PACKET SEQ PARITY ERR	UNMATCH ERR0	UNMATCH ERR1
45	CHA::CK2AST1							
	SOP PARITY ERR	BOP PARITY ERR	CKDFBA CNG CKDDL PARITY ERR	CKDFBA CNG SEQ PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
46	CHA::CK2AST1							
	SLAVE1 NEXTSEG ADR PARITY ERR	SLAVE1 HSN ADR PARITY ERR	SLAVE2 NEXTSEG ADR PARITY ERR	SLAVE2 HSN ADR PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
47	CHA::CHK2ASTS							
	WRBF RD ADR PNT PARITY ERR	RDBF WT ADR PNT PARITY ERR	SC CNT PARITY ERR	LOS CNT PARITY ERR	HARD TIMER PARITY ERR	WRBF PLRC ERR	HSN ADR PARITY ERR	WRBF OUT DT PARITY ERR
48	CHA::CHK2ASTS							
	P0 Recive PARITY ERR	P1 Recive PARITY ERR	X0 Recive PARITY ERR	X1 Recive PARITY ERR	ADRM PARITY ERR	ADRS PARITY ERR	ADRP PARITY ERR	CMD PARITY ERR
49	CHA::CHK2ASTS							
	XFR CNT PARITY ERR	ILPRM0	ILPRM1	ILPRM2	ILPRM3	ILPRM4	ILPRM5	ILPRM6
4A	CHA CHK2CSTS							
	TIME OUT0	TIME OUT1	TIME OUT2	HSN RD PLRC ERR	STATUS0 PLRC ERR	STATUS1 PLRC ERR	HSN RD PLRC-INV ERR	STATUS0 PLRC ERR
4B	CHA CHK2CSTS							
	STATUS1 PLRC ERR	HSN RD END-CODE ERR	STATUS0 END-CODE ERR	STATUS1 END-CODE ERR	(Not used)	(Not used)	STATUS0 PKID ERR	STATUS1 PKID ERR
4C	CHA CHK2CSTS							
	STATUS0 BYTE0/1 ERR	STATUS1 BYTE0/1 ERR	ACK ERR	ERRINT	LOS	P0 Send PARITY ERR	P1 Send PARITY ERR	X0 Send PARITY ERR
4D	CHA::DXBFERR0							
	STATUS0 BYTE0/1 ERR	STATUS1 BYTE0/1 ERR	ACK ERR	ERRINT	LOS	P0 Send PARITY ERR	P1 Send PARITY ERR	X0 Send PARITY ERR
4E	CHA::DXBFERR0							
	P0 ERR	P1 ERR	CARB ERRINT	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	CHA::CBP							
	CBUF Data Transfer Start Address							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 00/1F and Byte32 = 58/59 (2/4)

	0	1	2	3	4	5	6	7
50	CHA::CKDDL							
	Transfer Data Length (CKD)							
51	CHA::CKDDL							
	Transfer Data Length (CKD)							
52	CHA::FBADL							
	Transfer Data Length (FBA)							
53	CHA::FBADL							
	Transfer Data Length (FBA)							
54	CHA::MCSA00							
	Slave 1 CACHE Transfer Address (BYTE0)							
55	CHA::MCSA00							
	Slave 1 CACHE Transfer Address (BYTE1)							
56	CHA::MCSA01							
	Slave 1 CACHE Transfer Address (BYTE2)							
57	CHA::MCSA01							
	Slave 1 CACHE Transfer Address (BYTE3)							
58	CHA::SCSA00							
	Slave 2 CACHE Transfer Address (BYTE0)							
59	CHA::SCSA00							
	Slave 2 CACHE Transfer Address (BYTE1)							
5A	CHA::SCSA01							
	Slave 2 CACHE Transfer Address (BYTE2)							
5B	CHA::SCSA01							
	Slave 2 CACHE Transfer Address (BYTE3)							
5C	CHA::CSPGR							
	Slave 1 REG Access Flag	Slave 1 CARB Access Flag	(Not used)	(Not used)	Slave 2 REG Access Flag	Slave 2 CARB Access Flag	(Not used)	(Not used)
5D	CHA::POP							
	Packet Offset Pointer							
5E	CHA::PCMD2							
	CACHE Transfer Command (BYTE2)							
5F	CHA::LA0							
	LA Expected Value (BYTE0)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 00/1F and Byte32 = 58/59 (3/4)

	0	1	2	3	4	5	6	7
60	CHA::LA1							
	LA Expected Value (BYTE1)							
61	CHA::LA1							
	LA Expected Value (BYTE2)							
62	CHA::LA2							
	LA Expected Value (BYTE3)							
63	CHA::LA2							
	LA Expected Value (BYTE4)							
64	CHA::SOP							
	Subblock Offset Pointer							
65	CHA::SOP							
	Subblock Offset Pointer							
66	CHA::BOP							
	Block Offset Pointer							
67	CHA::BOP							
	Block Offset Pointer							
68	CHA::LPEN							
	C Path#00 ENABLE	C Path #01 ENABLE	C Path #02 ENABLE	C Path #03 ENABLE	C Path 04 ENABLE	C Path #05 ENABLE	C Path #06 ENABLE	C Path #07 ENABLE
69	CHA::LPEN							
	C Path #10 ENABLE	C Path #11 ENABLE	C Path #12 ENABLE	C Path #13 ENABLE	C Path #14 ENABLE	C Path #15 ENABLE	C Path #16 ENABLE	C Path #17 ENABLE
6A	CHA::SGOP							
	Segment Offset Pointer							
6B	CHA::SGOP							
	Segment Offset Pointer							
6C	CHA::BBP							
	BBF Pointer							
6D	CHA::BBP							
	BBF Pointer							
6E	CHA::BBP							
	BBF Pointer							
6F	CHA::BBP							
	BBF Pointer							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(4) Byte28 = 00/1F and Byte32 = 58/59 (4/4)

	0	1	2	3	4	5	6	7
70	CHA::PFC Packet Flow Counter							
71	CHA::DXOP DXBF OUTPUT Pointer							
72	CHA::DXOP DXBF OUTPUT Pointer							
73	CHA::DXOP DXBF OUTPUT Pointer							
74	CHA::CADR DXBF Current Address							
75	CHA::CADR DXBF Current Address							
76	CHA::CADR DXBF Current Address							
77	CHA::DXIP DXBF INPUT Pointer							
78	CHA::DXIP DXBF INPUT Pointer							
79	CHA::DXIP DXBF INPUT Pointer							
7A	CHA::DCTL DXBF TRF Counter							
7B	CHA::DCTL DXBF TRF Counter							
7C	CHA::RCCNT ASYNC XFR RC Counter							
7D	CHA::RCCNT ASYNC XFR RC Counter							
7E	CHA::DIB DIB Counter							
7F	CHA::DIB DIB Counter							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 60 or 62 or 68 or 6a (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'01'							
41	NDTM::XFS							
	TRANSFER END	ABORT	(Not used)	(Not used)	(Not used)	(Not used)	WCHK1 detected	CHK2 detected
42	NDTM::XFS							
	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK2E detected	(Not used)	(Not used)	(Not used)
43	NDTM::XFS							
	REQID			(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	NDTM::CHK2BSTSP0							
	DMA-PCI ARBT ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
45	NDTM::CHK2BSTSP0							
	REQ PCIADR PARITY ERR0	REQ PCIADR PARITY ERR1	REQ PCIADR PARITY ERR2	REQ PCIADR PARITY ERR3	REQ PCIADR PARITY ERR4	REQ PCIADR PARITY ERR5	REQ PCIADR PARITY ERR6	REQ PCIADR PARITY ERR7
46	NDTM::CHK2BSTSP0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	REQ LEN PARITYER R
47	NDTM::CHK2BSTSD0							
	PCI-DBF WR DATA PARITY ERR	PCI-DBF RD DATA ECC ERR	PCI-DBF RD DATA ECC ERR(A)	(Not used)	PCI-PBF WR DATA PARITY ERR	PCI-PBF RD DATA ECC ERR	PCI-PBF RD DATA ECC ERR(A)	(Not used)
48	NDTM::CHK2BSTSD0							
	PCI-DBF WR ADR PARITY ERR	PCI-DBF RD ADR PARITY ERR	PCI-PBF WR ADR PARITY ERR	PCI-PBF RD ADR PARITY ERR	DLRC PARITY ERR	DLRC HBUF CMP ERR	DLRC HBUF IN PNT PARITY ERR	DLRC HBUF OUT PNT PARITY ERR
49	NDTM::CHK2BSTSD0							
	DCRC BYTE0 PARITY ERR	DCRC BYTE1 PARITY ERR	DCRC BYTE2 PARITY ERR	DCRC BYTE3 PARITY ERR	LWERR BYTE0 PARITY ERR	LWERR BYTE1 PARITY ERR	(Not used)	(Not used)
4A	NDTM::CHK2BSTSD0							
	PCI XFR CNT PARITY ERR	PCI REQ LEN PARITY ERR	PCI-DBF DATA LEN0 PARITY ERR	PCI-DBF DATA LEN1 PARITY ERR	PCI REQ TIMER0 PARITY ERR	PCI REQ TIMER1 PARITY ERR	PCI REQ TIMER TIMEOUT	EXBF REQ TIMER TIMEOUT
4B	NDTM::CHK2BSTSD1							
	HSN-DBF WR DATA PARITY ERR	HSN-DBF RD DATA ECC ERR	HSN-DBF RD DATA ECC ERR(A)	(Not used)	(Not used)	HSN-PBF RD DATA ECC ERR	HSN-PBF RD DATA ECC ERR(A)	(Not used)
4C	NDTM::CHK2BSTSD1							
	HSN-DBF WR ADR PARITY ERR	HSN-DBF RD ADR PARITY ERR	(Not used)	HSN-PBF RD ADR PARITY ERR	(Not used)	DLRC PBUF CMP ERR	DLRC PBUF IN PNT PARITY ERR	DLRC PBUF OUT PNT PARITY ERR
4D	NDTM::CHK2BSTSD1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4E	NDTM::CHK2BSTSD1							
	HSN XFR CNT PARITY ERR	HSN REQ LEN PARITY ERR	HSN-DBF DATA LEN0 PARITY ERR	HSN-DBF DATA LEN1 PARITY ERR	HSN REQ TIMER0 PARITY ERR	HSN REQ TIMER1 PARITY ERR	HSN REQ TIMER TIMEOUT	HSN-DBF END ERR
4F	NDTM::CHK2BSTSD2							
	HSN WR DATA 0 PARITY ERR	HSN WR DATA 1 PARITY ERR	HSN WR DATA 2 PARITY ERR	HSN WR DATA 3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 60 or 62 or 68 or 6a (2/4)

	0	1	2	3	4	5	6	7
50	NDTM::CHK2BSTSD2							
	HSN MADR0 PARITY ERR	HSN MADR1 PARITY ERR	HSN MADR2 PARITY ERR	HSN MADR3 PARITY ERR	HSN MADR4 PARITY ERR	HSN MADR MASK ERR	(Not used)	(Not used)
51	NDTM::CHK2BSTSD2							
	HSN SADR0 PARITY ERR	HSN SADR1 PARITY ERR	HSN SADR2 PARITY ERR	HSN SADR3 PARITY ERR	HSN SADR4 PARITY ERR	HSN SADR MASK ERR	(Not used)	(Not used)
52	NDTM::CHK2BSTSD2							
	LA CNT0 PARITY ERR	LA CNT1 PARITY ERR	LA CNT2 PARITY ERR	LA CNT3 PARITY ERR	LA CNT4 PARITY ERR	LA CNT5 PARITY ERR	XFC REG PARITY ERR	PBFCTL REG PARITY ERR
53	NDTM::CHK2BSTSD3							
	DMA TARGET DATA RCV OVER RUN	DMA MASTER DATA RCV OVER RUN	DMA TARGET DATA SND OVER RUN	DMA MASTER DATA SND OVER RUN	DBF/PBF PCI TARGET ERR ACS	DBF RD ERR ACS	DBF WR ERR ACS	DMA TARGET DBF/PCI ADR ERR
54	NDTM::CHK2BSTSD3							
	DBF/PBF ADR0 PARITY ERR	DBF/PVF ADR1 PARITY ERR	DBF/PVF ADR2 PARITY ERR	DBF ADR CMP REG PARITY ERR	DBF BYTE ENABLE ERR	(Not used)	(Not used)	(Not used)
55	NDTM::CHK2BSTSD3							
	HSN MADR0 PARITY ERR	HSN MADR1 PARITY ERR	HSN MADR2 PARITY ERR	HSN MADR3 PARITY ERR	HSN MADR4 PARITY ERR	HSN MADR5 PARITY ERR	HSN MADR6 PARITY ERR	HSN MADR7 PARITY ERR
56	NDTM::CHK2BSTSD3							
	DBF-EXBF EXBFADR0 PARITY ERR	DBF-EXBF EXBFADR1 PARITY ERR	DBF-EXBF EXBFADR2 PARITY ERR	PBFCTL PARITY ERR	EXBFCTL PARITY ERR	(Not used)	(Not used)	(Not used)
57	NDTM::CHK2BSTSD4							
	DMA DOUBLE EXEC	XFC ILGAL CMD	DMA CHAIN CNT PARITY ERR	PCI CEND ERR	HSN CEND ERR	DBF DATA CTL ERR	(Not used)	(Not used)
58	NDTM::CHK2BSTSD4							
	PBFADR BYTE0 PARITY ERR	PBFADR BYTE1 PARITY ERR	PBFADR BYTE2 PARITY ERR	PBFADR BYTE3 PARITY ERR	PBFADR BYTE4 PARITY ERR	PBFADR BYTE5 PARITY ERR	PBFADR BYTE6 PARITY ERR	PBFADR BYTE7 PARITY ERR
59	NDTM::CHK2BSTSD4							
	XFC BYTE0 PARITY ERR	XFC BYTE1 PARITY ERR	XFC BYTE2 PARITY ERR	XFC BYTE3 PARITY ERR	IOREG WR CHK	(Not used)	(Not used)	(Not used)
5A	NDTM::CHK2BSTSH0							
	A/C PHASE CNT PARITY ERR	WR DATA CNT1 PARITY ERR	WR DATA CNT2 PARITY ERR	CSTAT0 CNT PARITY ERR	CSTA1 CNT PARITY ERR	(Not used)	RD DATA CNT PARITY ERR	RD DATA ADR PARITY ERR
5B	NDTM::CHK2BSTSH0							
	STATUS1 CNT1 PARITY ERR	STATUS1 CNT2 PARITY ERR	STATUS2 CNT1 PARITY ERR	STATUS2 CNT2 PARITY ERR	(Not used)	FT1,4 CNT PARITY ERR	FT2,5 CNT PARITY ERR	FT3,6 CNT PARITY
5C	NDTM::CHK2BSTSH0							
	FLRC CMP ERR	SLRC CMP ERR	LA CMP ERR	FW MODE ERR	FW CNT ERR	FW CNT PARITY ERR	CHSN PATH ERR	2SLV WR CMA# ERR
5D	NDTM::CHK2BSTSH0							
	CtoC COPY CMA# ERR	CinC COPY CMA# ERR	SLRC CHECKER PARITY ERR	SLRC GENERATER PARITY ERR	HSN ARBT ERR	HSN IT ERR	T00 CNT PARITY ERR	(Not used)
5E	NDTM::CHK2BSTSH1							
	HSN RD DATA PATH0 PARITY ERR	HSN RD DATA PARITY ERR PATH1	HSN RD DATA PATH2 PARITY ERR	HSN RD DATA PATH3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
5F	NDTM::CHK2BSTSH1							
	WR DATA PATH0 PARITY ERR	HSN WR DATA PATH1 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 60 or 62 or 68 or 6a (3/4)

	0	1	2	3	4	5	6	7
60	NDTM::CHK2BSTSH1							
	CSTAT2 PARITY ERR	CSTAT4 PARITY ERR	CSTAT3 PARITY ERR	CSTAT5 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
61	NDTM::CHK2BSTSH1							
	PLRC CHECKER0 PARITY ERR	PLRC CHECKER1 PARITY ERR	PRC GENERATE R0 PARITY ERR	PRC GENERATE R1 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
62	NDTM::CHK2BSTSH2							
	CHSNCTL WR DATA0 PARITY ERR	CHSNCTL WR DATA1 PARITY ERR	CHSNCTL WR DATA2 PARITY ERR	(Not used)	(Not used)	CSTATCTL WR DATA1 PARITY ERR	(Not used)	CSTATCTL WR DATA3 PARITY ERR
63	NDTM::CHK2BSTSH2							
	CTIMER0 WR DATA0 PARITY ERR	CTIMER0 WR DATA1 PARITY ERR	CTIMER0 WR DATA2 PARITY ERR	CTIMER0 WR DATA3 PARITY ERR	CTIMER1 WR DATA0 PARITY ERR	CTIMER1 WR DATA1 PARITY ERR	CTIMER1 WR DATA2 PARITY ERR	CTIMER1 WR DATA3 PARITY ERR
64	NDTM::CHK2BSTSH2							
	CTIMER2 WR DATA0 PARITY ERR	CTIMER2 WR DATA1 PARITY ERR	CTIMER2 WR DATA2 PARITY ERR	CTIMER2 WR DATA3 PARITY ERR	CHSN PRIORITY0 PARITY ERR	(Not used)	(Not used)	(Not used)
65	NDTM::CHK2BSTSH2							
	CHSN EIJ WR DATA0 PARITY ERR	(Not used)	(Not used)	(Not used)	CHSN00CN T WR DATA0 PARITY ERR	(Not used)	(Not used)	(Not used)
66	NDTM::CHNCNT							
	(Not used)	(Not used)	(Not used)	CHAIN#				
67	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE0)							
68	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE1)							
69	NDTM::HSNMADRU							
	HSN master address (BYTE0)							
6A	NDTM::HSNMADRL							
	HSN master address (BYTE1)							
6B	NDTM::HSNMADRL							
	HSN master address (BYTE2)							
6C	NDTM::HSNMADRL							
	HSN master address (BYTE3)							
6D	NDTM::HSNMADRL							
	HSN master address (BYTE3)							
6E	NDTM::HSNSADRU							
	HSN slave address (BYTE0)							
6F	NDTM::HSNSADRL							
	HSN slave address (BYTE1)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(5) Byte28 = 60 or 62 or 68 or 6a (4/4)

Byte28	0	1	2	3	4	5	6	7
70	NDTM::HSNSADRL							
	HSN slave address (BYTE2)							
71	NDTM::HSNSADRL							
	HSN slave address (BYTE3)							
72	NDTM::HSNSADRL							
	HSN slave address (BYTE4)							
73	NDTM::LA0							
	LA0 value (BYTE0)							
74	NDTM::LA0							
	LA0 value (BYTE1)							
75	NDTM::LA1							
	LA1 value (BYTE0)							
76	NDTM::LA1							
	LA1 value (BYTE1)							
77	NDTM::LA1							
	LA1 value (BYTE2)							
78	NDTM::LA1							
	LA1 value (BYTE3)							
79	NDTM::HSNDATAL							
	HSN data CNT (BYTE0)							
7A	NDTM::HSNDATAL							
	HSN data CNT (BYTE1)							
7B	NDTM::CHSNCTL							
	C Path#00 ENABLE	C Path#01 ENABLE	C Path #02 ENABLE	C Path #03 ENABLE	C Path #04 ENABLE	C Path #05 ENABLE	C Path #06 ENABLE	C Path #07 ENABLE
7C	NDTM::CHSNCTL							
	C Path #10 ENABLE	C Path #11 ENABLE	C Path #12 ENABLE	C Path #13 ENABLE	C Path #14 ENABLE	C Path #15 ENABLE	C Path #16 ENABLE	C Path #17 ENABLE
7D	NDTM::CHSNCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PKID		
7E	NDTM::CHSNPHASE							
	ADR/CMD PHASE	WR DATA PHASE	RD DATA PHASE	STATUS1 PHASE	STATUS2 PHASE	(Not used)	(Not used)	(Not used)
7F	NDTM::CHSNPHASE							
	HSN P0 XFR	HSN P1 XFR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 61 or 69 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'02'							
41	NDTM::XFS							
	TRANSFER END	ABORT	(Not used)	(Not used)	(Not used)	(Not used)	WCHK1 detected	CHK2 detected
42	NDTM::XFS							
	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK2E detected	(Not used)	(Not used)	(Not used)
43	NDTM::XFS							
	REQID			(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	NDTM::CHK2CSTS0							
	P0 RD DATA0 PARITY ERR	P0 RD DATA1 PARITY ERR	P0 RD DATA2 PARITY ERR	P0 RD DATA3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
45	NDTM::CHK2CSTS0							
	(Not used)	(Not used)	P0 SYNC ERR	P0 RD END-CODE ERR	P0 STATUS1 END-CODE ERR	P0 STATUS2 END-CODE ERR	P0 RD PLRC ERR	P0 STSTATUS1 PLRC ERR
46	NDTM::CHK2CSTS0							
	P0 STATUS2 PLRC ERR	P0 RD INV-PLRC ERR	P0 STATUS1 INV-PLRC ERR	P0 STATUS2 INV-PLRC ERR	P0 STATUS1 ST0,1 CMP ERR	P0 STATUS2 ST0,1 CMP ERR	P0 STATUS1 PKID ERR	P0 STATUS2 PKID ERR
47	NDTM::CHK2CSTS0							
	P0 ERRINT ERR	P0 ACK ERR	P0 ENTOUT ERR	P0 STENOUT ERR	(Not used)	P0 TIMER1 TIME OUT	P0 TIMER2 TIME OUT	P0 TIMER3 TIME OUT
48	NDTM::CHK2CSTS1							
	P0 RD DATA0 PARITY ERR	P0 RD DATA1 PARITY ERR	P0 RD DATA2 PARITY ERR	P0 RD DATA3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
49	NDTM::CHK2CSTS1							
	(Not used)	(Not used)	P0 SYNC ERR	P0 RD END-CODE ERR	P0 STATUS1 END-CODE ERR	P0 STATUS2 END-CODE ERR	P0 RD PLRC ERR	P0 STSTATUS1 PLRC ERR
4A	NDTM::CHK2CSTS1							
	P0 STATUS2 PLRC ERR	P0 RD INV-PLRC ERR	P0 STATUS1 INV-PLRC ERR	P0 STATUS2 INV-PLRC ERR	P0 STATUS1 ST0,1 CMP ERR	P0 STATUS2 ST0,1 CMP ERR	P0 STATUS1 PKID ERR	P0 STATUS2 PKID ERR
4B	NDTM::CHK2CSTS1							
	P0 ERRINT ERR	P0 ACK ERR	P0 ENTOUT ERR	P0 STENOUT ERR	(Not used)	P0 TIMER1 TIME OUT	P0 TIMER2 TIME OUT	P0 TIMER3 TIME OUT
4C	NDTM::CHK2DSTS							
	P0 ERRINT Received	P1 ERRINT Received	(Not used)	(Not used)	(Not used)	(Not used)	HSN STATUS1 ERR ON	HSN STATUS2 ERR ON
4D	NDTM::CHSNSTSPCK							
	P0 STATUS1 B0 PARITY ERR	P0 STATUS1 B1 PARITY ERR	P0 STATUS1 B2 PARITY ERR	P0 STATUS1 B3 PARITY ERR	P0 STATUS1 B4 PARITY ERR	P0 STATUS1 B5 PARITY ERR	P0 STATUS1 B6 PARITY ERR	P0 STATUS1 B7 PARITY ERR
4E	NDTM::CHSNSTSPCK							
	P0 STATUS2 B0 PARITY ERR	P0 STATUS2 B1 PARITY ERR	P0 STATUS2 B2 PARITY ERR	P0 STATUS2 B3 PARITY ERR	P0 STATUS2 B4 PARITY ERR	P0 STATUS2 B5 PARITY ERR	P0 STATUS2 B6 PARITY ERR	P0 STATUS2 B7 PARITY ERR
4F	NDTM::CHSNSTSPCK							
	P1 STATUS1 B0 PARITY ERR	P1 STATUS1 B1 PARITY ERR	P1 STATUS1 B2 PARITY ERR	P1 STATUS1 B3 PARITY ERR	P1 STATUS1 B4 PARITY ERR	P1 STATUS1 B5 PARITY ERR	P1 STATUS1 B6 PARITY ERR	P1 STATUS1 B7 PARITY ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 61 or 69 (2/4)

	0	1	2	3	4	5	6	7
50	NDTM::CHSNSTSPCK							
	P1 STATUS2 B0 PARITY ERR	P1 STATUS2 B1 PARITY ERR	P1 STATUS2 B2 PARITY ERR	P1 STATUS2 B3 PARITY ERR	P1 STATUS2 B4 PARITY ERR	P1 STATUS2 B5 PARITY ERR	P1 STATUS2 B6 PARITY ERR	P1 STATUS2 B7 PARITY ERR
51	NDTM::CHNCNT							
	(Not used)	(Not used)	(Not used)	CHAIN#				
52	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE0)							
53	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE1)							
54	NDTM::HSNMADRU							
	HSN mater address (BYTE0)							
55	NDTM::HSNMADRL							
	HSN mater address (BYTE1)							
56	NDTM::HSNMADRL							
	HSN mater address (BYTE2)							
57	NDTM::HSNMADRL							
	HSN mater address (BYTE3)							
58	NDTM::HSNMADRL							
	HSN mater address (BYTE4)							
59	NDTM::HSNSADRU							
	HSN slave address (BYTE0)							
5A	NDTM::HSNSADRL							
	HSN slave address (BYTE1)							
5B	NDTM::HSNSADRL							
	HSN slave address (BYTE2)							
5C	NDTM::HSNSADRL							
	HSN slave address (BYTE3)							
5D	NDTM::HSNSADRL							
	HSN slave address (BYTE4)							
5E	NDTM::LA0							
	LA0 value (BYTE0)							
5F	NDTM::LA0							
	LA0 value (BYTE1)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 61 or 69 (3/4)

	0	1	2	3	4	5	6	7
60	NDTM::LAI							
	LAI value (BYTE0)							
61	NDTM::LAI							
	LAI value (BYTE1)							
62	NDTM::LAI							
	LAI value (BYTE2)							
63	NDTM::LAI							
	LAI value (BYTE3)							
64	NDTM::HSNDATAL							
	HSN data CNT (BYTE0)							
65	NDTM::HSNDATAL							
	HSN data CNT (BYTE1)							
66	NDTM::CHSNCTL							
	C Path #00 ENABLE	C Path #01 ENABLE	C Path #02 ENABLE	C Path #03 ENABLE	C Path #04 ENABLE	C Path #05 ENABLE	C Path #06 ENABLE	C Path #07 ENABLE
67	NDTM::CHSNCTL							
	C Path #10 ENABLE	C Path #11 ENABLE	C Path #12 ENABLE	C Path #13 ENABLE	C Path #14 ENABLE	C Path #15 ENABLE	C Path #16 ENABLE	C Path #17 ENABLE
68	NDTM::CHSNCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PKID		
69	NDTM::CHSNPHASE							
	ADR/CMD PHASE	WR DATA PHASE	RD DATA PHASE	STATUS1 PHASE	STATUS2 PHASE	(Not used)	(Not used)	(Not used)
6A	NDTM::CHSNPHASE							
	HSN P0 XFR	HSN P1 XFR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(6) Byte28 = 61 or 69 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
77	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
78	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
79	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 63 or 6b (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'03'							
41	NDTM::XFS							
	TRANSFER END	ABORT	(Not used)	(Not used)	(Not used)	(Not used)	WCHK1 detected	CHK2 detected
42	NDTM::XFS							
	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK2E detected	(Not used)	(Not used)	(Not used)
43	NDTM::XFS							
	REQID			(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	NDTM::CHK2BSTSD1							
	HSN-DBF WR DATA PARITY ERR	HSN-DBF RD DATA ECC ERR	HSN-DBF RD DATA ECC ERR(A)	(Not used)	(Not used)	HSN-PBF RD DATA ECC ERR	HSN-PBF RD DATA ECC ERR(A)	(Not used)
45	NDTM::CHK2BSTSD1							
	HSN-DBF WR ADR PARITY ERR	HSN-DBF RD ADR PARITY ERR	(Not used)	HSN-PBF RD ADR PARITY ERR	(Not used)	DLRC PBUF CMP ERR	DLRC PBUF IN PNT PARITY ERR	DLRC PBUF OUT PNT PARITY ERR
46	NDTM::CHK2BSTSD1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
47	NDTM::CHK2BSTSD1							
	HSN XFR CNT PARITY ERR	HSN REQ LEN PARITY ERR	HSN-DBF DATA LEN0 PARITY ERR	HSN-DBF DATA LEN1 PARITY ERR	HSN REQ TIMER0 PARITY ERR	HSN REQ TIMER1 PARITY ERR	HSN REQ TIMER TIMEOUT	HSN-DBF END ERR
48	NDTM::CHK2BSTSD2							
	HSN WR DATA 0 PARITY ERR	HSN WR DATA 1 PARITY ERR	HSN WR DATA 2 PARITY ERR	HSN WR DATA 3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
49	NDTM::CHK2BSTSD2							
	HSN MADR0 PARITY ERR	HSN MADR1 PARITY ERR	HSN MADR2 PARITY ERR	HSN MADR3 PARITY ERR	HSN MADR4 PARITY ERR	HSN MADR MASK ERR	(Not used)	(Not used)
4A	NDTM::CHK2BSTSD2							
	HSN SADR0 PARITY ERR	HSN SADR1 PARITY ERR	HSN SADR2 PARITY ERR	HSN SADR3 PARITY ERR	HSN SADR4 PARITY ERR	HSN SADR MASK ERR	(Not used)	(Not used)
4B	NDTM::CHK2BSTSD2							
	LA CNT0 PARITY ERR	LA CNT1 PARITY ERR	LA CNT2 PARITY ERR	LA CNT3 PARITY ERR	LA CNT4 PARITY ERR	LA CNT5 PARITY ERR	XFC REG PARITY ERR	PBFCTL REG PARITY ERR
4C	NDTM::CHK2BSTSH0							
	A/C PHASE CNT PARITY ERR	WR DATA CNT1 PARITY ERR	WR DATA CNT2 PARITY ERR	CSTAT0 CNT PARITY ERR	CSTA1 CNT PARITY ERR	(Not used)	RD DATA CNT PARITY ERR	RD DATA ADR PARITY ERR
4D	NDTM::CHK2BSTSH0							
	STATUS1 CNT1 PARITY ERR	STATUS1 CNT2 PARITY ERR	STATUS2 CNT1 PARITY ERR	STATUS2 CNT2 PARITY ERR	(Not used)	FT1,4 CNT PARITY ERR	FT2,5 CNT PARITY ERR	FT3,6 CNT PARITY
4E	NDTM::CHK2BSTSH0							
	FLRC CMP ERR	SLRC CMP ERR	LA CMP ERR	FW MODE ERR	FW CNT ERR	FW CNT PARITY ERR	CHSN PATH ERR	2SLV WR CMA# ERR
4F	NDTM::CHK2BSTSH0							
	CtoC COPY CMA# ERR	CinC COPY CMA# ERR	SLRC CHECKER PARITY ERR	SLRC GENERATER PARITY ERR	HSN ARBT ERR	HSN IT ERR	TO0 CNT PARITY ERR	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 63 or 6b (2/4)

	0	1	2	3	4	5	6	7
50	NDTM::CHK2BSTSH1							
	HSN RD DATA PATH0 PARITY ERR	HSN RD DATA PATH1 PARITY ERR	HSN RD DATA PATH2 PARITY ERR	HSN RD DATA PATH3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
51	NDTM::CHK2BSTSH1							
	HSN WR DATA PATH0 PARITY ERR	HSN WR DATA PATH1 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
52	NDTM::CHK2BSTSH1							
	CSTAT2 PARITY ERR	CSTAT4 PARITY ERR	CSTAT3 PARITY ERR	CSTAT5 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
53	NDTM::CHK2BSTSH1							
	PLRC CHECKER0 PARITY ERR	PLRC CHECKER1 PARITY ERR	PRC GENERATE R0 PARITY ERR	PRC GENERATE R1 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
54	NDTM::CHK2BSTSH2							
	CHSNCTL WR DATA0 PARITY ERR	CHSNCTL WR DATA1 PARITY ERR	CHSNCTL WR DATA2 PARITY ERR	(Not used)	(Not used)	CSTATCTL WR DATA1 PARITY ERR	(Not used)	CSTATCTL WR DATA3 PARITY ERR
55	NDTM::CHK2BSTSH2							
	CTIMER0 WR DATA0 PARITY ERR	CTIMER0 WR DATA1 PARITY ERR	CTIMER0 WR DATA2 PARITY ERR	CTIMER0 WR DATA3 PARITY ERR	CTIMER1 WR DATA0 PARITY ERR	CTIMER1 WR DATA1 PARITY ERR	CTIMER1 WR DATA2 PARITY ERR	CTIMER1 WR DATA3 PARITY ERR
56	NDTM::CHK2BSTSH2							
	CTIMER2 WR DATA0 PARITY ERR	CTIMER2 WR DATA1 PARITY ERR	CTIMER2 WR DATA2 PARITY ERR	CTIMER2 WR DATA3 PARITY ERR	CHSN PRIORITY0 PARITY ERR	(Not used)	(Not used)	(Not used)
57	NDTM::CHK2BSTSH2							
	CHSN EIJ WR DATA0 PARITY ERR	(Not used)	(Not used)	(Not used)	CHSN00CNT WR DATA0 PARITY ERR	(Not used)	(Not used)	(Not used)
58	NDTM::CHK2CSTS0							
	P0 RD DATA0 PARITY ERR	P0 RD DATA1 PARITY ERR	P0 RD DATA2 PARITY ERR	P0 RD DATA3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
59	NDTM::CHK2CSTS0							
	(Not used)	(Not used)	P0 SYNC ERR	P0 RD END-CODE ERR	P0 STATUS1 END-CODE ERR	P0 STATUS2 END-CODE ERR	P0 RD PLRC ERR	P0 STSTUS1 PLRC ERR
5A	NDTM::CHK2CSTS0							
	P0 STATUS2 PLRC ERR	P0 RD INV-PLRC ERR	P0 STATUS1 INV-PLRC ERR	P0 STATUS2 INV-PLRC ERR	P0 STATUS1 ST0,1 CMP ERR	P0 STATUS2 ST0,1 CMP ERR	P0 STATUS1 PKID ERR	P0 STATUS2 PKID ERR
5B	NDTM::CHK2CSTS0							
	P0 ERRINT ERR	P0 ACK ERR	P0 ENTOUT ERR	P0 STENOUT ERR	(Not used)	P0 TIMER1 TIME OUT	P0 TIMER2 TIME OUT	P0 TIMER3 TIME OUT
5C	NDTM::CHK2CSTS1							
	P1 RD DATA0 PARITY ERR	P1 RD DATA1 PARITY ERR	P1 RD DATA2 PARITY ERR	P1 RD DATA3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
5D	NDTM::CHK2CSTS1							
	(Not used)	(Not used)	P1 SYNC ERR	P1 RD END-CODE ERR	P1 STATUS1 END-CODE ERR	P1 STATUS2 END-CODE ERR	P1 RD PLRC ERR	P1 STSTUS1 PLRC ERR
5E	NDTM::CHK2CSTS1							
	P1 STATUS2 PLRC ERR	P1 RD INV-PLRC ERR	P1 STATUS1 INV-PLRC ERR	P1 STATUS2 INV-PLRC ERR	P1 STATUS1 ST0,1 CMP ERR	P1 STATUS2 ST0,1 CMP ERR	P1 STATUS1 PKID ERR	P1 STATUS2 PKID ERR
5F	NDTM::CHK2CSTS1							
	P1 ERRINT ERR	P1 ACK ERR	P1 ENTOUT ERR	P1 STENOUT ERR	(Not used)	P1 TIMER1 TIME OUT	P1 TIMER2 TIME OUT	P1 TIMER3 TIME OUT

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 63 or 6b (3/4)

	0	1	2	3	4	5	6	7
60	NDTM::CHK2DSTS							
	P0 ERRINT Received	P1 ERRINT Received	(Not used)	(Not used)	(Not used)	(Not used)	HSN STATUS1 ERR ON	HSN STATUS2 ERR ON
61	NDTM::CHSNSTSPCK							
	P0 STATUS1 B0 PARITY ERR	P0 STATUS1 B1 PARITY ERR	P0 STATUS1 B2 PARITY ERR	P0 STATUS1 B3 PARITY ERR	P0 STATUS1 B4 PARITY ERR	P0 STATUS1 B5 PARITY ERR	P0 STATUS1 B6 PARITY ERR	P0 STATUS1 B7 PARITY ERR
62	NDTM::CHSNSTSPCK							
	P0 STATUS2 B0 PARITY ERR	P0 STATUS2 B1 PARITY ERR	P0 STATUS2 B2 PARITY ERR	P0 STATUS2 B3 PARITY ERR	P0 STATUS2 B4 PARITY ERR	P0 STATUS2 B5 PARITY ERR	P0 STATUS2 B6 PARITY ERR	P0 STATUS2 B7 PARITY ERR
63	NDTM::CHSNSTSPCK							
	P1 STATUS1 B0 PARITY ERR	P1 STATUS1 B1 PARITY ERR	P1 STATUS1 B2 PARITY ERR	P1 STATUS1 B3 PARITY ERR	P1 STATUS1 B4 PARITY ERR	P1 STATUS1 B5 PARITY ERR	P1 STATUS1 B6 PARITY ERR	P1 STATUS1 B7 PARITY ERR
64	NDTM::CHSNSTSPCK							
	P1 STATUS2 B0 PARITY ERR	P1 STATUS2 B1 PARITY ERR	P1 STATUS2 B2 PARITY ERR	P1 STATUS2 B3 PARITY ERR	P1 STATUS2 B4 PARITY ERR	P1 STATUS2 B5 PARITY ERR	P1 STATUS2 B6 PARITY ERR	P1 STATUS2 B7 PARITY ERR
65	NDTM::CHNCNT							
	(Not used)	(Not used)	(Not used)	CHAIN#				
66	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE0)							
67	NDTM::PCIDATAL							
	PCI-DBF data CNT (BYTE1)							
68	NDTM::HSNMADRU							
	HSN master address (BYTE0)							
69	NDTM::HSNMADRL							
	HSN master address (BYTE1)							
6A	NDTM::HSNMADRL							
	HSN master address (BYTE2)							
6B	NDTM::HSNMADRL							
	HSN master address (BYTE3)							
6C	NDTM::HSNMADRL							
	HSN master address (BYTE4)							
6D	NDTM::HSNSADRU							
	HSN slave address (BYTE0)							
6E	NDTM::HSNSADRL							
	HSN slave address (BYTE1)							
6F	NDTM::HSNSADRL							
	HSN slave address (BYTE2)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(7) Byte28 = 63 or 6b (4/4)

	0	1	2	3	4	5	6	7
70	NDTM::HSNSADRL							
	HSN slave address (BYTE3)							
71	NDTM::HSNSADRL							
	HSN slave address (BYTE4)							
72	NDTM::LA0							
	LA0 value (BYTE0)							
73	NDTM::LA0							
	LA0 value (BYTE1)							
74	NDTM::LA1							
	LA1 value (BYTE0)							
75	NDTM::LA1							
	LA1 value (BYTE1)							
76	NDTM::LA1							
	LA1 value (BYTE2)							
77	NDTM::LA1							
	LA1 value (BYTE3)							
78	NDTM::HSNDATAL							
	HSN data CNT (BYTE0)							
79	NDTM::HSNDATAL							
	HSN data CNT (BYTE1)							
7A	NDTM::CHSNCTL							
	C Path #00 ENABLE	C Path #01 ENABLE	C Path #02 ENABLE	C Path #03 ENABLE	C Path #04 ENABLE	C Path #05 ENABLE	C Path #06 ENABLE	C Path #07 ENABLE
7B	NDTM::CHSNCTL							
	C Path #10 ENABLE	C Path #11 ENABLE	C Path #12 ENABLE	C Path #13 ENABLE	C Path #14 ENABLE	C Path #15 ENABLE	C Path #16 ENABLE	C Path #17 ENABLE
7C	NDTM::CHSNCTL							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PKID		
7D	NDTM::CHSNPHASE							
	ADR/CMD PHASE	WR DATA PHASE	RD DATA PHASE	STATUS1 PHASE	STATUS2 PHASE	(Not used)	(Not used)	(Not used)
7E	NDTM::CHSNPHASE							
	HSN P0 XFR	HSN P1 XFR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 64 ~ 67 or 6c ~ 6f (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'04'							
41	NDTM::XFS							
	TRANSFER END	ABORT	(Not used)	(Not used)	(Not used)	(Not used)	WCHK1 detected	CHK2 detected
42	NDTM::XFS							
	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK2E detected	(Not used)	(Not used)	(Not used)
43	NDTM::XFS							
	REQID			(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	NDTM::CHK2ASTS0							
	PCI ADR PARITY ERR	PCI DATA PARITY ERR	PCI PERR detected	PCI MASTER ABORT	PCI TARGET ABORT	(Not used)	(Not used)	(Not used)
45	NDTM::CHK2ASTS1							
	MBF0 RD DATA0 PARITY ERR	MBF0 RD DATA1 PARITY ERR	MBF0 RD DATA2 PARITY ERR	MBF0 RD DATA3 PARITY ERR	MBF1 RD DATA0 PARITY ERR	MBF1 RD DATA1 PARITY ERR	MBF1 RD DATA2 PARITY ERR	MBF1 RD DATA3 PARITY ERR
46	NDTM::CHK2ASTS1							
	MBF RD DATA PARITY ERR	(Not used)	MBF OVERFLOW ERR	MBF CALCULATE ERR	(Not used)	(Not used)	MBF WT ADR PARITY ERR	MBF RD ADR PARITY ERR
47	NDTM::CHK2ASTS1							
	(Not used)	(Not used)	(Not used)	MBSE0 BYTE0 PARITY ERR	MBSE0 BYTE1 PARITY ERR	MBSE0 BYTE2 PARITY ERR	MBSE0 BYTE3 PARITY ERR	MBSE0 BYTE4 PARITY ERR
48	NDTM::CHK2ASTS1							
	IOBASE0 BYTE0 PARITY ERR	IOBASE0 BYTE1 PARITY ERR	IOBASE0 BYTE2 PARITY ERR	MBSE0 BYTE0 PARITY ERR	MBSE0 BYTE1 PARITY ERR	MBSE0 BYTE2 PARITY ERR	MBSE0 BYTE3 PARITY ERR	MBSE0 BYTE4 PARITY ERR
49	NDTM::CHK2ASTS2							
	NDTM->PCI DATA BYTE0 PARITY ERR	NDTM->PCI DATA BYTE1 PARITY ERR	NDTM->PCI DATA BYTE2 PARITY ERR	NDTM->PCI DATA BYTE3 PARITY ERR	NDTM->PCI DATA BYTE4 PARITY ERR	NDTM->PCI DATA BYTE5 PARITY ERR	NDTM->PCI DATA BYTE6 PARITY ERR	NDTM->PCI DATA BYTE7 PARITY ERR
4A	NDTM::CHK2ASTS2							
	(Not used)	(Not used)	MBF SEL PARITY ERR	TBF SEL PARITY ERR	(Not used)	(Not used)	(Not used)	PCI SEQ ERR
4B	NDTM::CHK2ASTS2							
	LAY TIMER PARITY ERR	CACHE SIZE PARITY ERR	LAST CNT PARITY ERR	TIMEOUT CNT PARITY ERR	MABT CNT PARITY ERR	DMA CNT PARITY ERR	EXBF CNT PARITY ERR	IOCNT PARITY ERR
4C	NDTM::CHK2ASTS2							
	NADRCAL0 PARITY ERR	NADRCAL1 PARITY ERR	NADRCAL2 PARITY ERR	NADRCAL3 PARITY ERR	NADRCAL4 PARITY ERR	NADRCAL5 PARITY ERR	NADRCAL6 PARITY ERR	NADRCAL 7 PARITY ERR
4D	NDTM::PCILOG0							
	PCI Address (BYTE0)							
4E	NDTM::PCILOG0							
	PCI Address (BYTE1)							
4F	NDTM::PCILOG0							
	PCI Address (BYTE2)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 64 ~ 67 or 6c ~ 6f (2/4)

	0	1	2	3	4	5	6	7
50	NDTM::PCILOG0 PCI Address (BYTE3)							
51	NDTM::PCILOG1 PCI Address (BYTE4)							
52	NDTM::PCILOG1 PCI Address (BYTE5)							
53	NDTM::PCILOG1 PCI Address (BYTE6)							
54	NDTM::PCILOG1 PCI Address (BYTE7)							
55	NDTM::PCILOG2							
	LOG HOLD	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
56	NDTM::PCILOG2							
	DMA#0 LOGGING	DMA#1 LOGGING	DMA#2 LOGGING	DMA#3 LOGGING	DMA#4 LOGGING	DMA#5 LOGGING	(Not used)	(Not used)
57	NDTM::PCILOG2							
	(Not used)	LOG STS 1: Master 0: Target	LOG STS 1: 64bit 0: 32bit	LOG STS 1: DAC 0: SCA	(Not used)	(Not used)	(Not used)	(Not used)
58	NDTM::PCILOG2							
	PCI Command				(Not used)	(Not used)	(Not used)	(Not used)
59	NDTM PCICFG01							
	DET PARITY ERR	SIG SYS ERR	MST ABORT	TAG ABORT	(Not used)	DEV SEL0	DEV SEL1	DPTY ERR DET
5A	NDTM PCICFG01							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	SERR EN
5B	NDTM PCICFG01							
	(Not used)	PTY ERR RESP	(Not used)	(Not used)	(Not used)	BUS MST	MEM SPACE	IO SPACE
5C	NDTM::CHK2BSTSD0							
	PCI-DBF WR DATA PARITY ERR	PCI-DBF RD DATA ECC ERR	PCI-DBF RD DATA ECC ERR(A)	(Not used)	PCI-PBF WR DATA PARITY ERR	PCI-PBF RD DATA ECC ERR	PCI-PBF RD DATA ECC ERR(A)	(Not used)
5D	NDTM::CHK2BSTSP0							
	DMA-PCI ARBT ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5E	NDTM::CHK2BSTSP0							
	REQ PCIADR PARITY ERR 0	REQ PCIADR PARITY ERR 1	REQ PCIADR PARITY ERR 2	REQ PCIADR PARITY ERR 3	REQ PCIADR PARITY ERR 4	REQ PCIADR PARITY ERR 5	REQ PCIADR PARITY ERR 6	REQ PCIADR PARITY ERR 7
5F	NDTM::CHK2BSTSP0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	REQ LEN PARITY ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 64 ~ 67 or 6c ~ 6f (3/4)

	0	1	2	3	4	5	6	7
60	XPA::PnXERR0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
61	XPA::PnXERR0							
	(Not used)	(Not used)	PCI Master ADR CNT PARITY ERR	PCI Master ADR PARITY ERR	PCI Master SEQ ERR	PCI Master RD PARITY ERR	PCI Master PERR IN	PCI Master PERR OUT
62	XPA::PnXERR0							
	(Not used)	PCI Target 100 ADR PARITY ERR	PCI Target 66 ADR PARITY ERR	PCI Target SEQ ERR	(Not used)	PCI Target READ PARITY ERR	PCI Target PERR IN	PCI Target PERR OUT
63	XPA::PnXERR0							
	XR TRG SEQ ERR	(Not used)	(Not used)	XR Master ADR PARITY ERR	XR Target ADR CNT PARITY ERR	XR DMA RD PARITY ERR	XR TOV	XR RD PARITY ERR
64	XPA::PnDIG0							
	Diag Reg. 0							
65	XPA::PnDIG0							
	Diag Reg. 0							
66	XPA::PnDIG0							
	Diag Reg. 0							
67	XPA::PnDIG0							
	Diag Reg. 0							
68	XPA::PnDIG1							
	Diag Reg. 1							
69	XPA::PnDIG1							
	Diag Reg. 1							
6A	XPA::PnDIG1							
	Diag Reg. 1							
6B	XPA::PnDIG1							
	Diag Reg. 1							
6C	XPA::PnSC							
	DETECTED PARITY ERR	SIGNALLED SYSTEM ERR	RECEIVED MASTER ABORT	RECEIVED TARGET ABORT	(Not used)	DEVSEL TIMING 01: Medium		DATA PARITY REPORT
6D	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	XPA::PnSC							
	(Not used)	ENABLE PARITY RSP	(Not used)	(Not used)	(Not used)	ENABLE BUS MASTERING	ENABLE MEMORY SPACE	ENABLE I/O SPACE

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(8) Byte28 = 64 ~ 67 or 6c ~ 6f (4/4)

	0	1	2	3	4	5	6	7
70	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XR Target RD 3 PARITY ERR	XR Target RD 2 PARITY ERR	XR Target RD 1 PARITY ERR	XR Target RD 0 PARITY ERR
71	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XP WR LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
72	XPA::PnXERR1							
	XR Master ADR 3 PARITY ERR	XR Master ADR 2 PARITY ERR	XR Master ADR 1 PARITY ERR	XR Master ADR 0 PARITY ERR	XP DMA LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
73	XPA::PnXERR1							
	XR Target ADR 3 PARITY ERR	XR Target ADR 2 PARITY ERR	XR Target ADR 1 PARITY ERR	XR Target ADR 0 PARITY ERR	XR Target WR DATA 3 PARITY ERR	XR Target WR DATA 2 PARITY ERR	XR Target WR DATA 1 PARITY ERR	XR Target WR DATA 0 PARITY ERR
74	XPA::PnDIG2							
	Diag Reg. 2							
75	XPA::PnDIG2							
	Diag Reg. 2							
76	XPA::PnDIG2							
	Diag Reg. 2							
77	XPA::PnDIG2							
	Diag Reg. 2							
78	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
79	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 81 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'02'							
41	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Other	(Not used)	(Not used)	OTHER PCI Master ERR
42	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Own	PCI SERR IN	PCI CONFIG ERR	XPA ERR
43	XPA::PnI STAT							
	FDTM END DMA Other	FDTM END PMA Other	PCI DEV INT1 Other	PCI DEV INT0 Other	(Not used)	(Not used)	(Not used)	(Not used)
44	XPA::PnI STAT							
	CHK2A ERR 3 Own	CHK2A ERR 2 Own	CHK2A ERR 1 Own	CHK2A ERR 0 Own	PCI INT	MP INT	XPA XFR END	(Not used)
45	XPA::PnXERR0							
	(Not used)	PX RD RSP CNT ERR	XR DMA ACS ERR	XR DMA CTN ERR	PX RD RSP TOV	XR DMA LRC ERR	XR ADR PARITY ERR	XR WR DATA PARITY ERR
46	XPA::PnXERR0							
	(Not used)	(Not used)	PCI Master ADR CNT PARITY ERR	PCI Master ADR PARITY ERR	PCI Master SEQ ERR	PCI Master RD PARITY ERR	PCI Master PERR IN	PCI Master PERR OUT
47	XPA::PnXERR0							
	(Not used)	PCI Target 100 ADR PARITY ERR	PCI Target 66 ADR PARITY ERR	PCI Target SEQ ERR	(Not used)	PCI Target READ PARITY ERR	PCI Target PERR IN	PCI Target PERR OUT
48	XPA::PnXERR0							
	XR TRG SEQ ERR	(Not used)	(Not used)	XR Master ADR PARITY ERR	XR Target ADR CNT PARITY ERR	XR DMA RD PARITY ERR	XR TOV	XR RD PARITY ERR
49	XPA::PnDIG0							
	Diag Reg. 0							
4A	XPA::PnDIG0							
	Diag Reg. 0							
4B	XPA::PnDIG0							
	Diag Reg. 0							
4C	XPA::PnDIG0							
	Diag Reg. 0							
4D	XPA::PnDIG1							
	Diag Reg. 1							
4E	XPA::PnDIG1							
	Diag Reg. 1							
4F	XPA::PnDIG1							
	Diag Reg. 1							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 81 (2/4)

	0	1	2	3	4	5	6	7
50	XPA::PnDIG1							
	Diag Reg. 1							
51	XPA::PnSC							
	DETECTED PARITY ERR	SIGNALLED SYSTEM ERR	RECEIVED MASTER ABORT	RECEIVED TARGET ABORT	(Not used)	DEVSEL TIMING 01: Medium		DATA PARITY REPORT
52	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
53	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
54	XPA::PnSC							
	(Not used)	ENABLE PARITY RSP	(Not used)	(Not used)	(Not used)	ENABLE BUS MASTERING	ENABLE MEMORY SPACE	ENABLE I/O SPACE
55	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XR Target RD 3 PARITY ERR	XR Target RD 2 PARITY ERR	XR Target RD 1 PARITY ERR	XR Target RD 0 PARITY ERR
56	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XP WR LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
57	XPA::PnXERR1							
	XR Master ADR 3 PARITY ERR	XR Master ADR 2 PARITY ERR	XR Master ADR 1 PARITY ERR	XR Master ADR 0 PARITY ERR	XP DMA LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
58	XPA::PnXERR1							
	XR Target ADR 3 PARITY ERR	XR Target ADR 2 PARITY ERR	XR Target ADR 1 PARITY ERR	XR Target ADR 0 PARITY ERR	XR Target WR DATA 3 PARITY ERR	XR Target WR DATA 2 PARITY ERR	XR Target WR DATA 1 PARITY ERR	XR Target WR DATA 0 PARITY ERR
59	XPA::PnDIG1							
	Diag Reg. 2							
5A	XPA::PnDIG1							
	Diag Reg. 2							
5B	XPA::PnDIG1							
	Diag Reg. 2							
5C	XPA::PnDIG1							
	Diag Reg. 2							
5D	CXL::CFGSTAT							
	PCI Configuration Reg. STATUS							
5E	CXL::CFGSTAT							
	PCI Configuration Reg. STATUS							
5F	CXL::CFGCMD							
	PCI Configuration Reg. COMMAND							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 81 (3/4)

	0	1	2	3	4	5	6	7
60	CXL::CFGCMD PCI Configuration Reg. COMMAND							
61	FDTA::PCICLK2A							
	MWB8B PARITY ERR	MWB4B PARITY ERR	MWB8B PARITY GEN CHK ERR	MWB4B PARITY GEN CHK ERR	MRB0 PARITY ERR	MRB1 PARITY ERR	(Not used)	PCI IN PARITY ERR
62	FDTA::PCICLK2A							
	(Not used)	(Not used)	PCI ADR PARITY ERR	PCI ADR PARITY GEN CHK ERR	(Not used)	(Not used)	(Not used)	PCI IN SERR
63	FDTA::PCICLK2A							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
64	FDTA::PCICLK2A							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
65	FDTA::PCICLK2B0							
	DBF0 B0 PARITY ERR	DBF0 B1 PARITY ERR	DBF0 B2 PARITY ERR	DBF0 B3 PARITY ERR	DBF0 B4 PARITY ERR	DBF0 B5 PARITY ERR	DBF0 B6 PARITY ERR	DBF0 B7 PARITY ERR
66	FDTA::PCICLK2B0							
	DBF1 B0 PARITY ERR	DBF1 B1 PARITY ERR	DBF1 B2 PARITY ERR	DBF1 B3 PARITY ERR	DBF1 B4 PARITY ERR	DBF1 B5 PARITY ERR	DBF1 B6 PARITY ERR	DBF1 B7 PARITY ERR
67	FDTA::PCICLK2B0							
	DBF IN PARITY ERR 00	DBF IN PARITY ERR 01	DBF IN PARITY ERR 10	DBF IN PARITY ERR 11	(Not used)	DBF 16BI PARITY ERR	DBF 16B0 PARITY ERR	(Not used)
68	FDTA::PCICLK2B0							
	XBF B0 PARITY ERR	XBF B1 PARITY ERR	XBF B2 PARITY ERR	XBF B3 PARITY ERR	(Not used)	(Not used)	(Not used)	XBF IN PARITY ERR
69	FDTA::PCICLK2B1							
	PCI SEQ PARITY ERR	WTBDF SEQ PARITY ERR	WTXBF SEQ PARITY ERR	RDBDF SEQ PARITY ERR	MRB8B PARITY GEN ERR	MRB4B PARITY GEN ERR	XWB WPTR PARITY ERR	XWB SPTR PARITY ERR
6A	FDTA::PCICLK2B1							
	HWB WPTR PARITY ERR	HWB SPTR PARITY ERR	HRB WPTR PARITY ERR	HRB SPTR PARITY ERR	DLRC CMP ERR	DLRC PARITY ERR	(Not used)	WDLRC PARITY ERR
6B	FDTA::PCICLK2B1							
	HWB PARITY ERR 00	HWB PARITY ERR 01	HWB PARITY ERR 10	HWB PARITY ERR 11	HRB PARITY ERR 00	HRB PARITY ERR 01	HRB PARITY ERR 10	HRB PARITY ERR 11
6C	FDTA::PCICLK2B1							
	XWB0 PARITY ERR	XWB1 PARITY ERR	MRB0 PARITY ERR	MRB1 PARITY ERR	DWA XAD PARITY ERR	PCI XAD PARITY ERR	DBFW ADRCNT PARITY ERR	DBFR ADRCNT PARITY ERR
6D	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	PCI OVER RUN ERR	INV MEMACS ERR	CRC POINTER ERR	DWA CMP ERR	DRA CMP ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(9) Byte28 = 81 (4/4)

	0	1	2	3	4	5	6	7
70	FDTA::PCCHK2B2							
	DBF WT ACSERR	DBF RD ACSERR	DMA DSTT ERR	XFC MODE ERR	ACC WIDTH ERR	STADR ACC ERR	DBF ACC ERR	XBF ACC ERR
71	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	80ns TIMER CNT PARITY ERR	60ns TIMER CNT PARITY ERR	(Not used)	ARB0 SEQ PARITY ERR	ARB1 SEQ PARITY ERR	ARB CMP ERR
73	FDTA::HCMD							
	CACHE Transfer Command (BYTE0)							
74	FDTA::HCMD							
	CACHE Transfer Command (BYTE1)							
75	FDTA::HCMD							
	CACHE Transfer Command (BYTE2)							
76	FDTA::PnMSADR							
	(Not used)	(Not used)	(Not used)	XBF Data Transfer Parameter Address (x'00' ~ x'1F')				
77	FDTA::CBEN							
	C PATH#00 ENABLE	C PATH#01 ENABLE	C PATH#02 ENABLE	C PATH#03 ENABLE	C PATH#04 ENABLE	C PATH#05 ENABLE	C PATH#06 ENABLE	C PATH#07 ENABLE
78	FDTA::CBEN							
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE	C PATH#17 ENABLE
79	FDTA::PKID							
	(Not used)	(Not used)	(Not used)	(Not used)	PKID#			
7A	FDTA::MONI							
	MPID#				LSI REV.			
7B	FDTA::MONI							
	(Not used)	(Not used)	(Not used)	(Not used)	LSI REV.-2			
7C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Devided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(10) Byte28 = 80 or 82 or 84 or 86 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'03'							
41	XPA::PnlSTAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Other	(Not used)	(Not used)	OTHER PCI Master ERR
42	XPA::PnlSTAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Own	PCI SERR IN	PCI CONFIG ERR	XPA ERR
43	XPA::PnlSTAT							
	FDTM END DMA Other	FDTM END PMA Other	PCI DEV INT1 Other	PCI DEV INT0 Other	(Not used)	(Not used)	(Not used)	(Not used)
44	XPA::PnlSTAT							
	CHK2A ERR 3 Own	CHK2A ERR 2 Own	CHK2A ERR 1 Own	CHK2A ERR 0 Own	PCI INT	MP INT	XPA XFR END	(Not used)
45	FDTA::DMACHK2BST0							
	DMA TJ0 PARITY ERR	DMA TJ1 PARITY ERR	DMA TJ2 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
46	FDTA::DMACHK2BST0							
	CHAIN CNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
47	FDTA::DMACHK2CST0							
	HSN ADR CNT1 PARITY ERR0	HSN ADR CNT1 PARITY ERR1	HSN ADR CNT1 PARITY ERR2	HSN ADR CNT1 PARITY ERR3	HSN ADR CNT1 PARITY ERR4	(Not used)	(Not used)	(Not used)
48	FDTA::DMACHK2CST0							
	HSN ADR CNT2 PARITY ERR0	HSN ADR CNT2 PARITY ERR1	HSN ADR CNT2 PARITY ERR2	HSN ADR CNT2 PARITY ERR3	HSN ADR CNT2 PARITY ERR4	(Not used)	(Not used)	(Not used)
49	FDTA::DMACHK2CST0							
	SUB BLOCK PARITY ERR	SUB BLOCK CNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4A	FDTA::DMACHK2CST0							
	XBF CMD PARITY ERR	LA CNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4B	FDTA::DMACHK2CST1							
	HSN RD ADR CNT PARITY ERR	HSN WT ADR CNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	FDTA::DMACHK2CST1							
	PWB LRC PARITY ERR0	PWB LRC PARITY ERR1	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4D	FDTA::DMACHK2CST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	DMA TIME OUT
4E	FDTA::DMACHK2CST2							
	SDBF0 PARITY ERR	SDBF1 PARITY ERR	SDBF2 PARITY ERR	SDBF3 PARITY ERR	SDLRC ERR	DBF PARITY ERR	XBF BLOCKCODE ERR	XBF PARITY ERR
4F	FDTA::DMACHK2CST2							
	SLRC GEN PARITY ERR	SLRC CHK PARITY ERR	LA ERR	SLRC ERR	(Not used)	(Not used)	RAID MODE ERR	BLKBD ACS ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(10) Byte28 = 80 or 82 or 84 or 86 (2/4)

	0	1	2	3	4	5	6	7
50	FDTA::DMACHK2CST2							
	RD DT END-CODE	RD DT END-CODE	RD DT END-CODE	RD DT END-CODE	(Not used)	(Not used)	RD DT INV-PLRC ERR0	RD DT INV-PLRC ERR1
51	FDTA::DMACHK2CST2							
	(Not used)	(Not used)	(Not used)	(Not used)	DMA TIMER PARITY ERR	(Not used)	(Not used)	DMA SEQ PARITY ERR
52	FDTA::H0CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 DBF PARITY ERR
53	FDTA::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB0 PARITY ERR
54	FDTA::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB1 PARITY ERR
55	FDTA::H0CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 SNB0 PARITY ERR	P0 SNB1 PARITY ERR
56	FDTA::H0CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 TJ1 PARITY ERR	P0 TJ2 PARITY ERR
57	FDTA::H0CHK2CST0							
	P0 TX1 TIMER0 PARITY ERR	(Not used)	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	P0 TX1 CNT PARITY ERR	(Not used)	P0 TX1 SEQ PARITY ERR
58	FDTA::H0CHK2CST1							
	P0 TX2 TIMER0 PARITY ERR	P0 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	(Not used)	(Not used)	P0 TX1 SEQ PARITY ERR
59	FDTA::H0CHK2CST2							
	P0 PRBPTR CMP ERR	P0 PRBPTR PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5A	FDTA::H0CHK2CST2							
	P0 RX TIMER0 PARITY ERR	P0 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P0 RX PTR PARITY ERR	P0 RX CNT PARITY ERR	(Not used)	P0 RX SEQ PARITY ERR
5B	FDTA::H1CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 DBF PARITY ERR
5C	FDTA::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB0 PARITY ERR
5D	FDTA::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB1 PARITY ERR
5E	FDTA::H1CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 SNB0 PARITY ERR	P1 SNB1 PARITY ERR
5F	FDTA::H1CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 TJ1 PARITY ERR	P1 TJ2 PARITY ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(10) Byte28 = 80 or 82 or 84 or 86 (3/4)

	0	1	2	3	4	5	6	7
60	FDTA::H1CHK2CST0							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
61	FDTA::H1CHK2CST1							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
62	FDTA::H1CHK2CST2							
	P1 PRBPTR CMP ERR	P1 PRBPTR PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
63	FDTA::H1CHK2CST2							
	P1 RX TIMER0 PARITY ERR	P1 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P1 RX PTR PARITY ERR	P1 RX CNT PARITY ERR	(Not used)	P1 RX SEQ PARITY ERR
64	FDTA::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE0)							
65	FDTA::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE1)							
66	FDTA::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE2)							
67	FDTA::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE3)							
68	FDTA::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE4)							
69	FDTA::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE0)							
6A	FDTA::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE1)							
6B	FDTA::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE2)							
6C	FDTA::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE3)							
6D	FDTA::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE4)							
6E	FDTA::HCMD							
	CACHE Transfer Command (BYTE0)							
6F	FDTA::HCMD							
	CACHE Transfer Command (BYTE1)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(10) Byte28 = 80 or 82 or 84 or 86 (4/4)

10)	Byte28	0	1	2	3	4	5	6	7
70	FDTA::HCMD								
	CACHE Transfer Command (BYTE2)								
71	FDTA::LA0								
	LA Expected Value (BYTE0)								
72	FDTA::LA1								
	LA Expected Value (BYTE1)								
73	FDTA::LA1								
	LA Expected Value (BYTE2)								
74	FDTA::LA1								
	LA Expected Value (BYTE3)								
75	FDTA::LA1								
	LA Expected Value (BYTE4)								
76	FDTA::PnMSADR								
	(Not used)	(Not used)	(Not used)	XBF Data Transfer Parameter Address (x'00' ~ x'1F')					
77	FDTA::CBEN								
	C PATH#00 ENABLE	C PATH#01 ENABLE	C PATH#02 ENABLE	C PATH#03 ENABLE	C PATH#04 ENABLE	C PATH#05 ENABLE	C PATH#06 ENABLE	C PATH#07 ENABLE	C PATH#07 ENABLE
78	FDTA::CBEN								
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE	C PATH#17 ENABLE	C PATH#17 ENABLE
79	FDTA::PKID								
	(Not used)	(Not used)	(Not used)	(Not used)	PKID#				
7A	FDTA::MONI								
	MPID#				LSI REV.				
7B	FDTA::MONI								
	(Not used)	(Not used)	(Not used)	(Not used)	LSI REV.-2				
7C	Reserved								
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved								
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information								
	Error Devided Result Code								
7F	Micro Information								
	CHK2 Detected Counter (Low)								

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(11) Byte28 = 90 or 93 or 95 or 97 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'11'							
41	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Other	(Not used)	(Not used)	OTHER PCI Master ERR
42	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Own	PCI SERR IN	PCI CONFIG ERR	XPA ERR
43	XPA::PnI STAT							
	FDTM END DMA Other	FDTM END PMA Other	PCI DEV INT1 Other	PCI DEV INT0 Other	(Not used)	(Not used)	(Not used)	(Not used)
44	XPA::PnI STAT							
	CHK2A ERR 3 Own	CHK2A ERR 2 Own	CHK2A ERR 1 Own	CHK2A ERR 0 Own	PCI INT	MP INT	XPA XFR END	(Not used)
45	FDTM::PCIC HK2A							
	MWB8B PARITY ERR	MWB4B PARITY ERR	MWB8B PARITY GEN CHK ERR	MWB4B PARITY GEN CHK ERR	PMA MRB0 PARITY ERR	PMA MRB1 PARITY ERR	(Not used)	PCI IN PARITY ERR
46	FDTM::PCIC HK2A							
	PMA MRB8B PARITY GEN ERR	PMA MRB4B PARITY GEN ERR	PCI ADR PARITY ERR	PCI ADR PARITY GEN CHK ERR	(Not used)	(Not used)	(Not used)	PCI IN SERR
47	FDTM::PCIC HK2A							
	PRFLRC PARITY ERR	PRFLRC ERR	PRA CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
48	FDTM::PCIC HK2A							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
49	FDTM::PCIC HK2B0							
	PPACNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	PPBFIN PARITY ERR	PCIPBF PARITY ERR	PBFOUT PARITY ERR
4A	FDTM::PCIC HK2B0							
	PWTRFCNT PARITY ERR	PWFLRC PARITY ERR	PRTFCNT PARITY ERR	PRFLRC PARITY ERR	PRFLRC ERR	(Not used)	(Not used)	(Not used)
4B	FDTM::PCIC HK2B0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	FDTM::PCIC HK2B0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4D	FDTM::PCIC HK2B1							
	(Not used)	(Not used)	(Not used)	PMA MRB8B PARITY GEN ERR	PMA MRB4B PARITY GEN ERR	(Not used)	(Not used)	(Not used)
4E	FDTM::PCIC HK2B1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	FDTM::PCIC HK2B1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(11) Byte28 = 90 or 93 or 95 or 97 (2/4)

	0	1	2	3	4	5	6	7
50	FDTA::PCICLK2B1							
	(Not used)	(Not used)	PMA MRB0 PARITY ERR	PMA MRB1 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
51	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
52	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
53	FDTA::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PWA CMP ERR	PRA CMP ERR
54	FDTA::PCICLK2B2							
	PBF WT ACS ERR	PBF RD ACS ERR	PMA DSTT ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
55	FDTA::PMACHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
56	FDTA::PMACHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
57	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
58	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
59	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5A	FDTA::PMACHK2CST0							
	HSN CMD LOAD PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5B	FDTA::PMACHK2CST1							
	HSN RD ADR CNT PARITY ERR	HSN WT ADR CNT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5C	FDTA::PMACHK2CST1							
	PWB LRC PARITY ERR0	PWB LRC PARITY ERR1	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5D	FDTA::PMACHK2CST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PMA TIME OUT
5E	FDTA::PMACHK2CST2							
	TX FLRC ERR	TX FLRC PARITY ERR	RX FLRC PARITY ERR	RX FLRC PARITY ERR	PBF0 PARITY ERR	PBF1 PARITY ERR	PBF2 PARITY ERR	PBF3 PARITY ERR
5F	FDTA::PMACHK2CST2							
	(Not used)	(Not used)	(Not used)	(Not used)	PMA ACCMD ERR	XFC MODE ERR	RAID MODE ERR	BLK MODE ACSERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(11) Byte28 = 90 or 93 or 95 or 97 (3/4)

	0	1	2	3	4	5	6	7
60	FDTM::PMACHK2CST2							
	RD DT END-CODE ERR0	RD DT END-CODE ERR1	RD DT END-CODE ERR2	RD DT END-CODE ERR3	TX FLRC 0 PADDING ERR	RX FLRC 0 PADDING ERR	RD DT INV-PLRC ERR0	RD DT INV-PLRC ERR1
61	FDTM::PMACHK2CST2							
	HSN PBF IN PARITY ERR	(Not used)	(Not used)	(Not used)	PMA TIMER PARITY ERR	(Not used)	PMA ARB SEQ PARITY ERR	PMA SEQ PARITY ERR
62	FDTM::H0CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 DBF PARITY ERR
63	FDTM::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB0 PARITY ERR
64	FDTM::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB1 PARITY ERR
65	FDTM::H0CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 SNB0 PARITY ERR	P0 SNB1 PARITY ERR
66	FDTM::H0CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 TJ1 PARITY ERR	P0 TJ2 PARITY ERR
67	FDTM::H0CHK2CST0							
	P0 TX1 TIMER0 PARITY ERR	(Not used)	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	P0 TX1 CNT PARITY ERR	(Not used)	P0 TX1 SEQ PARITY ERR
68	FDTM::H0CHK2CST1							
	P0 TX2 TIMER0 PARITY ERR	P0 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	(Not used)	(Not used)	P0 TX1 SEQ PARITY ERR
69	FDTM::H0CHK2CST2							
	(Not used)	(Not used)	P0 PRBPTR CMP ERR	P0 PRBPTR PARITY ERR	P0 PRBPTR 0 CMP ERR	P0 PRBPTR 1 CMP ERR	P0 PRBPTR 0 PARITY ERR	P0 PRBPTR 1 PARITY ERR
6A	FDTM::H0CHK2CST2							
	P0 RX TIMER0 PARITY ERR	P0 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P0 RX PTR PARITY ERR	P0 RX CNT PARITY ERR	(Not used)	P0 RX SEQ PARITY ERR
6B	FDTM::H1CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 DBF PARITY ERR
6C	FDTM::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB0 PARITY ERR
6D	FDTM::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB1 PARITY ERR
6E	FDTM::H1CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 SNB0 PARITY ERR	P1 SNB1 PARITY ERR
6F	FDTM::H1CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 TJ1 PARITY ERR	P1 TJ2 PARITY ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(11) Byte28 = 90 or 93 or 95 or 97 (4/4)

	0	1	2	3	4	5	6	7
70	FDTM::H1CHK2CST0							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
71	FDTM::H1CHK2CST1							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
72	FDTM::H1CHK2CST2							
	(Not used)	(Not used)	P1 PRBPTR CMP ERR	P1 PRBPTR PARITY ERR	P1 PRBPTR 0 CMP ERR	P1 PRBPTR 1 CMP ERR	P1 PRBPTR 0 PARITY ERR	P1 PRBPTR 1 PARITY ERR
73	FDTM::H1CHK2CST2							
	P1 RX TIMER0 PARITY ERR	P1 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P1 RX PTR PARITY ERR	P1 RX CNT PARITY ERR	(Not used)	P1 RX SEQ PARITY ERR
74	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	80ns TIMER CNT PARITY ERR	60ns TIMER CNT PARITY ERR	(Not used)	ARB0 SEQ PARITY ERR	ARB1 SEQ PARITY ERR	ARB CMP ERR
76	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
77	FDTA::CBEN							
	C PATH#00 ENABLE	C PATH#01 ENABLE	C PATH#02 ENABLE	C PATH#03 ENABLE	C PATH#04 ENABLE	C PATH#05 ENABLE	C PATH#06 ENABLE	C PATH#07 ENABLE
78	FDTA::CBEN							
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE	C PATH#17 ENABLE
79	FDTA::PKID							
	(Not used)	(Not used)	(Not used)	(Not used)	PKID#			
7A	FDTA::MONI							
	MPID#				LSI REV.			
7B	FDTA::MONI							
	(Not used)	(Not used)	(Not used)	(Not used)	LSI REV.-2			
7C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Devided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(12) Byte28 = 91 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'12'							
41	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Other	(Not used)	(Not used)	OTHER PCI Master ERR
42	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Own	PCI SERR IN	PCI CONFIG ERR	XPA ERR
43	XPA::PnI STAT							
	FDTM END DMA Other	FDTM END PMA Other	PCI DEV INT1 Other	PCI DEV INT0 Other	(Not used)	(Not used)	(Not used)	(Not used)
44	XPA::PnI STAT							
	CHK2A ERR 3 Own	CHK2A ERR 2 Own	CHK2A ERR 1 Own	CHK2A ERR 0 Own	PCI INT	MP INT	XPA XFR END	(Not used)
45	XPA::PnXERR0							
	(Not used)	PX RD RSP CNT ERR	XR DMA ACS ERR	XR DMA CTN ERR	PX RD RSP TOV	XR DMA LRC ERR	XR ADR PARITY ERR	XR WR DATA PARITY ERR
46	XPA::PnXERR0							
	(Not used)	(Not used)	PCI Master ADR CNT PARITY ERR	PCI Master ADR PARITY ERR	PCI Master SEQ ERR	PCI Master RD PARITY ERR	PCI Master PERR IN	PCI Master PERR OUT
47	XPA::PnXERR0							
	(Not used)	PCI Target 100 ADR PARITY ERR	PCI Target 66 ADR PARITY ERR	PCI Target SEQ ERR	(Not used)	PCI Target READ PARITY ERR	PCI Target PERR IN	PCI Target PERR OUT
48	XPA::PnXERR0							
	XR TRG SEQ ERR	(Not used)	(Not used)	XR Master ADR PARITY ERR	XR Target ADR CNT PARITY ERR	XR DMA RD PARITY ERR	XR TOV	XR RD PARITY ERR
49	XPA::PnDIG0							
	Diag Reg. 0							
4A	XPA::PnDIG0							
	Diag Reg. 0							
4B	XPA::PnDIG0							
	Diag Reg. 0							
4C	XPA::PnDIG0							
	Diag Reg. 0							
4D	XPA::PnDIG1							
	Diag Reg. 1							
4E	XPA::PnDIG1							
	Diag Reg. 1							
4F	XPA::PnDIG1							
	Diag Reg. 1							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(12) Byte28 = 91 (2/4)

	0	1	2	3	4	5	6	7
50	XPA::PnDIG1							
	Diag Reg. 1							
51	XPA::PnSC							
	DETECTED PARITY ERR	SIGNALLED SYSTEM ERR	RECEIVED MASTER ABORT	RECEIVED TARGET ABORT	(Not used)	DEVSEL TIMING 01: Medium		DATA PARITY REPORT
52	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
53	XPA::PnSC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
54	XPA::PnSC							
	(Not used)	ENABLE PARITY RSP	(Not used)	(Not used)	(Not used)	ENABLE BUS MASTERING	ENABLE MEMORY SPACE	ENABLE I/O SPACE
55	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XR Target RD 3 PARITY ERR	XR Target RD 2 PARITY ERR	XR Target RD 1 PARITY ERR	XR Target RD 0 PARITY ERR
56	XPA::PnXERR1							
	(Not used)	(Not used)	(Not used)	(Not used)	XP WR LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
57	XPA::PnXERR1							
	XR Master ADR 3 PARITY ERR	XR Master ADR 2 PARITY ERR	XR Master ADR 1 PARITY ERR	XR Master ADR 0 PARITY ERR	XP DMA LRC ERR	XP RD LRC ERR	PX WR LRC ERR	PCI DATA IN PARITY ERR
58	XPA::PnXERR1							
	XR Target ADR 3 PARITY ERR	XR Target ADR 2 PARITY ERR	XR Target ADR 1 PARITY ERR	XR Target ADR 0 PARITY ERR	XR Target WR DATA 3 PARITY ERR	XR Target WR DATA 2 PARITY ERR	XR Target WR DATA 1 PARITY ERR	XR Target WR DATA 0 PARITY ERR
59	XPA::PnDIG1							
	Diag Reg. 2							
5A	XPA::PnDIG1							
	Diag Reg. 2							
5B	XPA::PnDIG1							
	Diag Reg. 2							
5C	XPA::PnDIG1							
	Diag Reg. 2							
5D	CXL::CFGSTAT							
	PCI Configuration Reg. STATUS							
5E	CXL::CFGSTAT							
	PCI Configuration Reg. STATUS							
5F	CXL::CFGCMD							
	PCI Configuration Reg. COMMAND							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(12) Byte28 = 91 (3/4)

	0	1	2	3	4	5	6	7
60	CXL::CFGCMD PCI Configuration Reg. COMMAND							
61	FDTM::PCICLK2A							
	MWB8B PARITY ERR	MWB4B PARITY ERR	MWB8B PARITY GEN CHK ERR	MWB4B PARITY GEN CHK ERR	MRB0 PARITY ERR	MRB1 PARITY ERR	(Not used)	PCI IN PARITY ERR
62	FDTM::PCICLK2A							
	(Not used)	(Not used)	PCI ADR PARITY ERR	PCI ADR PARITY GEN CHK ERR	(Not used)	(Not used)	(Not used)	PCI IN SERR
63	FDTM::PCICLK2A							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
64	FDTM::PCICLK2A							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
65	FDTM::PCICLK2B0							
	DBF0 B0 PARITY ERR	DBF0 B1 PARITY ERR	DBF0 B2 PARITY ERR	DBF0 B3 PARITY ERR	DBF0 B4 PARITY ERR	DBF0 B5 PARITY ERR	DBF0 B6 PARITY ERR	DBF0 B7 PARITY ERR
66	FDTM::PCICLK2B0							
	DBF1 B0 PARITY ERR	DBF1 B1 PARITY ERR	DBF1 B2 PARITY ERR	DBF1 B3 PARITY ERR	DBF1 B4 PARITY ERR	DBF1 B5 PARITY ERR	DBF1 B6 PARITY ERR	DBF1 B7 PARITY ERR
67	FDTM::PCICLK2B0							
	DBF IN PARITY ERR 00	DBF IN PARITY ERR 01	DBF IN PARITY ERR 10	DBF IN PARITY ERR 11	(Not used)	(Not used)	(Not used)	(Not used)
68	FDTM::PCICLK2B0							
	XBF B0 PARITY ERR	XBF B1 PARITY ERR	XBF B2 PARITY ERR	XBF B3 PARITY ERR	(Not used)	(Not used)	(Not used)	XBR IN PARITY ERR
69	FDTM::PCICLK2B1							
	PCI SEQ PARITY ERR	WTBDF SEQ PARITY ERR	WTXBF SEQ PARITY ERR	RDBDF SEQ PARITY ERR	(Not used)	(Not used)	XWB WPTR PARITY ERR	XWB SPTR PARITY ERR
6A	FDTM::PCICLK2B1							
	HWB WPTR PARITY ERR	HWB SPTR PARITY ERR	HRB WPTR PARITY ERR	HRB SPTR PARITY ERR	DLRC CMP ERR	DLRC PARITY ERR	(Not used)	WDLRC PARITY ERR
6B	FDTM::PCICLK2B1							
	HWB PARITY ERR 00	HWB PARITY ERR 01	HWB PARITY ERR 10	HWB PARITY ERR 11	HRB PARITY ERR 00	HRB PARITY ERR 01	HRB PARITY ERR 10	HRB PARITY ERR 11
6C	FDTM::PCICLK2B1							
	XWB0 PARITY ERR	XWB1 PARITY ERR	MRB0 PARITY ERR	MRB1 PARITY ERR	DWA XAD PARITY ERR	PCI XAD PARITY ERR	DBFW ADRCNT PARITY ERR	DBFR ADRCNT PARITY ERR
6D	FDTM::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	FDTM::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	FDTM::PCICLK2B2							
	(Not used)	(Not used)	(Not used)	PCI OVER RUN ERR	(Not used)	(Not used)	DWA CMP ERR	DRA CMP ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(12) Byte28 = 91 (4/4)

	0	1	2	3	4	5	6	7
70	FDTM::PCICCHK2B2							
	(Not used)	(Not used)	(Not used)	XFC MODE ERR	ACC WIDTH ERR	STADR ACC ERR	DBF ACC ERR	XBF ACC ERR
71	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	FDTA::COMMCHK2CST0							
	(Not used)	(Not used)	80ns TIMER CNT PARITY ERR	60ns TIMER CNT PARITY ERR	(Not used)	ARB0 SEQ PARITY ERR	ARB1 SEQ PARITY ERR	ARB CMP ERR
73	FDTM::HCMD							
	CACHE Transfer Command (BYTE0)							
74	FDTM::HCMD							
	CACHE Transfer Command (BYTE1)							
75	FDTM::HCMD							
	CACHE Transfer Command (BYTE2)							
76	FDTM::HCMD2							
	CACHE Transfer Command2 (BYTE0)							
77	FDTM::HCMD2							
	CACHE Transfer Command2 (BYTE1)							
78	FDTM::HCMD2							
	CACHE Transfer Command2 (BYTE2)							
79	FDTA::CBEN							
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE	C PATH#17 ENABLE
7A	FDTA::CBEN							
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE	C PATH#17 ENABLE
7B	FDTA::PKID							
	(Not used)	(Not used)	(Not used)	(Not used)	PKID#			
7C	FDTA::MONI							
	MPID#				LSI REV.			
7D	FDTA::MONI							
	(Not used)	(Not used)	(Not used)	(Not used)	LSI REV.-2			
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(13) Byte28 = 92 or 94 or 96 (1/4)

	0	1	2	3	4	5	6	7
40	FORMAT NO. x'13'							
41	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Other	(Not used)	(Not used)	OTHER PCI Master ERR
42	XPA::PnI STAT							
	(Not used)	(Not used)	(Not used)	(Not used)	FDTM CHK2A Own	PCI SERR IN	PCI CONFIG ERR	XPA ERR
43	XPA::PnI STAT							
	FDTM END DMA Other	FDTM END PMA Other	PCI DEV INT1 Other	PCI DEV INT0 Other	(Not used)	(Not used)	(Not used)	(Not used)
44	XPA::PnI STAT							
	CHK2A ERR 3 Own	CHK2A ERR 2 Own	CHK2A ERR 1 Own	CHK2A ERR 0 Own	PCI INT	MP INT	XPA XFR END	(Not used)
45	FDTA::PMACHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
46	FDTA::PMACHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
47	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
48	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
49	FDTA::PMACHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4A	FDTA::PMACHK2CST0							
	HSN CMD LOAD PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4B	FDTA::PMACHK2CST1							
	HSN RD ADR CNT PARITY ERR	HSN WT LRC PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	FDTA::PMACHK2CST1							
	PWB LRC PARITY ERR0	PWB LRC PARITY ERR1	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4D	FDTA::PMACHK2CST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	PMA TIME OUT
4E	FDTA::PMACHK2CST2							
	TX FLRC ERR	TX FLRC PARITY ERR	RX FLRC PARITY ERR	RX FLRC PARITY ERR	PBF0 PARITY ERR	PBF1 PARITY ERR	PBF2 PARITY ERR	PBF3 PARITY ERR
4F	FDTA::PMACHK2CST2							
	(Not used)	(Not used)	(Not used)	(Not used)	PMA ACCMD ERR	XFC MODE ERR	RAID MODE ERR	BLK MODE ACSERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(13) Byte28 = 92 or 94 or 96 (2/4)

	0	1	2	3	4	5	6	7
50	FDTM::PMACHK2CST2							
	RD DT END-CODE ERR0	RD DT END-CODE ERR1	RD DT END-CODE ERR2	RD DT END-CODE ERR3	TX FLRC 0 PADDING ERR	RX FLRC 0 PADDING ERR	RD DT INV-PLRC ERR0	RD DT INV-PLRC ERR1
51	FDTM::PMACHK2CST2							
	HSN PBF IN PARITY ERR	(Not used)	(Not used)	(Not used)	PMA TIMER PARITY ERR	(Not used)	PMA ARB SEQ PARITY ERR	PMA SEQ PARITY ERR
52	FDTM::H0CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 DBF PARITY ERR
53	FDTM::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB0 PARITY ERR
54	FDTM::H0CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 PWB1 PARITY ERR
55	FDTM::H0CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 SNB0 PARITY ERR	P0 SNB1 PARITY ERR
56	FDTM::H0CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P0 TJ1 PARITY ERR	P0 TJ2 PARITY ERR
57	FDTM::H0CHK2CST1							
	P0 TX1 TIMER0 PARITY ERR	(Not used)	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	P0 TX1 CNT PARITY ERR	(Not used)	P0 TX1 SEQ PARITY ERR
58	FDTM::H0CHK2CST2							
	P0 TX2 TIMER0 PARITY ERR	P0 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P0 TX1 PTR PARITY ERR	(Not used)	(Not used)	P0 TX1 SEQ PARITY ERR
59	FDTM::H0CHK2CST2							
	(Not used)	(Not used)	P0 PRBPTR CMP ERR	P0 PRBPTR PARITY ERR	P0 PRBPTR 0 CMP ERR	P0 PRBPTR 1 CMP ERR	P0 PRBPTR 0 PARITY ERR	P0 PRBPTR 1 PARITY ERR
5A	FDTM::H0CHK2CST2							
	P0 RX TIMER0 PARITY ERR	P0 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P0 RX PTR PARITY ERR	P0 RX CNT PARITY ERR	(Not used)	P0 RX SEQ PARITY ERR
5B	FDTM::H1CHK2BST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 DBF PARITY ERR
5C	FDTM::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB0 PARITY ERR
5D	FDTM::H1CHK2BST1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 PWB1 PARITY ERR
5E	FDTM::H1CHK2BST2							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 SNB0 PARITY ERR	P1 SNB1 PARITY ERR
5F	FDTM::H1CHK2CST0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	P1 TJ1 PARITY ERR	P1 TJ2 PARITY ERR

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(13) Byte28 = 92 or 94 or 96 (3/4)

	0	1	2	3	4	5	6	7
60	FDTM::H1CHK2CST0							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
61	FDTM::H1CHK2CST1							
	P1 TX2 TIMER0 PARITY ERR	P1 TX2 TIMER1 PARITY ERR	(Not used)	(Not used)	P1 TX1 PTR PARITY ERR	(Not used)	(Not used)	P1 TX1 SEQ PARITY ERR
62	FDTM::H1CHK2CST2							
	(Not used)	(Not used)	P1 PRBPTR CMP ERR	P1 PRBPTR PARITY ERR	P1 PRBPTR 0 CMP ERR	P1 PRBPTR 1 CMP ERR	P1 PRBPTR 0 PARITY ERR	P1 PRBPTR 1 PARITY ERR
63	FDTM::H1CHK2CST2							
	P1 RX TIMER0 PARITY ERR	P1 RX TIMER1 PARITY ERR	(Not used)	(Not used)	P1 RX PTR PARITY ERR	P1 RX CNT PARITY ERR	(Not used)	P1 RX SEQ PARITY ERR
64	FDTM::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE0)							
65	FDTM::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE1)							
66	FDTM::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE2)							
67	FDTM::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE3)							
68	FDTM::HADRIUCNT							
	Slave 1 CACHE Tranfer Address (BYTE4)							
69	FDTM::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE0)							
6A	FDTM::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE1)							
6B	FDTM::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE2)							
6C	FDTM::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE3)							
6D	FDTM::HADRIUCNT							
	Slave 2 CACHE Tranfer Address (BYTE4)							
6E	FDTM::HCMD							
	CACHE Transfer Command (BYTE0)							
6F	FDTM::HCMD							
	CACHE Transfer Command (BYTE1)							

Byte27 = 89 (CHA CHK2) Byte40-7F Detail

(13) Byte28 = 92 or 94 or 96 (4/4)

0	1	2	3	4	5	6	7
70	FDTM::HCMD						
	CACHE Transfer Command (BYTE2)						
71	FDTM::HCMD2						
	CACHE Transfer Command 2 (BYTE0)						
72	FDTM::HCMD2						
	CACHE Transfer Command 2 (BYTE1)						
73	FDTM::HCMD2						
	CACHE Transfer Command 2 (BYTE2)						
74	Reserved						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Reserved						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
77	FDTA::CBEN						
	C PATH#00 ENABLE	C PATH#01 ENABLE	C PATH#02 ENABLE	C PATH#03 ENABLE	C PATH#04 ENABLE	C PATH#05 ENABLE	C PATH#06 ENABLE
78	FDTA::CBEN						
	C PATH#10 ENABLE	C PATH#11 ENABLE	C PATH#12 ENABLE	C PATH#13 ENABLE	C PATH#14 ENABLE	C PATH#15 ENABLE	C PATH#16 ENABLE
79	FDTA::PKID						
	(Not used)	(Not used)	(Not used)	(Not used)	PKID#		
7A	FDTA::MONI						
	MPID#			LSI REV.			
7B	FDTA::MONI						
	(Not used)	(Not used)	(Not used)	(Not used)	LSI REV.-2		
7C	Reserved						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	Reserved						
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information						
	Error Divided Result Code						
7F	Micro Information						
	CHK2 Detected Counter (Low)						

Byte27 = 8A (FCA CHK2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	PORT#		RCU#			
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device number				
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'8')				Message (x'A')			
28	Subcode							
29	Detail Information (See following pages)							
31								
32	Module ID							
33	Routine ID							
34	PCB number (*1)				Message code (x'0')			
35	SSID of self subsystem (low)							
36	Symptom code (x'FF8B')							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Detail Information (See following pages)							
7F								

Byte27 = 8A (FCA CHK2) Byte28-31 Detail

	0	1	2	3	4	5	6	7
28	Subcode (x'10')							
29	MPA::MPASTS							
	(Not used)	(Not used)	FCA 1 INT	DRR 1 INT	(Not used)	(Not used)	FCA 0 INT	DRR 0 INT
2A	MPA::MPASTS							
	(Not used)	(Not used)	FCA 1 CHK2 INT	DRR 1 CHK2 INT	(Not used)	(Not used)	FCA 0 CHK2 INT	DRR 0 CHK2 INT
2B	MPA::MPASTS							
	(Not used)	(Not used)	(Not used)	(Not used)	PCI CHK1B_Q INT	PCI CHK1B_M INT	FCA CHK1B INT	DRR CHK1B INT
2C	FCA::CHK1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CHK1B
2D	FCA::CHK2							
	(Not used)	(Not used)	(Not used)	(Not used)	PCIX-CHK2 LA CMP ERR2	CHK2E	CHK2F	CHK2G
2E	FCA:BPMLD2L							
	(Not used)	(Not used)	(Not used)	(Not used)	Transfer Mode '0': HDD->CACHE (Normal), '1': CACHE->HDD '4': HDD->CACHE (Double), Others: CACHE COPY			
2F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
30	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
31	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

*1: Subcode

- x'10' = CTL CHK2 detected on CACHE write command
- x'11' = CTL CHK2 detected on CACHE read command
- x'20' = Other CHK2 detected on CACHE write command
- x'21' = Other CHK2 detected on CACHE read command
- x'30' = PCI-X CHK2
- x'40' = DTN time out

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(1) Byte28 = 10 (1/4)

	0	1	2	3	4	5	6	7
40	FCA::DTNCHK2E0							
	SEQTXC DEC ERR1	SEQTXC DEC ERR0	SEQTXC ENC ERR1	SEQTXC ENC ERR0	SEQTXC PARITY ERR1	SEQTXC PARITY ERR0	SEQTXC CMP ERR	SQ50 DEC ERR1
41	FCA::DTNCHK2E0							
	SQ50 DEC ERR0	SQ50 ENC ERR1	SQ50 ENC ERR0	SQ50 PARITY ERR1	SQ50 PARITY ERR0	SQ50 CMP ERR	ADR/CMD CMPMDL ERR	CLDPL ERR ERR
42	FCA::DTNCHK2E0							
	DTN TIMEOUT	FLGERR01	FLGERR00	ADR/CMD ERR	CXOP HCNT PARITY ERR	CXOR XPT PARITY ERR	CXOR XPT PARITY ERR	CXOR YPT PARITY ERR
43	FCA::DTNCHK2E0							
	BPCNT PARITY ERR	BMPCNT PARITY ERR	BCNT PARITY ERR	BITMAP B0 PARITY ERR	BITMAP B1 PARITY ERR	BITMAP B2 PARITY ERR	BITMAP B3 PARITY ERR	(Not used)
44	FCA::DTNCHK2E1							
	CSAW0CNT B0 PARITY ERR	CSAW0CNT B1 PARITY ERR	CSAW0CNT B2 PARITY ERR	CSAW0CNT B3 PARITY ERR	CSAW0CNT B4 PARITY ERR	CSAW0CNT B5 PARITY ERR	CSAW0CNT B6 PARITY ERR	CSAW0CNT B7 PARITY ERR
45	FCA::DTNCHK2E1							
	CSARCNT B0 PARITY ERR	CSARCNT B1 PARITY ERR	CSARCNT B2 PARITY ERR	CSARCNT B3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
46	FCA::CWK2E0							
	SQ92 DEC ERR1	SQ92 DEC ERR0	SQ92 ENC ERR1	SQ92 ENC ERR0	SQ92 PARITY ERR1	SQ92 PARITY ERR0	SQ92 CMP ERR	SQ70 DEC ERR1
47	FCA::CWK2E0							
	SQ70 DEC ERR0	SQ70 ENC ERR1	SQ70 ENC ERR0	SQ70 PARITY ERR1	SQ70 PARITY ERR0	SQ70 CMP ERR	SQ72 DEC ERR1	SQ72 DEC ERR0
48	FCA::CWCHK2E0							
	SQ72 ENC ERR1	SQ72 ENC ERR0	SQ72 PARITY ERR1	SQ72 PARITY ERR0	SQ72 CMP ERR	SQ75 DEC ERR1	SQ75 DEC ERR0	SQ75 ENC ERR1
49	FCA::CWCHK2E0							
	SQ75 ENC ERR0	SQ75 PARITY ERR1	SQ75 PARITY ERR0	SQ75 CMP ERR	SQ77 DEC ERR1	SQ77 DEC ERR0	SQ77 ENC ERR1	SQ77 ENC ERR0
4A	FCA::CWCHK2E1							
	SQ77 PARITY ERR1	SQ77 PARITY ERR0	SQ77 CMP ERR	SQ79 DEC ERR1	SQ79 DEC ERR0	SQ79 ENC ERR1	SQ79 ENC ERR0	SQ79 PARITY ERR1
4B	FCA::CWCHK2E1							
	SQ79 PARITY ERR0	SQ79 CMP ERR	REQIN0 CMP ERR	REQIN1 CMP ERR	REQIN2 CMP ERR	REQIN3 CMP ERR	PBFFSB PL CMPMDL ERR	PBFFSB - SGL CMPMDL ERR
4C	FCA::CWCHK2E1							
	SGL PL CMPMDL ERR	ABF XPNT PARITY ERR	ABF YPNT PARITY ERR	BBF XPNT PARITY ERR	BBF YPNT PARITY ERR	FBF XPT PARITY ERR	FBF YPT PARITY ERR	GBF XPT PARITY ERR
4D	FCA::CWCHK2E1							
	GBF YPT PARITY ERR	FSB CNT PARITY ERR	FSB RACNT PARITY ERR	FSB WACNT PARITY ERR	FCNT PARITY ERR	FPCNT PARITY ERR	ACNT PARITY ERR	APCNT PARITY ERR
4E	FCA::CWCHK2E2							
	PBF RACNT1 PARITY ERR	PBF RACNT0 PARITY ERR	PBF WACNT1 PARITY ERR	PBF WACNT0 PARITY ERR	PBF CNT1 PARITY ERR	PBF CNT0 PARITY ERR	CWPSBC CNTB0 PARITY ERR	CWPSBC CNTB1 PARITY ERR
4F	FCA::CWCHK2E2							
	CWPSBC CNTB2 PARITY ERR	CWPSBC CNTB3 PARITY ERR	CWDSBC CNTB0 PARITY ERR	CWDSBC CNTB1 PARITY ERR	CWTSBC PARITY ERR	LACNT CHKB0 PARITY ERR	LACNT CHKB1 PARITY ERR	LACNT CHKB2 PARITY ERR

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(1) Byte28 = 10 (2/4)

	0	1	2	3	4	5	6	7
50	FCA::CWCHK2E2							
	LACNT CHKB3 PARITY ERR	LACNT CHKB4 PARITY ERR	LACNT GENB0 PARITY ERR	LACNT GENB1 PARITY ERR	LACNT GENB2 PARITY ERR	LACNT GENB3 PARITY ERR	LACNT GENB4 PARITY ERR	PBF XCNT PARITY ERR
51	FCA::CWCHK2E2							
	PBF XCNT TNEXT CMP ERR	ECC ADR1 ERR	ECC CIRCUIT ERR	ECC UNCRE ERR	CW SEGPNT ERR	(Not used)	(Not used)	(Not used)
52	FCA::CWCHK2E3							
	CWSEG63 CNT PARITY ERR	CWSEG62 CNT PARITY ERR	CWSEG61 CNT PARITY ERR	CWSEG60 CNT PARITY ERR	CWSEG59 CNT PARITY ERR	CWSEG58 CNT PARITY ERR	CWSEG57 CNT PARITY ERR	CWSEG56 CNT PARITY ERR
53	FCA::CWCHK2E3							
	CWSEG55 CNT PARITY ERR	CWSEG54 CNT PARITY ERR	CWSEG53 CNT PARITY ERR	CWSEG52 CNT PARITY ERR	CWSEG51 CNT PARITY ERR	CWSEG50 CNT PARITY ERR	CWSEG49 CNT PARITY ERR	CWSEG48 CNT PARITY ERR
54	FCA::CWCHK2E3							
	CWSEG47 CNT PARITY ERR	CWSEG46 CNT PARITY ERR	CWSEG45 CNT PARITY ERR	CWSEG44 CNT PARITY ERR	CWSEG43 CNT PARITY ERR	CWSEG42 CNT PARITY ERR	CWSEG41 CNT PARITY ERR	CWSEG40 CNT PARITY ERR
55	FCA::CWCHK2E3							
	CWSEG39 CNT PARITY ERR	CWSEG38 CNT PARITY ERR	CWSEG37 CNT PARITY ERR	CWSEG36 CNT PARITY ERR	CWSEG35 CNT PARITY ERR	CWSEG34 CNT PARITY ERR	CWSEG33 CNT PARITY ERR	CWSEG32 CNT PARITY ERR
56	FCA::CWCHK2E4							
	CWSEG31 CNT PARITY ERR	CWSEG30 CNT PARITY ERR	CWSEG29 CNT PARITY ERR	CWSEG28 CNT PARITY ERR	CWSEG27 CNT PARITY ERR	CWSEG26 CNT PARITY ERR	CWSEG25 CNT PARITY ERR	CWSEG24 CNT PARITY ERR
57	FCA::CWCHK2E4							
	CWSEG23 CNT PARITY ERR	CWSEG22 CNT PARITY ERR	CWSEG21 CNT PARITY ERR	CWSEG20 CNT PARITY ERR	CWSEG19 CNT PARITY ERR	CWSEG18 CNT PARITY ERR	CWSEG17 CNT PARITY ERR	CWSEG16 CNT PARITY ERR
58	FCA::CWCHK2E4							
	CWSEG15 CNT PARITY ERR	CWSEG14 CNT PARITY ERR	CWSEG13 CNT PARITY ERR	CWSEG12 CNT PARITY ERR	CWSEG11 CNT PARITY ERR	CWSEG10 CNT PARITY ERR	CWSEG09 CNT PARITY ERR	CWSEG08 CNT PARITY ERR
59	FCA::CWCHK2E4							
	CWSEG07 CNT PARITY ERR	CWSEG06 CNT PARITY ERR	CWSEG05 CNT PARITY ERR	CWSEG04 CNT PARITY ERR	CWSEG03 CNT PARITY ERR	CWSEG02 CNT PARITY ERR	CWSEG01 CNT PARITY ERR	CWSEG00 CNT PARITY ERR
5A	FCA::PCICLK2E							
	SQ20 PARITY ERR1	SQ20 PARITY ERR2	SQ20 CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5B	FCA::PMEMCHK2E0							
	SQ30 DEC ERR1	SQ30 DEC ERR0	SQ30 ENC ERR1	SQ30 ENC ERR0	SQ30 PARITY ERR1	SQ30 PARITY ERR0	SQ30 CMP ERR	SQ31 DEC ERR1
5C	FCA::PMEMCHK2E0							
	SQ31 DEC ERR0	SQ31 ENC ERR1	SQ31 ENC ERR0	SQ31 PARITY ERR1	SQ31 PARITY ERR0	SQ31 CMP ERR	SQ31A DEC ERR1	SQ31A DEC ERR0
5D	FCA::PMEMCHK2E0							
	SQ31A ENC ERR1	SQ31A ENC ERR0	SQ31A PARITY ERR1	SQ31A PARITY ERR0	SQ31A CMP ERR	MSTSEQ DEC ERR1	MSTSEQ DEC ERR0	MSTSEQ ENC ERR1
5E	FCA::PMEMCHK2E0							
	MSTSEQ ENC ERR0	MSTSEQ PARITY ERR1	MSTSEQ PARITY ERR0	MSTSEQ CMP ERR	MSTWT SEQ DEC ERR1	MSTWT SEQ DEC ERR0	MSTWT SEQ ENC ERR1	MSTWT SEQ ENC ERR0
5F	FCA::PMEMCHK2E1							
	MSTWT SEQ PARITY ERR1	MSTWT SEQ PARITY ERR0	MSTWT SEQ CMP ERR	MST ABT SEQ DEC ERR1	MST ABT SEQ DEC ERR0	MST ABT SEQ ENC ERR1	MST ABT SEQ ENC ERR0	MST ABT SEQ PARITY ERR1

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(1) Byte28 = 10 (3/4)

	0	1	2	3	4	5	6	7
60	FCA::PMEMCHK2E1							
	MST ABT SEQ PARITY ERR0	MST ABT SEQ CMP ERR	INTCTL SEQ DEC ERR1	INTCTL SEQ DEC ERR0	INTCTL SEQ ENC ERR1	INTCTL SEQ ENC ERR0	INTCTL SEQ PARITY ERR1	INTCTL SEQ PARITY ERR0
61	FCA::PMEMCHK2E2							
	MSTB XPT PARITY ERR	MSTB XPT PARITY ERR	JBF XPT PARITY ERR	JBF XPT PARITY ERR	LBF XPT PARITY ERR	LBF XPT PARITY ERR	INTCNT B0 PARITY ERR	INTCNT B1 PARITY ERR
62	FCA::PMEMCHK2E2							
	INTDIFCNT B0 PARITY ERR	INTDIFCNT B1 PARITY ERR	MSTBCNT PARITY ERR	WCMD ERR	PRB WACNT PARITY ERR	LBF RDCNT PARITY ERR	INTCNT OVER FLOW	INTCNT UNDER FLOW
63	FCA::PMEMCHK2E2							
	OVF CMP ERR	FCAID ERR	PARA- METER LRC ERR	ECC ADR ERR	ECC CIRCUIT ERR	ECC UN- CORRECT- ABLE ERR	ADR CMP ERR	PQCODE ERR
64	FCA::PMEMCHK2E2							
	XFR MODE ERR	PRMFLG DPL ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
65	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
66	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
68	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(1) Byte28 = 10 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(2) Byte28 = 11 (1/4)

	0	1	2	3	4	5	6	7
40	FCA::DTNCHK2E0							
	SEQTXC DEC ERR1	SEQTXC DEC ERR0	SEQTXC ENC ERR1	SEQTXC ENC ERR0	SEQTXC PARITY ERR1	SEQTXC PARITY ERR0	SEQTXC CMP ERR	SQ50 DEC ERR1
41	FCA::DTNCHK2E0							
	SQ50 DEC ERR0	SQ50 ENC ERR1	SQ50 ENC ERR0	SQ50 PARITY ERR1	SQ50 PARITY ERR0	SQ50 CMP ERR	ADR/CMD CMPMDL ERR	CLDPL ERR ERR
42	FCA::DTNCHK2E0							
	DTN TIMEOUT	FLGERR01	FLGERR00	ADR/CMD ERR	CXOP HCNT PARITY ERR	CXOR XPT PARITY ERR	CXOR XPT PARITY ERR	CXOR YPT PARITY ERR
43	FCA::DTNCHK2E0							
	BPCNT PARITY ERR	BMPCNT PARITY ERR	BCNT PARITY ERR	BITMAP B0 PARITY ERR	BITMAP B1 PARITY ERR	BITMAP B2 PARITY ERR	BITMAP B3 PARITY ERR	(Not used)
44	FCA::DTNCHK2E1							
	CSAW0CNT B0 PARITY ERR	CSAW0CNT B1 PARITY ERR	CSAW0CNT B2 PARITY ERR	CSAW0CNT B3 PARITY ERR	CSAW0CNT B4 PARITY ERR	CSAW0CNT B5 PARITY ERR	CSAW0CNT B6 PARITY ERR	CSAW0CNT B7 PARITY ERR
45	FCA::DTNCHK2E1							
	CSARCNT B0 PARITY ERR	CSARCNT B1 PARITY ERR	CSARCNT B2 PARITY ERR	CSARCNT B3 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)
46	FCA::CRCHK2E0							
	SQ90 DEC ERR1	SQ90 DEC ERR0	SQ90 ENC ERR1	SQ90 ENC ERR0	SQ90 PARITY ERR01	SQ90 PARITY ERR00	SQ90 CMP ERR	SQ40 DEC ERR1
47	FCA::CRCHK2E0							
	SQ40 DEC ERR0	SQ40 ENC ERR1	SQ40 ENC ERR0	SQ40 PARITY ERR01	SQ40 PARITY ERR00	SQ40 CMP ERR	SQ45 DEC ERR1	SQ45 DEC ERR0
48	FCA::CRCHK2E0							
	SQ45 ENC ERR1	SQ45 ENC ERR0	SQ45 PARITY ERR1	SQ45 PARITY ERR0	SQ45 CMP ERR	SQ49 DEC ERR1	SQ49 DEC ERR0	SQ49 ENC ERR1
49	FCA::CRCHK2E0							
	SQ49 ENC ERR0	SQ49 PARITY ERR1	SQ49 PARITY ERR0	SQ49 CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)
4A	FCA::CRCHK2E1							
	PKT CMP ERR	SGL PL CMPMDL ERR	AMARI COMP ERR	QUO COMP ERR	CBF XPT PARITY ERR	CBF YPT PARITY ERR	HBF XPT PARITY ERR	HBF YPT PARITY ERR
4B	FCA::CRCHK2E1							
	NCNT PARITY ERR	NPNT PARITY ERR	CCNT PARITY ERR	YCNT PARITY ERR	CRPSBC CNTB0 PARITY ERR	CRPSBC CNTB1 PARITY ERR	FDB DCNTB0 PARITY ERR	FDB DCNTB1 PARITY ERR
4C	FCA::CRCHK2E1							
	FDB RACNTB0 PARITY ERR	FDB RACNTB1 PARITY ERR	FDB WACNTB0 PARITY ERR	FDB WACNTB1 PARITY ERR	CRSSBC CNT PARITY ERR	ECC CIRCUIT ERR	ECC UNCRE ERR	ECC ADR1 ERR
4D	FCA::CRCHK2E1							
	CR SEGPNT ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4E	FCA::CRCHK2E2							
	CRSEG 00 CNT PARITY ERR	CRSEG 01 CNT PARITY ERR	CRSEG 02 CNT PARITY ERR	CRSEG 03 CNT PARITY ERR	CRSEG 04 CNT PARITY ERR	CRSEG 05 CNT PARITY ERR	CRSEG 06 CNT PARITY ERR	CRSEG 07 CNT PARITY ERR
4F	FCA::CRCHK2E2							
	CRSEG 08 CNT PARITY ERR	CRSEG 09 CNT PARITY ERR	CRSEG 10 CNT PARITY ERR	CRSEG 11 CNT PARITY ERR	CRSEG 12 CNT PARITY ERR	CRSEG 13 CNT PARITY ERR	CRSEG 14 CNT PARITY ERR	CRSEG 15 CNT PARITY ERR

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(2) Byte28 = 11 (2/4)

	0	1	2	3	4	5	6	7
50	FCA::CRCHK2E2							
	CRSEG 16 CNT PARITY ERR	CRSEG 17 CNT PARITY ERR	CRSEG 18 CNT PARITY ERR	CRSEG 19 CNT PARITY ERR	CRSEG 20 CNT PARITY ERR	CRSEG 21 CNT PARITY ERR	CRSEG 22 CNT PARITY ERR	CRSEG 23 CNT PARITY ERR
51	FCA::CRCHK2E2							
	CRSEG 24 CNT PARITY ERR	CRSEG 25 CNT PARITY ERR	CRSEG 26 CNT PARITY ERR	CRSEG 27 CNT PARITY ERR	CRSEG 28 CNT PARITY ERR	CRSEG 29 CNT PARITY ERR	CRSEG 30 CNT PARITY ERR	CRSEG 31 CNT PARITY ERR
52	FCA::CRCHK2E3							
	CRSEG 32 CNT PARITY ERR	CRSEG 33 CNT PARITY ERR	CRSEG 34 CNT PARITY ERR	CRSEG 35 CNT PARITY ERR	CRSEG 36 CNT PARITY ERR	CRSEG 37 CNT PARITY ERR	CRSEG 38 CNT PARITY ERR	CRSEG 39 CNT PARITY ERR
53	FCA::CRCHK2E3							
	CRSEG 40 CNT PARITY ERR	CRSEG 41 CNT PARITY ERR	CRSEG 42 CNT PARITY ERR	CRSEG 43 CNT PARITY ERR	CRSEG 44 CNT PARITY ERR	CRSEG 45 CNT PARITY ERR	CRSEG 46 CNT PARITY ERR	CRSEG 47 CNT PARITY ERR
54	FCA::CRCHK2E3							
	CRSEG 48 CNT PARITY ERR	CRSEG 49 CNT PARITY ERR	CRSEG 50 CNT PARITY ERR	CRSEG 51 CNT PARITY ERR	CRSEG 52 CNT PARITY ERR	CRSEG 53 CNT PARITY ERR	CRSEG 54 CNT PARITY ERR	CRSEG 55 CNT PARITY ERR
55	FCA::CRCHK2E3							
	CRSEG 56 CNT PARITY ERR	CRSEG 57 CNT PARITY ERR	CRSEG 58 CNT PARITY ERR	CRSEG 59 CNT PARITY ERR	CRSEG 60 CNT PARITY ERR	CRSEG 61 CNT PARITY ERR	CRSEG 62 CNT PARITY ERR	CRSEG 63 CNT PARITY ERR
56	FCA::CRCHK2E4							
	CRDSBC CNTB0 PARITY ERR	CRDSBC CNTB1 PARITY ERR	ECNT B0 PARITY ERR	ECNT B1 PARITY ERR	LACNT B0 PARITY ERR	LACNT B1 PARITY ERR	LACNT B2 PARITY ERR	LACNT B3 PARITY ERR
57	FCA::CRCHK2E4							
	LACNT B4 PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
58	FCA::PCCHK2E							
	SQ20 PARITY ERR1	SQ20 PARITY ERR0	SQ20 CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
59	FCA::PMEMCHK2E0							
	SQ30 DEC ERR1	SQ30 DEC ERR0	SQ30 ENC ERR1	SQ30 ENC ERR0	SQ30 PARITY ERR1	SQ30 PARITY ERR0	SQ30 CMP ERR	SQ31 DEC ERR1
5A	FCA::PMEMCHK2E0							
	SQ31 DEC ERR0	SQ31 ENC ERR1	SQ31 ENC ERR0	SQ31 PARITY ERR1	SQ31 PARITY ERR0	SQ31 CMP ERR	SQ31A DEC ERR1	SQ31A DEC ERR0
5B	FCA::PMEMCHK2E0							
	SQ31A ENC ERR1	SQ31A ENC ERR0	SQ31A PARITY ERR1	SQ31A PARITY ERR0	SQ31A CMP ERR	MSTSEQ DEC ERR1	MSTSEQ DEC ERR0	MSTSEQ ENC ERR1
5C	FCA::PMEMCHK2E0							
	MSTSEQ ENC ERR0	MSTSEQ PARITY ERR1	MSTSEQ PARITY ERR0	MSTSEQ CMP ERR	MSTWT SEQ DEC ERR1	MSTWT SEQ DEC ERR0	MSTWT SEQ ENC ERR1	MSTWT SEQ ENC ERR0
5D	FCA::PMEMCHK2E1							
	MSTWT SEQ PARITY ERR1	MSTWT SEQ PARITY ERR0	MSTWT SEQ CMP ERR	MST ABT SEQ DEC ERR1	MST ABT SEQ DEC ERR0	MST ABT SEQ ENC ERR1	MST ABT SEQ ENC ERR0	MST ABT SEQ PARITY ERR1
5E	FCA::PMEMCHK2E1							
	MST ABT SEQ PARITY ERR0	MST ABT SEQ CMP ERR	INTCTL SEQ DEC ERR1	INTCTL SEQ DEC ERR0	INTCTL SEQ ENC ERR1	INTCTL SEQ ENC ERR0	INTCTL SEQ PARITY ERR1	INTCTL SEQ PARITY ERR0
5F	FCA::PMEMCHK2E2							
	MSTB XPT PARITY ERR	MSTB YPT PARITY ERR	JBF XPT PARITY ERR	JBF YPT PARITY ERR	LBF XPT PARITY ERR	LBF YPT PARITY ERR	INTCNT B0 PARITY ERR	INTCNT B1 PARITY ERR

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(2) Byte28 = 11 (3/4)

	0	1	2	3	4	5	6	7
60	FCA::PMEMCHK2E2							
	INTDIFCNT B0 PARITY ERR	INTDIFCNT B1 PARITY ERR	MSTBCNT PARITY ERR	WCMD ERR	PRB WACNT PARITY ERR	LBF RDCNT PARITY ERR	INTCNT OVER FLOW	INTCNT UNDER FLOW
61	FCA::PMEMCHK2E2							
	OVF CMP ERR	FCAID ERR	PARAME- TER LRC ERR	ECC ADR ERR	ECC CIRCUIT ERR	ECC UN- CORRECT- ABLE ERR	ADR CMP ERR	PCODE ERR
62	FCA::PMEMCHK2E2							
	XFR MODE ERR	PRMFLG DPL ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
63	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
64	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
65	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
66	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
68	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(2) Byte28 = 11 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(3) Byte28 = 20 (1/4)

	0	1	2	3	4	5	6	7
40	FCA::DTNCHK2F0							
	CXIP DT B0 PARITY ERR	CXIP DT B1 PARITY ERR	CXIP DT B2 PARITY ERR	CXIP DT B3 PARITY ERR	CXIP DT B4 PARITY ERR	CXIP DT B5 PARITY ERR	CXIP DT B6 PARITY ERR	CXIP DT B7 PARITY ERR
41	FCA::DTNCHK2F0							
	CXOR DT B0 PARITY ERR	CXOR DT B1 PARITY ERR	CXOR DT B2 PARITY ERR	CXOR DT B3 PARITY ERR	CXOR DT B4 PARITY ERR	CXOR DT B5 PARITY ERR	CXOR DT B6 PARITY ERR	CXOR DT B7 PARITY ERR
42	FCA::DTNCHK2F0							
	BBF OUTDT B0 PARITY ERR	BBF OUTDT B1 PARITY ERR	BBF OUTDT B2 PARITY ERR	BBF OUTDT B3 PARITY ERR	BBF OUTDT B4 PARITY ERR	BBF OUTDT B5 PARITY ERR	BBF OUTDT B6 PARITY ERR	BBF OUTDT B7 PARITY ERR
43	FCA::DTNCHK2F1							
	STS0 B0 PARITY ERR	STS0 B1 PARITY ERR	STS0 B2P PARITY ERR	STS0 B3 PARITY ERR	STS0 B4 PARITY ERR	STS0 B5 PARITY ERR	STS0 B6 PARITY ERR	STS0 B7 PARITY ERR
44	FCA::DTNCHK2F1							
	STS0P B0 PARITY ERR	STS0P B1 PARITY ERR	STS0P B2 PARITY ERR	STS0P B3 PARITY ERR	STS0P B4 PARITY ERR	STS0P B5 PARITY ERR	STS0P B6 PARITY ERR	STS0P B7 PARITY ERR
45	FCA::DTNCHK2F1							
	STS1 B0 PARITY ERR	STS1 B1 PARITY ERR	STS1 B2 PARITY ERR	STS1 B3 PARITY ERR	STS1 B4 PARITY ERR	STS1 B5 PARITY ERR	STS1 B6 PARITY ERR	STS1 B7 PARITY ERR
46	FCA::DTNCHK2F1							
	STS1P B0 PARITY ERR	STS1P B1 PARITY ERR	STS1P B2 PARITY ERR	STS1P B3 PARITY ERR	STS1P B4 PARITY ERR	STS1P B5 PARITY ERR	STS1P B6 PARITY ERR	STS1P B7 PARITY ERR
47	FCA::DTNCHK2F2							
	QDSTS0 B0 PARITY ERR	QDSTS0 B1 PARITY ERR	QDSTS0 B2 PARITY ERR	QDSTS0 B3 PARITY ERR	QDSTS0 B4 PARITY ERR	QDSTS0 B5 PARITY ERR	QDSTS0 B6 PARITY ERR	QDSTS0 B7 PARITY ERR
48	FCA::DTNCHK2F2							
	QDSTS1 B0 PARITY ERR	QDSTS1 B1 PARITY ERR	QDSTS1 B2 PARITY ERR	QDSTS1 B3 PARITY ERR	QDSTS1 B4 PARITY ERR	QDSTS1 B5 PARITY ERR	QDSTS1 B6 PARITY ERR	QDSTS1 B7 PARITY ERR
49	FCA::DTNCHK2F2							
	STS0 ERR	STS1 ERR	QDTASTS ERR	STS0 CRC ERR	STS1 CRC ERR	RTNCRC CMP ERR0	RTNCRC CMP ERR1	ST0ECODE ERR
4A	FCA::DTNCHK2F2							
	ST1ECODE ERR	PATHID ERR0	PATHID ERR1	SLVID ERR0	SLVID ERR1	(Not used)	(Not used)	(Not used)
4B	FCA::CWCHK2F0							
	BBF DT B0 PARITY ERR	BBF DT B1 PARITY ERR	BBF DT B2 PARITY ERR	BBF DT B3 PARITY ERR	BBF DT B4 PARITY ERR	BBF DT B5 PARITY ERR	BBF DT B6 PARITY ERR	BBF DT B7 PARITY ERR
4C	FCA::CWCHK2F0							
	FBF DT B0 PARITY ERR	FBF DT B1 PARITY ERR	FBF DT B2 PARITY ERR	FBF DT B3 PARITY ERR	FBF DT B4 PARITY ERR	FBF DT B5 PARITY ERR	FBF DT B6 PARITY ERR	FBF DT B7 PARITY ERR
4D	FCA::CWCHK2F0							
	GBF DT B0 PARITY ERR	GBF DT B1 PARITY ERR	GBF DT B2 PARITY ERR	GBF DT B3 PARITY ERR	GBF DT B4 PARITY ERR	GBF DT B5 PARITY ERR	GBF DT B6 PARITY ERR	GBF DT B7 PARITY ERR
4E	FCA::CWCHK2F0							
	SLRC ERR	WTSEQ ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	FCA::CWCHK2G0							
	CW LA ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(3) Byte28 = 20 (2/4)

	0	1	2	3	4	5	6	7
50	FCA::PMEMCHK2F							
	MSTB DT B0 PARITY ERR	MSTB DT B1 PARITY ERR	MSTB DT B2 PARITY ERR	MSTB DT B3 PARITY ERR	MSTB DT B4 PARITY ERR	MSTB DT B5 PARITY ERR	MSTB DT B6 PARITY ERR	MSTB DT B7 PARITY ERR
51	FCA::PMEMCHK2F							
	LBF DT B0 PARITY ERR	LBF DT B1 PARITY ERR	LBF DT B2 PARITY ERR	LBF DT B3 PARITY ERR	LBF DT B4 PARITY ERR	LBF DT B5 PARITY ERR	LBF DT B6 PARITY ERR	LBF DT B7 PARITY ERR
52	FCA::PMEMCHK2F							
	MSTWT DT B0 PARITY ERR	MSTWT DT B1 PARITY ERR	MSTWT DT B2 PARITY ERR	MSTWT DT B3 PARITY ERR	MSTWT DT B4 PARITY ERR	MSTWT DT B5 PARITY ERR	MSTWT DT B6 PARITY ERR	MSTWT DT B7 PARITY ERR
53	FCA::CWLAFIX							
	The expected value of LA (Byte0)							
54	FCA::CWLAFIX							
	The expected value of LA (Byte1)							
55	FCA::CWFLA14							
	The expected value of LA (Byte2)							
56	FCA::CWFLA14							
	The expected value of LA (Byte3)							
57	FCA::CWFLA14							
	The expected value of LA (Byte4)							
58	FCA::CWFLA14							
	The expected value of LA (Byte5)							
59	FCA::CWEXLAH							
	The read value of LA (Byte0)							
5A	FCA::CWEXLAH							
	The read value of LA (Byte1)							
5B	FCA::CWEXLAL							
	The read value of LA (Byte2)							
5C	FCA::CWEXLAL							
	The read value of LA (Byte3)							
5D	FCA::CWEXLAL							
	The read value of LA (Byte4)							
5E	FCA::CWEXLAL							
	The read value of LA (Byte5)							
5F	FCA::CWLAFIX							
	The first value of LA (Byte0)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(3) Byte28 = 20 (3/4)

	0	1	2	3	4	5	6	7
60	FCA::CWLAFIX							
	The first value of LA (Byte1)							
61	FCA::CWELA14							
	The first value of LA (Byte2)							
62	FCA::CWELA14							
	The first value of LA (Byte3)							
63	FCA::CWELA14							
	The first value of LA (Byte4)							
64	FCA::CWELA14							
	The first value of LA (Byte5)							
65	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
66	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
68	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(3) Byte28 = 20 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(4) Byte28 = 21 (1/4)

	0	1	2	3	4	5	6	7
40	FCA::DTNCHK2F0							
	CXIP DT B0 PARITY ERR	CXIP DT B1 PARITY ERR	CXIP DT B2 PARITY ERR	CXIP DT B3 PARITY ERR	CXIP DT B4 PARITY ERR	CXIP DT B5 PARITY ERR	CXIP DT B6 PARITY ERR	CXIP DT B7 PARITY ERR
41	FCA::DTNCHK2F0							
	CXOR DT B0 PARITY ERR	CXOR DT B1 PARITY ERR	CXOR DT B2 PARITY ERR	CXOR DT B3 PARITY ERR	CXOR DT B4 PARITY ERR	CXOR DT B5 PARITY ERR	CXOR DT B6 PARITY ERR	CXOR DT B7 PARITY ERR
42	FCA::DTNCHK2F0							
	BBF OUTDT B0 PARITY ERR	BBF OUTDT B1 PARITY ERR	BBF OUTDT B2 PARITY ERR	BBF OUTDT B3 PARITY ERR	BBF OUTDT B4 PARITY ERR	BBF OUTDT B5 PARITY ERR	BBF OUTDT B6 PARITY ERR	BBF OUTDT B7 PARITY ERR
43	FCA::DTNCHK2F1							
	STS0 B0 PARITY ERR	STS0 B1 PARITY ERR	STS0 B2 PARITY ERR	STS0 B3 PARITY ERR	STS0 B4 PARITY ERR	STS0 B5 PARITY ERR	STS0 B6 PARITY ERR	STS0 B7 PARITY ERR
44	FCA::DTNCHK2F1							
	STS0P B0 PARITY ERR	STS0P B1 PARITY ERR	STS0P B2 PARITY ERR	STS0P B3 PARITY ERR	STS0P B4 PARITY ERR	STS0P B5 PARITY ERR	STS0P B6 PARITY ERR	STS0P B7 PARITY ERR
45	FCA::DTNCHK2F1							
	STS1 B0 PARITY ERR	STS1 B1 PARITY ERR	STS1 B2 PARITY ERR	STS1 B3 PARITY ERR	STS1 B4 PARITY ERR	STS1 B5 PARITY ERR	STS1 B6 PARITY ERR	STS1 B7 PARITY ERR
46	FCA::DTNCHK2F1							
	STS1P B0 PARITY ERR	STS1P B1 PARITY ERR	STS1P B2 PARITY ERR	STS1P B3 PARITY ERR	STS1P B4 PARITY ERR	STS1P B5 PARITY ERR	STS1P B6 PARITY ERR	STS1P B7 PARITY ERR
47	FCA::DTNCHK2F2							
	QDSTS0 B0 PARITY ERR	QDSTS0 B1 PARITY ERR	QDSTS0 B2 PARITY ERR	QDSTS0 B3 PARITY ERR	QDSTS0 B4 PARITY ERR	QDSTS0 B5 PARITY ERR	QDSTS0 B6 PARITY ERR	QDSTS0 B7 PARITY ERR
48	FCA::DTNCHK2F2							
	QDSTS1 B0 PARITY ERR	QDSTS1 B1 PARITY ERR	QDSTS1 B2 PARITY ERR	QDSTS1 B3 PARITY ERR	QDSTS1 B4 PARITY ERR	QDSTS1 B5 PARITY ERR	QDSTS1 B6 PARITY ERR	QDSTS1 B7 PARITY ERR
49	FCA::DTNCHK2F2							
	STS0 ERR	STS1 ERR	QDTASTS ERR	STS0 CRC ERR	STS1 CRC ERR	RTNCRC CMP ERR0	RTNCRC CMP ERR1	ST0ECODE ERR
4A	FCA::DTNCHK2F2							
	ST1ECODE ERR	PATHID ERR0	PATHID ERR1	SLVID ERR0	SLVID ERR1	(Not used)	(Not used)	(Not used)
4B	FCA::CRCHK2F0							
	PAD B0 PARITY ERR	PAD B1 PARITY ERR	PAD B2 PARITY ERR	PAD B3 PARITY ERR	PAD B4 PARITY ERR	PAD B5 PARITY ERR	PAD B6 PARITY ERR	PAD B7 PARITY ERR
4C	FCA::CRCHK2F0							
	HBF DT B0 PARITY ERR	HBF DT B1 PARITY ERR	HBF DT B2 PARITY ERR	HBF DT B3 PARITY ERR	HBF DT B4 PARITY ERR	HBF DT B5 PARITY ERR	HBF DT B6 PARITY ERR	HBF DT B7 PARITY ERR
4D	FCA::CRCHK2F0							
	OUTDT B0 PARITY ERR	OUTDT B1 PARITY ERR	OUTDT B2 PARITY ERR	OUTDT B3 PARITY ERR	OUTDT B4 PARITY ERR	OUTDT B5 PARITY ERR	OUTDT B6 PARITY ERR	OUTDT B7 PARITY ERR
4E	FCA::CRCHK2F0							
	ENDCODE ERR	RD CRC ERR	CR SLRC ERR	LLRC ERR	CRWTSEQ ERR	(Not used)	(Not used)	(Not used)
4F	FCA::CRCHK2G0							
	CR LA ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(4) Byte28 = 21 (2/4)

	0	1	2	3	4	5	6	7
50	FCA::PMEMCHK2F							
	MSTB DT B0 PARITY ERR	MSTB DT B1 PARITY ERR	MSTB DT B2 PARITY ERR	MSTB DT B3 PARITY ERR	MSTB DT B4 PARITY ERR	MSTB DT B5 PARITY ERR	MSTB DT B6 PARITY ERR	MSTB DT B7 PARITY ERR
51	FCA::PMEMCHK2F							
	LBF DT B0 PARITY ERR	LBF DT B1 PARITY ERR	LBF DT B2 PARITY ERR	LBF DT B3 PARITY ERR	LBF DT B4 PARITY ERR	LBF DT B5 PARITY ERR	LBF DT B6 PARITY ERR	LBF DT B7 PARITY ERR
52	FCA::PMEMCHK2F							
	MSTWT DT B0 PARITY ERR	MSTWT DT B1 PARITY ERR	MSTWT DT B2 PARITY ERR	MSTWT DT B3 PARITY ERR	MSTWT DT B4 PARITY ERR	MSTWT DT B5 PARITY ERR	MSTWT DT B6 PARITY ERR	MSTWT DT B7 PARITY ERR
53	FCA::CRLAFIX							
	The expected value of LA (Byte0)							
54	FCA::CRLAFIX							
	The expected value of LA (Byte1)							
55	FCA::CRFLA14							
	The expected value of LA (Byte2)							
56	FCA::CRFLA14							
	The expected value of LA (Byte3)							
57	FCA::CRFLA14							
	The expected value of LA (Byte4)							
58	FCA::CRFLA14							
	The expected value of LA (Byte5)							
59	FCA::CREXLAH							
	The read value of LA (Byte0)							
5A	FCA::CREXLAH							
	The read value of LA (Byte1)							
5B	FCA::CREXLAL							
	The read value of LA (Byte2)							
5C	FCA::CREXLAL							
	The read value of LA (Byte3)							
5D	FCA::CREXLAL							
	The read value of LA (Byte4)							
5E	FCA::CREXLAL							
	The read value of LA (Byte5)							
5F	FCA::CRLAFIX							
	The first value of LA (Byte0)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(4) Byte28 = 21 (3/4)

	0	1	2	3	4	5	6	7
60	FCA::CWLAFIX							
	The first value of LA (Byte1)							
61	FCA::CWELA14							
	The first value of LA (Byte2)							
62	FCA::CWELA14							
	The first value of LA (Byte3)							
63	FCA::CWELA14							
	The first value of LA (Byte4)							
64	FCA::CWELA14							
	The first value of LA (Byte5)							
65	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
66	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
68	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6A	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(4) Byte28 = 21 (4/4)

	0	1	2	3	4	5	6	7
70	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
71	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
72	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(5) Byte28 = 30 (1/4)

	0	1	2	3	4	5	6	7
40	FCA::PCI10 ERROR0							
	TXAD PARITY ERR0	TXAD PARITY ERR1	TXAD PARITY ERR2	TXAD PARITY ERR3	TXAD PARITY ERR4	TXAD PARITY ERR5	TXAD PARITY ERR6	TXAD PARITY ERR7
41	FCA::PCI10 ERROR0							
	(Not used)	PARITY ERR	SERR	(Not used)	(Not used)	PARITY ERR	PCI64 PARITY ERR	PCI PARITY ERR
42	FCA::PCI12 ERROR0							
	RXAD RPEH0	RXAD RPEH1	RXAD RPEH2	RXAD RPEH3	RXAD RPEL0	RXAD RPEL1	RXAD RPEL2	RXAD RPEL3
43	FCA::PCI12 ERROR0							
	(Not used)	ATR1 PARITY ERR0	ATR1 PARITY ERR1	ATR1 PARITY ERR2	ATR1 PARITY ERR3	BLKACS PARITY ERR	ATR0 PARITY ERR	RXCMD PARITY ERR
44	FCA::PCI12 ERROR1							
	OUTADRC PARITY ERR0	OUTADRC PARITY ERR1	OUTCNT PARITY ERR0	OUTCNT PARITY ERR1	INADRCNT PARITY ERR0	INADRCNT PARITY ERR1	IWC PARITY ERR0	IWC PARITY ERR1
45	FCA::PCI12 ERROR1							
	(Not used)	EQLSTC PARITY ERR	OV1 PARITY ERR	OV4 PARITY ERR	DETIWC PARITY ERR	(Not used)	OUT ADROVRN	IN ADROVRN
46	FCA::PCI10 ERROR1							
	(Not used)	TIMCNT PARITY ERR0	TIMCNT PARITY ERR1	TIMCNT PARITY ERR2	TIMOVR1	TIMOVR0	PTSQ CMPER	SELBCPE
47	FCA::PCI10 ERROR1							
	TGTABT1	TGTABT0	(Not used)	XLASTD PARITY ERR	DCNT PARITY ERR0	DCNT PARITY ERR1	SEQ PARITY ERR1	SEQ PARITY ERR0
48	FCA::PCI12 ERROR2							
	OUTADRC PARITY ERR0	OUTADRC PARITY ERR1	OUTCNT PARITY ERR0	OUTCNT PARITY ERR1	INADRCNT PARITY ERR0	INADRCNT PARITY ERR1	IWC PARITY ERR0	IWC PARITY ERR1
49	FCA::PCI12 ERROR2							
	SQ CMP ERR	(Not used)	OV CMP ERR	OVN CMP ERR	DETIWCUP PARITY ERR	(Not used)	OUT ADROVRN	IN ADROVRN
4A	FCA::PCI10 ERROR2							
	OUTPOINT PARITY ERR	INPOINT PARITY ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4B	FCA::PCI10 ERROR2							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
4C	FCA::PCI12 ERROR3							
	SET ERR1	SET ERR0	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
4D	FCA::PCI12 ERROR3							
	SEQ CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4E	FCA::PCI12 ERROR3							
	(Not used)	TGTTX HUNCR	TGTTX HADER	TGTTX HECCHDE	(Not used)	TGTTX LUNCR	TGTTX LADER	TGTTX LECCHDE
4F	FCA::PCI12 ERROR3							
	TXDTH PARITY ERR0	TXDTH PARITY ERR1	TXDTH PARITY ERR2	TXDTH PARITY ERR3	TXDTL PARITY ERR0	TXDTL PARITY ERR1	TXDTL PARITY ERR2	TXDTL PARITY ERR3

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(5) Byte28 = 30 (2/4)

	0	1	2	3	4	5	6	7
50	FCA::PCI10 ERROR4							
	(Not used)	(Not used)	(Not used)	(Not used)	RTRYCNT PARITY ERR	DWCNT PARITY ERR	DCNT PARITY ERR0	DCNT PARITY ERR1
51	FCA::PCI10 ERROR4							
	LASTP8EV	LAST2P8EV	LAST3P8EV	LAST4P8EV	NLASTDT	LASTDT	LASTIWDE	RTRYCNT OVPE
52	FCA::PCI12 ERROR4							
	LASTP4	LAST2P4	LAST3P4	LAST4P4	(Not used)	(Not used)	(Not used)	(Not used)
53	FCA::PCI12 ERROR4							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTPOINT PARITY ERR	INPOINT PARITY ERR	TXBE PARITY ERR	TXBEL PARITY ERR
54	FCA::PCI10 ERROR5							
	(Not used)	(Not used)	(Not used)	CMD ERR1	CMD ERR0	SEQ PARITY ERR1	SEQ PARITY ERR0	SEQ CMP ERR
55	FCA::PCI10 ERROR5							
	SDISCT1	SDISCT0	TGTABT1	TGTABT0	MSTABT1	MSTABT0	RTRY OVER1	RTRY OVER0
56	FCA::PCI12 ERROR5							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
57	FCA::PCI12 ERROR5							
	ARBEN ERR1	ARB ERR1	DEC ERR1	ENC ERR1	ARBEN ERR0	ARB ERR0	DEC ERR0	ENC ERR0
58	FCA::PCI12 ERROR6							
	SEQ CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
59	FCA::PCI12 ERROR6							
	SEQ CMP ERR	CMD ERR0	OR ERR0	UR ERR0	PCI ERR0	SEQ PARITY ERR0	DEC ERR0	ENC ERR0
5A	FCA::PCI12 ERROR6							
	(Not used)	CMD ERR1	OR ERR1	UR ERR1	PCI ERR1	SEQ PARITY ERR1	DEC ERR1	ENC ERR1
5B	FCA::PCI12 ERROR6							
	(Not used)	(Not used)	XFPCI ERR	XEPCI ERR	XDPCI ERR	XCPCI ERR	XBPCI ERR	XAPCI ERR
5C	FCA::PCI12 ERROR7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTPOINT PARITY ERR	INPOINT PARITY ERR	TXBEH PARITY ERR	TXBEL PARITY ERR
5D	FCA::PCI12 ERROR7							
	TXDTH PARITY ERR0	TXDTH PARITY ERR1	TXDTH PARITY ERR2	TXDTH PARITY ERR3	TXDTL PARITY ERR0	TXDTL PARITY ERR1	TXDTL PARITY ERR2	TXDTL PARITY ERR3
5E	FCA::PCI12 ERROR7							
	(Not used)	(Not used)	(Not used)	CLAST PARITY ERR	NLAST PARITY ERR	SELBC PARITY ERR	XBCNT PARITY ERR0	XBCNT PARITY ERR1
5F	FCA::PCI12 ERROR7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTPOINT PARITY ERR	INPOINT PARITY ERR	TXBEH PARITY ERR	TXBEL PARITY ERR

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(5) Byte28 = 30 (3/4)

	0	1	2	3	4	5	6	7
60	FCA::PCI12 ERROR8							
	TXDTH PARITY ERR0	TXDTH PARITY ERR1	TXDTH PARITY ERR2	TXDTH PARITY ERR3	TXDTL PARITY ERR0	TXDTL PARITY ERR1	TXDTL PARITY ERR2	TXDTL PARITY ERR3
61	FCA::PCI12 ERROR8							
	SEQ CMP ERR	(Not used)	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
62	FCA::PCI12 ERROR8							
	ADRCNT PARITY ERR0	ADRCNT PARITY ERR1	ADRCNT PARITY ERR2	ADRCNT PARITY ERR3	ADRCNT PARITY ERR4	ADRCNT PARITY ERR5	ADRCNT PARITY ERR6	ADRCNT PARITY ERR7
63	FCA::PCI12 ERROR8							
	(Not used)	(Not used)	(Not used)	(Not used)	BURST PARITY ERR0	BURST PARITY ERR1	BCNT PARITY ERR0	BCNT PARITY ERR1
64	FCA::PCI12 ERROR9							
	OUTADRC PARITY ERR0	OUTADRC PARITY ERR1	OUTCNT PARITY ERR0	OUTCNT PARITY ERR1	INADRCNT PARITY ERR0	INADRCNT PARITY ERR1	IWC PARITY ERR0	IWC PARITY ERR1
65	FCA::PCI12 ERROR9							
	(Not used)	OV32 PARITY ERR	OV2 PARITY ERR	SOOV4 PARITY ERR	DETIWC PARITY ERR	BRDY PARITY ERR	OUT ADROVRN	IN ADROVRN
66	FCA::PCI12 ERROR9							
	SEQ CMP ERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	FCA::PCI10 ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	OUPIN PARITY ERR	INPOINT PARITY ERR
68	FCA::PCI10 ERROR10							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
69	FCA::PCI12 ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTSERR	OUTMPERR	PCI64 PARITY ERR	PCI PARITY ERR
6A	FCA::PCI10 ERROR10							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
6B	FCA::PCI12 ERROR11							
	OUTADRC PARITY ERR0	OUTADRC PARITY ERR1	OUTCNT PARITY ERR0	OUTCNT PARITY ERR1	INADRCNT PARITY ERR0	INADRCNT PARITY ERR1	IWCPA RITYE RR0	IWC PARITY ERR1
6C	FCA::PCI12 ERROR11							
	(Not used)	EQLSTCNT PARITY ERR	OVI PARITY ERR	OV4 PARITY ERR	DETIWCUP PARITY ERR	(Not used)	OUT ADROVRN	IN ADROVRN
6D	FCA::PCI12 ERROR11							
	(Not used)	MSTRX HUNCR	MSTRX HADER	MSTRX HECCHDE	(Not used)	MSTRX LUNCR	MSTRX LADER	MSTRX LECCHDE
6E	FCA::PCI12 ERROR11							
	(Not used)	TGTRXHUN CR	TGTRXHAD ER	TGTRXHEC CHDE	(Not used)	TGTRX LUNCR	TGTRX LADER	TGTRX LECCHDE
6F	FCA::PCI12 ERROR12							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(5) Byte28 = 30 (4/4)

	0	1	2	3	4	5	6	7
70	FCA::PCI12 ERROR12							
	(Not used)	(Not used)	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
71	FCA::PCI12 ERROR12							
	(Not used)	SEQ CMP ERR	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
72	FCA::PCI12 ERROR12							
	(Not used)	(Not used)	SEQ PARITY ERR1	SEQ PARITY ERR0	DEC ERR1	DEC ERR0	ENC ERR1	ENC ERR0
73	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(6) Byte28 = 40 (1/4)

	0	1	2	3	4	5	6	7
40	DHUB::DTSTATS							
	CHK2	CHK2A DTN CTL	CHK2B DTN DT	CHK2C DTN_MICRO	CHK2D CHSN CTL	CHK2E CNSN DT	CHK2F TO/ABT	YWING ERR
41	DHUB::DTSTATS							
	P0USE	(Not used)	P0 ERRINT	P1 ERRINT	ST0 VALID	ST0 CRAB/CMA ERR	ST1 VALID	ST1 CRAB/CMA ERR
42	DHUB::DTSTATS							
	REV NO #3	REV NO #2	REV NO #1	REV NO #0	MP NO #3	MP NO #2	MP NO #1	MP NO #0
43	DHUB::DTSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
44	DHUB::DTSTATS							
	ABORT	DSK TIMEOUT	P0 DSKTOUT	P1 DSKTOUT	(Not used)	(Not used)	(Not used)	(Not used)
45	DHUB::DTERR0							
	DBFE1 XPTPER	DBFE1 YPTPER	DBFE1 XXPTPER	DBFE1 YYPTPER	DBFE3 XPTPER	DBFE3 YPTPER	DBFE3 XXPTPER	DBFE3 YYPTPER
46	DHUB::DTERR0							
	DBF3 XPTPER	DBF3 YPTPER	DBF3 XXPTPER	DBF3 YYPTPER	DBF4 XPTPER	DBF4 YPTPER	DBF4 XXPTPER	DBF4 YYPTPER
47	DHUB::DTERR0							
	DBF5 XPTPER	DBF5 YPTPER	DBF5 XXPTPER	DBF5 YYPTPER	SQF0 ENCERR M	SQF0 SEQPER M	SQF0 DECERR M	SQF0 STERR M
48	DHUB::DTERR0							
	SQF0 HWERR M	SQF0 ENCERR S	SQF0 SEQPER S	SQF0 DECERR S	SQF0 STERR S	SQF0 HWERR S	SQF0CMP CMPERR	SQF1CMP CMPERR
49	DHUB::DTERR0							
	SQF1 ENCERR M	SQF1 SEQPER M	SQF1 DECERR M	SQF1 HWERR M	SQF1 ENCERR S	SQF1 SEQPER S	SQF1 DECERR S	SQF1 HWERR S
4A	DHUB::DTERR0							
	SQF2 ENCERR M	SQF2 SEQPER M	SQF2 DECERR M	SQF2 HWERR M	SQF2 ENCERR S	SQF2 SEQPER S	SQF2 DECERR S	SQF2 HWERR S
4B	DHUB::DTERR0							
	SQFE2 ENCERR M	SQFE2 SEQPER M	SQFE2 DECERR M	SQFE2 HWERR M	SQFE2 ENCERR S	SQFE2 SEQPER S	SQFE2 DECERR S	SQFE2 HWERR S
4C	DHUB::DTERR0							
	SQF2CMP CMPERR	SQFE2CMP CMPERR	SQF3 ENCERR M	SQF3 SEQPER M	SQF3 DECERR M	SQF3 HWERR M	SQF3 ENCERR S	SQF3 SEQPER S
4D	DHUB::DTERR1							
	SQF3 DECERR S	SQF3 HWERR S	SQF3CMP CMPERR	SQF4 ENCERR M	SQF4 SEQPER M	SQF4 DECERR M	SQF4 HWERR M	SQF4 ENCERR S
4E	DHUB::DTERR1							
	SQF4 SEQPER S	SQF4 DECERR S	SQF4 HWERR S	SQF4CMP CMPERR	SQF5 ENCERR M	SQF5 SEQPER M	SQF5 DECERR M	SQF5 HWERR M
4F	DHUB::DTERR1							
	SQF5 ENCERR S	SQF5 SEQPER S	SQF5 DECERR S	SQF5 HWERR S	SQF6 ENCERR M	SQF6 SEQPER M	SQF6 DECERR M	SQF6 HWERR M

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(6) Byte28 = 40 (2/4)

	0	1	2	3	4	5	6	7
50	DHUB::DTERR1							
	PLCNTF0 CPLCNT PER	PLCNTF0 NPLCNT PER	SQF6 ENCERR S	SQF6 SEQPER S	SQF6 DECERR S	SQF6 HWERR S	SQF5CMP CMPERR	SQF6CMP CMPERR
51	DHUB::DTERR1							
	PLCNTF4 CPLCNT PER	ADRCNTF ADRCNT PERX	ADRCNTF ADRCNT PERXX	ADRCNTF DTNACS PER	ADRCNTF ERR CHK2A0	ADRCNTF ERR CHK2A1	ADRCNTF ERR CHK2A2	ADRCNTF ERR CHK2A3
52	DHUB::DTERR1							
	(Not used)	RXACFF CRC0ERR	RXACFF CRC1ERR	RXACFF SENDERR	RXACFF ACPER	PLCNTF4 NPLCNT PER	PLCNTF5 CPLCNT PER	PLCNTF5 NPLCNT PER
53	DHUB::DTERR2							
	ST1CHKF STDTPER S	ST1CHKF PKIDERR D	ST1CHKF SENDERR D	ST1CHKF SLVIDERR D	ST1CHKF STERR D	ST1CHKF CRCERR D	(Not used)	TSLRCCHK RSLRC CMPERR D
54	DHUB::DTERR2							
	(Not used)	(Not used)	ST0CHKF STDTPER S	ST0CHKF PKIDERR D	ST0CHKF SENDERR D	ST0CHKF SLVIDERR D	ST0CHKF STERR D	ST0CHKF CRCERR D
55	DHUB::DTERR3							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	SQF3 PRERR M	SQF3 PRERR S	RXACFF CMDERR
56	DHUB::DTERR4							
	C0 WAITCNT TIMPER0	C0 ACKTCNT TIMPER0	C0 STTCNT TIMPER0	C0 ENTTCNT TIMPER0	C0 CHABT HWERR M	C0 CHABT HWERR S	C0 ENTTCNT TIMCMPPER	C0 ACKTCNT TIMCMPPER
57	DHUB::DTERR4							
	C0 CHABT ENCERR M	C0 CHABT ENCERR S	C0 CHABT DECERR M	C0 CHABT DECERR S	C0 CHABT SEQPER M	C0 CHABT SEQPER S	C0 RXSEQ HWERR M	C0 RXSEQ HWERR S
58	DHUB::DTERR4							
	C0 RXSEQ ENCERR M	C0 RXSEQ ENCERR S	C0 RXSEQ DECERR M	C0 RXSEQ DECERR S	C0 RXSEQ SEQPER M	C0 RXSEQ SEQPER S	C0 SOP3 HWERR M	C0 SOP3 HWERR S
59	DHUB::DTERR4							
	C0 SOP3 ENCERR M	C0 SOP3 ENCERR S	C0 SOP3 DECERR M	C0 SOP3 DECERR S	C0 SOP3 SEQPER M	C0 SOP3 SEQPER S	(Not used)	(Not used)
5A	DHUB::DTERR4							
	C0 TXSEQ ENCERR M	C0 TXSEQ ENCERR S	C0 TXSEQ DECERR M	C0 TXSEQ DECERR S	C0 TXSEQ SEQPER M	C0 TXSEQ SEQPER S	C0 TXXSEQ HWERR M	C0 TXXSEQ HWERR S
5B	DHUB::DTERR4							
	C0 TXXSEQ ENCERR M	C0 TXXSEQ ENCERR S	C0 TXXSEQ DECERR M	C0 TXXSEQ DECERR S	C0 TXXSEQ SEQPER M	C0 TXXSEQ SEQPER S	C0 DBP0 XTPERR	C0 DBP0 XXTPERR
5C	DHUB::DTERR4							
	C0 DBP0 YTPERR	C0 DBP0 YYTPERR	C0 DBP3 XTPERR	C0 DBP3 XXTPERR	C0 TXSEQ HWERR M	C0 TXSEQ HWERR S	C0 DBP4 XTPERR	C0 DBP4 XXTPERR
5D	DHUB::DTERR4							
	C0 DBP4 YTPERR	C0 DBP4 YYTPERR	C0 DBP1 XTPERR	C0 DBP1 XXTPERR	C0 DBP1 YTPERR	C0 DBP1 YYTPERR	C0 WAITCNT TIMCMPPER	C0 STTCNT TIMCMP PER
5E	DHUB::DTERR5							
	C0 WAITCNT TIMPER0	C0 ACKTCNT TIMPER0	C0 STTCNT TIMPER0	C0 ENTTCNT TIMPER0	C1 CHABT HWERR M	C1 CHABT HWERR S	C1 ENTTCNT TIMCMPPER	C1 ACKTCNT TIMCMPPER
5F	DHUB::DTERR5							
	C1 CHABT ENCERR M	C1 CHABT ENCERR S	C1 CHABT DECERR M	C1 CHABT DECERR S	C1 CHABT SEQPER M	C1 CHABT SEQPER S	C1 RXSEQ HWERR M	C1 RXSEQ HWERR S

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(6) Byte28 = 40 (3/4)

	0	1	2	3	4	5	6	7
60	DHUB::DTERR5							
	C1 RXSEQ ENCERR M	C1 RXSEQ ENCERR S	C1 RXSEQ DECERR M	C1 RXSEQ DECERR S	C1 RXSEQ SEQPER M	C1 RXSEQ SEQPER S	C1 SQP3 HWERR M	C1 SQP3 HWERR S
61	DHUB::DTERR5							
	C1 SQP3 ENCERR M	C1 SQP3 ENCERR S	C1 SQP3 DECERR M	C1 SQP3 DECERR S	C1 SQP3 SEQPER M	C1 SQP3 SEQPER S	(Not used)	(Not used)
62	DHUB::DTERR5							
	C1 TXSEQ ENCERR M	C1 TXSEQ ENCERR S	C1 TXSEQ DECERR M	C1 TXSEQ DECERR S	C1 TXSEQ SEQPER M	C1 TXSEQ SEQPER S	C1 TXXSEQ HWERR M	C1 TXXSEQ HWERR S
63	DHUB::DTERR5							
	C1 TXXSEQ ENCERR M	C1 TXXSEQ ENCERR S	C1 TXXSEQ DECERR M	C1 TXXSEQ DECERR S	C1 TXXSEQ SEQPER M	C1 TXXSEQ SEQPER S	C1 DBP0 XPTPERR	C1 DBP0 XXPTPERR
64	DHUB::DTERR5							
	C1 DBP0 YPTPERR	C1 DBP0 YYPTPERR	C1 DBP3 XPTPERR	C1 DBP3 XXPTPERR	C1 TXSEQ HWERR M	C1 TXSEQ HWERR S	C1 DBP4 XPTPERR	C1 DBP4 XXPTPERR
65	DHUB::DTERR5							
	C1 DBP4 YPTPERR	C1 DBP4 YYPTPERR	C1 DBP1 XPTPERR	C1 DBP1 XXPTPERR	C1 DBP1 YPTPERR	C1 DBP1 YYPTPERR	C1 WAITCNT TIMCMPPER	C1 STTCNT TIMCMP PER
66	DHUB::DTERR6							
	C1 PLCNTRX NPLCNT PER S	C1 PLCNTRX CPLCNT PER S	C1 PLCNTTX NPLCNT PER S	C1 PLCNTTX CPLCNT PER S	C0 PLCNTRX NPLCNT PER S	C0 PLCNTRX CPLCNT PER S	C0 PLCNTTX NPLCNT PER S	C0 PLCNTTX CPLCNT PER S
67	DHUB::DTERR6							
	C1 TXXSEQ CMPCMP ERR	C0 TXXSEQ CMPCMP ERR	C1 TXSEQ CMPCMP ERR	C0 TXSEQ CMPCMP ERR	C1 SQP3 CMPCMP ERR	C0 SQP3 CMPCMP ERR	C1 CHABT CMPCMP PER	C0 CHABT CMPCMP PER
68	DHUB::DTERR6							
	(Not used)	(Not used)	C1 TXSEQ ACERR M	C1 TXSEQ ACERR S	C0 TXSEQ ACERR M	C0 TXSEQ ACERR S	C1 RXSEQ CMPCMP ERR	C0 RXSEQ CMPCMP ERR
69	DHUB::DRACDT0							
	CACHE access address 1 (Byte0)							
6A	DHUB::DRACDT0							
	CACHE access address 1 (Byte1)							
6B	DHUB::DRACDT1							
	CACHE access address 2 (Byte0)							
6C	DHUB::DRACDT1							
	CACHE access address 2 (Byte1)							
6D	DHUB::DTST00							
	CACHE status (Byte0)							
6E	DHUB::DTST10							
	CACHE status (Byte1)							
6F	DHUB::SQ0M							
	The sequencer value of SQ0M							

Byte27 = 8A (FCA CHK2) Byte40-7F Detail

(6) Byte28 = 40 (4/4)

	0	1	2	3	4	5	6	7
70	DHUB::SQ3M							
	The sequencer value of SQ3M							
71	DHUB::SQ5M							
	The sequencer value of S SQ5M							
72	FCA::SQ50_A							
	The sequencer value of SQ50_A							
73	FCA::SQ50_B							
	The sequencer value of SQ50_B							
74	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	Reserved							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
76	Micro Information							
	CDEV#							
77	Micro Information							
	RDEV#							
78	Micro Information							
	SCSI command							
79	Micro Information							
	LBA# (byte0)							
7A	Micro Information							
	LBA# (byte1)							
7B	Micro Information							
	LBA# (byte2)							
7C	Micro Information							
	LBA# (byte3)							
7D	Micro Information							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7E	Micro Information							
	Error Divided Result Code							
7F	Micro Information							
	CHK2 Detected Counter (Low)							

Byte27 = 8B (DRR CHK2)

	0	1	2	3	4	5	6	7
00	Time Stamp							
05								
06	Format Message							
07	Processor ID							
08	System Error Code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB Log Number							
17								
18	Related Failure Log Number							
19								
1A	Related SIM Log Number							
1B								
1C	Related SSB Log Number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP# 2 ¹	SP# 2 ⁰	CNTL#	DKU Physical Device#				
25	Cylinder Address (Low)							
26	Cylinder Address (High)				Head Address			
27	Format (x'8')				Message (x'B')			
28	SubCode							
29	Detail Information (See Following Pages)							
31								
32	Module ID							
33	Routine ID							
34	P/K ID			Message Code (x'0')				
35	SSID for Self Subsystem (Low)							
36	Symptom Code (x'FF8B')							
37	(This information is not available before SSB reported to HOST)							
38	Log and Message Control (x'00')							
39	Program Action Control (x'00')							
3A	Configuration Information							
3B								
3C	(Reserved)							
3D	Cylinder Address							
3E								
3F	Head Address							
40	Detail Information (See Following Pages)							
7F								

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(1) Byte28 = 10 or 13 or 21 or 23

	0	1	2	3	4	5	6	7
28	Subcode (x'10' or x'13' or x'21' or x'23')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR4							
	DBD1M DATA PARITY ERR	DBD1N DATA PARITY ERR	DBD2 DATA PARITY ERR	DBD3 DATA PARITY ERR	DBD4M DATA PARITY ERR	DBD4N DATA PARITY ERR	(RSV)	(RSV)
2D	DRR::DRERR4							
	(RSV)	DBDE4M DATA PARITY ERR	DBDE4N DATA PARITY ERR	WRITE LA CMP ERR1	WRITE LA CMP ERR2	WRITE SLRC CMP ERR	WRITE SLRC PARITY ERR	SLRC DATA PARITY ERR
2E	DRR::DRERR4							
	DTSEL DATA PARITY ERR	ACGEN DATA PARITY ERR	READ LA CMP ERR1	READ LA CMP ERR2	READ LA PARITY ERR	READ SLRC CMP ERR	READ SLRC PARITY ERR	PLRC PARITY ERR
2F	DRR::DRERR4							
	(RSV)	(RSV)	(RSV)	DBDE4M VERIFY ERR	DBDE4N VERIFY ERR	RPLRCCHK CPLRC0PER	RPLRCCHK DTENDERR	(RSV)
30	DRR::DRERR4							
	ST1 DTPER	ST1CHK ST0CMPER	DBD1M C2 UCRCTERR	DBD1M C2 ADREERR	DBD1M C2 ECCERR	DBD1N C2 UCRCTERR	DBD1N C2 ADREERR	DBD1N C2 ECCERR
31	DRR::DRERR4							
	(RSV)	(RSV)	(RSV)	(RSV)	DBD3X DTPER	ST0 DTPER	ST0 BYTE6 RTNCODEE RR	ST0 BYTE7 RTNCODE ERR

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(2) Byte28 = 11 or 20 or 22 or 40

	0	1	2	3	4	5	6	7
28	Subcode (x'11' or x'20' or x'22' or x'40')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRSTATS							
	P0 ABT ERR	P0 ENT TOV	P0 ACK TOV	P0 ST TOV	P0 ABT ERR	P0 ENT TOV	P0 ACK TOV	P0 ST TOV
2D	DRR::TPRMA2							
	PARM_A VERIFY_N	PARM_A VERIFY_M	PARM_A MRCF	PARM_A 2WR MODE	PARM_A XFRMODE '2': Read1, '3': Read2 'C': Write, 'E': CACHE Copy			
2E	DRR::TPRMA2							
	PARM_A Parity Calculate Parameter0							
2F	DRR::TPRMB2							
	PARM_B VERIFY_N	PARM_B VERIFY_M	PARM_B MRCF	PARM_B 2WR MODE	PARM_B XFRMODE '2': Read1, '3': Read2 'C': Write, 'E': CACHE Copy			
30	DRR::TPRMB2							
	PARM_B Parity Calculate Parameter0							
31	DRR::DRMODE0							
	CHK2 INT ENABLE	CHK2A ERR ENABLE	CHK2B ERR ENABLE	CHK2C ERR ENABLE	CHK2D ERR ENABLE	CHK2E ERR ENABLE	CHK2F ERR ENABLE	(RSV)

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(3) Byte28 = 21

	0	1	2	3	4	5	6	7
28	Subcode (x'21')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR5							
	(RSV)	(RSV)	(RSV)	2WRITE SAME CLS ERR	2WRITE SAME CMA ERR	SQD2 PARM ERR (M)	SQD2 PARM ERR (S)	PRM64 EOR ERR
2D	DRR::DRERR4							
	(RSV)	DBDE4M DATA PARITY ERR	DBDE4N DATA PARITY ERR	WRITE LA CMP ERR1	WRITE LA CMP ERR2	WRITE SLRC CMP ERR	WRITE SLRC PARITY ERR	SLRC DATA PARITY ERR
2E	DRR::DRERR4							
	DTSEL DATA PARITY ERR	ACGEN DATA PARITY ERR	READ LA CMP ERR1	READ LA CMP ERR2	READ LA PARITY ERR	READ SLRC CMP ERR	READ SLRC PARITY ERR	PLRC PARITY ERR
2F	DRR::DRERR4							
	(RSV)	(RSV)	(RSV)	DBDE4M VERIFY ERR	DBDE4N VERIFY ERR	RPLRCCHK CPLRC0PER	RPLRCCHK DTENDERR	(RSV)
30	DRR::DRERR4							
	ST1 DTPER	ST1CHK ST0CMPER	DBD1M C2 UCRCTERR	DBD1M C2 ADREERR	DBD1M C2 ECCERR	DBD1N C2 UCRCTERR	DBD1N C2 ADREERR	DBD1N C2 ECCERR
31	DRR::DRERR4							
	(RSV)	(RSV)	(RSV)	(RSV)	DBD3X DTPER	ST0 DTPER	ST0 BYTE6 RTN CODEERR	ST0 BYTE7 RTN CODEERR

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(4) Byte28 = 30

	0	1	2	3	4	5	6	7
28	Subcode (x'30')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR1							
	SQD3 ENCERR (M)	SQD3 SEPPER (M)	SQD3 DECERR (M)	SQD3 HW ERR (M)	SQD3 ENCERR (S)	SQD3 SEPPER (S)	SQD3 DECERR (S)	SQD3 HW ERR (S)
2D	DRR::DRERR1							
	SQD4M ENCERR (M)	SQD4M SEPPER (M)	SQD4M DECERR (M)	SQD4M HW ERR (M)	SQD4M ENCERR (S)	SQD4M SEPPER (S)	SQD4M DECERR (S)	SQD4M HW ERR (S)
2E	DRR::DRERR1							
	SQD4N ENCERR (M)	SQD4N SEPPER (M)	SQD4N DECERR (M)	SQD4N HW ERR (M)	SQD4N ENCERR (S)	SQD4N SEPPER (S)	SQD4N DECERR (S)	SQD4N HW ERR (S)
2F	DRR::DRERR1							
	SQDE0M ENCERR (M)	SQDE0M SEPPER (M)	SQDE0M DECERR (M)	SQDE0M HW ERR (M)	SQDE0M ENCERR (S)	SQDE0M SEPPER (S)	SQDE0M DECERR (S)	SQDE0M HW ERR (S)
30	DRR::DRERR1							
	SQDE0N ENCERR (M)	SQDE0N SEPPER (M)	SQDE0N DECERR (M)	SQDE0N HW ERR (M)	SQDE0N ENCERR (S)	SQDE0N SEPPER (S)	SQDE0N DECERR (S)	SQDE0N HW ERR (S)
31	DRR::DRERR1							
	S64T ENCERR (M)	S64T SEPPER (M)	S64T DECERR (M)	S64T HW ERR (M)	S64T ENCERR (S)	S64T SEPPER (S)	S64T DECERR (S)	S64T HW ERR (S)

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(5) Byte28 = 31

	0	1	2	3	4	5	6	7
28	Subcode (x'31')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR2							
	DBD2 XPT PARITY ERR	DBD2 YPT PARITY ERR	DBD2 XXPT PARITY ERR	DBD2 YYPT PARITY ERR	DBD3 XPT PARITY ERR	DBD3 YPT PARITY ERR	DBD3 XXPT PARITY ERR	DBD3 YYPT PARITY ERR
2D	DRR::DRERR2							
	DBD4N PRMSNM PARITY ERR	DBD4M PGENCM PARITY ERR	DBD4M CRCTCM (QQ) PARITY ERR	DBD4M CRCTCM (PP) PARITY ERR	DBD4N XPT PARITY ERR	DBD4N YPT PARITY ERR	DBD4N XXPT PARITY ERR	DBD4N YYPT PARITY ERR
2E	DRR::DRERR2							
	DBDE1N XPT PARITY ERR	DBDE1N YPT PARITY ERR	DBDE1N XXPT PARITY ERR	DBDE1N YYPT PARITY ERR	DBDE1M XPT PARITY ERR	DBDE1M YPT PARITY ERR	DBDE1M XXPT PARITY ERR	DBDE1M YYPT PARITY ERR
2F	DRR::DRERR2							
	ADRCNT64 NWADR CNT PARITY ERR	ADRCNT64 CWADR CNT PARITY ERR	RD ADRCNTN CPLCNT PARITY ERR	RD ADRCNTM CPLCNT PARITY ERR	PRM64 CCMDCNT PARITY ERR	PRM64 NCMDCNT PARITY ERR	WR ADRCNTN CPLCNT PARITY ERR	WR ADRCNTM CPLCNT PARITY ERR
30	DRR::DRERR2							
	PLCNT5281 CPLCNT PARITY ERR	PLCNT5281 NPLCNT PARITY ERR	PLCNT5283 CPLCNT PARITY ERR	PLCNT5283 NPLCNT PARITY ERR	SBCNT1 CSBCNT PARITY ERR	SBCNT1 NSBCNT PARITY ERR	SBCNT3 CSBCNT PARITY ERR	SBCNT3 NSBCNT PARITY ERR
31	DRR::DRERR2							
	PLCNTD4M CPLCNT PARITY ERR	PLCNTD4M NPLCNT PARITY ERR	PLCNTD4N CPLCNT PARITY ERR	PLCNTD4N NPLCNT PARITY ERR	PLCNTD0M CPLCNT PARITY ERR	PLCNTD0M NPLCNT PARITY ERR	PLCNTD0N CPLCNT PARITY ERR	PLCNTD0N NPLCNT PARITY ERR

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(6) Byte28 = 32

	0	1	2	3	4	5	6	7
28	Subcode (x'32')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR3							
	SQD3X ENCERR (M)	SQD3X SEOPER (M)	SQD3X DECERR (M)	SQD3X HW ERR (M)	SQD3X ENCERR (S)	SQD3X SEOPER (S)	SQD3X DECERR (S)	SQD3X HW ERR (S)
2D	DRR::DRERR3							
	(RSV)	(RSV)	(RSV)	SQD3X CMP ERR	DBD3X XPT PARITY ERR	DBD3X YPT PARITY ERR	DBD3X XXPT PARITY ERR	DBD3X YYPT PARITY ERR
2E	DRR::TPRMA2							
	PARM_A VERIFY_N	PARM_A VERIFY_M	PARM_A MRCF	PARM_A 2WR MODE	PARM_A XFRMODE '2': Read1, '3': Read2 'C': Write, 'E': CACHE Copy			
2F	DRR::TPRMA2							
	PARM_A Parity Calculate Parameter0							
30	DRR::TPRMB2							
	PARM_B VERIFY_N	PARM_B VERIFY_M	PARM_B MRCF	PARM_B 2WR MODE	PARM_B XFRMODE '2': Read1, '3': Read2 'C': Write, 'E': CACHE Copy			
31	DRR::TPRMB2							
	PARM_B Parity Calculate Parameter0							

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(7) Byte28 = 36

	0	1	2	3	4	5	6	7
28	Subcode (x'36')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR6							
	C0 WAITCNT TIMER PARITY ERR	C0 ACKTCNT TIMER PARITY ERR	C0 STTCNT TIMER PARITY ERR	C0 ENTTCNT TIMER PARITY ERR	C0 CHABT HWERR (M)	C0 CHABT HWERR (S)	C0 ENTTCNT TIMER CMP ERR	C0 ACKTCNT TIMER CMP ERR
2D	DRR::DRERR6							
	C0 CHABT ENCERR (M)	C0 CHABT ENCERR (S)	C0 CHABT DECERR (M)	C0 CHABT DECERR (S)	C0 CHABT SEPPER (M)	C0 CHABT SEPPER (S)	C0 RXSEQ HWERR (M)	C0 RXSEQ HWERR (S)
2E	DRR::DRERR6							
	C0 RXSEQ ENCERR (M)	C0 RXSEQ ENCERR (S)	C0 RXSEQ DECERR (M)	C0 RXSEQ DECERR (S)	C0 RXSEQ SEPPER (M)	C0 RXSEQ SEPPER (S)	C0 SQP3 HWERR (M)	C0 SQP3 HWERR (S)
2F	DRR::DRERR6							
	C0 SQP3 ENCERR (M)	C0 SQP3 ENCERR (S)	C0 SQP3 DECERR (M)	C0 SQP3 DECERR (S)	C0 SQP3 SEPPER (M)	C0 SQP3 SEPPER (S)	(RSV)	(RSV)
30	DRR::DRERR6							
	C0 TXSEQ ENCERR (M)	C0 TXSEQ ENCERR (S)	C0 TXSEQ DECERR (M)	C0 TXSEQ DECERR (S)	C0 TXSEQ SEPPER (M)	C0 TXSEQ SEPPER (S)	C0 TXXSEQ HWERR (M)	C0 TXXSEQ HWERR (S)
31	DRR::DRERR6							
	C0 TXXSEQ ENCERR (M)	C0 TXXSEQ ENCERR (S)	C0 TXXSEQ DECERR (M)	C0 TXXSEQ DECERR (S)	C0 TXXSEQ SEPPER (M)	C0 TXXSEQ SEPPER (S)	C0 DBP0 XPTPERR	C0 DBP0 XXPTPER

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(8) Byte28 = 37

	0	1	2	3	4	5	6	7
28	Subcode (x'37')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR7							
	C1 WAITCNT TIMER PARITY ERR	C1 ACKTCNT TIMER PARITY ERR	C1 STTCNT TIMER PARITY ERR	C1 ENTTCNT TIMER PARITY ERR	C1 CHABT HWERR (M)	C1 CHABT HWERR (S)	C1 ENTTCNT TIMER CMP ERR	C1 ACKTCNT TIMER CMP ERR
2D	DRR::DRERR7							
	C1 CHABT ENCERR (M)	C1 CHABT ENCERR (S)	C1 CHABT DECERR (M)	C1 CHABT DECERR (S)	C1 CHABT SEPPER (M)	C1 CHABT SEPPER (S)	C1 RXSEQ HWERR (M)	C1 RXSEQ HWERR (S)
2E	DRR::DRERR7							
	C1 RXSEQ ENCERR (M)	C1 RXSEQ ENCERR (S)	C1 RXSEQ DECERR (M)	C1 RXSEQ DECERR (S)	C1 RXSEQ SEPPER (M)	C1 RXSEQ SEPPER (S)	C1 SQP3 HWERR (M)	C1 SQP3 HWERR (S)
2F	DRR::DRERR7							
	C1 SQP3 ENCERR (M)	C1 SQP3 ENCERR (S)	C1 SQP3 DECERR (M)	C1 SQP3 DECERR (S)	C1 SQP3 SEPPER (M)	C1 SQP3 SEPPER (S)	(RSV)	(RSV)
30	DRR::DRERR7							
	C1 TXSEQ ENCERR (M)	C1 TXSEQ ENCERR (S)	C1 TXSEQ DECERR (M)	C1 TXSEQ DECERR (S)	C1 TXSEQ SEPPER (M)	C1 TXSEQ SEPPER (S)	C1 TXXSEQ HWERR (M)	C1 TXXSEQ HWERR (S)
31	DRR::DRERR7							
	C1 TXXSEQ ENCERR (M)	C1 TXXSEQ ENCERR (S)	C1 TXXSEQ DECERR (M)	C1 TXXSEQ DECERR (S)	C1 TXXSEQ SEPPER (M)	C1 TXXSEQ SEPPER (S)	C1 DBP0 XPTPERR	C1 DBP0 XXPTPER

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(9) Byte28 = 38

	0	1	2	3	4	5	6	7
28	Subcode (x'38')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRERR8							
	C1 LCNTRX NPLCNT PARITY ERR	C1 LCNTRX CPLCNT PARITY ERR	C1 PLCNTTX NPLCNT PARITY ERR	C1 PLCNTTX CPLCNT PARITY ERR	C0 LCNTRX NPLCNT PARITY ERR	C0 LCNTRX CPLCNT PARITY ERR	C0 PLCNTTX NPLCNT PARITY ERR	C0 PLCNTTX CPLCNT PARITY ERR
2D	DRR::DRERR8							
	C1 TXXSEQ CMP ERR	C0 TXXSEQ CMP ERR	C1 TXSEQ CMP ERR	C0 TXSEQ CMP ERR	C1 SQP3 CMP ERR	C0 SQP3 CMP ERR	C1 CHABT CMP ERR	C0 CHABT CMP ERR
2E	DRR::DRERR8							
	(RSV)	(RSV)	C1 TXSEQ ACS ERR (M)	C1 TXSEQ ACS ERR (S)	C0 TXSEQ ACS ERR (M)	C0 TXSEQ ACS ERR (S)	C1 RXSEQ CMP ERR	C0 RXSEQ CMP ERR
2F	DRR::DRERR8							
	(RSV)	C1 ENTTCNT ERRINTER	C1 ENTTCNT ACKERR	C1 ENTTCNT ENTERR	(RSV)	C0 ENTTCNT ERRINTER	C0 ENTTCNT ACKERR	C0 ENTTCNT ENTERR
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8B (DRR CHK2) Byte28-31 Detail

(10) Byte28 = A0

	0	1	2	3	4	5	6	7
28	Subcode (x'A0')							
29	DRR::DRSTATS							
	XFR END	XFR EXEC	(RSV)	(RSV)	XFR END (CMD A)	XFR EXEC (CMD A)	XFR END (CMD B)	XFR EXEC (CMD B)
2A	DRR::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C (DRR SOFT)	CHK2D (CHSN CTL)	CHK2E (CHSN DT)	CHK2F (TOV/ABT)	CHK1B
2B	DRR::DRSTATS							
	P0 USE FLAG	DSK TIMEOUT	P0 ERR INT	P1 ERR INT	ST0 VALID	ST0 CARB/CMA ERR	ST1 VALID	ST1 CARB/CMA ERR
2C	DRR::DRSTATS							
	P0 ABT ERR	P0 ENT TOV	P0 ACK TOV	P0 ST TOV	P0 ABT ERR	P0 ENT TOV	P0 ACK TOV	P0 ST TOV
2D	DRR::DRSTATS							
	ABORT	YWING ERR	(RSV)	(RSV)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64 ERR
2E	DRR::TPRMA2							
	PARM_A VERIFY_N	PARM_A VERIFY_M	PARM_A MRCF	PARM_A 2WR MODE	PARM_A XFRMODE '2': Read1, '3': Read2 'C': Write, 'E': CACHE Copy			
2F	DRR::P0DSKCTL1							
	P0 DLYADR(5)	P0 DLYADR(4)	P0 DLYADR(3)	P0 DLYADR(2)	P0 DLYADR(1)	P0 DLYADR(0)	P0 CALLEV1	P0 CHKLEV1
30	DRR::P1DSKCTL1							
	P1 DLYADR(5)	P1 DLYADR(4)	P1 DLYADR(3)	P1 DLYADR(2)	P1 DLYADR(1)	P1 DLYADR(0)	P1 CALLEV1	P1 CHKLEV1
31	DRR::DRMODE0							
	CHK2 INT ENABLE	CHK2A ERR ENABLE	CHK2B ERR ENABLE	CHK2C ERR ENABLE	CHK2D ERR ENABLE	CHK2E ERR ENABLE	CHK2F ERR ENABLE	(RSV)

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 ≠ 30, 31, 36, 37 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::MPASTS							
	SYSRST INT	SELRST INT	CHK1A INT	CHK1B INT	SMP INT	LAN/SIO INT	CHA/DRR INT	ADP/SCA0 INT
41	MPA::MPASTS							
	(RSV)	CHK3 INT	(RSV)	SCA1 INT	SCA2 INT	SCA3 INT	SCA4 INT	SCA5 INT
42	DRR::DRSTATS							
	DRR CUDG MODE	DRR SYS CLK MASK	DRR FRC ERR	(RSV)	(RSV)	DRB W/R ENABLE	SLRC Check MASK	Subblock Change
43	DRR::DRSTATS							
	64-CMD	Slave 2 ADR OUT enable	DATA ERR REPORT ENABLE	CHSN ABORT	(RSV)	ROSC test enable	DRR ABORT	DRR CHK2 Reset
44	DRR::DRMODE0							
	C PATH#00 enable	C PATH#01 enable	C PATH#02 enable	C PATH#03 enable	(RSV)	(RSV)	(RSV)	(RSV)
45	DRR::DRMODE0							
	C PATH#10 enable	C PATH#11 enable	C PATH#12 enable	C PATH#13 enable	(RSV)	(RSV)	(RSV)	(RSV)
46	DRR::PRM0ST							
	CMD A SLRC CHK	(RSV)	(RSV)	CMD A SP ACS	CMD A Diag ACS	(RSV)	(RSV)	(RSV)
47	DRR::PRM0ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
48	DRR::PRM0ADR							
	CMD A slave 1 CACHE XFR Address (BYTE0)							
49	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR Address (BYTE0)							
4A	DRR::PRM0ST							
	CMD A slave 1 CACHE XFR Address (BYTE1)							
4B	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR Address (BYTE1)							
4C	DRR::PRM0ADR							
	CMD A SYND CHK	(RSV)	CMD A MRCF SET	CMD A 2WRITE	CMD A XFR Command			
4D	DRR::PRM1LA0							
	LA0(LBA) CHK EN	LA1 CHK EN	LA2 CHK EN	(RSV)	LA EXP	(RSV)	(RSV)	(RSV)
4E	DRR::PRM1LA0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
4F	DRR::PRM1LA0							
	CMD A LA EXP (BYTE0)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 ≠ 30, 31, 36, 37 (2/4)

	0	1	2	3	4	5	6	7
50	DRR::PRM1LA0							
	CMD A LA EXP (BYTE1)							
51	DRR::PRM1LA0							
	CMD A LA EXP (BYTE2)							
52	DRR::PRM1LA0							
	CMD A LA EXP (BYTE3)							
53	DRR::PRM1LA0							
	CMD A LA EXP (BYTE4)							
54	DRR::PRM1LA0							
	CMD B SLRC CHK	(RSV)	(RSV)	CMD B SP ACS	CMD B Diag ACS	(RSV)	(RSV)	(RSV)
55	DRR::LACHKDT0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
56	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE0)							
57	DRR::LACHKDT0							
	CMD B slave 2 CACHE XFR ADDRESS (BYTE0)							
58	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
59	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
5A	DRR::LACHKDT0							
	CMD B SYND CHK	(RSV)	CMD B MRCF SET	CMD B 2WRITE	CMD B XFR CMD			
5B	DRR::PRM1ST							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA exp value CNT STOP	(RSV)	(RSV)	(RSV)
5C	DRR::PRM1ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5D	DRR::PRM1ST							
	CMD B LA EXP (BYTE0)							
5E	DRR::PRM1ST							
	CMD B LA EXP (BYTE1)							
5F	DRR::ACGEN0							
	CMD B LA EXP (BYTE2)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 ≠ 30, 31, 36, 37 (3/4)

	0	1	2	3	4	5	6	7
60	DRR::ACGEN0							
	CMD B LA EXP (BYTE3)							
61	DRR::ACGEN0							
	CMD B LA EXP (BYTE4)							
62	DRR::ACGEN0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
63	DRR::ACGEN0							
	LA Read Value (Byte 0)							
64	DRR::ACGEN0							
	LA Read Value (Byte 1)							
65	DRR::ACGEN0							
	LA Read Value (Byte 2)							
66	DRR::ACGEN0							
	LA Read Value (Byte 3)							
67	DRR::ACGEN1							
	LA Read Value (Byte 4)							
68	DRR::ACGEN1							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	CMD PNT (Hi)	
69	DRR::ACGEN1							
	CMD PNT (Lo)							
6A	DRR::ACGEN1							
	DRSEQM							
6B	DRR::ACGEN1							
	PBSEQM							
6C	DRR::ACGEN1							
	DMISQMREG							
6D	DRR::ACGEN1							
	TRISQMREG							
6E	DRR::ACGEN1							
	P0TXSQM							
6F	DRR::DRSTATS							
	P0RXSQM							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(1) Byte28 ≠ 30, 31, 36, 37 (4/4)

	0	1	2	3	4	5	6	7
70	DRR::DRMODE0 PITXSQM							
71	DRR::TPRMA2 PITXSQM							
72	DRR::TPRMB2							
	QDTA bit (1:QDTA)	(RSV)	MP#		LSI Rev.			
73	DRR::DRERR9							
	MP Out ALARM	MP Out MESSAGE	MP Out CUDG3A FAIL	MP Out CUDG3B FAIL	LSIMP ALARM	LSIMP MESSAGE	LSIMP CUDG3A FAIL	LSIMP CUDG3B FAIL
74	DRR::DRERR9							
	CMD A TRANSFER END	(RSV)	CMD A Exec	CMD A Active	CMD B TRANSFER END	(RSV)	CMD B Exec	CMD B Active
75	DRR::DRERR9							
	CHK2	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK1B detected	XFR ABORT detect	Force ERR detect
76	DRR::DRERR5							
	ABT SEQ PARITY ERR (M)	ABT SEQ PARITY ERR (S)	P0 ABT CMP CHK ERR	P1 ABT CMP CHK ERR	PKIDCHK ERR	(RSV)	(RSV)	(RSV)
77	DRR::DRERR5							
	CBUF0/1 DATA0 PARITY ERR	CBUF0/1 DATA1 PARITY ERR	CBUF0/1 DATA2 PARITY ERR	CBUF0/1 DATA3 PARITY ERR	CBUF0/1 DATA4 PARITY ERR	CBUF0/1 DATA5 PARITY ERR	CBUF0/1 DATA6 PARITY ERR	CBUF0/1 DATA7 PARITY ERR
78	Micro Information Scheduled XFR LIST number							
79	Micro Information Scheduled XFR LIST number							
7A	Micro Information Finished XFR LIST number							
7B	Micro Information Finished XFR LIST number							
7C	Micro Information JOB Restart bit							
7D	Micro Information							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7E	Micro Information Error function result code							
7F	Micro Information CHK2 ERROR# (LOW)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 30 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::MPASTS							
	SYSRST INT MONI	SELRST INT MONI	CHK1A INT MONI	CHK1B INT MONI	SMP INT MONI	LAN/SIO INT MONI	CHA/DRR INT MONI	ADP/SCA0 INT MONI
41	MPA::MPASTS							
	(RSV)	CHK3 INT MONI	(RSV)	SCA1 INT MONI	SCA2 INT MONI	SCA3 INT MONI	SCA4 INT MONI	SCA5 INT MONI
42	DRR::DRSTATS							
	DRR CUDG MODE	DRR System CLK MASK	DRR Force ERR	(RSV)	(RSV)	DRB W/R enable	SLRC CHK MASK	Sub Block lengt Variable
43	DRR::DRSTATS							
	64-CMD	Slave 2 ADR OUT enable	Data Error Report	CHSN ABORT	(RSV)	ROSC test enable	DRR ABORT	DRR CHK2 Reset
44	DRR::DRMODE0							
	C PATH#00 enable	C PATH#01 enable	C PATH#02 enable	C PATH#03 enable	(RSV)	(RSV)	(RSV)	(RSV)
45	DRR::DRMODE0							
	C PATH#10 enable	C PATH#11 enable	C PATH#12 enable	C PATH#13 enable	(RSV)	(RSV)	(RSV)	(RSV)
46	DRR::PRM0ST							
	CMD A SLRC CHK	(RSV)	(RSV)	CMD A SP ACS	CMD A Diag ACS	(RSV)	(RSV)	(RSV)
47	DRR::PRM0ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
48	DRR::PRM0ADR							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE0)							
49	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE0)							
4A	DRR::PRM0ST							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE1)							
4B	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE1)							
4C	DRR::PRM0ADR							
	CMD A SYND CHK	(RSV)	CMD A MRCF SET	CMD A 2WRITE	CMD A XFR CMD			
4D	DRR::PRM1LA0							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
4E	DRR::PRM1LA0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
4F	DRR::PRM1LA0							
	CMD A LA EXP (BYTE0)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 30 (2/4)

	0	1	2	3	4	5	6	7
50	DRR::PRM1LA0							
	CMD A LA EXP (BYTE1)							
51	DRR::PRM1LA0							
	CMD A LA EXP (BYTE2)							
52	DRR::PRM1LA0							
	CMD A LA EXP (BYTE3)							
53	DRR::PRM1LA0							
	CMD A LA EXP (BYTE4)							
54	DRR::PRM1LA0							
	CMD B SLRC CHK	(RSV)	(RSV)	CMD B SP ACS	CMD B Diag ACS	(RSV)	(RSV)	(RSV)
55	DRR::LACHKDT0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
56	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE0)							
57	DRR::LACHKDT0							
	CMD B slave 2 CACHE XFR ADDRESS (BYTE0)							
58	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
59	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
5A	DRR::LACHKDT0							
	CMD B SYND CHK	(RSV)	CMD B MRCF SET	CMD B 2WRITE	CMD B XFR CMD			
5B	DRR::PRM1ST							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
5C	DRR::PRM1ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5D	DRR::PRM1ST							
	CMD B LA EXP (BYTE0)							
5E	DRR::PRM1ST							
	CMD B LA EXP (BYTE1)							
5F	DRR::ACGEN0							
	CMD B LA EXP (BYTE2)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 30 (3/4)

	0	1	2	3	4	5	6	7
60	DRR::ACGEN0							
	CMD B LA EXP (BYTE3)							
61	DRR::ACGEN0							
	CMD B LA EXP (BYTE4)							
62	DRR::ACGEN0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
63	DRR::ACGEN0							
	LA Read Value (Byte 0)							
64	DRR::ACGEN0							
	LA Read Value (Byte 1)							
65	DRR::ACGEN0							
	LA Read Value (Byte 2)							
66	DRR::ACGEN0							
	LA Read Value (Byte 3)							
67	DRR::ACGEN1							
	LA Read Value (Byte 4)							
68	DRR::ACGEN1							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	CMD PNT (Hi)	
69	DRR::ACGEN1							
	CMD PNT (Lo)							
6A	DRR::ACGEN1							
	DRSEQM							
6B	DRR::ACGEN1							
	PBSEQM							
6C	DRR::ACGEN1							
	DMISQMREG							
6D	DRR::ACGEN1							
	TRISQMREG							
6E	DRR::ACGEN1							
	P0TXSQM							
6F	DRR::DRERR2							
	P0RXSQM							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(2) Byte28 = 30 (4/4)

	0	1	2	3	4	5	6	7
70	DRR::DRERR2 PITXSQM							
71	DRR::DRERR2 PITXSQM							
72	DRR::STATS							
	(RSV)	(RSV)	MP#		LSI Rev.			
73	DRR::TPRMA2							
	MP OUTPUT ALARM	MP OUTPUT MESSAGE	MP OUTPUT CUDG3A FAIL	MP OUTPUT CUDG3B FAIL	LSIMP ALARM	LSIMP MESSAGE	LSIMP CUDG3A FAIL	LSIMP CUDG3B FAIL
74	DRR::TPRMA2							
	CMD A TRANSFER END	(RSV)	CMD A Exec	CMD A Active	CMD B TRANSFER END	(RSV)	CMD B Exec	CMD B Active
75	DRR::TPRMB2							
	CHK2	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK1B detected	XFR ABORT detect	Force ERR detect
76	DRR::TPRMB2							
	ABT SEQ PARITY ERR (M)	ABT SEQ PARITY ERR (S)	P0 ABT CMP CHK ERR	P1 ABT CMP CHK ERR	PKID CHK ERR	(RSV)	(RSV)	(RSV)
77	DRR::DRMODE0							
	CBUF0/1 DATA0 PARITY ERR	CBUF0/1 DATA1 PARITY ERR	CBUF0/1 DATA2 PARITY ERR	CBUF0/1 DATA3 PARITY ERR	CBUF0/1 DATA4 PARITY ERR	CBUF0/1 DATA5 PARITY ERR	CBUF0/1 DATA6 PARITY ERR	CBUF0/1 DATA7 PARITY ERR
78	Micro Information Scheduled XFR LIST number							
79	Micro Information Scheduled XFR LIST number							
7A	Micro Information Finished XFR LIST number							
7B	Micro Information Finished XFR LIST number							
7C	Micro Information JOB Restart bit							
7D	Micro Information							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7E	Micro Information Error function result code							
7F	Micro Information CHK2 ERROR# (LOW)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(3) Byte28 = 36 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::MPASTS							
	SYSRST INT MONI	SELRST INT MONI	CHK1A INT MONI	CHK1B INT MONI	SMP INT MONI	LAN/SIO INT MONI	CHA/DRR INT MONI	ADP/SCA0 INT MONI
41	MPA::MPASTS							
	(RSV)	CHK3 INT MONI	(RSV)	SCA1 INT MONI	SCA2 INT MONI	SCA3 INT MONI	SCA4 INT MONI	SCA5 INT MONI
42	DRR::DRSTATS							
	DRR CUDG MODE	DRR System CLK MASK	DRR Force ERR	(RSV)	(RSV)	DRB W/R enable	SLRC CHK MASK	Sub Block lengt Variable
43	DRR::DRSTATS							
	64-CMD	Slave 2 ADR OUT enable	Data Error Report	CHSN ABORT	(RSV)	ROSC test enable	DRRABORT	DRR CHK2 Reset
44	DRR::DRMODE0							
	C PATH#00 enable	C PATH#01 enable	C PATH#02 enable	C PATH#03 enable	(RSV)	(RSV)	(RSV)	(RSV)
45	DRR::DRMODE0							
	C PATH#10 enable	C PATH#11 enable	C PATH#12 enable	C PATH#13 enable	(RSV)	(RSV)	(RSV)	(RSV)
46	DRR::PRM0ST							
	CMD A SLRC CHK	(RSV)	(RSV)	CMD A SP ACS	CMD A Diag ACS	(RSV)	(RSV)	(RSV)
47	DRR::PRM0ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
48	DRR::PRM0ADR							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE0)							
49	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE0)							
4A	DRR::PRM0ST							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE1)							
4B	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE1)							
4C	DRR::PRM0ADR							
	CMD A SYND CHK	(RSV)	CMD A MRCF SET	CMD A 2WRITE	CMD A XFR CMD			
4D	DRR::PRM1LA0							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
4E	DRR::PRM1LA0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
4F	DRR::PRM1LA0							
	CMD A LA EXP (BYTE0)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(3) Byte28 = 36 (2/4)

	0	1	2	3	4	5	6	7
50	DRR::PRM1LA0							
	CMD A LA EXP (BYTE1)							
51	DRR::PRM1LA0							
	CMD A LA EXP (BYTE2)							
52	DRR::PRM1LA0							
	CMD A LA EXP (BYTE3)							
53	DRR::PRM1LA0							
	CMD A LA EXP (BYTE4)							
54	DRR::PRM1LA0							
	CMD B SLRC CHK	(RSV)	(RSV)	CMD B SP ACS	CMD B Diag ACS	(RSV)	(RSV)	(RSV)
55	DRR::LACHKDT0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
56	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE0)							
57	DRR::LACHKDT0							
	CMD B slave 2 CACHE XFR ADDRESS (BYTE0)							
58	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
59	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
5A	DRR::LACHKDT0							
	CMD B SYND CHK	(RSV)	CMD B MRCF SET	CMD B 2WRITE	CMD B XFR CMD			
5B	DRR::PRM1ST							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
5C	DRR::PRM1ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5D	DRR::PRM1ST							
	CMD B LA EXP (BYTE0)							
5E	DRR::PRM1ST							
	CMD B LA EXP (BYTE1)							
5F	DRR::ACGEN0							
	CMD B LA EXP (BYTE2)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(3) Byte28 = 36 (3/4)

	0	1	2	3	4	5	6	7
60	DRR::ACGEN0							
	CMD B LA EXP (BYTE3)							
61	DRR::ACGEN0							
	CMD B LA EXP (BYTE4)							
62	DRR::ACGEN0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
63	DRR::ACGEN0							
	LA Read Value (Byte 0)							
64	DRR::ACGEN0							
	LA Read Value (Byte 1)							
65	DRR::ACGEN0							
	LA Read Value (Byte 2)							
66	DRR::ACGEN0							
	LA Read Value (Byte 3)							
67	DRR::ACGEN1							
	LA Read Value (Byte 4)							
68	DRR::ACGEN1							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	CMD PNT (Hi)	
69	DRR::ACGEN1							
	CMD PNT (Lo)							
6A	DRR::ACGEN1							
	DRSEQM							
6B	DRR::ACGEN1							
	PBSEQM							
6C	DRR::ACGEN1							
	DMISQMREG							
6D	DRR::ACGEN1							
	TRISQMREG							
6E	DRR::ACGEN1							
	P0TXSQM							
6F	DRR::DRERR6							
	P0RXSQM							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(3) Byte28 = 36 (4/4)

	0	1	2	3	4	5	6	7
70	DRR::DRERR6 PITXSQM							
71	DRR::DRERR6 PITXSQM							
72	DRR::STATS							
	QDTA bit (1:QDTA)	(RSV)	MP#		LSI Rev.			
73	DRR::TPRMA2							
	MP Out ALARM	MP Out MESSAGE	MP Out CUDG3A FAIL	MP Out CUDG3B FAIL	LSIMP ALARM	LSIMP MESSAGE	LSIMP CUDG3A FAIL	LSIMP CUDG3B FAIL
74	DRR::TPRMA2							
	CMD A TRANSFER END	(RSV)	CMD A Exec	CMD A Active	CMD B TRANSFER END	(RSV)	CMD B Exec	CMD B Active
75	DRR::TPRMB2							
	CHK2	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK1B detected	XFR ABORT detect	Force ERR detect
76	DRR::TPRMB2							
	ABT SEQ PARITY ERR (M)	ABT SEQ PARITY ERR (S)	P0 ABT CMP CHK ERR	P1 ABT CMP CHK ERR	PKID CHK ERR	(RSV)	(RSV)	(RSV)
77	DRR::DRMODE0							
	CBUF0/1 DATA0 PARITY ERR	CBUF0/1 DATA1 PARITY ERR	CBUF0/1 DATA2 PARITY ERR	CBUF0/1 DATA3 PARITY ERR	CBUF0/1 DATA4 PARITY ERR	CBUF0/1 DATA5 PARITY ERR	CBUF0/1 DATA6 PARITY ERR	CBUF0/1 DATA7 PARITY ERR
78	Micro Information Scheduled XFR LIST number							
79	Micro Information Scheduled XFR LIST number							
7A	Micro Information Finished XFR LIST number							
7B	Micro Information Finished XFR LIST number							
7C	Micro Information JOB Restart bit							
7D	Micro Information							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7E	Micro Information Error function result code							
7F	Micro Information CHK2 ERROR# (LOW)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(4) Byte28 = 37 (1/4)

	0	1	2	3	4	5	6	7
40	MPA::MPASTS							
	SYSRST INT MONI	SELRST INT MONI	CHK1A INT MONI	CHK1B INT MONI	SMP INT MONI	LAN/SIO INT MONI	CHA/DRR INT MONI	ADP/SCA0 INT MONI
41	MPA::MPASTS							
	(RSV)	CHK3 INT MONI	(RSV)	SCA1 INT MONI	SCA2 INT MONI	SCA3 INT MONI	SCA4 INT MONI	SCA5 INT MONI
42	DRR::DRSTATS							
	DRR CUDG MODE	DRR System CLK MASK	DRR Force ERR	(RSV)	(RSV)	DRB W/R enable	SLRC CHK MASK	Sub Block lengt Variable
43	DRR::DRSTATS							
	64-CMD	Slave 2 ADR OUT enable	Data Error Report	CHSN ABORT	(RSV)	ROSC test enable	DRR ABORT	DRR CHK2 Reset
44	DRR::DRMODE0							
	C PATH#00 enable	C PATH#01 enable	C PATH#02 enable	C PATH#03 enable	(RSV)	(RSV)	(RSV)	(RSV)
45	DRR::DRMODE0							
	C PATH#10 enable	C PATH#11 enable	C PATH#12 enable	C PATH#13 enable	(RSV)	(RSV)	(RSV)	(RSV)
46	DRR::PRM0ST							
	CMD A SLRC CHK	(RSV)	(RSV)	CMD A SP ACS	CMD A Diag ACS	(RSV)	(RSV)	(RSV)
47	DRR::PRM0ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
48	DRR::PRM0ADR							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE0)							
49	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE0)							
4A	DRR::PRM0ST							
	CMD A slave 1 CACHE XFR ADDRESS (BYTE1)							
4B	DRR::PRM0ADR							
	CMD A slave 2 CACHE XFR ADDRESS (BYTE1)							
4C	DRR::PRM0ADR							
	CMD A SYND CHK	(RSV)	CMD A MRCF SET	CMD A 2WRITE	CMD A XFR CMD			
4D	DRR::PRM1LA0							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
4E	DRR::PRM1LA0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
4F	DRR::PRM1LA0							
	CMD A LA EXP (BYTE0)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(4) Byte28 = 37 (2/4)

	0	1	2	3	4	5	6	7
50	DRR::PRM1LA0							
	CMD A LA EXP (BYTE1)							
51	DRR::PRM1LA0							
	CMD A LA EXP (BYTE2)							
52	DRR::PRM1LA0							
	CMD A LA EXP (BYTE3)							
53	DRR::PRM1LA0							
	CMD A LA EXP (BYTE4)							
54	DRR::PRM1LA0							
	CMD B SLRC CHK	(RSV)	(RSV)	CMD B SP ACS	CMD B Diag ACS	(RSV)	(RSV)	(RSV)
55	DRR::LACHKDT0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
56	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE0)							
57	DRR::LACHKDT0							
	CMD B slave 2 CACHE XFR ADDRESS (BYTE0)							
58	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
59	DRR::LACHKDT0							
	CMD B slave 1 CACHE XFR ADDRESS (BYTE1)							
5A	DRR::LACHKDT0							
	CMD B SYND CHK	(RSV)	CMD B MRCF SET	CMD B 2WRITE	CMD B XFR CMD			
5B	DRR::PRM1ST							
	LA0(LBA) CMP CHK Enable	LA1 CMP CHK Enable	LA2 CMP CHK Enable	(RSV)	LA EXP Count Up Stop	(RSV)	(RSV)	(RSV)
5C	DRR::PRM1ST							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5D	DRR::PRM1ST							
	CMD B LA EXP (BYTE0)							
5E	DRR::PRM1ST							
	CMD B LA EXP (BYTE1)							
5F	DRR::ACGEN0							
	CMD B LA EXP (BYTE2)							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(4) Byte28 = 37 (3/4)

	0	1	2	3	4	5	6	7
60	DRR::ACGEN0							
	CMD B LA EXP (BYTE3)							
61	DRR::ACGEN0							
	CMD B LA EXP (BYTE4)							
62	DRR::ACGEN0							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
63	DRR::ACGEN0							
	LA Read Value (Byte 0)							
64	DRR::ACGEN0							
	LA Read Value (Byte 1)							
65	DRR::ACGEN0							
	LA Read Value (Byte 2)							
66	DRR::ACGEN0							
	LA Read Value (Byte 3)							
67	DRR::ACGEN1							
	LA Read Value (Byte 4)							
68	DRR::ACGEN1							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	CMD PNT (Hi)	
69	DRR::ACGEN1							
	CMD PNT (Lo)							
6A	DRR::ACGEN1							
	DRSEQM							
6B	DRR::ACGEN1							
	PBSEQM							
6C	DRR::ACGEN1							
	DMISQMREG							
6D	DRR::ACGEN1							
	TRISQMREG							
6E	DRR::ACGEN1							
	P0TXSQM							
6F	DRR::DRERR6							
	P0RXSQM							

Byte27 = 8B (DRR CHK2) Byte40-7F Detail

(4) Byte28 = 37 (4/4)

	0	1	2	3	4	5	6	7
70	DRR::DRERR6 PITXSQM							
71	DRR::DRERR6 PITXSQM							
72	DRR::STATS							
	QDTA bit (1:QDTA)	(RSV)	MP#		LSI Rev.			
73	DRR::TPRMA2							
	MP Out ALARM	MP Out MESSAGE	MP Out CUDG3A FAIL	MP Out CUDG3B FAIL	LSIMP ALARM	LSIMP MESSAGE	LSIMP CUDG3A FAIL	LSIMP CUDG3B FAIL
74	DRR::TPRMA2							
	CMD A TRANSFER END	(RSV)	CMD A Exec	CMD A Active	CMD B TRANSFER END	(RSV)	CMD B Exec	CMD B Active
75	DRR::TPRMB2							
	CHK2	CHK2A detected	CHK2B detected	CHK2C detected	CHK2D detected	CHK1B detected	XFR ABORT detect	Force ERR detect
76	DRR::TPRMB2							
	ABT SEQ PARITY ERR (M)	ABT SEQ PARITY ERR (S)	P0 ABT CMP CHK ERR	P1 ABT CMP CHK ERR	PKID CHK ERR	(RSV)	(RSV)	(RSV)
77	DRR::DRMODE0							
	CBUF0/1 DATA0 PARITY ERR	CBUF0/1 DATA1 PARITY ERR	CBUF0/1 DATA2 PARITY ERR	CBUF0/1 DATA3 PARITY ERR	CBUF0/1 DATA4 PARITY ERR	CBUF0/1 DATA5 PARITY ERR	CBUF0/1 DATA6 PARITY ERR	CBUF0/1 DATA7 PARITY ERR
78	Micro Information Scheduled XFR LIST number							
79	Micro Information Scheduled XFR LIST number							
7A	Micro Information Finished XFR LIST number							
7B	Micro Information Finished XFR LIST number							
7C	Micro Information JOB Restart bit							
7D	Micro Information							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7E	Micro Information Error function result code							
7F	Micro Information CHK2 ERROR# (LOW)							

Byte27 = 8C (SVP Failure:SVP (PC)) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'C')			
28	Error information (See following pages)							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (*1)							
31								
32	Error information (See following pages)							
33								
34								
35								
36	Symptom code (x'FF8C')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (x'00')							
3B								
3C	Not used							
3D	Cylinder address (x'0000')							
3E								
3F	Head address (x'00')							

Byte27 = 8C (SVP Failure:SVP (PC)) (2/2)

	0	1	2	3	4	5	6	7
40	Error information (See following pages)							
7F								

*1: Error type code

- x'0000' : Logical inconsistency
- x'0001' : HEAP related error
- x'0002' : File related error
- x'0003' : LAN failure
- x'0004' : SSVP failure
- x'0005' : Windows failure
- x'0006' : CUDG detected error
- x'0009' : BOOT detected error
- x'000A' : SVP message
- x'000B' : Router failure
- x'000C' : Disable SVP reboot
- x'000D' : Stop SSB overwrite
- x'000E' : Stop SSBS overwrite
- x'000F' : Flush memory failure
- x'0015' : Battery failure
- x'0030' : Delay of time
- x'0031' : Warning of a stack
- x'0032' : Single task error
- x'0033' : SVP launcher error
- x'0034' : SVP reboot
- x'0035' : SVP reboot (WSAENOBUFF)
- x'0036' : Standby SVP fail
- x'0037' : SVP fail over
- x'0038' : SVP change refusal
- x'0039' : Ip address setting error
- x'003A' : Subnet mask setting error
- x'003B' : Virus detection error
- x'003C' : Virus detection error
- x'003D' : Virus detection error
- x'003E' : SVP environment error
- x'003F' : Situation observation error
- x'0040' : Dummy PS/Off starting (normal)

Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail

- (1) Logical inconsistency / HEEP / File / LAN / Windows failure / SVP message / Router failure / Disable SVP reboot / Stop SSB overwrite / Stop SSBS overwrite / Flash memory failure / Battery failure / Delay of time / Warning of a stack / Single task error / SVP launcher error / SVP reboot / SVP reboot (WSAENOBUFF) / Standby SVP fail / SVP fail over / SVP change refusal / Ip address setting error / Subnet mask setting error / Virus detection error / SVP environment error / Situation observation error / Dummy PS/Off starting (normal)

	0	1	2	3	4	5	6	7
28	Failed AP code (*1)							
29								
2A	Reserved							
2B								
2F	Error type code (*2)							
30								
31	Error code							
32								
33								
34								
35								
40	Failed Window class							
41								
47	Failed function name							
48								
49	Failed line number							
50								
5F	Error code							
60								
61	Reserved							
62								
63								
64								
65								
66								
67	Reserved							
68								
7F								

*1: Failed AP code

x'0000' : Dump task

x'0001' : DKU inline

x'0002' : Microprogram change (online)

x'0003' : Information

x'0004' : LOGOUT

x'0005' : PATH inline

x'0006' : Back ground

x'0007' : DIAG

x'0008' : Install

x'0009' : On-line

x'000A' : Micro-program change (off-line)

x'000B' : Special package change

x'000C' : Common function for communication

x'00FF' : Others

***2: Error type code**

x'0000' : Logical inconsistency
x'0001' : HEAP related error
x'0002' : File related error
x'0003' : LAN failure
x'0005' : Windows failure
x'000A' : SVP message
x'000B' : Router failure
x'000C' : Disable SVP reboot
x'000D' : Stop SSB overwrite
x'000E' : Stop SSBS overwrite
x'000F' : Flash memory failure
x'0015' : Battery failure
x'0030' : Delay of time
x'0031' : Warning of a stack
x'0032' : SVP launcher error
x'0033' : Single task error
x'0034' : SVP reboot
x'0035' : SVP reboot (WSAENOBUFF)
x'0036' : Standby SVP fail
x'0037' : SVP fail over
x'0038' : SVP change refusal
x'0039' : Ip address setting error
x'003A' : Subnet mask setting error
x'003B' : Virus detection error
x'003C' : Virus detection error
x'003D' : Virus detection error
x'003E' : SVP environment error
x'003F' : Situation observation error
x'0040' : Dummy PS/Off starting (normal)

Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail

(2) SSVP failure

	0	1	2	3	4	5	6	7
28	Failed AP code (x'00FF')							
29								
2A	Reserved							
2B								
2F	Error type code (x'0004')							
30								
31	Reserved							
32								
33	SSVP Error code							
34								
35								

40	SSVP IMPL Sequence						
41	Detect Code						
42	Part Code						
43	SSVP Error						
44							
45	Detail Information						
46							
47							
48							
49							
4A							
4B							
4C							
4D							
4E							
4F							

50	Reserved						
51							
52							
53							
7F							

Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail

(3) CUDG detected error

	0	1	2	3	4	5	6	7
28	'CUDG ERR' (x'43 55 44 47 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F	Error type code (x'0006)							
30								
31								
32	Error code (x'00000000')							
33								
34								
35								
40	Error occurred time (year)							
41	Error occurred time (month)							
42	Error occurred time (day)							
43	Error occurred time (hour)							
44	Error occurred time (minute)							
45	Error occurred time (second)							
46	Report MPID							
47	Test object							
48	Test type (x'01' : CUDG3, x'10' : LCDG3/FCDG3/PCDG3)							
49	Failed LCP number							
4A	Test contents							
4B	Test contents							
4C	Error code							
4D	Error code							
4E	Not used							
4F	Not used							
50	Cache module group number (slave 1)							
51	Cache module group number (slave 1)							
52	Cache module group number (slave 1)							
53	Cache module group number (slave 1)							
54	Cache module group number (slave 2)							
55	Cache module group number (slave 2)							
56	Cache module group number (slave 2)							
57	Cache module group number (slave 2)							
58	PCB location (main platter)							
59	PCB location (main platter)							
5A	PCB location (main platter)							
5B	PCB location (cache platter 1)							
5C	PCB location (cache platter 1)							
5D	PCB location (cache platter 2)							
5E	PCB location (cache platter 2)							
5F	Run option							
60	Additional information (*1)							
7F								

Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail

*1: Additional information (for LCDG3/FCDG3/PCDG3 only)

	0	1	2	3	4	5	6	7
60	Reserved							
61								
62								
63								
64	Record type (x'0F' : LCP IMPL diagnostic, x'10' : HTP IMPL diagnostic, x'20' : PCDG)							
65	Reserved							
66	MP ID (of CHP : x'10'~x'17')							
67	LCP ID (LCP#A : x'80'~LCP#H : x'01')							
68	Error code (*2)							
69	Process type (*3)							
6A	Error status 1 (Record type = x'0F' (*4), x'10' (*6), x'20' (*7))							
6B								
6C	Error status 2 (*5)							
6D								
6E								
6F								
70								
71								
72								
73								
74								
7F								

*2: Error code

Code	Meaning	Function	Remarks
0A	LCDG3'/FCDG3' load error	LCDG3'/FCDG3'	Make status in loading process
10	Memory clear and test error	FCDG3'	Set LCP address into error status 1
18	LCDG3/FCDG3/PCDG3 MP detected error/LCDG3' (Memory clear and test error)	LCDG3/FCDG3/PCDG3/LCDG3'	Detail information in error status 1 and 2
1A	LCDG3/FCDG3 load error	LCDG3/FCDG3	Make status in loading process
1F	LCDG3' error before LCP loading	Main loading	LCDG 3 information in status
20	SCAN detected error	All	MP detected error
2A	LCP main load error	Main loading	Make status in loading process
30	LCP detected error	LCDG3'	Detail information in error status 1 and 2
		LCDG3	
FF			

*3: Process type

Code	Process	Remarks
01	LCDG3/LCDG3'/FCDG3'	Set loading error status when a loading error occurred.
02	FCDG3/PCDG3	Set loading error status when a loading error occurred.
03	Loading	

*4: Error status 1

Code	Meaning
0011	Waiting timeout for LCDG3' set REQ
0012	No notification after LCDG3' set REQ
0013	Waiting timeout for LCDG3' clear REQ
0014	No notification after LCDG3' clear REQ
0015	LCDG3' Error Status detection
0021	Waiting timeout for CRC check set REQ
0022	No notification after CRC check set REQ
0023	Waiting timeout for CRC check clear REQ
0024	No notification after CRC check clear REQ
0025	CRC check Error Status detection
0031	Waiting timeout for Internal diagnosis set REQ
0032	No notification after Internal diagnosis set REQ
0033	Waiting timeout for Internal diagnosis clear REQ
0034	No notification after Internal diagnosis clear REQ
0035	Error Status occurred in Internal diagnosis
0041	Waiting timeout for Communication diagnosis set REQ after diagnosis
0042	No notification after Communication diagnosis set REQ after diagnosis
0043	Waiting timeout for Communication diagnosis clear REQ after diagnosis
0044	No notification after Communication diagnosis clear REQ after diagnosis
0046	Waiting timeout for Communication diagnosis set REQ at the time of starting
0047	CHK1 occurred in Communication diagnosis at the time of starting
0048	No notification after Communication diagnosis set REQ at the time of starting
0049	Waiting timeout for Communication diagnosis clear REQ at the time of starting
004a	Error occurred in Communication diagnosis LCP Check diagnosis
004b	Error occurred in Communication diagnosis Wire Path diagnosis (1)
004c	Error occurred in Communication diagnosis Wire Path diagnosis (2)
004d	Error occurred in Communication diagnosis Micro Transmission diagnosis
004e	Error occurred in Communication diagnosis Hard Ware Transmission diagnosis
004f	Error occurred in Communication diagnosis Hot Line Transmission diagnosis
0051	Waiting timeout for LCDG3'(CS memory clear) set REQ
0052	No notification after LCDG3'(CS memory clear) set REQ
0053	Waiting timeout for LCDG3'(CS memory clear) clear REQ
0054	No notification after LCDG3'(CS memory clear) clear REQ
0055	LCDG3'(CS memory clear) Error Status detection

*5: Error status 2

- (1) When process type = x'01' and error code $\geq$ x'30' and error status 1 $\geq$ x'1000', LCP error code for LCDG3'.
- (2) When process type = x'02' and error code $\geq$ x'30' and error status 1 $\geq$ x'1000', LCP error code for LCDG3'.
- (3) When process type = x'02' and error code = x'18',
Byte 70-73 : Error register address
Byte 74-77 : Active register value
Byte 78-7B : Expected register value
Byte 7C-7F : Return sequence
- (4) When process type = x'01' and error code = x'18',
Byte 70-73 : Error register address
Byte 74-77 : Expected register value
Byte 78-7B : Active register value
Byte 7C-7F : Return sequence
- (5) When other case, this value is invalid.

*6: Error status 1 (Record type (Byte 64) = x'10' : FCDG3)

Code	Meaning
0101	FCDG3' Waiting for end of diagnosis timeout
0102	FCDG3' SVC I/F error
0103	FCDG3' ASTS Initialization Error
0104	FCDG3' Invalid Parameter(s) Received
0105	FCDG3' CHK1B Detected
0110	FCDG3' Failure detected in preliminary diagnosis
0111	FCDG3' Failure detected in Memory diagnosis (Fixed pattern)
0112	FCDG3' Failure detected in Memory diagnosis (Sequential access)
0113	FCDG3' Failure detected in Memory diagnosis (Byte increment data)
0114	FCDG3' Failure detected in CS access diagnosis
08xx	FCDG3 FPP Processor Diagnosis
09xx	FCDG3 FPP → CHP Transfer diagnosis (Pointer Addressing Area)
0Axx	FCDG3 FPP → CHP Transfer diagnosis (Direct Addressing COMM Area)
0Bxx	FCDG3 FPP → CHP Transfer diagnosis (Direct Addressing DXBF Area)
0Cxx	FCDG3 CHP → FPP Transfer diagnosis
0Dxx	FCDG3 Hot Line Communication diagnosis
0Exx	FCDG3 IntReq diagnosis
0Fxx	FCDG3 Info Transmission diagnosis
10xx	FCDG3 Signal Diagnosis
11xx	FCDG3 Transfer diagnosis
12xx	FCDG3 Link Diagnosis/LSI Rev. Check
13xx	FCDG3 Synchronized Transfer diagnosis
20xx	Bus Bridge Transfer diagnosis
21xx	CHP ↔ FPP Transfer diagnosis
22xx	DXBF ↔ FPP Transfer diagnosis

xx: 01	ASTS Initialization Error	40	Compare Error
02	MPCK Detected	50	MMC Status Error
03	FPP Illegal Access Detected	51	XFS Status Error
10	SVC I/F Error	52	HTPSTS Status Error
20	Time-out	53	ONLSTS Status Error
21	Frame transmission time-out	60	Frame transmission Error
22	Frame reception time-out	61	Burst transmission Error
23	DXBF Access Time-out	70	Invalid Parameter(s) Received
24	XFS Time-out	80	MCK Detected
25	PXSTS Time-out	81	CHK1B or CHK2AB Detected
30	CHK1B Detected	90	DMA Write Transfer error
31	CHK2 Detected	91	DMA Read Transfer error
32	CHK2E Detected		

*7: Error status 1 (Record type = x'20' : PCDG3)

Code	Meaning
20xx	Micro Code Load Process (into Xeon)
21xx	Communication Memory Diagnosis
22xx	Door Bell Diagnosis
23xx	Direct Mapping Area Memory Diagnosis
24xx	DMA Diagnosis
25xx	NIC Diagnosis
26xx	Full Memory Access Diagnosis
27xx	ECC Diagnosis
28xx	Memory Access Diagnosis
29xx	Loop Back Diagnosis (PCDG3 Not Supported)

xx: 01 Aborted Error in Pentium

- 10 ECC Error
- 20 Time-out
- 21 Time-out in Door Bell Send
- 22 Time-out in Door Bell Received
- 23 Door Bell Clear Error
- 30 CHK1B Detected
- 31 CHK2 Detected
- 32 CHK2E Detected
- 40 Compare Error
- 60 Data Transmit Error
- 61 Data Transmit/Receive Time-out
- 62 Illegal Parameter(s) Detected in Data Transmit
- 63 Packet receive error
- 70 Illegal parameter(s) Detected (I/F Error)
- 71 Illegal parameter(s) Detected (Pentium Reported)
- 81 NIC : Data Parity Error
- 82 NIC : Parity Error
- 83 NIC : System Error
- 84 NIC : Aborted Error
- 85 NIC : Target Abort Error
- 8F NIC : etc.
- 90 Interrupted Clear error
- 91 Interrupted Set error

Byte27 = 8C (SVP Failure) Byte28-35, 40-7F detail

(4) BOOT detected error

	0	1	2	3	4	5	6	7
28	'BOOT ERR' (x'42 4F 4F 54 20 45 52 52')							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30	Error type code (x'0009')							
31								
32	Error code (x'00000000' X:Processor number)							
33								
34								
35								
40	Error detection point							
4F								
50	Reserved							
51								
52								
53								
54	Error code for loading error							
55								
56								
57	Error code of internal interface							
58								
59								
5A								
5B	Error code of initialization function (*1)							
5C								
5D								
5E								
5F	Reserved							
60								
61								
62	Reserved							
63								
64								
65								
66								
67								
68	Reserved							
69								
6A								
6B								
6C								
6D								
6E	Reserved							
6F								
70								
71								
72								
73								
74	Reserved							
75								
76								
77								
78								
79								
7A	Reserved							
7B								
7C								
7D								
7E								
7F								

*1: Error code of initialization function.

x'31000041' : Forcible start with jumper pin.

x'40000041' : Load module ID in FM is incorrect.

(FM writing was failed or load module is incorrect)

x'42000041' : Sum check error of FM.

(Failure occurred at FM writing or input / output error of FM occurred)

x'48000041' : This MP has the micro-programs which type is different from the PCB name.

(In the MP-Install procedure, the package may have been set which type is different from one that is specified in the define configuration.)

x'56000041' : Sum check error of the load module.

(SVP Failure occurred)

Others : Loading for the load module impossible.

(The load module was incorrect)

Byte27 = 8D (Power Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'D')			
28	Hardware information (See following pages)							
31								
32	Not used							
33								
34	PCB							
35	SSID of self subsystem (low order)							
36	Sympton code (x'FF8D' : LDEV type = DKU87I/x'EF8D' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 8D (Power Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte27 = 8D (DKU/HDU Power Failure) Hard information

	0	1	2	3	4	5	6	7
28	Subcode (x'80' : DKA detected DKU/HDU Power Failure)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Hardware Information							
	ACDCALARM Value							
2B	Micro Information							
	Power Failure Status							
2C	Micro Information							
	SM control area information							
2D	Micro Information							
	SM control area information							
2E	Micro Information							
	SM control area information							
2F	Micro Information							
	SM control area information							
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8D (DKU/HDU Power Failure) Hard information

	0	1	2	3	4	5	6	7
28	Subcode (x'40' : DKC Power Failure)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Hardware Information							
	ACOFDTCT Value							
2B	Micro Information							
	Power Failure Status							
2C	Micro Information							
	SM control area information							
2D	Micro Information							
	SM control area information							
2E	Micro Information							
	SM control area information							
2F	Micro Information							
	SM control area information							
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8D (DKU/HDU Power Failure) Hard information

	0	1	2	3	4	5	6	7
28	Subcode (x'90' : DKA detected DKU/HDU Power Recover)							
29	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
2A	Hardware Information							
	ACDCALARM Value							
2B	Micro Information							
	Power Failure Status							
2C	Micro Information							
	SM control area information							
2D	Micro Information							
	SM control area information							
2E	Micro Information							
	SM control area information							
2F	Micro Information							
	SM control area information							
30	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
31	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '00'							
41	(Not used)							
42	Y_Wing::BIUOCBERRINFO2 (Not used)							
43	Y_Wing::BIUOCBERRINFO2 (Not used)							
44	Y_Wing::BIUOCBERRINFO2							
	Bus Error Info				Bus Error Cmd			
45	Y_Wing::BIUOCBERRINFO2							
	Bus Error Cmd				Bus Error Addr2			
46	Y_Wing::BIUOCBERRINFO1 Bus Error Addr1							
47	Y_Wing::BIUOCBERRINFO1 Bus Error Addr1							
48	Y_Wing::BIUOCBERRINFO1 Bus Error Addr1							
49	Y_Wing::BIUOCBERRINFO1 Bus Error Addr1							
4A	Y_Wing::SDERRADDR ERRADD							
4B	Y_Wing::SDERRADDR ERRADD							
						(Not used)	(Not used)	
4C	Y_Wing::SDERRADDR ERRADD							
4D	Y_Wing::SDERRADDR ERRADD							
4E	Y_Wing::SDERRSYNHI (Not used)							
4F	Y_Wing::SDERRSYNHI (Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A) (3/5)

	0	1	2	3	4	5	6	7
50	Y Wing::SDERRSYNHI (Not used)							
51	Y Wing::SDERRSYNHI ES0HL							
52	Y Wing::SDERRSYNLO (Not used)							
53	Y Wing::SDERRSYNLO (Not used)							
54	Y Wing::SDERRSYNLO (Not used)							
55	Y Wing::SDERRSYNLO ES0LL							
56	Y Wing::GPIO DATA2							
	GPD31	GPD30	GPD29	GPD28	GPD27	GPD26	GPD25	GPD24
57	Y Wing::GPIO DATA2							
	GPD23	GPD22	GPD21	GPD20	GPD19	GPD18	GPD17	GPD16
58	Y Wing::GPIO DATA3							
	GPD47	GPD46	GPD45	GPD44	GPD43	GPD42	GPD41	GPD40
59	Y Wing::GPIO DATA3							
	GPD39	GPD38	GPD37	GPD36	GPD35	GPD34	GPD33	GPD32
5A	Y Wing::RFCSC RFCSC							
5B	Y Wing::RFCSC RFCSC							
5C	Y Wing::TFCSC TFCSC							
5D	Y Wing::TFCSC TFCSC							
5E	Y Wing::FM STATUS (Not used)							
5F	Y Wing::FM STATUS (Not used)							
							FM1 BSY	FM0 BSY

Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A) (4/5)

	0	1	2	3	4	5	6	7
60	Y_Wing::ERROR STATUS							
	(Not used)	FMWP ERR	FM WERR	SEQ CNT PERR	SEQ CNT ERR	SEQ WERR	SEQ PERR	SEQ PERR
61	Y_Wing::ERROR STATUS							
	YWING ERR	(Not used)	FMRD AERR	EMRD PERR	(Not used)	LCE2 ERR	LDAT PERR	LADR PERR
62	Y_Wing::HardRESETenable							
	(Not used)	(Not used)	(Not used)	SEQCNT PERR EN	SEQ CNT ERR EN	SEQ WERR EN	SEQ RERR EN	SEQ PERR EN
63	Y_Wing::HardRESETenable							
	YWING ERR EN	(Not used)	FMRD AERR EN	FMRD PERR EN	(Not used)	LCE2 ERR EN	LDAT PERR EN	LADR PERR EN
64	Y_Wing::PLD CONTROL							
	DEBUG MD	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	FMWP REG
65	Y_Wing::PLD CONTROL							
	SEQ ERR MODE	BT OK	ERR CLK	(Not used)	BYPS CLKS	(Not used)	LRDY EN	RST REQ
66	Y_Wing::BadVAddr							
	BadVAddr							
67	Y_Wing::BadVAddr							
	BadVAddr							
68	Y_Wing::BadVAddr							
	BadVAddr							
69	Y_Wing::BadVAddr							
	BadVAddr							
6A	Y_Wing::BadVAddr							
	BadVAddr							
6B	Y_Wing::BadVAddr							
	BadVAddr							
6C	Y_Wing::BadVAddr							
	BadVAddr							
6D	Y_Wing::BadVAddr							
	BadVAddr							
6E	Y_Wing::STATUS							
	XX	CU(2:0)			(Not used)	FR	(Not used)	DS
6F	Y_Wing::STATUS							
	DS							

Byte27 = 8E & Byte34 = X0 & Byte28 = 20 (CHK1A) (5/5)

	0	1	2	3	4	5	6	7
70	Y Wing::STATUS IM(7:0)							
71	Y Wing::STATUS							
	KX	SX	UX	KSU		ERL	EXL	IE
72	Y Wing::Reason							
	BD	(Not used)	CE		(Not used)			
73	Y Wing:: Reason (Not used)							
74	Y Wing:: Reason IP(7:0)							
75	Y Wing:: Reason							
	(Not used)	ExcCode					(Not used)	(Not used)
76	Y Wing::EPC EPC							
77	Y Wing::EPC EPC							
78	Y Wing::EPC EPC							
79	Y Wing::EPC EPC							
7A	Y Wing::EPC EPC							
7B	Y Wing::EPC EPC							
7C	Y Wing::EPC EPC							
7D	Y Wing::EPC EPC							
7E	Y Wing::PARITY ERROR Parity							
7F	Y Wing::CACHE ERROR							
	ER	EC	ED	ET	ES	EE	EB	(Not used)

Format 8, Message E (processor failure : CHK1A)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'20' : PLD/FM CHK1A)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	Y-Wing:: PLD_ERR STATUS							
	(Not used)	FMWP ERR	FM WERR	SEQ CNT PERR	SEQ CNT ERR	SEQ WERR	SEQ RERR	SEQ PERR
2E	Y-Wing:: PLD_ERR STATUS							
	YWING ERR	(Not used)	FM RD AERR	FM RD PERR	(Not used)	LCE2 ERR	LDAT PERR	LADR PERR
2F	(Not used)							
30	(Not used)							
31	(Not used)							
32	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'23'							
41	CMPA::CHK1 ADRS							
	Not used	DBDE4M_DTPER	DBDE4N_DTPER	WLACHK_LA1CPM_ERR	WLACHK_LA2CPM_ERR	SLRCCHK_WSLRCCMPER	SLRCCHK_WSLRCPEER	SLRCCHK_SLRCDTPEER
42	CMPA::CHK1 ADRS							
	DTSEL_DTPER	ACGEN_DTPER	RLACHK_LA1CMPER_R	RLACHK_LA2CMPER_R	RLACHK_DTPER	SLRCCHK_RSLRCCMPER	SLRCCHK_RSLRCPEER	RPLRCCHK_PLRCDTPEER
43	CMPA::CHK1 ADRS							
	Not used	Not used	Not used	DBD4M_VE_RIFYERR	DBD4N_VE_RIFYERR	RPLRCCHK_CPLRCCOPEER	RPLRCCHK_DTENDER_R	Not used
44	CMPA::CHK1 ADRS							
	ST1_DTPER	ST1CHK_STOCPMER	DBD1M_C2_UCRCTER_R	DBD1M_C2_ADRERR	DBD1M_C2_ECCEER	DBD1N_C2_UCRCTER	DBD1N_C2_ADRERR	DBD1N_C2_ECCERR
45	CMPA::CHK1 ADRS							
	Not used	Not used	Not used	Not used	DBD3X_DTPER	ST0_DTPER	ST0CHK_RTNCODE_ERR_ST6	ST0CHK_RTNCODE_ERR_ST7
46	CMPA::RXRX ERR STS							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPEER3	CMPEER2	CMPEER1	CMPEER0
47	CMPA::RXRX ERR STS							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
48	CMPA::RXRX ERR STS							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
49	CMPA::ROTX ERR STS							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
4A	CMPA::MPASTS							
	Not used	Not used	FCA_CHK1_B_4MP	DRR_CHK1_B_4MP	PCI_CHK1_B_Q	PCI_CHK1_B_M	FCA_CHK1_B_8/4MP	DRR_CHK1_B_8/4MP
4B	CMPA::MPASTS							
	BSINT	CHK3H_SNMST	CHK3H_SNSLV	CHK1B_FM	Not used	Not used	CHK1B_XR	MPINT
4C	CMPA::BMD							
	Not used	COPY_CD	SMDA_CD	SAMDB_CD	C0DS	C1DS	D0DS	D1DS
4D	CMPA::BMD							
	Not used	COPY_AB	SMDA_AB	SAMDB_AB	A0DS	A1DS	B0DS	B1DS
4E	CMPA::MSTCHK3 STS							
	Not used	Not used	XR_CMP	XR_SMRX	XR_SMTX	XR_RCV	XR_FIFO	XR_PCI
4F	CMPA::MSTCHK3 STS							
	A0_CTLER	A1_CTLER	B0_CTLER	B1_CTLER	C0_CTLER	C1_CTLER	D0_CTLER	D1_CTLER

Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	CMPA::MSTCHK3 STS							
	A0_DTER	A1_DTER	B0_DTER	B1_DTER	C0_DTER	C1_DTER	D0_DTER	D1_DTER
51	CMPA::MSTCHK3 STS							
	A0 TIMEOUT	A1 TIMEOUT	B0 TIMEOUT	B1 TIMEOUT	C0 TIMEOUT	C1 TIMEOUT	D0 TIMEOUT	D1 TIMEOUT
52	CMPA::CTL1							
	EN2R_CD	NOLOCK_ CD	BUNSAN(1) CD	BUNSAN(0) CD	Not used	Not used	Not used	Not used
53	CMPA::CTL1							
	EN2R_AB	NOLOCK_ AB	BUNSAN(1) AB	BUNSAN(0) AB	Not used	Not used	Not used	Not used
54	CMPA::PASSWORD CHK1B							
	Not used	Not used	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWDCHK1 B
55	CMPA::MPID							
	PKID7	PKID6	PKID5	PKID4	Not used	PKID3	PCINUM	XRNUM
56	CMPA::DIR ADR							
	C1 SQP3 ENCERR(M)	C1 SQP3 ENCERR(S)	C1 SQP3 DECERR(M)	C1 SQP3 DECERR(S)	C1 SQP3 SEQPER(M)	C1 SQP3 SEQPER(S)	Not used	Not used
57	CMPA::DIR ADR							
	C1 TXSEQ ENCERR(M)	C1 TXSEQ ENCERR(S)	C1 TXSEQ DECERR(M)	C1 TXSEQ DECERR(S)	C1 TXSEQ SEQPER(M)	C1 TXSEQ SEQPER(S)	C1 TXXSE Q_HWERR (M)	C1 TXXSE Q_HWERR (S)
58	CMPA::DIR ADR							
	C1 TXXSE Q_ENCERR (M)	C1 TXXSE Q_ENCERR (S)	C1 TXXSE Q_DECERR (M)	C1 TXXSE Q_DECERR (S)	C1 TXXSE Q_SEQPER (M)	C1 TXXSE Q_SEQPER (S)	C1 DBP0 XPTPER	C1 DBP0 XXPTPER
59	CMPA::DIR ADR							
	C1 DBP0 YPTPER	C1 DBP0 YYPT PERR	C1 DBP3 XPT PERR	C1 DBP03 XXPT PER	C1 TXSEQ HWERR(M)	C1 TXSEQ HWERR(S)	C1 DBP4 XPT PERR	C1 DBP4 XXPT PERR
5A	CMPA::DIR ADR							
	C1 DBP4 YPT PERR	C1 DBP4 YYPT PERR	C1 DBP1 XPT PERR	C1 DBP1 XXPT PERR	C1 DBP1 YPT PERR	C1 DBP1 YYPT PERR	C1 WAITC NT_TIMCM PPER	C1 STTCN T_TIMCMP PER
5B	CMPA::LCK ADR							
	Not used							
5C	CMPA::LCK ADR							
	Not used							
5D	CMPA::LCK ADR							
	Not used							
5E	CMPA::LCK ADR							
	Not used							
5F	CMPA::LCK ADR							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	CMPA::BC ADR							
	Not used							
61	CMPA::BC ADR							
	Not used							
62	CMPA::BC ADR							
	Not used							
63	CMPA::BC ADR							
	Not used	Not used	Not used	PRM1 SAMECLER M	PRM1 SAMEER_M	SQD2 PRER R(M)	SQD2 PRER R(S)	PRM0 PRM 64EOERR M
64	CMPA::BC ADR							
	WADB0 MPIDERR_ M	WADB0 LMCNTCM PER0 M	Not used	WADB0 LM CNTCMPER 2 M	DCMDSEQ MATER(M)	DCMDSEQ MBTER(M)	DCMDSEQ MATER(S)	DCMDSEQ _MBTER(S)
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 40/50 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	CMPA::PCIERRO/1							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	CMPA::PCIERRO/1							
	TGTRXBUF 1 ERR	TGTRXBUF 2 ERR	TGTSEQ_ ERR	TGTTXBU0 ERR	TGTTXBUF 1 ERR	XR_MSTSE Q ERR	TGTTXBU0 ERR	PXRXBUF0 ERR
7E	CMPA::PCIERRO/1							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0 ERR	XPCMDBUF ERR	XPRXBUF0 ERR	MSTTXBUF 0 ERR
7F	CMPA::PCIERRO/1							
	MSTTXBUF 1 ERR	MSTTXBUF2 ERR	MSTRXBUF 0 ERR	PXRXBUF0 ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	Not used	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'40' : MPA XR CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	CMPA::MPA STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2D	CMPA::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	Not used	Not used	CHK1B XR	MPINT
2E	CMPA::PASSWORD CHK1B							
	Not used	Not used	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWD CHK1B
2F		CMPA::ROTX ERR STS 1						
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
30	CMPA::ROTX ERR STS 1							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPER3	CMPER2	CMPER1	CMPER0
31	CMPA::ROTX ERR STS 1							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
32	CMPA::ROTX ERR STS 1							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'50' : MPA XR CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	CMPA::MPA STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2D	CMPA::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	Not used	Not used	CHK1B XR	MPINT
2E	CMPA::PASSWORD CHK1B							
	Not used	Not used	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWD CHK1B
2F	CMPA::ROTX ERR STS 1							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
30	CMPA::ROTX ERR STS 1							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPER3	CMPER2	CMPER1	CMPER0
31	CMPA::ROTX ERR STS 1							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
32	CMPA::ROTX ERR STS 1							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0

Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'24'							
41	CMPA::FMSEQ ERRREG							
	Not used							
42	CMPA::FMSEQ ERRREG							
	Not used							
43	CMPA::FMSEQ ERRREG							
	ILLACS_ MP3	DECODE_ MP3	ENCODE_ MP3	CST_MP3	ILLACS_ MP2	DECODE_ MP2	ENCODE_ MP2	CST_MP2
44	CMPA::FMSEQ ERRREG							
	ILLACS_ MP1	DECODE_ MP1	ENCODE_ MP1	CST_MP1	ILLACS_ MP0	DECODE_ MP0	ENCODE_ MP0	CST_MP0
45	CMPA::FMSEQCNT ERRREG							
	Not used	Not used	ACC_TOUT_ M	WEH_TOUT_ M	WP_TOUT_ MP3	ACC_PER_ MP3	WEH_PER_ MP3	WP_PER_ MP3
46	CMPA::FMSEQCNT ERRREG							
	Not used	Not used	ACC_TOUT_ M	WEH_TOUT_ M	WP_TOUT_ MP2	ACC_PER_ MP2	WEH_PER_ MP2	WP_PER_ MP2
47	CMPA::FMSEQCNT ERRREG							
	Not used	Not used	ACC_TOUT_ M	WEH_TOUT_ M	WP_TOUT_ MP1	ACC_PER_ MP1	WEH_PER_ MP1	WP_PER_ MP1
48	CMPA::FMSEQCNT ERRREG							
	Not used	Not used	ACC_TOUT_ M	WEH_TOUT_ M	WP_TOUT_ MP0	ACC_PER_ MP0	WEH_PER_ MP0	WP_PER_ MP0
49	CMPA::CMP ERRREG							
	Not used	Not used	Not used	Not used	FMSEQ_MP 3	FSEQ2_MP3	FSEQ1_MP3	FMARB_MP 3
4A	CMPA::CMP ERRREG							
	Not used	Not used	Not used	Not used	FMSEQ_MP2	FSEQ2_MP2	FSEQ1_MP2	FMARB_MP2
4B	CMPA::CMP ERRREG							
	Not used	Not used	Not used	Not used	FMSEQ_MP1	FSEQ2_MP1	FSEQ1_MP1	FMARB_MP1
4C	CMPA::CMP ERRREG							
	Not used	Not used	Not used	Not used	FMSEQ_MP0	FSEQ2_MP0	FSEQ1_MP0	FMARB_MP0
4D	CMPA::FMARB ERRREG							
	Not used							
4E	CMPA::FMARB ERRREG							
	Not used							
4F	CMPA::FMARB ERRREG							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	CMPA::FMARB ERRREG							
	Not used	Not used	Not used	Not used	CST_MP0	CST_MP1	CST_MP2	CST_MP3
51	CMPA::FREGSEQ2 ERRREG							
	Not used							
52	CMPA::FREGSEQ2 ERRREG							
	Not used							
53	CMPA::FREGSEQ2 ERRREG							
	Not used							
54	CMPA::FREGSEQ2 ERRREG							
	Not used	Not used	Not used	Not used	CST_MP0	CST_MP1	CST_MP2	CST_MP3
55	CMPA::MPi ERRSTS0							
	Not used							
56	CMPA::MPi ERRSTS0							
	BNKWR 2ERR7	BNKWR 2ERR6	BNKWR2 ERR5	BNKWR2 ERR4	BNKWR2 ERR3	BNKWR2 ERR2	BNKWR 2ERR1	BNKWR 2ERR0
57	CMPA::MPi ERRSTS0							
	BNKWR 1ERR7	BNKWR 1ERR6	BNKWR 1ERR5	BNKWR 1ERR4	BNKWR 1ERR3	BNKWR 1ERR2	BNKWR 1ERR1	BNKWR 1ERR0
58	CMPA::MPi ERRSTS0							
	BNKWR 0ERR7	BNKWR 0ERR6	BNKWR 0ERR5	BNKWR 0ERR4	BNKWR 0ERR3	BNKWR 0ERR2	BNKWR 0ERR1	BNKWR 0ERR0
59	CMPA::MPi ERRSTS0							
	Not used							
5A	CMPA::MPi ERRSTS0							
	Not used	Not used	Not used	Not used	Not used	Not used	WRPRTCT	JISOERR
5B	CMPA::MPi ERRSTS0							
	BNKRD 1ERR7	BNKRD 1ERR6	BNKRD 1ERR5	BNKRD 1ERR4	BNKRD 1ERR3	BNKRD 1ERR2	BNKRD 1ERR1	BNKRD 1ERR0
5C	CMPA::MPi ERRSTS0							
	BNKRD 0ERR7	BNKRD 0ERR6	BNKRD 0ERR5	BNKRD 0ERR4	BNKRD 0ERR3	BNKRD 0ERR2	BNKRD 0ERR1	BNKRD 0ERR0
5D	CMPA::MPi FREGBUF ERR							
	Not used							
5E	CMPA::MPi FREGBUF ERR							
	Not used							
5F	CMPA::MPi FREGBUF ERR							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	CMPA::MPi FREGBUF ERR							
	DT(3)	DT(2)	DT(1)	DT(0)	ADR(3)	ADR(2)	ADR(1)	ADR(0)
61	CMPA::MPi FREGBUF ERR							
	Not used							
62	CMPA::MPi FREGBUF ERR							
	Not used	Not used	Not used	Not used	RDDT_PER (3)	RDDT_PER (2)	RDDT_PER (1)	RDDT_PER (0)
63	CMPA::MPi FREGBUF ERR							
	Not used	ADOUT_ PER(3)	ADOUT_ PER(2)	ADOUT_ PER(1)	ADOUT_ PER(0)	DTOUT_ PER	CMP_DT	CMP ADRCMP
64	CMPA::MPi FREGBUF ERR							
	Not used	Not used	CMP_OE1	CMP_OE0	Not used	Not used	CMP_CS1	CMP_CS1
65	CMPA::MPi FREGSEQ1 ERR							
	Not used							
66	CMPA::MPi FREGSEQ1 ERR							
	Not used							
67	CMPA::MPi FREGSEQ1 ERR							
	Not used							
68	CMPA::MPi FREGSEQ1 ERR							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	CST
69	CMPA::WR BNK EXL SET							
	MP0BN K0SET	MP0BN K1SET	MP0BN K2SET	MP0BN K3SET	MP0BN K4SET	MP0BN K5SET	MP0BN K6SET	MP0BN K7SET
6A	CMPA::WR BNK EXL SET							
	MP1BN K0SET	MP1BN K1SET	MP1BN K2SET	MP1BN K3SET	MP1BN K4SET	MP1BN K5SET	MP1BN K6SET	MP1BN K7SET
6B	CMPA::WR BNK EXL SET							
	MP2BN K0SET	MP2BN K1SET	MP2BN K2SET	MP2BN K3SET	MP2BN K4SET	MP2BN K5SET	MP2BN K6SET	MP2BN K7SET
6C	CMPA::WR BNK EXL SET							
	MP3BN K0SET	MP3BN K1SET	MP3BN K2SET	MP3BN K3SET	MP3BN K4SET	MP3BN K5SET	MP3BN K6SET	MP3BN K7SET
6D	CMPA::WR BNK EXL SET							
	MP0BN K0SET	MP0BN K1SET	MP0BN K2SET	MP0BN K3SET	MP0BN K4SET	MP0BN K5SET	MP0BN K6SET	MP0BN K7SET
6E	CMPA::WR BNK EXL SET							
	MP1BN K0SET	MP1BN K1SET	MP1BN K2SET	MP1BN K3SET	MP1BN K4SET	MP1BN K5SET	MP1BN K6SET	MP1BN K7SET
6F	CMPA::WR BNK EXL SET							
	MP2BN K0SET	MP2BN K1SET	MP2BN K2SET	MP2BN K3SET	MP2BN K4SET	MP2BN K5SET	MP2BN K6SET	MP2BN K7SET

Byte27 = 8E & Byte34 = X0 & Byte28 = 41/51 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	CMPA::RD BNK EXL SET							
	MP3BN K0SET	MP3BN K1SET	MP3BN K2SET	MP3BN K3SET	MP3BN K4SET	MP3BN K5SET	MP3BN K6SET	MP3BN K7SET
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	CMPA::PCIERRO/1							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	CMPA::PCIERRO/1							
	TGTRXBU F1 ERR	TGTRXBUF 2 ERR	TGTSEQ_ ERR	TGTTXBUF 0 ERR	TGTTXBUF 1 ERR	XR_MSTSE Q ERR	TGTTXBUF 0 ERR	PXRXBUF0 ERR
7E	CMPA::PCIERRO/1							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBU_ ERR	XPCMDBUF ERR	XPRXBUF0 ERR	MSTTXBUF 0 ERR
7F	CMPA::PCIERRO/1							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF 0 ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	Not used	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'41' : FM CHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	CMPA::MPA_STS							
	Not used	Not used	FCA INT 4MP	DRR INT 4MP	Not used	Not used	FCA INT B 8/4MP	DRR INT B 8/4MP
2D	CMPA::MPA_STS							
	Not used	Not used	FCA CHK2 4MP	DRR CHK2 4MP	Not used	Not used	FCA CHK2 B 8/4MP	DRR CHK2 B 8/4MP
2E	CMPA::MPA_STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2F	CMPA::MPA_STS							
	BSINT	CHK3 HSNMST	CHK3 HSNSLV	CHK1BFM	Not used	Not used	CHK1BXR	MPINT
30	CMPA::BSINTSTS							
	Not used	Not used	Not used	Not used	Not used	Not used	LL INT	SCINT A
31	CMPA::BSINTSTS							
	SCINT B	SCINT C	SCINT D	BC0	BC1	BC2	BC3	BC4
32	CMPA::BSINTSTS							
	BC5	BC6	BC7	BCREQ	BCINT A	BCINT B	BCINT C	BCINT D

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'51' : FM CHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	CMPA::MPA_STS							
	Not used	Not used	FCA INT 4MP	DRR INT 4MP	Not used	Not used	FCA INT B 8/4MP	DRR INT B 8/4MP
2D	CMPA::MPA_STS							
	Not used	Not used	FCA CHK2 4MP	DRR CHK2 4MP	Not used	Not used	FCA CHK2 B 8/4MP	DRR CHK2 B 8/4MP
2E	CMPA::MPA_STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2F	CMPA::MPA_STS							
	BSINT	CHK3H SNMST	CHK3H SNSLV	CHK1B FM	Not used	Not used	CHK1B XR	MPINT
30	CMPA::BSINTSTS							
	Not used	Not used	Not used	Not used	Not used	Not used	LL INT	SCINT A
31	CMPA::BSINTSTS							
	SCINT B	SCINT C	SCINT D	BC0	BC1	BC2	BC3	BC4
32	CMPA::BSINTSTS							
	BC5	BC6	BC7	BCREQ	BCINT A	BCINT B	BCINT C	BCINT D

Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	‘25’							
41	Not used							
42	Y-Wing::PCI Error Status							
	RCO	SCCO	USC	SCD	RSCE	DCD	DAP	DDP
43	Y-Wing::PCI Error Status/SERR Status/OnChipBus Error Status							
	SIA	DMDP	RTA	RMA	SS			OPE
44	Y-Wing::OnChipBus Error Status							
	BL64E	BL32E	BL16E	BL8E	BL4E	RCOE	DNDE	BEE
45	OHUB::REGCHK							
	Not used	Not used	Not used	TXRCHK1	TXRCHK0	CPUACCCH	0	1
46	OHUB::REGCHK							
	ADCHK(7)	ADCHK(6)	ADCHK(5)	ADCHK(4)	ADCHK(3)	ADCHK(2)	ADCHK(1)	ADCHK(0)
47	OHUB::REGCHK							
	RWDCHKU (3)	RWDCHKU (2)	RWDCHKU (1)	RWDCHKU (0)	RRDCHKU (3)	RRDCHKU (2)	RRDCHKU (1)	RRDCHKU (0)
48	OHUB::REGCHK							
	TIMECNT1	TIMECNT0	TIMEBASE	TOCHK	BEPCKU	Not used	ATPCKU	Not used
49	OHUB::REGCHKADR							
	31	30	29	28	27	26	25	24
4A	OHUB::REGCHKADR							
	23	21	21	20	19	18	17	16
4B	OHUB::REGCHKADR							
	15	14	13	12	11	10	9	8
4C	OHUB::REGCHKADR							
	7	6	5	4	3	2	1	0
4D	OHUB::REGCHKADR							
	PCIINT	REG	CHSN	DOORBELL	DXBF	DMA	BFCTL	BRG
4E	OHUB::CPUID							
	Not used						YPCIID	YCPUID
4F	OHUB::DxCHK1STS36							
	PDZZ CHK2B0(31)	PDZZ CHK2B0(30)	PDZZ CHK2B0(29)	PDZZ CHK2B0(28)	PDZZ CHK2B0(27)	PDZZ CHK2B0(26)	PDZZ CHK2B0(25)	PDZZ CHK2B0(24)

Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	OHUB::DxCHK1STS36							
	PDZZ CHK2B0(23)	PDZZ CHK2B0(22)	PDZZ CHK2B0(21)	PDZZ CHK2B0(20)	PDZZ CHK2B0(19)	PDZZ CHK2B0(18)	PDZZ CHK2B0(17)	PDZZ CHK2B0(16)
51	OHUB::DxCHK1STS36							
	PDZZ CHK2B0(15)	PDZZ CHK2B0(14)	PDZZ CHK2B0(13)	PDZZ CHK2B0(12)	PDZZ CHK2B0(11)	PDZZ CHK2B0(10)	PDZZ CHK2B0(9)	PDZZ CHK2B0(8)
52	OHUB::DxCHK1STS36							
	PDZZ CHK2B0(7)	PDZZ CHK2B0(6)	PDZZ CHK2B0(5)	PDZZ CHK2B0(4)	PDZZ CHK2B0(3)	PDZZ CHK2B0(2)	PDZZ CHK2B0(1)	PDZZ CHK2B0(0)
53	OHUB::DxCHK1STS37							
	PDZZ CHK2B1(31)	PDZZ CHK2B1(30)	PDZZ CHK2B1(29)	PDZZ CHK2B1(28)	PDZZ CHK2B1(27)	PDZZ CHK2B1(26)	PDZZ CHK2B1(25)	PDZZ CHK2B1(24)
54	OHUB::DxCHK1STS37							
	PDZZ CHK2B1(23)	PDZZ CHK2B1(22)	PDZZ CHK2B1(21)	PDZZ CHK2B1(20)	PDZZ CHK2B1(19)	PDZZ CHK2B1(18)	PDZZ CHK2B1(17)	PDZZ CHK2B1(16)
55	OHUB::DxCHK1STS37							
	PDZZ CHK2B1(15)	PDZZ CHK2B1(14)	PDZZ CHK2B1(13)	PDZZ CHK2B1(12)	PDZZ CHK2B1(11)	PDZZ CHK2B1(10)	PDZZ CHK2B1(9)	PDZZ CHK2B1(8)
56	OHUB::DxCHK1STS37							
	PDZZ CHK2B1(7)	PDZZ CHK2B1(6)	PDZZ CHK2B1(5)	PDZZ CHK2B1(4)	PDZZ CHK2B1(3)	PDZZ CHK2B1(2)	PDZZ CHK2B1(1)	PDZZ CHK2B1(0)
57	OHUB::DxCHK1STS38							
	Not used	PDZZ CHK2B2(5)	PDZZ CHK2B2(4)	PDZZ CHK2B2(3)	PDZZ CHK2B2(2)	PDZZ CHK2B2(1)	PDZZ CHK2B2(0)	PDZZ CHK2B2(0)
58	OHUB::DxCHK1STS5							
	PARMRBUF (31)	PARMRBUF (30)	PARMRBUF (29)	PARMRBUF (28)	PARMRBUF (27)	PARMRBUF (26)	PARMRBUF (25)	PARMRBUF (24)
59	OHUB::DxCHK1STS5							
	PARMRBUF (23)	PARMRBUF (22)	PARMRBUF (21)	PARMRBUF (20)	PARMRBUF (19)	PARMRBUF (18)	PARMRBUF (17)	PARMRBUF (16)
5A	OHUB::DxCHK1STS7							
	PARMSEQ (31)	PARMSEQ (30)	PARMSEQ (29)	PARMSEQ (28)	Not used	Not used	Not used	Not used
5B	OHUB::DxCHK1STS8							
	SEQPF(31)	SEQPF(30)	SEQPF(29)	SEQPF(28)	Not used	Not used	Not used	Not used
5C	OHUB::DxCHK1STS9							
	PARMWSEQ (31)	PARMWSEQ (30)	PARMWSEQ (29)	PARMWSEQ (28)	Not used	Not used	Not used	Not used
5D	OHUB::DxCHK1STS10							
	SEQAB(31)	SEQAB(30)	SEQAB(29)	SEQAB(28)	Not used	Not used	Not used	Not used
5E	OHUB::DxCHK1STS10							
	SEQAB(31)	SEQAB(30)	SEQAB(29)	SEQAB(28)	Not used	Not used	Not used	Not used
5F	OHUB::DxCHK1STS11							
	SEQA(31)	SEQA(30)	SEQA(29)	SEQA(28)	Not used	Not used	Not used	Not used

Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	OHUB::DxCHK1STS11							
	SEQA(31)	SEQA(30)	SEQA(29)	SEQA(28)	Not used	Not used	Not used	Not used
61	OHUB::DxCHK1STS12							
	SEQAA(31)	SEQAA(30)	SEQAA(29)	SEQAA(28)	SEQAA(27)	SEQAA(26)	SEQAA(25)	SEQAA(24)
62	OHUB::DxCHK1STS14							
	Not used	PMRWSEQ (31)	PMRWSEQ (30)	PMRWSEQ (29)	Not used	PMWSEQ (31)	PMWSEQ (30)	PMWSEQ (29)
63	OHUB::DxCHK1STS14							
	PTRWSEQ (31)	PTRWSEQ (30)	PTRWSEQ (29)	PTRWSEQ (28)	Not used	Not used	Not used	Not used
64	OHUB::DxCHK1STS14							
	Not used	PMRWSEQ (31)	PMRWSEQ (30)	PMRWSEQ (29)	Not used	PMWSEQ (31)	PMWSEQ (30)	PMWSEQ (29)
65	OHUB::DxCHK1STS14							
	PTRWSEQ (31)	PTRWSEQ (30)	PTRWSEQ (29)	PTRWSEQ (28)	Not used	Not used	Not used	Not used
66	OHUB::YX0ERR							
	REGIF TOV(S02)	REGIF TOV(S05)	REGIF TOV(S0B)	REGIF TIMER PERR	Not used	SEQ WR ERROR	SEQ DEC ERROR	SEQ ENC ERROR
67	OHUB::YX0ERR							
	Address PERR(7)	Address PERR(6)	Address PERR(5)	Address PERR(4)	Address PERR(3)	Address PERR(2)	Address PERR(1)	Address PERR(0)
68	OHUB::YX0ERR							
	Write Data PERR(3)	Write Data PERR(2)	Write Data PERR(1)	Write Data PERR(0)	Read Data PERR(3)	Read Data PERR(2)	Read Data PERR(1)	Read Data PERR(0)
69	OHUB::YX0ERR							
	REGIF TOV(S02)	REGIF TOV(S05)	REGIF TOV(S0B)	REGIF TIMER PERR	Not used	SEQ WR ERROR	SEQ DEC ERROR	SEQ ENC ERROR
6A	OHUB::YX0ERR							
	Address PERR(7)	Address PERR(6)	Address PERR(5)	Address PERR(4)	Address PERR(3)	Address PERR(2)	Address PERR(1)	Address PERR(0)
6B	OHUB::YX0ERR							
	Write Data PERR(3)	Write Data PERR(2)	Write Data PERR(1)	Write Data PERR(0)	Read Data PERR(3)	Read Data PERR(2)	Read Data PERR(1)	Read Data PERR(0)
6C	OHUB::YX0ADRLOG							
	ADRLOG (31)	ADRLOG (30)	ADRLOG (29)	ADRLOG (28)	ADRLOG (27)	ADRLOG (26)	ADRLOG (25)	ADRLOG (24)
6D	OHUB::YX0ADRLOG							
	ADRLOG (23)	ADRLOG (22)	ADRLOG (21)	ADRLOG (20)	ADRLOG (19)	ADRLOG (18)	ADRLOG (17)	ADRLOG (16)
6E	OHUB::YX0ADRLOG							
	ADRLOG (15)	ADRLOG (14)	ADRLOG (13)	ADRLOG (12)	ADRLOG (11)	ADRLOG (10)	ADRLOG (9)	ADRLOG (8)
6F	OHUB::YX0ADRLOG							
	ADRLOG (7)	ADRLOG (6)	ADRLOG (5)	ADRLOG (4)	ADRLOG (3)	ADRLOG (2)	ADRLOG (1)	ADRLOG (0)

Byte27 = 8E & Byte34 = X0 & Byte28 = 43 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	OHUB::YX1ADRLOG							
	ADRLOG (31)	ADRLOG (30)	ADRLOG (29)	ADRLOG (28)	ADRLOG (27)	ADRLOG (26)	ADRLOG (25)	ADRLOG (24)
71	OHUB::YX1ADRLOG							
	ADRLOG (23)	ADRLOG (22)	ADRLOG (21)	ADRLOG (20)	ADRLOG (19)	ADRLOG (18)	ADRLOG (17)	ADRLOG (16)
72	OHUB::YX1ADRLOG							
	ADRLOG (15)	ADRLOG (14)	ADRLOG (13)	ADRLOG (12)	ADRLOG (11)	ADRLOG (10)	ADRLOG (9)	ADRLOG (8)
73	OHUB::YX1ADRLOG							
	ADRLOG (7)	ADRLOG (6)	ADRLOG (5)	ADRLOG (4)	ADRLOG (3)	ADRLOG (2)	ADRLOG (1)	ADRLOG (0)
74	OHUB::YX1ADRLOG							
	FERDR_ W/R DTPER	FERDR_ RDPER	ERMDR_ W/R DTPER	ERMDR_ RDPER	DRMD0 PASSWORD	DRRERR_ RDPER	Not used	Not used
75	OHUB::YX1ADRLOG							
	WADB0 ADR2PER	WADB0 ADR3PER	WADB0 WTPER	WADB0 RDPER	DRMD0 ADR2PER	DRMD0 ADR3PER	DRMD0 WTPER	DRMD0 RDPER
76	OHUB::YX1ADRLOG							
	Not used	WADB0 XRBCERR	WADB0 XRADREERR	XRSEQ BLKACSERR S	XRSEQ BLKACSERR M	Not used	Not used	Not used
77	OHUB::YX1ADRLOG							
	Not used	Not used	Not used	Not used	Not used	RDB0 DTPER	Not used	Not used
78	OHUB::SMBERR							
	Not used	SMBUS ACCESS ERR	TIMING CNT PERR(1)	TIMING CNT PERR(0)	CYCLE CNT PERR	DOWN CNT PERR	IDLE CNT PERR(1)	IDLE CNT PERR(0)
79	OHUB::CHIPSETERR							
	Not used	Not used	Not used	Not used	P64H2ERR	NMI	IERR	SMBALERT
7A	OHUB::BRGACSERR							
	YP RSTACS ERROR	PY RSTACS ERROR	YP ENABLE ERROR	YP BURST ERROR	YP RANGE ERROR	PY ENABLE ERROR	PY BURST ERROR	PY RANGE ERROR
7B	OHUB::PxCHK1STS5							
	PARMRBUF (31)	PARMRBUF (30)	PARMRBUF (29)	PARMRBUF (28)	PARMRBUF (27)	PARMRBUF (26)	PARMRBUF (25)	PARMRBUF (24)
7C	OHUB::PxCHK1STS5							
	PARMRBUF (23)	PARMRBUF (22)	Not used	Not used	Not used	Not used	Not used	Not used
7D	OHUB::PxCHK1STS7							
	PARMSEQ (31)	PARMSEQ (30)	PARMSEQ (29)	PARMSEQ (28)	Not used	Not used	Not used	Not used
7E	OHUB::PxCHK1STS8							
	SEQPF(31)	SEQPF(30)	SEQPF(29)	SEQPF(28)	Not used	Not used	Not used	Not used
7F	OHUB::PxCHK1STS11							
	SEQA(31)	SEQA(30)	SEQA(29)	SEQA(28)	SEQA(27)	Not used	Not used	Not used

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'43' : OHUB CHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	OHUBCHK1B							
	YPCI1	YPCI0	TPCI1	TPCI0	PMA3	PMA2	PMA1	PMA0
2E	OHUBCHK1B							
	DMA7	DMA6	DMA5	DMA4	DMA3	DMA2	DMA1	DMA0
2F	OHUBCHK1B							
	Not used	REG1	DMMARB1	DMAARB0	Doorbell	PCIINT	REG0	Not used
30	OHUBCHK1B							
	Not used	Not used	BFCTL1	BFCTL0	Not used	Not used	Not used	Not used
31	Not used							
32	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'26'							
41	Y-Wing::PERRADR PCI error address MSW							
42	Y-Wing::PERRADR PCI error address MSW							
43	Y-Wing::PERRADR PCI error address MSW							
44	Y-Wing::PERRADR PCI error address MSW							
45	MHUB::REGCHKADR							
	31	30	29	28	27	26	25	24
46	MHUB::REGCHKADR							
	23	22	21	20	19	18	17	16
47	MHUB::REGCHKADR							
	15	14	13	12	11	10	9	8
48	MHUB::REGCHKADR							
	7	6	5	4	3	2	1	0
49	MHUB::CHUBREGCHK1SRC Not used							
4A	MHUB::CHUBREGCHK1SRC							
	PCIINT	REG	CHSN	DOORBELL	DXBF	DMA	BFCTL	BRG
4B	MHUB::REGCHK							
	Not used	Not used	Not used	TXRCHK1	TXRCHK0	CPUACCCH	0	1
4C	MHUB::REGCHK							
	ADCHK(7)	ADCHK(6)	ADCHK(5)	ADCHK(4)	ADCHK(3)	ADCHK(2)	ADCHK(1)	ADCHK(0)
4D	MHUB::REGCHK							
	RWDCHKU (3)	RWDCHKU (2)	RWDCHKU (1)	RWDCHKU (0)	RRDCHKU (3)	RRDCHKU (2)	RRDCHKU (1)	RRDCHKU (0)
4E	MHUB::REGCHK							
	TIMECNT1	TIMECNT0	TIMEBASE	TOCHK	BEPCKU	Not used	ATPCKU	Not used
4F	MHUB::CBMPCHK1							
	CB1YWD CHK3	CB1YWD CHK2	CB1YWD CHK1	CB1YWD CHK0	CB0YWD CHK3	CB0YWD CHK2	CB0YWD CHK1	CB0YWD CHK0

Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	MHUB::CBMPCHK1							
	Not used	Not used	CBMPBE CHK1	CBMPBE CHK0	CB1XRAD CHK1	CB1XRAD CHK0	CB0XRAD CHK1	CB0XRAD CHK0
51	MHUB::CBMPCHK1							
	Not used							
52	MHUB::CBMPCHK1							
	Not used	Not used	CI1BEPCK	CI0BEPCK	CI1XRAD CHK1	CI1XRAD CHK0	CI0XRAD CHK1	CI0XRAD CHK0
53	MHUB::CBMPCHK1							
	CI1XRRD CHK3	CI1XRRD CHK2	CI1XRRD CHK1	CI1XRRD CHK0	CI0XRRD CHK3	CI0XRRD CHK3	CI0XRRD CHK3	CI0XRRD CHK3
54	MHUB::CBMPCHK1							
	CI1XRWD CHK3	CI1XRWD CHK2	CI1XRWD CHK1	CI1XRWD CHK0	CI0XRWD CHK3	CI0XRWD CHK2	CI0XRWD CHK1	CI0XRWD CHK0
55	MHUB::DA0PCHK1BSTS							
	Not used	PCICHK1B	Not used	XRSELERR	Not used	XRBEPER	XRWDPER	XRADRPERR
56	MHUB::D0CHK1BSTS							
	XRADR0	XRADR1	XRBE1	Not used	XRWDT0	XRWDT1	XRWDT2	XRWDT3
57	MHUB::DB0PCHK1BSTS							
	Not used	PCICHK1B	Not used	XRSELERR	Not used	XRBEPER	XRWDPER	XRADRPERR
58	MHUB::DB0PCHK1BSTS							
	Not used	PCICHK1B	Not used	XRSELERR	Not used	XRBEPER	XRWDPER	XRADRPERR
59	MHUB::D1CHK1BSTS							
	XRADR0	XRADR1	XRBE1	Not used	XRWDT0	XRWDT1	XRWDT2	XRWDT3
5A	MHUB::P PCHK1BSTS							
	Not used	PCICHK1B	Not used	XRSELERR	Not used	XRBEPER	XRWDPER	XRADRPERR
5B	MHUB::P CHK1BSTS							
	XRADR0	XRADR1	XRBE1	Not used	XRWDT0	XRWDT1	XRWDT2	XRWDT3
5C	MHUB::DXBF0 REG ERR							
	Not used	Not used	Not used	REG SEQ PERR	REG BE PERR	REG SEL PERR	REG ADR PERR	REG WRDT PERR
5D	MHUB::DXBF1 REG ERR							
	Not used	Not used	Not used	REG EQ PERR	REG BE PERR	REG SEL PERR	REG ADR PERR	REG WRDT PERR
5E	MHUB::REGIF CHK1 D A							
	IOD SEQ ERR1	IOD SEQ ERR0	ADR PERR1	ADR PERR0	WDT PERR3	WDT PERR2	WDT PERR1	WDT PERR0
5F	MHUB::REGIF CHK1 D A							
	BE PERR	SEL ERR	ERR END1	ERR END2	Not used	Not used	Not used	Not used

Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	MHUB::REGIF CHK1 C A							
	IOC SEQ ERR1	IOC SEQ ERR0	ADR PERR1	ADR PERR0	WDT PERR3	WDT PERR2	WDT PERR1	WDT PERR0
61	MHUB::REGIF CHK1 C A							
	BE PERR	Not used	ERR END	Not used	Not used	Not used	Not used	Not used
62	MHUB::REGIF CHK1 D B							
	IOD SEQ ERR1	IOD SEQ ERR0	ADR PERR1	ADR PERR0	WDT PERR3	WDT PERR2	WDT PERR1	WDT PERR0
63	MHUB::REGIF CHK1 D B							
	BE PERR	SEL ERR	ERR END1	ERR END2	Not used	Not used	Not used	Not used
64	MHUB::REGIF CHK1 D B							
	IOC SEQ ERR1	IOC SEQ ERR0	ADR PERR1	ADR PERR0	WDT PERR3	WDT PERR2	WDT PERR1	WDT PERR0
65	MHUB::REGIF CHK1 D B							
	BEPERR	Not used	ERREND	Not used	Not used	Not used	Not used	Not used
66	MHUB::BFCTLCHK1STS0							
	Not used	Y0XR BE PCK	Y0XR ADR0 PCK	Y0XR ADR1 PCK	Y0XR WD0 PCK	Y0XR WD1 PCK	Y0XR WD2 PCK	Y0XR WD3 PCK
67	MHUB::BFCTLCHK1STS1							
	Not used	Y1XR BE PCK	Y1XR ADR0 PCK	Y1XR ADR1 PCK	Y1XR WD0 PCK	Y1XR WD1 PCK	Y1XR WD2 PCK	Y1XR WD3 PCK
68	MHUB::BRIDGECHK1STS0							
	Not used	Y0XR BE PCK	Y0XR ADR0 PCK	Y0XR ADR1 PCK	Y0XR WD0 PCK	Y0XR WD1 PCK	Y0XR WD2 PCK	Y0XR WD3 PCK
69	MHUB::BRIDGECHK1STS1							
	Not used	Y1XR BE PCK	Y1XR ADR0 PCK	Y1XR ADR1 PCK	Y1XR WD0 PCK	Y1XR WD1 PCK	Y1XR WD2 PCK	Y1XR WD3 PCK
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 44 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'44' : OHUB CHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C		Y-Wing::INT_STATUS						
	RFU						RCO	SCCO
2D	OHUBCHK1B							
	YPC11	YPC10	TPC11	TPC10	PMA3	PMA2	PMA1	PMA0
2E	OHUBCHK1B							
	DMA7	DMA6	DMA5	DMA4	DMA3	DMA2	DMA1	DMA0
2F	OHUBCHK1B							
	Not used	REG1	DMMARB1	DMAARB0	Doorbell	PCIINT	REG0	Not used
30	OHUBCHK1B							
	Not used	Not used	BFCTL1	BFCTL0	Not used	Not used	Not used	Not used
31	CHUBREPCHK							
	Not used	FPP1DCHK	FPP1CHK	FPPMCHK1	Not used	FPP0DCHK	FPP0CHK	FPPMCHK0
32	CHUBREPCHK							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	Not used

Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'27'							
41	M(O)HUB::PCI0(1) ERR0R0							
	TXADPE0	TXADPE1	TXADPE2	TXADPE3	TXADPE4	TXADPE5	TXADPE6	TXADPE7
42	M(O)HUB::PCI0(1) ERR0R0							
	Not used	PERR	SERR	Not used	Not used	PERR	PCI64PE	PCIPE
43	M(O)HUB::PCI0(1) ERR0R0							
	RXADRPEH0	RXADRPEH1	RXADRPEH2	RXADRPEH3	RXADRPEL0	RXADRPEL1	RXADRPEL2	RXADRPEL3
44	M(O)HUB::PCI0(1) ERR0R0							
	Not used	ATR1PE0	ATR1PE1	ATR1PE2	ATR1PE3	BLKACSPE	ATROPE	RXCMDPE
45	M(O)HUB::PCI0(1) ERR0R1							
	OUTADRCPE0	OUTADRCPE1	OUTCNTPE0	OUTCNTPE1	INADRCNTPE0	INADRCNTPE1	IWCPE0	IWCPE1
46	M(O)HUB::PCI0(1) ERR0R1							
	Not used	EQLSTCPE	OV1PE	OV4PE	DETIWCPE	Not used	OUTADROVRN	INADROVRN
47	M(O)HUB::PCI0(1) ERR0R1							
	Not used	TIMCNTPE0	TIMCNTPE1	TIMCNTPE2	TIMOVR1	TIMOVR0	PTSQCMPE R	SELBCPE
48	M(O)HUB::PCI0(1) ERR0R1							
	TGTABT1	TGTABT0	Not used	XLASTDPE	DCNTPE0	DCNTPE1	SEQPE1	SEQPE0
49	M(O)HUB::PCI0(1) ERR0R2							
	OUTADRCPE0	OUTADRCPE1	OUTCNTPE0	OUTCNTPE1	INADRCNTPE0	INADRCNTPE1	IWCPE0	IWCPE1
4A	M(O)HUB::PCI0(1) ERR0R2							
	SQCMPE R	Not used	OVCMPERR	OVNCMPERR	DETIWCUPPE	Not used	OUTADROVRN	INADROVRN
4B	M(O)HUB::PCI0(1) ERR0R2							
	OUTPOINTPE	INPOINTPE	Not used	Not used	Not used	Not used	Not used	Not used
4C	M(O)HUB::PCI0(1) ERR0R2							
	Not used	SEQCMPE R	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4D	M(O)HUB::PCI0(1) ERR0R3							
	SETERR1	SETERR0	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4E	M(O)HUB::PCI0(1) ERR0R3							
	SEQCMPE R	Not used	Not used	Not used	Not used	Not used	Not used	Not used
4F	M(O)HUB::PCI0(1) ERR0R3							
	Not used	TGTTXHUNCR	TGTTXHADER	TGTTXHECHDE	Not used	TGTTXLUNCR	TGTTXHLADER	TGTTXLECHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	M(O)HUB::PCI0(1) ERR0R3							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
51	M(O)HUB::PCI0(1) ERR0R4							
	Not used	Not used	Not used	Not used	RTRYCNTPE	DWCNTPE	DCNTPE0	DCNTPE1
52	M(O)HUB::PCI0(1) ERR0R4							
	LASTP8EV	LAST2P8EV	LAST3P8EV	LAST4P8EV	NLASTDT	LASTDT	LASTIWDE	RTRYCNTPE
53	M(O)HUB::PCI0(1) ERR0R4							
	LASTP4	LAST2P4	LAST3P4	LAST4P4	Not used	Not used	Not used	Not used
54	M(O)HUB::PCI0(1) ERR0R4							
	Not used	Not used	Not used	Not used	OUTPOINTPE	INPOINTPE	TXBEPE	TXBELPE
55	MM(O)HUB::PCI0(1) ERR0R5							
	Not used	Not used	Not used	CMDERR1	CMDERR0	SEQPE1	SEQPE0	SEQCMERR
56	MM(O)HUB::PCI0(1) ERR0R5							
	SDISCT1	SDISCT0	TGTABT1	TGTABT0	MSTABT1	MSTABT0	RTRYOVER1	RTRYOVER0
57	MM(O)HUB::PCI0(1) ERR0R5							
	Not used	SEQCMERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
58	MM(O)HUB::PCI0(1) ERR0R5							
	ARBENERR1	ARBERR1	DECERR1	ENCERR1	ARBENERR0	ARBERR0	DECERR0	ENCERR0
59	M(O)HUB::PCI0(1) ERR0R6							
	SEQCMERR	Not used	Not used	Not used	Not used	Not used	Not used	Not used
5A	M(O)HUB::PCI0(1) ERR0R6							
	SEQCMERR	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5B	M(O)HUB::PCI0(1) ERR0R6							
	Not used	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5C	M(O)HUB::PCI0(1) ERR0R6							
	Not used	Not used	XFPCIERR	XEPCIERR	XDPCIERR	XCPCIERR	XBPCIERR	XAPCIERR
5D	M(O)HUB::PCI0(1) ERR0R7							
	Not used	Not used	Not used	Not used	OUTPOINTPE	INPOINTPE	TXBEHPE	TXBELPE
5E	M(O)HUB::PCI0(1) ERR0R7							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
5F	M(O)HUB::PCI0(1) ERR0R7							
	Not used	Not used	Not used	CLASTPER	NLASTPER	SELBCPER	XBCNTPE0	XBCNTPE1

Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	M(O)HUB::PCI0(1) ERR0R7							
	Not used	Not used	Not used	Not used	OUTPOINT PE	INPOINTPE	TXBEHPE	TXBELPE
61	M(O)HUB::PCI0(1) ERR0R8							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
62	M(O)HUB::PCI0(1) ERR0R8							
	SEQCMPERR	Not used	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
63	M(O)HUB::PCI0(1) ERR0R8							
	ADRCNTPE 0	ADRCNTPE 1	ADRCNTPE 2	ADRCNTPE 3	ADRCNTPE 4	ADRCNTPE 5	ADRCNTPE 6	ADRCNTPE 7
64	M(O)HUB::PCI0(1) ERR0R8							
	Not used	Not used	Not used	Not used	BURSTPER 0	BURSTPER 1	BCNTPER0	BCNTPER1
65	M(O)HUB::PCI0(1) ERR0R9							
	OUTADRC PE0	OUTADRC PE1	OUTCNTPE 0	OUTCNTPE 1	INADRCNT PE0	INADRCNT PE1	IWCPE0	IWCPE1
66	M(O)HUB::PCI0(1) ERR0R9							
	Not used	OV32PER	OV2PER	SOOV4PER	DETIWCPE R	BRDYPER	OUTADRO VRN	INADROVR N
67	M(O)HUB::PCI0(1) ERR0R9							
	SEQCMPERR	Not used						
68	M(O)HUB::PCI0(1) ERR0R10							
	Not used	Not used	Not used	Not used	Not used	Not used	OUTPOINP ER	INPOINTP ER
69	M(O)HUB::PCI0(1) ERR0R10							
	Not used	SEQ CM PERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6A	M(O)HUB::PCI0(1) ERR0R10							
	Not used	Not used	Not used	Not used	OUTSERR	OUTMPERR	PCI64PE	PCIPE
6B	M(O)HUB::PCI0(1) ERR0R10							
	Not used	SEQ CM PERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6C	M(O)HUB::PCI12 ERR0R11							
	OUTADRC PE0	OUTADRC PE1	OUTCNTPE 0	OUTCNTPE 1	INADRCNT PE0	INADRCNT PE1	IWCPE0	IWCPE1
6D	M(O)HUB::PCI0(1) ERR0R11							
	Not used	EQLSTCNT PE	OV1PER	OV4PER	DETIWCUP PER	Not used	OUTADRO VRN	INADROVR N
6E	M(O)HUB::PCI0(1) ERR0R11							
	Not used	MSTRXHUN NCR	MSTRXHAD DER	MSTRXHEC CHDE	Not used	MSTRXLUN CR	MSTRXLAD ER	MSTRXLE CCHDE
6F	M(O)HUB::PCI0(1) ERR0R11							
	Not used	TGTRXHUN CR	TGTRXHAD ER	TGTRXHEC CHDE	Not used	TGTRXLUN CR	TGTRXLAD ER	TGTRXLE CCHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 42/45/46/48/52/55/56/58 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	M(O)HUB::PCI0(1) ERROR12							
	Not used	SEQCMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
71	M(O)HUB::PCI0(1) ERROR12							
	Not used	Not used	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
72	M(O)HUB::PCI0(1) ERROR12							
	Not used	SEQCMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
73	M(O)HUB::PCI0(1) ERROR12							
	Not used	Not used	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
74	M(O)HUB::BRGERR							
	BARB SEQPER	BARB DECERR	BARB ENCERR	BARB CMPERR	BRGBUFA OPTPE	BRGBUFA IPTPE	BRGBUFB OPTPE	BRGBUFB IPTPE
75	M(O)HUB::BRGERR							
	BRGSEQA CMDERR	BRGSEQA SEQPE	BRGSEQA DECERR	BRGSEQA ENCERR	BRGSEQA CMPERR	BRGERRA NLASTPE	BRGERRA SELBCPE	BRGERRA BCNTPE
76	M(O)HUB::BRGERR							
	BRGSEQB CMDERR	BRGSEQB SEQPE	BRGSEQB DECERR	BRGSEQB ENCERR	BRGSEQB CMPERR	BRGERRB NLASTPE	BRGERRB SELBCPE	BRGERRB BCNTPE
77	M(O)HUB::BRGERR							
	XTRXDB IPPE	XTRXDB OPPE	XTSQ ENCDECERR	XTSQ SEQPE	XTTXDB IPPE	XTTXDB OPPE	XTTXB IPPE	XTTXB OPPE
78	M(O)HUB::PCI0(1) CK1TLOG0							
	CK1TADR63	CK1TADR62	CK1TADR61	CK1TADR60	CK1TADR59	CK1TADR58	CK1TADR57	CK1TADR56
79	M(O)HUB::PCI0(1) CK1TLOG0							
	CK1TADR55	CK1TADR54	CK1TADR53	CK1TADR52	CK1TADR51	CK1TADR50	CK1TADR49	CK1TADR48
7A	M(O)HUB::PCI0(1) CK1TLOG0							
	CK1TADR55	CK1TADR54	CK1TADR53	CK1TADR52	CK1TADR51	CK1TADR50	CK1TADR49	CK1TADR48
7B	M(O)HUB::PCI0(1) CK1TLOG0							
	CK1TADR39	CK1TADR38	CK1TADR37	CK1TADR36	CK1TADR35	CK1TADR34	CK1TADR33	CK1TADR32
7C	M(O)HUB::PCI0(1) CK1TLOG1							
	CK1TADR31	CK1TADR30	CK1TADR29	CK1TADR28	CK1TADR27	CK1TADR26	CK1TADR25	CK1TADR24
7D	M(O)HUB::PCI0(1) CK1TLOG1							
	CK1TADR23	CK1TADR22	CK1TADR21	CK1TADR20	CK1TADR19	CK1TADR18	CK1TADR17	CK1TADR16
7E	M(O)HUB::PCI0(1) CK1TLOG1							
	CK1TADR15	CK1TADR14	CK1TADR13	CK1TADR12	CK1TADR11	CK1TADR10	CK1TADR09	CK1TADR08
7F	M(O)HUB::PCI0(1) CK1TLOG1							
	CK1TADR07	CK1TADR06	CK1TADR05	CK1TADR04	CK1TADR03	CK1TADR02	CK1TADR01	CK1TADR00

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'42' : OHUB CHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	CMPA::MPA_STS							
	Not used	Not used	FCA CHK 1B 4MP	DRR CHK 1B 4MP	PCI CHK 1B Q	PCI CHK 1B M	FCA CHK 1B 8/4MP	DRR CHK 1B 8/4MP
2E	CMPA::MPA_STS							
	BSINT	CHK3 HSNMST	CHK3 HSNSLV	CHK1BFM	Not used	Not used	CHK1BXR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
		RFU		RCOE	DNDE	BEE		RFU

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'45' : PCI CHK1B OHUB/MHUB PPCI)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	Y-Wing::PCI Error Status							
	RCO	SCCO	USC	SCD	RSCE	DCD	DAP	DDP
2E	Y-Wing::PCI Error Status/SERR Status/OnChipBus Error Status							
	STA	DMDP	RTA	RMA	SS			OPE
2F	Y-Wing:: OnChipBus Error Status							
	BL64E	BL32E	BL16E	BL8E	BL4E	RCOE	DNDE	BEE
30	Not used							
31	Not used							
32	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'46' : PCI CHK1B OHUB/MHUB YPCI)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	Y-Wing::PCI Error Status							
	RCO	SCCO	USC	SCD	RSCE	DCD	DAP	DDP
2E	Y-Wing::PCI Error Status/SERR Status/OnChipBus Error Status							
	STA	DMDP	RTA	RMA	SS			OPE
2F	Y-Wing:: OnChipBus Error Status							
	BL64E	BL32E	BL16E	BL8E	BL4E	RCOE	DNDE	BEE
30	Not used							
31	Not used							
32	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'48' : PCI CHK1B(ESA))							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	ESA::ESA_CHK1B							
	LMADP CK1B	XTRG CK1B	XMST CK1B	PCI CK1B	Not used	XTRG_EX CK1B	Not used	PCI-EX CK1B
2E	ESA::ESA_CHK1B							
	CBFCK1B	PLCK1B	HSN_XRERR	HSN_CLKE RR	PCI_CLKE RR	ESA_CLKE RR	ALCP_DGCK1B	BLCP_DGCK1B
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'52' : PCI CHK1B(CMPA))							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	CMPA::MPA_STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	CMPA::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	Not used	Not used	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'55' : PCI CHK1B,OHUB/MHUB PPCI)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	Y-Wing::PCI Error Status							
	RCO	SCCO	USC	SCD	RSCE	DCD	DAP	DDP
2E	Y-Wing::PCI Error Status/SERR Status/OnChipBus Error Status							
	STA	DMDP	RTA	RMA	SS	OPE	STA	DMDP
2F	Y-Wing:: OnChipBus Error Status							
	BL64E	BL32E	BL16E	BL8E	BL4E	RCOE	DNDE	BEE
30	Not used							
31	Not used							
32	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'56' : PCI CHK1B,OHUB/MHUB YPCI)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	Y-Wing::PCI Error Status							
	RCO	SCCO	USC	SCD	RSCE	DCD	DAP	DDP
2E	Y-Wing::PCI Error Status/SERR Status/OnChipBus Error Status							
	STA	DMDP	RTA	RMA	SS			OPE
2F	Y-Wing:: OnChipBus Error Status							
	BL64E	BL32E	BL16E	BL8E	BL4E	RCOE	DNDE	BEE
30	Not used							
31	Not used							
32	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'47' : PCI_CHK1B(ESA))							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	ESA::ESA_CHK1B							
	LMADP CK1B	XTRG CK1B	XMST CK1B	PCI CK1B	Not used	XTRG_EX CK1B	Not used	PCI-EX CK1B
2E	ESA::ESA_CHK1B							
	CBFCK1B	PLCK1B	HSN_XR ERR	HSN_CLK ERR	PCI_CLK ERR	ESA_CLK ERR	ALCP_DGC K1B	BLCP_DGC K1B
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'28'							
41	Not used							
42	Not used							
43	Not used							
44	BESA::CHK1B							
	LMADP CK1B	XTRG CK1B	XMST CK1B	PCI CK1B	Not used	XTRG EX CK1B	Not used	PCI-EX CK1B
45	BESA::CHK1B							
	Not used							
46	BESA::CHK1B							
	CBFCK1B	PLCK1B	HSN_ XR ERR	HSN_ CLK ERR	PCI CLK ERR	ESA CLK ERR	ALCP_ DG ERR	BLCP_ DG ERR
47	BESA::CHK1B							
	Not used							
48	ESA::CHK1BEN							
	LMADPEN	XTRGEN	XMSTEN	PCIEN	Not used	XTRG EXEN	Not used	PCI- EXEN
49	ESA::CHK1BEN							
	Not used							
4A	ESA::CHK1BEN							
	CBFCK1B_ EN	PLCK1B_ EN	HSN_XRER R_EN	HSN_CLKE RR_EN	PCI CLK ERR	ESA CLK ERR	ALCP DGERR_EN	BLCP DGERR_EN
4B	ESA::CHK1BEN							
	Not used							
4C	ESA::CHK2							
	ESCONCK2	Not used	Not used	Not used	EXDMACK2	Not used	Not used	Not used
4D	ESA::CHK2							
	Not used							
4E	ESA::CHK2							
	Not used							
4F	ESA::CHK2							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	ESA::XRTRGCK1B							
	SEQERR	ADRPERR	CNTERR	SELERR	WR_TOV	RD_TOV	WRPERR	RDERR
51	ESA::XRTRGCK1B							
	AD23	AD22	AD21	AD20	AD19	AD18	AD17	AD16
52	ESA::XRTRGCK1B							
	AD15	AD14	AD13	AD12	AD11	AD10	AD9	AD8
53	ESA::XRTRGCK1B							
	AD7	AD6	AD5	AD4	AD3	AD2	AD1	WR
54	ESA::XRMSTCK1B							
	SEQERR	ADRPERR	DTOPERR	Not used	Not used	Not used	Not used	Not used
55	ESA::XRMSTCK1B							
	Not used							
56	ESA::XRMSTCK1B							
	PDMALD ACK	LDEND ACK	CBFEND ACK	END2 ACK	LAM10 ACK	LAM14 ACK	LAM18 ACK	Not used
57	ESA::XRMSTCK1B							
	SEQ7	SEQ6	SEQ5	SEQ4	SEQ3	SEQ2	SEQ1	SEQ0
58	ESA::EXTRGCK1B							
	SEQERR	ADRPERR	CNTERR	SELERR	WR_TOV	RD_TOV	WRPERR	RDERR
59	ESA::EXTRGCK1B							
	AD23	AD22	AD21	AD20	AD19	AD18	AD17	AD16
5A	ESA::EXTRGCK1B							
	AD15	AD14	AD13	AD12	AD11	AD10	AD9	AD8
5B	ESA::EXTRGCK1B							
	AD7	AD6	AD5	AD4	AD3	AD2	AD1	WR
5C	ESA::LMAERR							
	COMM0 PERR	COMM1 PERR	COMM2 PERR	COMM3 PERR	1031C PERR	10304 PERR	10314 PERR	Not used
5D	ESA::LMAERR							
	1030C PERR	10318 PERR	Not used	Not used	Not used	Not used	Not used	Not used
5E	ESA::LMAERR							
	Not used							
5F	ESA::LMAERR							
	Not used	Not used	Not used	Not used	Not used	Not used	Not used	LMA ERR RST

Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	ESA::LMARQSTS							
	LMA10 REQ	LMA14 REQ	LMA18 REQ	Not used	Not used	Not used	Not used	Not used
61	ESA::LMARQSTS							
	Not used							
62	ESA::LMARQSTS							
	Not used							
63	ESA::LMARQSTS							
	Not used							
64	ESA::EXCHK2B							
	BRD PCIWDT PCK0	BRD PCIWDT PCK1	BRD PCIWDT PCK2	BRD PCIWDT PCK3	BRD PCIWDT PCK4	BRD PCIWDT PCK5	BRD PCIWD TPCK6	BRD PCIWDT PCK7
65	ESA::EXCHK2B							
	BRD RACNT PCK0	BRD RACNT PCK1	RAM A UC ERR	RAM B UC ERR	RAM A LRC0 ERR	RAM B LRC0 ERR	RAM A LRC1 ERR	RAM B LRC1 ERR
66	ESA::EXCHK2B							
	Not used							
67	ESA::EXCHK2B							
	Not used							
68	ESA:: REGIF CHK1 D A							
	IOD SEQERR		ADR PERR		WDT PERR			
69	ESA:: REGIF CHK1 D A							
	BE PERR	SE LERR	ERR END1	ERR END2	Not used	Not used	Not used	Not used
6A	ESA:: REGIF CHK1 D A							
	Not used							
6B	ESA:: REGIF CHK1 D A							
	Not used							
6C	ESA:: REGIF CHK1 D B							
	IOD SEQERR		ADR PERR		WDT PERR			
6D	ESA:: REGIF CHK1 D B							
	BE PERR	SE LERR	ERR END1	ERR END2	Not used	Not used	Not used	Not used
6E	ESA:: REGIF CHK1 D B							
	Not used							
6F	ESA:: REGIF CHK1 D B							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 47 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	ESA:: REGIF CHK1 C A							
	IOD SEQERR		ADR PERR		WDT PERR			
71	ESA:: REGIF CHK1 C A							
	BEPERR	Not used	ERREND	Not used	Not used	Not used	Not used	Not used
72	ESA:: REGIF CHK1 C A							
	Not used							
73	ESA:: REGIF CHK1 C A							
	Not used							
74	ESA:: REGIF CHK1 C B							
	IOD SEQERR		ADR PERR		WDT PERR			
75	ESA:: REGIF CHK1 C B							
	BEPERR	Not used	ERREND	Not used	Not used	Not used	Not used	Not used
76	ESA:: REGIF CHK1 C B							
	Not used							
77	ESA:: REGIF CHK1 C B							
	Not used							
78	ESA::ESASTAT							
	Not used							
79	ESA::ESASTAT							
	Not used	Not used	Not used	Not used	CLCE0	CLCE1	CLCE2	CLCE MODE
7A	ESA::ESASTAT							
	DEV-NOEN	Not used	Not used	Not used	Not used	Not used	Not used	Not used
7B	ESA::ESASTAT							
	Not used							
7C	ESA::ESASTAT							
	LMADPCH K1B	XTRG CHK1B	XMST CHK1B	PCI CHK1B	Not used	XTRG_EX CHK1B	Not used	PCI-EX CHK1B
7D	ESA::OTHER CK1B STS							
	Not used							
7E	ESA::OTHER CK1B STS							
	CBF CHK1B	PL CHK1B	HSN XR ERR	HSN CLK ERR	PCI CLK ERR	ESA CLK ERR	ALCP DG ERR	BLCP DG ERR
7F	ESA::OTHER CK1B STS							
	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'47' : ESA CHK1B)							
2A	MPID							
2B	ESA::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	ESA::ESA_CHK1B							
	LMADP CK1B	XTRG CK1B	XMST CK1B	PCI CK1B	Not used	XTRG_EX CK1B	Not used	PCI-EX CK1B
2D	ESA::ESA_CHK1B							
	LMADP CK1B	XTRG CK1B	XMST CK1B	PCICK1B	Not used	XTRG_EX CK1B	Not used	LMADP CK1B
2E	ESA::ESA_LMAERR							
	COMMO PERR	COM1 PERR	COM2 PERR	COM3 PERR	1031C PERR	10304 PERR	10314 PERR	Not used
2F	ESA::ESA_LMAERR							
	1030CPERR	10318PERR	Not used	Not used	Not used	Not used	Not used	Not used
30	ESA::ESA_XRTRGCK1B							
	SEQERR	ADRPERR	CNTERR	SELERR	WR_TOV	RD_TOV	WRPERR	RDPERR
31	ESA::ESA_XRMSTCK1B							
	SEQERR	ADRPERR	DTOPERR	Not used	Not used	Not used	Not used	Not used
32	ESA::ESA_EXTRGCK1B							
	SEQERR	ADRPERR	CNTERR	SELERR	WR_TOV	RD_TOV	WRPERR	RDPERR

Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	'29'							
41	Y WING::							
	Not used							
42	Y WING::							
	Not used							
43	Y WING::							
	Not used							
44	Y WING::PERRSTAT							
	Not used							
45	Y WING::PERRSTAT							
	Not used							
46	Y WING::PERRSTAT							
	Not used							
47	Y WING::PERRSTAT							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
48	Y WING::PERRSTAT							
	Not used							
49	Y WING::PERRSTAT							
	Not used							
4A	Y WING::SERRSTAT							
	Not used							
4B	Y WING::SERRSTAT							
	Not used							
4C	Y WING::OCBERRSTAT							
	Not used							
4D	Y WING::OCBERRSTAT							
	Not used							OPE
4E	Y WING::OCBERRSTAT							
	Not used			BL64E	BL32E	BL16E	BL8E	BL4E
4F	Y WING::OCBERRSTAT							
	Not used			RCOE	DNDE	BEE	Not used	

Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	Y_WING::PINTXSTAT							
	Not used							
51	Y_WING::PINTXSTAT							
	Not used							
52	Y_WING::PINTXSTAT							
	Not used							
53	Y_WING::PINTXSTAT							
	Not used				INTD	INTC	INTB	INTA
54	Y_WING::PERRADDRH							
	PCI error address MSW							
55	Y_WING::PERRADDRH							
	PCI error address MSW							
56	Y_WING::PERRADDRH							
	PCI error address MSW							
57	Y_WING::PERRADDRH							
	PCI error address MSW							
58	Y_WING::PERRADDRL							
	PCI error address MSW							
59	Y_WING::PERRADDRL							
	PCI error address MSW							
5A	Y_WING::PERRADDRL							
	PCI error address MSW							
5B	Y_WING::PERRADDRL							
	PCI error address MSW							
5C	Y_WING::PCIXSTAT							
	RFU		SCEM	DMCRS			DMOST	
5D	Y_WING::PCIXSTAT							
	DMOST	DMMRBC		DC	USC	SCD	133MHz capable	64bit device
5E	Y_WING::PCIXSTAT							
	Bus number							
5F	Y_WING::PCIXSTAT							
	Device number					Function number		

Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	Y_WING::STATUS							
	DPE	SSE	RMA	RTA	STA	DEV timing		MDPE
61								
	FBBC	Not used	66MHz Cap	CL	Not used			
62	Y_WING::STATUS							
	Not used					FBBE	SERR# EN	
63	Y_WING::STATUS							
	Step Cont	PER	VPS	MWIE	Special cyc	Bus Master	Mem Space	IO Space
64	Y_WING::UNITCNT							
	INIT_End.	Not used						
65	Y_WING::UNITCNT							
	Not used							
66	Y_WING::UNITCNT							
	Not used			PER	Not used			MAttr cache
67	Y_WING::UNITCNT							
	Not used		PBA	Host mode	Not used	PCI CLK Freq	Not used	PCI mode
68	Y_WING::PERRMASK							
	Not used							
69	Y_WING::PERRMASK							
	Not used					MRCO	MSCCO	
6A	Y_WING::PERRMASK							
	Not used				MUSC	MSCD	MRSCE	
6B	Y_WING::PERRMASK							
	MDCD	MDAP	MDDP	MSTA	Not used	MDMDP	MTRA	MRMA
6C	Y_WING::PINTXMASK							
	Not used							
6D	Y_WING::PINTXMASK							
	Not used							
6E	Y_WING::PINTXMASK							
	Not used							
6F	Y_WING::PINTXMASK							
	Not used				Mask INTD	Mask INTC	Mask INTB	Mask INTA

Byte27 = 8E & Byte34 = X0 & Byte28 = 49 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Y_WING::OCBERRMASK							
	Not used							
71	Y_WING::OCBERRMASK							
	Not used							MOPE
72	Y_WING::OCBERRMASK							
	Not used			MBL64E	MBL32E	MBL16E	MBL8E	MBL4E
73	Y_WING::OCBERRMASK							
	Not used			MRCOE	MDNDE	MBEE	Not used	
74	Y_WING::PCIXCOMMAND							
	Not used							
75	Y_WING::PCIXCOMMAND							
	MOST			MMRBC		ERO	DPERE	
76	Y_WING::PCIXCOMMAND							
	PCI-X next capability							
77	Y_WING::PCIXCOMMAND							
	PCI-X capability ID							
78	Y_WING::							
	Not used							
79	Y_WING::							
	Not used							
7A	Y_WING::							
	Not used							
7B	Y_WING::							
	Not used							
7C	Y_WING::							
	Not used							
7D	Y_WING::							
	Not used							
7E	Y_WING::							
	Not used							
7F	Y_WING::							
	Not used							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	SubCode (x'49' : Y-Wing PCICHK1B)							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	Not used	Not used	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 HSNMST	CHK3 HSNSLV	CHK1B FM	Not used	Not used	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP NO. 2 ¹	SP NO. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV TYPE =6587 / x'EF8E' : LDEV TYPE =6586)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	‘2A’							
41	Not used							
42	Y Wing::BIUCBERRINFO2							
	Not used							
43	Y Wing::BIUCBERRINFO2							
	Not used							
44	Y Wing::BIUCBERRINFO2							
	Bus Error Info				Bus Error Cmd			
45	Y Wing::BIUCBERRINFO2							
	Bus Error Cmd				Bus Error Addr2			
46	Y Wing::BIUCBERRINFO1							
	Bus Error Addr1							
47	Y Wing::BIUCBERRINFO1							
	Bus Error Addr1							
48	Y Wing::BIUCBERRINFO1							
	Bus Error Addr1							
49	Y Wing::BIUCBERRINFO1							
	Bus Error Addr1							
4A	Y Wing::SDERRADDR							
	ERRADD							
4B	Y Wing::SDERRADDR							
	ERRADD						Not used	Not used
4C	Y Wing::SDERRADDR							
	ERRADD							
4D	Y Wing::SDERRADDR							
	ERRADD							
4E	Y Wing::SDERRSYNHI							
	Not used							
4F	Y Wing::SDERRSYNHI							
	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	Y Wing::SDERRSYNHI							
	Not used							
51	Y Wing::SDERRSYNHI							
	ESOHL							
52	Y Wing::SDERRSYNLO							
	Not used							
53	Y Wing::SDERRSYNLO							
	Not used							
54	Y Wing::SDERRSYNLO							
	Not used							
55	Y Wing::SDERRSYNLO							
	ESOLL							
56	Y Wing::GPIO DATA2							
	GPD31	GPD30	GPD29	GPD28	GPD27	GPD26	GPD25	GPD24
57	Y Wing::GPIO DATA2							
	GPD23	GPD22	GPD21	GPD20	GPD19	GPD18	GPD17	GPD16
58	Y Wing::GPIO DATA3							
	GPD47	GPD46	GPD45	GPD44	GPD43	GPD42	GPD41	GPD40
59	Y Wing::GPIO DATA3							
	GPD39	GPD38	GPD37	GPD36	GPD35	GPD34	GPD33	GPD32
5A	Y Wing::RFCSC							
	RFCSC							
5B	Y Wing::RFCSC							
	RFCSC							
5C	Y Wing::TFCSC							
	TFCSC							
5D	Y Wing::TFCSC							
	TFCSC							
5E	Y Wing::FM STATUS							
	Not used							
5F	Y Wing::FM STATUS							
	Not used						FM1 BSY	FM0 BSY

Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	Y Wing::ERROR STATUS							
	Not used	FMWP ERR	FMW ERR	SEQ CNT PERR	SEQ CNT ERR	SEQ WERR	SEQ PERR	SEQ PERR
61	Y Wing::ERROR STATUS							
	YWING ERR	Not used	FMRD AERR	EMRD PERR	Not used	LCE2 ERR	LDAT PERR	LADR PERR
62	Y Wing::HardRESETenable							
	Not used	Not used	Not used	SEQ CNT PERR EN	SEQ CNT ERR EN	SEQ WERR EN	SEQ RERR EN	SEQ PERR EN
63	Y Wing::HardRESETenable							
	YWING ERR EN	Not used	FMRD AERR EN	FMRD PERR EN	Not used	LCE2 ERR EN	LDAT PERR EN	LADR PERR EN
64	Y Wing::PLD CONTROL							
	DEBUG MD	Not used	Not used	Not used	Not used	Not used	Not used	FMWP REG
65	Y Wing::PLD CONTROL							
	SEQ ERR MODE	BT OK	ERR CLK	Not used	BYPS CLKS	Not used	LRDY EN	RST REQ
66	Y Wing::BadVAddr							
	BadVAddr							
67	Y Wing::BadVAddr							
	BadVAddr							
68	Y Wing::BadVAddr							
	BadVAddr							
69	Y Wing::BadVAddr							
	BadVAddr							
6A	Y Wing::BadVAddr							
	BadVAddr							
6B	Y Wing::BadVAddr							
	BadVAddr							
6C	Y Wing::BadVAddr							
	BadVAddr							
6D	Y Wing::BadVAddr							
	BadVAddr							
6E	Y Wing:: Status							
	XX	CU(2:0)			Not used	FR	Not used	DS
6F	Y Wing:: Status							
	DS							

Byte27 = 8E & Byte34 = X0 & Byte28 = 4A (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Y_Wing::Status							
	IM(7:0)							
71	Y_Wing::Status							
	KX	SX	UX	KSU		ERL	EXL	IE
72	Y_Wing::Reason							
	BD	Not used		CE	Not used			
73	Y_Wing:: Reason							
	Not used							
74	Y_Wing:: Reason							
	IP(7:0)							
75	Y_Wing:: Reason							
	Not used	ExcCode					Not used	Not used
76	Y_Wing::EPC							
	EPC							
77	Y_Wing::EPC							
	EPC							
78	Y_Wing::EPC							
	EPC							
79	Y_Wing::EPC							
	EPC							
7A	Y_Wing::EPC							
	EPC							
7B	Y_Wing::EPC							
	EPC							
7C	Y_Wing::EPC							
	EPC							
7D	Y_Wing::EPC							
	EPC							
7E	Y_Wing:: PARITY ERROR							
	EPC							
7F	Y_Wing:: CACHE ERROR							
	ER	EC	ED	ET	ES	EE	EB	Not used

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
27	Format (x'8')				Message (x'E')			
28	SubCode (x'4A' : Y-Wing CHK1B MNI)							
29	MPID							
2A	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2B	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	NMI_STATUS							
	Not used					OnChipBus Parity Error	Ethernet#2 Parity Error	Ethernet#1 Parity Error
2D	NMI_STATUS							
	BCU Parity Error	SC memory un correctable Error	SC memory correctable Error	SC memory Parity Error	SDRAM un correctable Error	SDRAM correctable Error	BIU-OnChipBus Error	Outside NMI (MPHOLD)
2E	Not used							
2F	Not used							
30	Not used							
31	Not used							

Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '03'							
41	DHUB::CHK1 ADRS							
	(Not used)	DBDE4M_ DTPER	DBDE4N_ DTPER	WLACHK LA1CPMER R	WLACHK LA2CPMER R	SLRCCHK WSLRCCMP ER	SLRCCHK WSLRCPER	SLRCCHK SLRCDTPE R
42	DHUB::CHK1 ADRS							
	DTSEL_ DTPER	ACGEN_ DTPER	RLACHK LA1CPMER R	RLACHK LA2CPMER R	RLACHK_ DTPER	SLRCCHK RSLRCCMP ER	SLRCCHK RSLRCPER	RPLRCCHK PLRCDTPE R
43	DHUB::CHK1 ADRS							
	(Not used)	(Not used)	(Not used)	DBD4M VERIFYERR	DBD4N VERIFYERR	RPLRCCHK CPLRCOPER	RPLRCCHK DTENDERR	(Not used)
44	DHUB::CHK1 ADRS							
	ST1_DTPER	ST1CHK STOCPMER	DBD1M C2 UCRCTERR	DBD1M C2 ADRERR	DBD1M C2 ECCERR	DBD1N C2 UCRCTERR	DBD1N C2 ADRERR	DBD1N C2 ECCERR
45	DHUB::CHK1 ADRS							
	(Not used)	(Not used)	(Not used)	(Not used)	DBD3X_ DTPER	ST0_DTPER	ST0CHK RTNCODEE RR ST6	ST0CHK RTNCODEE RR ST7
46	DHUB::RXRX ERR STS							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPER3	CMPER2	CMPER1	CMPER0
47	DHUB::RXRX ERR STS							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
48	DHUB::RXRX ERR STS							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
49	DHUB::ROTX ERR STS							
	MMOLPER3	MMOLPER2	MMOLPER1	MMOLPER0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
4A	DHUB::MPASTS							
	(Not used)	(Not used)	FCA CHK1B 4MP	DRR CHK1B 4MP	PCI CHK1B Q	PCI CHK1B M	FCA CHK1B 8/4MP	DRR CHK1B 8/4MP
4B	DHUB::MPASTS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
4C	DHUB::BMD							
	(Not used)	COPY_CD	SMDA_CD	SAMDB_CD	C0DS	C1DS	D0DS	D1DS
4D	DHUB::BMD							
	(Not used)	COPY_AB	SMDA_AB	SAMDB_AB	A0DS	A1DS	B0DS	B1DS
4E	DHUB::MSTCHK3 STS							
	(Not used)	(Not used)	XR_CMP	XR_SMRX	XR_SMTX	XR_RCV	XR_FIFO	XR_PCI
4F	DHUB::MSTCHK3 STS							
	A0_CTLER	A1_CTLER	B0_CTLER	B1_CTLER	C0_CTLER	C1_CTLER	D0_CTLER	D1_CTLER

Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB::MSTCHK3 STS							
	A0 DTER	A1 DTER	B0 DTER	B1 DTER	C0 DTER	C1 DTER	D0 DTER	D1 DTER
51	DHUB::MSTCHK3 STS							
	A0 TIMEOUT	A1 TIMEOUT	B0 TIMEOUT	B1 TIMEOUT	C0 TIMEOUT	C1 TIMEOUT	D0 TIMEOUT	D1 TIMEOUT
52	DHUB::CTL1							
	EN2R_ CD	NOLOCK CD	BUNSAN(1) CD	BUNSAN(0) CD	(Not used)	(Not used)	(Not used)	(Not used)
53	DHUB::CTL1							
	EN2R_ AB	NOLOCK AB	BUNSAN(1) AB	BUNSAN(0) AB	(Not used)	(Not used)	(Not used)	(Not used)
54	DHUB::PASSWORD_CHK1B							
	(Not used)	(Not used)	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWD CHK1B
55	DHUB::MPID							
	PKID7	PKID6	PKID5	PKID4	(Not used)	PKID3	PCINUM	XRNUM
56	DHUB::DIR ADR							
	C1 SQP3 ENCERR(M)	C1 SQP3 ENCERR(S)	C1 SQP3 DECERR(M)	C1 SQP3 DECERR(S)	C1 SQP3 SEQPER(M)	C1 SQP3 SEQPER(S)	(Not used)	(Not used)
57	DHUB::DIR ADR							
	C1 TXSEQ ENCERR(M)	C1 TXSEQ ENCERR(S)	C1 TXSEQ DECERR(M)	C1 TXSEQ DECERR(M)	C1 TXSEQ SEQPER(M)	C1 TXSEQ SEQPER(S)	C1 TXXSEQ _HWERR(M)	C1 TXXSE Q_HWERR (S)
58	DHUB::DIR ADR							
	C1 TXXSE Q_ENCERR (M)	C1 TXXSEQ _ENCERR(S)	C1 TXXSEQ _DECERR (M)	C1 TXXSEQ _DECERR(S)	C1 TXXSEQ _SEQPER(M)	C1 TXXSEQ _SEQPER(S)	C1 DBP0 XPTPER	C1 DBP0 XXPTPER
59	DHUB::DIR ADR							
	C1 DBP0 YPTPER	C1 DBP0 YYPTPER	C1 DBP3 XPTPER	C1 DBP03 XXPTPER	C1 TXSEQ HWERR(M)	C1 TXSEQ HWERR(S)	C1 DBP4 XPTPER	C1 DBP4 XXPTPER
5A	DHUB::DIR ADR							
	C1 DBP4 YPTPER	C1 DBP4 YYPTPER	C1 DBP1 XPTPER	C1 DBP1 XXPTPER	C1 DBP1 YPTPER	C1 DBP1 YYPTPER	C1 WAITCNT TIMCMPPER	C1 STTCNT TIMCMPPE R
5B	DHUB::LCK ADR							
	(Not used)							
5C	DHUB::LCK ADR							
	(Not used)							
5D	DHUB::LCK ADR							
	(Not used)							
5E	DHUB::LCK ADR							
	(Not used)							
5F	DHUB::LCK ADR							
	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB::BC ADR (Not used)							
61	DHUB::BC ADR (Not used)							
62	DHUB::BC ADR (Not used)							
63	DHUB::BC ADR							
	(Not used)	(Not used)	(Not used)	PRM1 SAMECLER _M	PRM1 SAMEER _M	SQD2 PRERR (M)	SQD2 PRERR (S)	PRM0 PRM64 EORERR _M
64	DHUB::BC ADR							
	WADB0 MPIDER _M	WADB0 LMCNT _M	(Not used)	WADB0 LMCNT _M	DCMDSEQ MATER (M)	DCMDSEQ MBTER (M)	DCMDSEQ MATER (S)	DCMDSEQ MBTER (S)
65	(Not used)							
66	(Not used)							
67	(Not used)							
68	(Not used)							
69	(Not used)							
6A	(Not used)							
6B	(Not used)							
6C	(Not used)							
6D	(Not used)							
6E	(Not used)							
6F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 60/70 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	(Not used)							
71	(Not used)							
72	(Not used)							
73	(Not used)							
74	(Not used)							
75	(Not used)							
76	(Not used)							
77	(Not used)							
78	(Not used)							
79	(Not used)							
7A	(Not used)							
7B	(Not used)							
7C	DHUB::PCIERRO/I							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	DHUB::PCIERRO/I							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ_ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	DHUB::PCIERRO/I							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7F	DHUB::PCIERRO/I							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'60' : MPA XR CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2D	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2E	DHUB::PASSWORD CHK1B							
	(Not used)	(Not used)	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWD CHK1B
2F	DHUB::ROTX ERR STS 1							
	MMOLPER 3	MMOLPER 2	MMOLPER 1	MMOLPER 0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
30	DHUB::RXXR ERR STS 1							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPER3	CMPER2	CMPER1	CMPER0
31	DHUB::RXXR ERR STS 1							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
32	DHUB::RXXR ERR STS 1							
	MMOLPER 3	MMOLPER 2	MMOLPER 1	MMOLPER 0	MMOPER3	MMOPER2	MMOPER1	MMOPER0

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'70' : MPA XR CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2D	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2E	DHUB::PASSWORD CHK1B							
	(Not used)	(Not used)	FMDIR	ILBCNT	ILADR	BSTRD	BSTWR	PWD CHK1B
2F	DHUB::ROTX ERR STS 1							
	MMOLPER 3	MMOLPER 2	MMOLPER 1	MMOLPER 0	MMOPER3	MMOPER2	MMOPER1	MMOPER0
30	DHUB::RXXR ERR STS 1							
	BEHPER	BELPER	BYTPER1	BYTPER0	CMPER3	CMPER2	CMPER1	CMPER0
31	DHUB::RXXR ERR STS 1							
	ADRPER7	ADRPER6	ADRPER5	ADRPER4	ADRPER3	ADRPER2	ADRPER1	ADRPER0
32	DHUB::RXXR ERR STS 1							
	MMOLPER 3	MMOLPER 2	MMOLPER 1	MMOLPER 0	MMOPER3	MMOPER2	MMOPER1	MMOPER0

Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '04'							
41	DHUB::FMSEQ ERRREG (Not used)							
42	DHUB::FMSEQ ERRREG (Not used)							
43	DHUB::FMSEQ ERRREG							
	ILLACS MP3	DECODE MP3	ENCODE MP3	CST MP3	ILLACS MP2	DECODE MP2	ENCODE MP2	CST MP2
44	DHUB::FMSEQ ERRREG							
	ILLACS MP1	DECODE MP1	ENCODE MP1	CST MP1	ILLACS MP0	DECODE MP0	ENCODE MP0	CST MP0
45	DHUB::FMSEQCNT ERRREG							
	(Not used)	(Not used)	ACC TOUT M	WEH TOUT M	WP TOUT MP3	ACC PER MP3	WEH PER MP3	WP PER MP3
46	DHUB::FMSEQCNT ERRREG							
	(Not used)	(Not used)	ACC TOUT M	WEH TOUT M	WP TOUT MP2	ACC PER MP2	WEH PER MP2	WP PER MP2
47	DHUB::FMSEQCNT ERRREG							
	(Not used)	(Not used)	ACC TOUT M	WEH TOUT M	WP TOUT MP1	ACC PER MP1	WEH PER MP1	WP PER MP1
48	DHUB::FMSEQCNT ERRREG							
	(Not used)	(Not used)	ACC TOUT M	WEH TOUT M	WP TOUT MP0	ACC PER MP0	WEH PER MP0	WP PER MP0
49	DHUB::CMP ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	FMSEQ MP3	FSEQ2 MP3	FSEQ1 MP3	FMARB MP3
4A	DHUB::CMP ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	FMSEQ MP2	FSEQ2 MP2	FSEQ1 MP2	FMARB MP2
4B	DHUB::CMP ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	FMSEQ MP1	FSEQ2 MP1	FSEQ1 MP1	FMARB MP1
4C	DHUB::CMP ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	FMSEQ MP0	FSEQ2 MP0	FSEQ1 MP0	FMARB_MP 0
4D	DHUB::FMARB ERRREG (Not used)							
4E	DHUB::FMARB ERRREG (Not used)							
4F	DHUB::FMARB ERRREG (Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB::FMARB ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	CST_MP0	CST_MP1	CST_MP2	CST_MP3
51	DHUB::FREGSEQ2 ERRREG							
	(Not used)							
52	DHUB::FREGSEQ2 ERRREG							
	(Not used)							
53	DHUB::FREGSEQ2 ERRREG							
	(Not used)							
54	DHUB::FREGSEQ2 ERRREG							
	(Not used)	(Not used)	(Not used)	(Not used)	CST_MP0	CST_MP1	CST_MP2	CST_MP3
55	DHUB::MPi ERRSTS0							
	(Not used)							
56	DHUB::MPi ERRSTS0							
	BNKWR 2ERR7	BNKWR 2ERR6	BNKWR 2ERR5	BNKWR 2ERR4	BNKWR 2ERR3	BNKWR 2ERR2	BNKWR 2ERR1	BNKWR 2ERR0
57	DHUB::MPi ERRSTS0							
	BNKWR 1ERR7	BNKWR 1ERR6	BNKWR 1ERR5	BNKWR 1ERR4	BNKWR 1ERR3	BNKWR 1ERR2	BNKWR 1ERR1	BNKWR 1ERR0
58	DHUB::MPi ERRSTS0							
	BNKWR 0ERR7	BNKWR 0ERR6	BNKWR 0ERR5	BNKWR 0ERR4	BNKWR 0ERR3	BNKWR 0ERR2	BNKWR 0ERR1	BNKWR 0ERR0
59	DHUB::MPi ERRSTS1							
	(Not used)							
5A	DHUB::MPi ERRSTS1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	WRPRTCT	JISOERR
5B	DHUB::MPi ERRSTS1							
	BNKRD 1ERR7	BNKRD 1ERR6	BNKRD 1ERR5	BNKRD 1ERR4	BNKRD 1ERR3	BNKRD 1ERR2	BNKRD 1ERR1	BNKRD 1ERR0
5C	DHUB::MPi ERRSTS1							
	BNKRD 0ERR7	BNKRD 0ERR6	BNKRD 0ERR5	BNKRD 0ERR4	BNKRD 0ERR3	BNKRD 0ERR2	BNKRD 0ERR1	BNKRD 0ERR0
5D	DHUB::MPi FREGBUF ERR							
	(Not used)							
5E	DHUB::MPi FREGBUF ERR							
	(Not used)							
5F	DHUB::MPi FREGBUF ERR							
	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB::MPi FREGBUF ERR							
	DT (3)	DT (2)	DT (1)	DT (0)	ADR (3)	ADR (2)	ADR (1)	ADR (0)
61	DHUB::MPi FMPATH ERR							
	(Not used)							
62	DHUB::MPi FMPATH ERR							
	(Not used)	(Not used)	(Not used)	(Not used)	RDDT_PER (3)	RDDT_PER (2)	RDDT_PER (1)	RDDT_PER (0)
63	DHUB::MPi FMPATH ERR							
	(Not used)	ADOUT_PER (3)	ADOUT_PER (2)	ADOUT_PER (1)	ADOUT_PER (0)	DTOUT_PER	CMP_DT	CMP_ADRC MP
64	DHUB::MPi FMPATH ERR							
	(Not used)	(Not used)	CMP_OE1	CMP_OE0	(Not used)	(Not used)	CMP_CS1	CMP_CS1
65	DHUB::MPi FREGSEQ1 ERR							
	(Not used)							
66	DHUB::MPi FREGSEQ1 ERR							
	(Not used)							
67	DHUB::MPi FREGSEQ1 ERR							
	(Not used)							
68	DHUB::MPi FREGSEQ1 ERR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	CST
69	DHUB::WR BNK EXL SET							
	MP0BN K0SET	MP0BN K1SET	MP0BN K2SET	MP0BN K3SET	MP0BN K4SET	MP0BN K5SET	MP0BN K6SET	MP0BN K7SET
6A	DHUB::WR BNK EXL SET							
	MP1BN K0SET	MP1BN K1SET	MP1BN K2SET	MP1BN K3SET	MP1BN K4SET	MP1BN K5SET	MP1BN K6SET	MP1BN K7SET
6B	DHUB::WR BNK EXL SET							
	MP2BN K0SET	MP2BN K1SET	MP2BN K2SET	MP2BN K3SET	MP2BN K4SET	MP2BN K5SET	MP2BN K6SET	MP2BN K7SET
6C	DHUB::WR BNK EXL SET							
	MP3BN K0SET	MP3BN K1SET	MP3BN K2SET	MP3BN K3SET	MP3BN K4SET	MP3BN K5SET	MP3BN K6SET	MP3BN K7SET
6D	DHUB::RD BNK EXL SET							
	MP0BN K0SET	MP0BN K1SET	MP0BN K2SET	MP0BN K3SET	MP0BN K4SET	MP0BN K5SET	MP0BN K6SET	MP0BN K7SET
6E	DHUB::RD BNK EXL SET							
	MP1BN K0SET	MP1BN K1SET	MP1BN K2SET	MP1BN K3SET	MP1BN K4SET	MP1BN K5SET	MP1BN K6SET	MP1BN K7SET
6F	DHUB::RD BNK EXL SET							
	MP2BN K0SET	MP2BN K1SET	MP2BN K2SET	MP2BN K3SET	MP2BN K4SET	MP2BN K5SET	MP2BN K6SET	MP2BN K7SET

Byte27 = 8E & Byte34 = X0 & Byte28 = 61/71 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	DHUB::RD BNK EXL SET							
	MP3BN K0SET	MP3BN K1SET	MP3BN K2SET	MP3BN K3SET	MP3BN K4SET	MP3BN K5SET	MP3BN K6SET	MP3BN K7SET
71	(Not used)							
72	(Not used)							
73	(Not used)							
74	(Not used)							
75	(Not used)							
76	(Not used)							
77	(Not used)							
78	(Not used)							
79	(Not used)							
7A	(Not used)							
7B	(Not used)							
7C	DHUB::PCIERRO/I							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	DHUB::PCIERRO/I							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ_ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	DHUB::PCIERRO/I							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7F	DHUB::PCIERRO/I							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'61' : FM CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT 4MP	DRR INT 4MP	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP	(Not used)	(Not used)	FCA CHK2 B 8/4MP	DRR CHK2 B 8/4MP
2E	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1B Q	PCI CHK1 M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2F	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
30	DHUB::BSINTSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	LL INT	SCINT A
31	DHUB::BSINTSTS							
	SCINT B	SCINT C	SCINT D	BC0	BC1	BC2	BC3	BC4
32	DHUB::BSINTSTS							
	BC5	BC6	BC7	BCREQ	BCINT A	BCINT B	BCINT C	BCINT D

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'71' : FM CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT 4MP	DRR INT 4MP	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP	(Not used)	(Not used)	FCA CHK2 B 8/4MP	DRR CHK2 B 8/4MP
2E	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2F	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
30	DHUB::BSINTSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	LL INT	SCINT A
31	DHUB::BSINTSTS							
	SCINT B	SCINT C	SCINT D	BC0	BC1	BC2	BC3	BC4
32	DHUB::BSINTSTS							
	BC5	BC6	BC7	BCREQ	BCINT A	BCINT B	BCINT C	BCINT D

Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '05'							
41	FCA::OTHERCHK1							
	XRSEQ ENCODER ERROR	XRSEQ DECODER ERROR	PCI-X OUTDTH PARITY ERROR (3)	PCI-X OUTDTH PARITY ERROR (2)	PCI-X OUTDTH PARITY ERROR (1)	PCI-X OUTDTH PARITY ERROR (0)	PCI-X OUTDTL PARITY ERROR (3)	PCI-X OUTDTL PARITY ERROR (2)
42	FCA::OTHERCHK1							
	PCI-X OUTDTL PARITY ERROR (1)	PCI-X OUTDTL PARITY ERROR (0)	YWING ERROR	ADR _{x8} ERROR	BLKACC ERROR	BYTECNT ERROR	XR MP RDT PER (3)	XR MP RDT PER (2)
43	FCA::OTHERCHK1							
	XR MP RDT PER (1)	XR MP RDT PER (0)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	FCA::CHK1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Bit00
45	FCA::DTNCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
46	FCA::CWCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
47	FCA::CRCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
48	FCA::PCICCHK1							
	Bit31	Bit30	Bit29	Bit28	Bit27	Bit26	Bit25	Bit24
49	FCA::PMEMCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
4A	FCA::ROSCCHK1B							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
4B	(Not used)							
4C	(Not used)							
4D	(Not used)							
4E	(Not used)							
4F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	FCA::OTHERCHK1							
	XRSEQ ENCODER ERROR	XRSEQ DECODER ERROR	PCI-X OUTDTH PARITY ERROR (3)	PCI-X OUTDTH PARITY ERROR (2)	PCI-X OUTDTH PARITY ERROR (1)	PCI-X OUTDTH PARITY ERROR (0)	PCI-X OUTDTL PARITY ERROR (3)	PCI-X OUTDTL PARITY ERROR (2)
51	FCA::OTHERCHK1							
	PCI-X OUTDTL PARITY ERROR (1)	PCI-X OUTDTL PARITY ERROR (0)	YWING ERROR	ADR _{x8} ERROR	BLKACC ERROR	BYTECNT ERROR	XR MP RDT PER (3)	XR MP RDT PER (2)
52	FCA::OTHERCHK1 (Not used)							
53	FCA::CHK1							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Bit00
54	FCA::DTNCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
55	FCA::CWCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
56	FCA::CRCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
57	FCA::PCICLK1							
	Bit31	Bit30	Bit29	Bit28	Bit27	Bit26	Bit25	Bit24
58	FCA::PMEMCHK1							
	Bit31	Bit30	Bit29	Bit28	(Not used)	(Not used)	(Not used)	(Not used)
59	(Not used)							
5A	(Not used)							
5B	(Not used)							
5C	(Not used)							
5D	(Not used)							
5E	(Not used)							
5F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	(Not used)							
61	(Not used)							
62	(Not used)							
63	(Not used)							
64	(Not used)							
65	(Not used)							
66	(Not used)							
67	(Not used)							
68	(Not used)							
69	(Not used)							
6A	(Not used)							
6B	(Not used)							
6C	(Not used)							
6D	(Not used)							
6E	(Not used)							
6F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 62 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	(Not used)							
71	(Not used)							
72	(Not used)							
73	(Not used)							
74	(Not used)							
75	(Not used)							
76	(Not used)							
77	(Not used)							
78	FCA::PCIERR0							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
79	FCA::PCIERR0							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7A	FCA::PCIERR0							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7B	FCA::PCIERR0							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR
7C	FCA::PCIERR12							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	FCA::PCIERR12							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	FCA::PCIERR12							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7F	FCA::PCIERR12							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'62' : FCA CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT 4MP	DRR INT 4MP	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP
2E	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP	(Not used)	(Not used)	FCA CHK2 B 8/4MP	DRR CHK2 B 8/4MP
2F	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
30	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
31	FCA::FCA CHK1B							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	bit00
32	FCA:: FCA CHK1B							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	bit00

Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information	0
0F	SEMI-SYNC	RCU SIM	0	0	0	0	0	0
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '06'							
41	DHUB::DRSTATS							
	END_64	EXEC_64	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
42	DHUB::DRSTATS							
	CHK2	CHK2A (DRR_CTL)	CHK2B (DRR_DT)	CHK2C (DRR_MICRO)	CHK2D (CHSN_CTL)	CHK2E (CNSN_DT)	CHK2F (TO/ABT)	CHK1B
43	DHUB::DRSTATS							
	POUSE	DSKTIMOU T	P0_ERRINT	P1_ERRINT	ST0_VALID	ST0_CRAB/ CMA ERR	ST1_VALID	ST1_CRAB/ CMA ERR
44	DHUB::DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO_#3	MP NO_#2	MP NO_#1	MP NO_#0
45	DHUB::DRSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
46	DHUB::DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
47	DHUB::DRERRB							
	XRSEQ_EN CERR (M)	XRSEQ(S) EQPER (M)	XRSEQ_DEC ERR (M)	XRSEQ(S) TERR (M)	XRSEQ_HW ERR (M)	XRSEQ RDDTPER (M)	DTLCSEQ RDDTPER (M)	DTLCSEQ RDDTPER (S)
48	DHUB::DRERRB							
	XRSEQ_EN CERR (S)	XRSEQ (S) EQPER (S)	XRSEQ_DEC ERR (S)	XRSEQ(S) TERR (S)	XRSEQ_HW ERR (S)	XRSEQ RDDTPER (S)	SQD0M RDDTPER (M)	SQD0M RDDTPER (S)
49	DHUB::DRERRB							
	XRSEQCMP _CMPERR	DCMDSEQ RDDTPER (M)	DCMDSEQ RDDTPER (S)	SQD4M RDDTPER (M)	SQD4M RDDTPER (S)	SQD4N RDDTPER (M)	SQD4N RDDTPER (S)	DBD2 RDDTPER
4A	DHUB::DRERRB							
	SQDON RDDTPER (M)	SQDON RDDTPER (S)	SQD1 RDDTPER (M)	SQD1 RDDTPER (S)	SQD2 RDDTPER (S)	SQD2 RDDTPER (S)	SQD3 RDDTPER (S)	SQD3 RDDTPER (S)
4B	DHUB::DRERRB							
	SQDE0M RDDTPER (M)	SQDE0M RDDTPER (S)	SQDE0N RDDTPER (M)	SQDE0N RDDTPER (S)	DETDR RDDTPER	DETDR W/R_DTPER	C0 PLCNTR X_RDDTPER	C0 PLCNTT X_RDDTPE R
4C	DHUB::DRERRB							
	S64T RDDTPER (M)	S64T RDDTPER (S)	DBD3 RDDTPER	DBD4M RDDTPER	DBD4N RDDTPER	DBD1M RDDTPER	DBD1N RDDTPER	ACGEN RDDTPER
4D	DHUB::DRERRB							
	DBDE1N RDDTPER	DBDE1M RDDTPER	DBDE4N RDDTPER	DBDE4M RDDTPER	S64RCNT RDDTPER	RDADRCNT M_DDTPER	RDADRCNT N RDDTPER	WTADRCN TM DDTPER
4E	DHUB::DRERRB							
	WTADRCN TN DDTPER	PLCNTD0M RDDTPER	PLCNTD0N RDDTPER	PLCNTD4M RDDTPER	PLCNTD4N RDDTPER	SBCNT1 RDDTPER	SBCNT3 RDDTPER	PLCNT5281 _RDDTPER
4F	DHUB::DRERRC							
	PLCNT5283 _RDDTPER	RPLRCCHK RDDTPER	ST0CHK RDPER	ST1CHK RDPER	WLACHK PDPER	WSLRCCHK _PDPER	LACHG PDPER	ADRCNT64 _PDPER

Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB::DRERRC							
	FERDR W/ R DTPER	FERDR RDPER	ERMDR W/R DTPER	ERMDR RDPER	DRMD0 PASSWORD	DRRERR RDPER	DTCNT RDPER	CMD64SEQ HWERR(S)
51	DHUB::DRERRC							
	WADB0 ADR2PER	WADB0 ADR3PER	WADB0 WTPER	WADB0 RDPER	DRMD0 ADR2PER	DRMD0 ADR3PER	DRMD0 WTPER	DRMD0 RDPER
52	DHUB::DRERRC							
	(Not used)	WADB0 XRBCERR	WADB0 XRADREER	XRSEQ BLKACSERR S	XRSEQ BLKACSERR M	WADB0 LM RDDTPER	PRM0 RDP ER	PRM1 RDP ER
53	DHUB::DRERRC							
	CMD64SEQ _HWERR (M)	CMD64SEQ RDPER (M)	CMD64SEQ ENCERR (S)	PRM64 WTP ER	PRM64 RDP ER	RDB0 DTPE R	CMD64SEQ SEQPER (M)	CMD64SEQ _DECERR (M)
54	DHUB::DRERRC							
	(Not used)	(Not used)	(Not used)	CMD64SEQ SEQPER (S)	CMD64SEQC MP_CMPE R	CMD64SEQ DECERR (S)	CMD64SEQ ENCERR (M)	CMD64SEQ _RDPER (S)
55	DHUB::DRERRC							
	MSTSEQ ENCERR (M)	MSTSEQ SEQPER (M)	MSTSEQ DECERR (M)	MSTSEQ HWERR (M)	MSTSEQ RDPER (M)	(Not used)	(Not used)	(Not used)
56	DHUB::DRERRC							
	DBD3X RD PER	SQD3X RDPER (M)	SQD3X RDPER (S)	MSTSEQ ENCERR (S)	MSTSEQ SEQPER (S)	MSTSEQ DECERR (S)	MSTSEQ HWERR (S)	MSTSEQ RDPER (S)
57	DHUB::DRERRD							
	C0 STTCNT _RDDTPER	C0 WAITCN T_RDDTPER	C0 ACKTCN T_RDDTPER	C0 ENTTCN T_RDDTPER	C0 CHABT RDDTPER (M)	C0 CHABT RDDTPER (S)	C0 RXSEQ RDDTPER (M)	C0 RXSEQ RDDTPER (S)
58	DHUB::DRERRD							
	C0 SQP3 RDDTPER (M)	C0 SQP3 RDDTPER (S)	C0 TXSEQ RDDTPER (M)	C0 TXSEQ RDDTPER (S)	C0 TXXSEQ _RDDTPER (M)	C0 TXXSEQ _RDDTPER (S)	C0 TOKEIC NT_RDDTPE R	C0 DBP0 RDDTPER
59	DHUB::DRERRD							
	C1 STTCNT _RDDTPER	C1 WAITCN T_RDDTPER	C1 ACKTCN T_RDDTPER	C1 ENTTCN T_RDDTPER	C1 CHABT RDDTPER (M)	C1 CHABT RDDTPER (S)	C1 RXSEQ RDDTPER (M)	C1 RXSEQ RDDTPER (S)
5A	DHUB::DRERRD							
	C1 SQP3 RDDTPER (M)	C1 SQP3 RDDTPER (S)	C1 TXSEQ RDDTPER (M)	C1 TXSEQ RDDTPER (S)	C1 TXXSEQ _RDDTPER (M)	C1 TXXSEQ _RDDTPER (S)	C1 TOKEIC NT_RDDTPE R	C1 DBP0 RDDTPER
5B	DHUB::DRERRD							
	FERP0 FERDTPER	FERP1 FERDTPER	C1 DBP3 RDDTPER	C1 DBP4 RDDTPER	C1 DBP1 RDDTPER	C0 DBP3 RDDTPER	C0 DBP4 RDDTPER	C0 DBP1 RDDTPER
5C	DHUB::DRERRD							
	P0MD0 WTDTPER (S)	P1MD0 WTDTPER (S)	P0MD0 RDDTPER	P0MD0 ADR2PER	P0MD0 ADR3PER	P1MD0 RDDTPER	P1MD0 ADR2PER	P1MD0 ADR3PER
5D	DHUB::DRERRD							
	FERP0 RDDTPER	FERP1 RDDTPER	XRABT DECERR (S)	XRABT ENCERR (S)	XRABT GNTERR (S)	XRABT HWERR (S)	XRABT (S) EQPER (S)	XRABT RDDTPER (S)
5E	DHUB::DRERRD							
	TDTCNT RDDTPER	(Not used)	XRABT DECERR (M)	XRABT ENCERR (M)	XRABT GNTERR (M)	XRABT HWERR (M)	XRABT(S) EQPER (M)	XRABT RDDTPER (M)
5F	DHUB::DRERRE							
	FERDT RDDTPER	ERMDT RDDTPER	DETD RDDTPER	DBFE1 RDDTPER	DBF0 RDDTPER	DBF1 RDDTPER	DBFE3 RDDTPER	DBF3 RDDTPER

Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB::DRERRE							
	DBF4 RDDTPER	DBF5 RDDTPER	SQF0 RDDTPER (M)	SQF0 RDDTPER (S)	SQF1 RDDTPER (M)	SQF1 RDDTPER (S)	SQF2 RDDTPER (M)	SQF2 RDDTPER (S)
61	DHUB::DRERRE							
	SQF3 RDDTPER (M)	SQF3 RDDTPER (S)	SQF4 RDDTPER (M)	SQF4 RDDTPER (S)	SQF5 RDDTPER (M)	SQF5 RDDTPER (S)	SQFE2 RDDTPER (M)	SQFE2 RDDTPER (S)
62	DHUB::DRERRE							
	WDTCHK R DDTPER	RDTCHK RDDTPER	DTMD0 ADR2PER	DTMD0 ADR3PER	DTMD0 RDDTPER	DTMD0 WDDTPER (S)	SQF6 RDDTPER (M)	SQF6 RDDTPER (S)
63	DHUB::DRERRE							
	RXACFF RDDTPER	ADRCNTF RDDTPER	PLCNTF0 RDDTPER	PLCNTF4 RDDTPER	PLCNTF5 RDDTPER	ST0CHKF RDDTPER	ST1CHKF RDDTPER	TSLRCCHK RDDTPER
64	DHUB::DRERRE							
	DB1M C1 CRCTERR	DB1M C1 UCRCTERR	DB1M C1 ADRERR	DB1M C1 ECCERR	DB1N C1 CRCTERR	DB1N C1 UCRCTERR	DB1N C1 ADRERR	DB1N C1 ECCERR
65	DHUB::DRERRE							
	DBF3 C1 ECCERR	(Not used)	(Not used)	C1_DSKCNT RDDTPER	C0_DSKCNT _RDDTPER	PCCHK1B_ MONI	YWINGERR _MONI	QDTACHK1 B MONI
66	DHUB::DRERRE							
	C1 PLCNTRX RDDTPER	C1 PLCNTIX RDDTPER	(Not used)	(Not used)	(Not used)	DBF3 C1 CRCTERR	DBF3 C1 UCRCTERR	DBF3 C1 ADRERR
67	(Not used)							
68	(Not used)							
69	(Not used)							
6A	(Not used)							
6B	(Not used)							
6C	(Not used)							
6D	(Not used)							
6E	(Not used)							
6F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 63/73 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	(Not used)							
71	(Not used)							
72	(Not used)							
73	(Not used)							
74	(Not used)							
75	(Not used)							
76	(Not used)							
77	(Not used)							
78	(Not used)							
79	(Not used)							
7A	(Not used)							
7B	(Not used)							
7C	DHUB::PCIERR0/1							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	DHUB::PCIERR0/1							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ_ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	DHUB::PCIERR0/1							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7F	DHUB::PCIERR0/1							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'63' : QDTA CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INTB 8/4MP	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP
2D	DHUB::QDTA DRMODEO							
	TREND_ER_EN	DRRCHK1B_EREN	PCICHK1B_EREN	YER_EREN	(Not used)	DRBACS2	DRBACS1	DRBACS0
2E	DHUB::QDTA DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DRR MICRO)	CHK2D(CH SN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
2F	DHUB::QDTA DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO#3	MP NO#2	MP NO#1	MP NO#0
30	DHUB::QDTA DRSTATS							
	ABORT	YWING_ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
31	DHUB::QDTA DRERRC							
	SQDEOM R DDTPER(M)	SQDEOM R DDTPER(S)	SQDEON R DDTPER(M)	SQDEON R DDTPER(S)	DETDR RDDTPER	DETDR_W/R DTPER	CO PLCNT RX_RDDTP_ER	CO PLCNT TX_RDDTP_ER
32	DHUB::QDTA DRERRE							
	DBF3 C1_ECCERR	(Not used)	(Not used)	C1_DSKCNT RDDTPER	C0_SKCNT RDDTPER	PCICHK1B_MONI	YWINGERR_MONI	QDTACHK1B_MONI

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'73' : QDTA-MICRO CHK1B)							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP
2D	DHUB::QDTA DRMODEO							
	TREND_ER_EN	DRRCHK1B_EREN	PCICHK1B_EREN	YER_EREN	(Not used)	DRBACS2	DRBACS1	DRBACS0
2E	DHUB::QDTA DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DR R MICRO)	CHK2D(CH SN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
2F	DHUB::QDTA DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO#3	MP NO#2	MP NO#1	MP NO#0
30	DHUB::QDTA DRSTATS							
	ABORT	YWING_ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
31	DHUB::QDTA DRERRC							
	(Not used)	WADBO XRBCERR	WADBO X RADRERR	XRSEQ BL KACSERR_S	XRSEQ BL KACSERR_M	WADBO L MRDDTPER	PRM0 RDPER	PRM1 RDPER
32	DHUB::QDTA DRERRC							
	CMD64SEQ_HWERR (M)	CMD64SEQ_RDPER (M)	CMD64SEQ_ENCERR (S)	PRM64 WTPER	PRM64 RDPER	RDBO DTPER	CMD64SEQ_SEQPER (M)	CMD64SEQ_DECERR (M)

Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '07'							
41	DHUB/FCA::PCI0(1) ERROR0							
	TXADPE0	TXADPE1	TXADPE2	TXADPE3	TXADPE4	TXADPE5	TXADPE6	TXADPE7
42	DHUB/FCA::PCI0(1) ERROR0							
	(Not used)	PERR	SERR	(Not used)	(Not used)	PERR	PCI64PE	PCIPE
43	DHUB/FCA::PCI0(1) ERROR0							
	RXADRPEH 0	RXADRPEH 1	RXADRPEH 2	RXADRPEH 3	RXADRPEL0	RXADRPEL1	RXADRPEL2	RXADRPEL 3
44	DHUB/FCA::PCI0(1) ERROR0							
	(Not used)	ATRIPE0	ATRIPE1	ATRIPE2	ATRIPE3	BLKACSP	ATROPE	RXCMDPE
45	DHUB/FCA::PCI0(1) ERROR1							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
46	DHUB/FCA::PCI0(1) ERROR1							
	(Not used)	EQLSTCPE	OVIPE	OV4PE	DETIWCPE	(Not used)	OUT ADROVRN	IN ADROVRN
47	DHUB/FCA::PCI0(1) ERROR1							
	(Not used)	TIMCNTPE0	TIMCNTPE1	TIMCNTPE2	TIMOVR1	TIMOVR0	PTSQCMPER	SELBCPE
48	DHUB/FCA::PCI0(1) ERROR1							
	TGTABT1	TGTABT0	(Not used)	XLASTDPE	DCNTPE0	DCNTPE1	SEQPE1	SEQPE0
49	DHUB/FCA::PCI0(1) ERROR2							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
4A	DHUB/FCA::PCI0(1) ERROR2							
	SQCMPERR	(Not used)	OVCMPERR	OVNCMPER R	DETIWCUPP E	(Not used)	OUTADROV RN	INADROVR N
4B	DHUB/FCA::PCI0(1) ERROR2							
	OUT POINTPE	IN POINTPE	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	DHUB/FCA::PCI0(1) ERROR2							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4D	DHUB/FCA::PCI0(1) ERROR3							
	SETERR1	SETERR0	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4E	DHUB/FCA::PCI0(1) ERROR3							
	SEQ CMPERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	DHUB/FCA::PCI0(1) ERROR3							
	(Not used)	TGTTXHUN CR	TGTTXHAD ER	TGTTXHEC CHDE	(Not used)	TGTTXLUN CR	TGTTXHLA DER	TGTTXLEC CHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB/FCA::PCI0(1) ERR0R3							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
51	DHUB/FCA::PCI0(1) ERR0R4							
	(Not used)	(Not used)	(Not used)	(Not used)	RTR YCNTPE	DWCNTPE	DCNTPE0	DCNTPE1
52	DHUB/FCA::PCI0(1) ERR0R4							
	LASTP8EV	LAST2P8EV	LAST3P8EV	LAST4P8EV	NLASTDT	LASTDT	LAST1WDE	RTRY CNTOVPE
53	DHUB/FCA::PCI0(1) ERR0R4							
	LASTP4	LAST2P4	LAST3P4	LAST4P4	(Not used)	(Not used)	(Not used)	(Not used)
54	DHUB/FCA::PCI0(1) ERR0R4							
	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINTPE	IN POINTPE	TXBEPE	TXBELPE
55	DHUB/FCA::PCI0(1) ERR0R5							
	(Not used)	(Not used)	(Not used)	CMDERR1	CMDERR0	SEQPE1	SEQPE0	SEQ CMPERR
56	DHUB/FCA::PCI0(1) ERR0R5							
	SDISCT1	SDISCT0	TGTABT1	TGTABT0	MSTABT1	MSTABT0	RTRY OVER1	RTRY OVER0
57	DHUB/FCA::PCI0(1) ERR0R5							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
58	DHUB/FCA::PCI0(1) ERR0R5							
	ARBENERR 1	ARBERR1	DECERR1	ENCERR1	ARBENERR0	ARBERR0	DECERR0	ENCERR0
59	DHUB/FCA::PCI0(1) ERR0R6							
	SEQ CMPERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5A	DHUB/FCA::PCI0(1) ERR0R6							
	SEQ CMPERR	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5B	DHUB/FCA::PCI0(1) ERR0R6							
	(Not used)	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5C	DHUB/FCA::PCI0(1) ERR0R6							
	(Not used)	(Not used)	XFPCIERR	XEPCIERR	XDPCIERR	XCPCIERR	XBPCIERR	XAPCIERR
5D	DHUB/FCA::PCI0(1) ERR0R7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTPOINTP E	INPOINTPE	TXBEHPE	TXBELPE
5E	DHUB/FCA::PCI12 ERR0R7							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
5F	DHUB/FCA::PCI0(1) ERR0R7							
	(Not used)	(Not used)	(Not used)	CLASTPER	NLASTPER	SELBCPER	XBCNTPE0	XBCNTPE1

Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB/FCA::PCI0(1) ERROR7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINTPE	INPOINTPE	TXBEHPE	TXBELPE
61	DHUB/FCA::PCI0(1) ERROR8							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
62	DHUB/FCA::PCI0(1) ERROR8							
	SEQ CMPERR	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
63	DHUB/FCA::PCI0(1) ERROR8							
	ADRCNTPE 0	ADRCNTPE1	ADRCNTPE2	ADRCNTPE3	ADRCNTPE4	ADRCNTPE5	ADRCNTPE6	ADRCNTPE 7
64	DHUB/FCA::PCI0(1) ERROR8							
	(Not used)	(Not used)	(Not used)	(Not used)	BURSTPER0	BURSTPER1	BCNTPER0	BCNTPER1
65	DHUB/FCA::PCI0(1) ERROR9							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
66	DHUB/FCA::PCI0(1) ERROR9							
	(Not used)	OV32PER	OV2PER	SOOV4PER	DETIWCPE	BRDYPER	OUTADROV RN	INADROVR N
67	DHUB/FCA::PCI0(1) ERROR9							
	SEQ CMPERR	(Not used)						
68	DHUB/FCA::PCI0(1) ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	OUTPOINPE R	INPOINTPE R
69	DHUB/FCA::PCI0(1) ERROR10							
	(Not used)	SEQCMPE R	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6A	DHUB/FCA::PCI0(1) ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTSERR	OUTMPERR	PCI64PE	PCIPE
6B	DHUB/FCA::PCI0(1) ERROR10							
	(Not used)	SEQCMPE R	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6C	DHUB/FCA::PCI12 ERROR11							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
6D	DHUB/FCA::PCI0(1) ERROR11							
	(Not used)	EQLSTCNTP E	OV1PER	OV4PER	DETIWCUPP ER	(Not used)	OUT ADROVRN	IN ADROVRN
6E	DHUB/FCA::PCI0(1) ERROR11							
	(Not used)	MSTRXHUN CR	MSTRXHAD ER	MSTRXHEC CHDE	(Not used)	MSTRXLUN CR	MSTRXLAD ER	MSTRXLEC CHDE
6F	DHUB/FCA::PCI0(1) ERROR11							
	(Not used)	TGTRXHUN CR	TGTRXHAD ER	TGTRXHEC CHDE	(Not used)	TGTRXLUN CR	TGTRXLAD ER	TGTRXLEC CHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 64/65/74/75 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	DHUB/FCA::PCI0(1) ERROR12							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
71	DHUB/FCA::PCI0(1) ERROR12							
	(Not used)	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
72	DHUB/FCA::PCI0(1) ERROR12							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
73	DHUB/FCA::PCI0(1) ERROR12							
	(Not used)	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
74	DHUB/FCA::BRGERR							
	BARB SEQPER	BARB DECERR	BARB ENCERR	BARB CMP ERR	BRGBUFA OPTPE	BRGBUFA IPTPE	BRGBUFB OPTPE	BRGBUFB IPTPE
75	DHUB/FCA::BRGERR							
	BRGSEQA CMDERR	BRGSEQA SEQPE	BRGSEQA DECERR	BRGSEQA ENCERR	BRGSEQA CMPERR	BRGERRA NLASTPE	BRGERRA SELBCPE	BRGERRA BCNTPE
76	DHUB/FCA::BRGERR							
	BRGSEQB CMDERR	BRGSEQB SEQPE	BRGSEQB DECERR	BRGSEQB ENCERR	BRGSEQB CMPERR	BRGERRB NLASTPE	BRGERRB SELBCPE	BRGERRB BCNTPE
77	DHUB/FCA::BRGERR							
	XTRXDB IPPE	XTRXDB OPPE	XTSQ ENCD ECERR	XTSQ SEQPE	XTTXDB IPPE	XTTXDB OPPE	XTTXB IPPE	XTTXB OPPE
78	DHUB/FCA::PCI0(1) CK1TLOG0							
	CK1TADR6 3	CK1TADR62	CK1TADR61	CK1TADR60	CK1TADR59	CK1TADR58	CK1TADR57	CK1TADR5 6
79	DHUB/FCA::PCI0(1) CK1TLOG0							
	CK1TADR5 5	CK1TADR54	CK1TADR53	CK1TADR52	CK1TADR51	CK1TADR50	CK1TADR49	CK1TADR4 8
7A	DHUB/FCA::PCI0(1) CK1TLOG0							
	CK1TADR4 7	CK1TADR46	CK1TADR45	CK1TADR44	CK1TADR43	CK1TADR42	CK1TADR41	CK1TADR4 0
7B	DHUB/FCA::PCI0(1) CK1TLOG0							
	CK1TADR3 9	CK1TADR38	CK1TADR37	CK1TADR36	CK1TADR35	CK1TADR34	CK1TADR33	CK1TADR3 2
7C	DHUB/FCA::PCI0(1) CK1TLOG1							
	CK1TADR3 1	CK1TADR30	CK1TADR29	CK1TADR28	CK1TADR27	CK1TADR26	CK1TADR25	CK1TADR2 4
7D	DHUB/FCA::PCI0(1) CK1TLOG1							
	CK1TADR2 3	CK1TADR22	CK1TADR21	CK1TADR20	CK1TADR19	CK1TADR18	CK1TADR17	CK1TADR1 6
7E	DHUB/FCA::PCI0(1) CK1TLOG1							
	CK1TADR1 5	CK1TADR14	CK1TADR13	CK1TADR12	CK1TADR11	CK1TADR10	CK1TADR09	CK1TADR0 8
7F	DHUB/FCA::PCI0(1) CK1TLOG1							
	CK1TADR0 7	CK1TADR06	CK1TADR05	CK1TADR04	CK1TADR03	CK1TADR02	CK1TADR01	CK1TADR0 0

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'64' : PCI_CHK1B(DHUB))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'65' : PCI_CHK1B(FCA))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'74' : PCI_CHK1B(DHUB))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'75' : PCI_CHK1B(FCA))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '08'							
41	FCA::PCI10 ERR0R0							
	TXADPE0	TXADPE1	TXADPE2	TXADPE3	TXADPE4	TXADPE5	TXADPE6	TXADPE7
42	FCA::PCI10 ERR0R0							
	(Not used)	PERR	SERR	(Not used)	(Not used)	PERR	PCI64PE	PCIPE
43	FCA::PCI12 ERR0R0							
	RXA DRPEH0	RXA DRPEH1	RXA DRPEH2	RXA DRPEH3	RXA DRPEL0	RXA DRPEL1	RXA DRPEL2	RXA DRPEL3
44	FCA::PCI12 ERR0R0							
	(Not used)	ATRIPE0	ATRIPE1	ATRIPE2	ATRIPE3	BLKACSP	ATROPE	RXCMDPE
45	FCA::PCI12 ERR0R1							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
46	FCA::PCI12 ERR0R1							
	(Not used)	EQLSTCPE	OV1PE	OV4PE	DETIWCPE	(Not used)	OUT ADROVRN	IN ADROVRN
47	FCA::PCI10 ERR0R1							
	(Not used)	TIMCNTPE0	TIMCNTPE1	TIMCNTPE2	TIMOVR1	TIMOVR0	PTSQCMPER	SELBCPE
48	FCA::PCI10 ERR0R1							
	TGTABT1	TGTABT0	(Not used)	XLASTDPE	DCNTPE0	DCNTPE1	SEQPE1	SEQPE0
49	FCA::PCI12 ERR0R2							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
4A	FCA::PCI12 ERR0R2							
	SQCMPEERR	(Not used)	OVCMPERR	OVNCMPER R	DETIWCUPP E	(Not used)	OUT ADROVRN	INA DROVRN
4B	FCA::PCI10 ERR0R2							
	OUT POINTPE	IN POINTPE	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	FCA::PCI10 ERR0R2							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4D	FCA::PCI12 ERR0R3							
	SETERR1	SETERR0	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
4E	FCA::PCI12 ERR0R3							
	SEQ CMPERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	FCA::PCI12 ERR0R3							
	(Not used)	TGTTX HUNCR	TGTTX HADER	TGTTX HECCHDE	(Not used)	TGTTX LUNCR	TGTTX HLADER	TGTTX LECCHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	FCA::PCI12 ERR0R3							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
51	FCA::PCI10 ERR0R4							
	(Not used)	(Not used)	(Not used)	(Not used)	RTRYCNTPE	DWCNTPE	DCNTPE0	DCNTPE1
52	FCA::PCI10 ERR0R4							
	LASTP8EV	LAST2P8EV	LAST3P8EV	LAST4P8EV	NLASTDT	LASTDT	LAST1WDE	RTRYCNT0 VPE
53	FCA::PCI10 ERR0R4							
	LASTP4	LAST2P4	LAST3P4	LAST4P4	(Not used)	(Not used)	(Not used)	(Not used)
54	FCA::PCI12 ERR0R4							
	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINTPE	INP OINTPE	TXBEPE	TXBELPE
55	FCA::PCI10 ERR0R5							
	(Not used)	(Not used)	(Not used)	CMDERR1	CMDERR0	SEQPE1	SEQPE0	SEQ CMPERR
56	FCA::PCI10 ERR0R5							
	SDISCT1	SDISCT0	TGTABT1	TGTABT0	MSTABT1	MSTABT0	RTRY OVER1	RTRY OVER0
57	FCA::PCI10 ERR0R5							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
58	FCA::PCI12 ERR0R5							
	ARBENERR 1	ARBERR1	DECERR1	ENCERR1	ARBENERR0	ARBERR0	DECERR0	ENCERR0
59	FCA::PCI12 ERR0R6							
	SEQCMPER R	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5A	FCA::PCI12 ERR0R6							
	SEQ CMPERR	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5B	FCA::PCI12 ERR0R6							
	(Not used)	CMDERR0	ORERR0	URERR0	PCIERR0	SEQPERR0	DECERR0	ENCERR0
5C	FCA::PCI12 ERR0R6							
	(Not used)	(Not used)	XFPCIERR	XEPCIERR	XDPCIERR	XCPCIERR	XBPCIERR	XAPCIERR
5D	FCA::PCI12 ERR0R7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINTPE	IN POINTPE	TXBEHPE	TXBELPE
5E	FCA::PCI12 ERR0R7							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
5F	FCA::PCI12 ERR0R7							
	(Not used)	(Not used)	(Not used)	CLASTPER	NLASTPER	SELBCPER	XBCNTPE0	XBCNTPE1

Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	FCA::PCI12 ERROR7							
	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINTPE	IN POINTPE	TXBEHPE	TXBELPE
61	FCA::PCI12 ERROR8							
	TXDTHPE0	TXDTHPE1	TXDTHPE2	TXDTHPE3	TXDTLPE0	TXDTLPE1	TXDTLPE2	TXDTLPE3
62	FCA::PCI12 ERROR8							
	SEQ CMPERR	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
63	FCA::PCI12 ERROR8							
	ADRCNTPE 0	ADRCNTPE1	ADRCNTPE2	ADRCNTPE3	ADRCNTPE4	ADRCNTPE5	ADRCNTPE6	ADRCNTPE 7
64	FCA::PCI12 ERROR8							
	(Not used)	(Not used)	(Not used)	(Not used)	BURSTPER0	BURSTPER1	BCNTPER0	BCNTPER1
65	FCA::PCI12 ERROR9							
	OUT ADRCPE0	OUT ADRCPE1	OUT CNTPE0	OUT CNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
66	FCA::PCI12 ERROR9							
	(Not used)	OV32PER	OV2PER	SOOV4PER	DETIWCPER	BRDYPER	OUTADROV RN	INADROVR N
67	FCA::PCI12 ERROR9							
	SEQ CMPERR	(Not used)						
68	FCA::PCI10 ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	OUT POINPER	IN POINTPER
69	FCA::PCI10 ERROR10							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6A	FCA::PCI10 ERROR10							
	(Not used)	(Not used)	(Not used)	(Not used)	OUTSERR	OUTMPERR	PCI64PE	PCIPE
6B	FCA::PCI12 ERROR10							
	(Not used)	SEQCMPE R	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
6C	FCA::PCI12 ERROR11							
	OUT ADRCPE0	OUT ADRCPE1	OUTCNTPE0	OUTCNTPE1	IN ADRCNTPE0	IN ADRCNTPE1	IWCPE0	IWCPE1
6D	FCA::PCI12 ERROR11							
	(Not used)	EQLSTCNT PE	OV1PER	OV4PER	DETIWCUP PER	(Not used)	OUTADROV RN	INADROVR N
6E	FCA::PCI12 ERROR11							
	(Not used)	MSTRXHUN CR	MSTRXHAD ER	MSTRXHEC CHDE	(Not used)	MSTRXLUN CR	MSTRXLAD ER	MSTRXLEC CHDE
6F	FCA::PCI12 ERROR11							
	(Not used)	TGTRXHUN CR	TGTRXHAD ER	TGTRXHEC CHDE	(Not used)	TGTRXLUN CR	TGTRXLAD ER	TGTRXLEC CHDE

Byte27 = 8E & Byte34 = X0 & Byte28 = 66/76 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	FCA::PCI12 ERR0R12							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
71	FCA::PCI12 ERR0R12							
	(Not used)	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
72	FCA::PCI12 ERR0R12							
	(Not used)	SEQ CMPERR	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
73	FCA::PCI12 ERR0R12							
	(Not used)	(Not used)	SEQPE1	SEQPE0	DECERR1	DECERR0	ENCERR1	ENCERR0
74	FCA::BRGERR							
	BARB_SEQ PER	BARB_DECE RR	BARB_ENCE RR	BARB_CMP ERR	BRGBUFA_ OPTPE	BRGBUFA_ IPTPE	BRGBUFB_ OPTPE	BRGBUFB_ IPTPE
75	FCA::BRGERR							
	BRGSEQA_ CMDERR	BRGSEQA_ SEQPE	BRGSEQA_ DECERR	BRGSEQA_ ENCERR	BRGSEQA_ CMPERR	BRGERRA_ NLAPE	BRGERRA_ SELBCPE	BRGERRA_ BCNTPE
76	FCA::BRGERR							
	BRGSEQB_ CMDERR	BRGSEQB_ SEQPE	BRGSEQB_ DECERR	BRGSEQB_ ENCERR	BRGSEQB_ CMPERR	BRGERRB_ NLAPE	BRGERRB_ SELBCPE	BRGERRB_ BCNTPE
77	FCA::BRGERR							
	XTRXDB_ IPPE	XTRXDB_ OPPE	XTSQ_ENCD ECERR	XTSQ_ SEQPE	XTTXDB_ IPPE	XTTXDB_ OPPE	XTTXB_ IPPE	XTTXB_ OPPE
78	FCA::PCIERR0							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
79	FCA::PCIERR0							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7A	FCA::PCIERR0							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB ERR	XR_SLV ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7B	FCA::PCIERR0							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR
7C	FCA::PCI12 CK1TLOG1							
	CK1TADR3 1	CK1TADR30	CK1TADR29	CK1TADR28	CK1TADR27	CK1TADR26	CK1TADR25	CK1TADR2 4
7D	FCA::PCI0(1) CK1TLOG1							
	CK1TADR2 3	CK1TADR22	CK1TADR21	CK1TADR20	CK1TADR19	CK1TADR18	CK1TADR17	CK1TADR1 6
7E	FCA::PCI0(1) CK1TLOG1							
	CK1TADR1 5	CK1TADR14	CK1TADR13	CK1TADR12	CK1TADR11	CK1TADR10	CK1TADR09	CK1TADR0 8
7F	FCA::PCI0(1) CK1TLOG1							
	CK1TADR0 7	CK1TADR06	CK1TADR05	CK1TADR04	CK1TADR03	CK1TADR02	CK1TADR01	CK1TADR0 0

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'66' : PCI_CHK1B(FCA_DX2))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	(Not used)	(Not used)	FCA_CHK1 B 4MP	DRR_CHK1 B 4MP	PCI_CHK1 B Q	PCI_CHK1 B M	FCA_CHK1 B 8/4MP	DRR_CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'76' : PCI_CHK1B(FCA_DX2))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	(Not used)	(Not used)	FCA_CHK1 B 4MP	DRR_CHK1 B 4MP	PCI_CHK1 B Q	PCI_CHK1 B M	FCA_CHK1 B 8/4MP	DRR_CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B) (1/5)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '09'							
41	Y WING:: (Not used)							
42	Y WING:: (Not used)							
43	Y WING:: (Not used)							
44	Y WING::PERRSTAT (Not used)							
45	Y WING::PERRSTAT (Not used)							
46	Y WING::PERRSTAT (Not used)							
47	Y WING::PERRSTAT							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
48	Y WING::SERRSTAT (Not used)							
49	Y WING::SERRSTAT (Not used)							
4A	Y WING::SERRSTAT (Not used)							
4B	Y WING::SERRSTAT (Not used)							
4C	Y WING::OCBERRSTAT (Not used)							
4D	Y WING::OCBERRSTAT							
	(Not used)							OPE
4E	Y WING::OCBERRSTAT							
	(Not used)			BL64E	BL32E	BL16E	BL8E	BL4E
4F	Y WING::OCBERRSTAT							
	(Not used)			RCOE	DNDE	BEE	(Not used)	

Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	Y_WING::PINTXSTAT (Not used)							
51	Y_WING::PINTXSTAT (Not used)							
52	Y_WING::PINTXSTAT (Not used)							
53	Y_WING::PINTXSTAT (Not used)							
			INTD	INTC	INTB	INTA		
54	Y_WING::PERRADDRH PCI error address MSW							
55	Y_WING::PERRADDRH PCI error address MSW							
56	Y_WING::PERRADDRH PCI error address MSW							
57	Y_WING::PERRADDRH PCI error address MSW							
58	Y_WING::PERRADDRL PCI error address LSW							
59	Y_WING::PERRADDRL PCI error address LSW							
5A	Y_WING::PERRADDRL PCI error address LSW							
5B	Y_WING::PERRADDRL PCI error address LSW							
5C	Y_WING::PCIXSTAT							
	RFU		SCEM	DMCRS			DMOST	
5D	Y_WING::PCIXSTAT							
	DMOST	DMMRBC		DC	USC	SCD	133MHz capable	64bit device
5E	Y_WING::PCIXSTAT Bus number							
5F	Y_WING::PCIXSTAT							
	Device number					Function number		

Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	Y_WING::STATUS							
	DPE	SSE	RMA	RTA	STA	DEV timing		MDPE
61								
	FBBC	(Not used)	66MHz Cap	CL	(Not used)			
62	Y_WING::STATUS							
	(Not used)						FBBE	SERR# EN
63	Y_WING::STATUS							
	Step Cont	PER	VPS	MWIE	Special cyc	Bus Master	Mem Space	IO Space
64	Y_WING::UNITCNT							
	INIT_End	(Not used)						
65	Y_WING::UNITCNT							
	(Not used)							
66	Y_WING::UNITCNT							
	(Not used)			PER	(Not used)			MAttr cache
67	Y_WING::UNITCNT							
	(Not used)		PBA	Host mode	(Not used)	PCI CLK Freq	(Not used)	PCI mode
68	Y_WING::PERRMASK							
	(Not used)							
69	Y_WING::PERRMASK							
	(Not used)						MRCO	MSCCO
6A	Y_WING::PERRMASK							
	(Not used)					MUSC	MSCD	MRSCE
6B	Y_WING::PERRMASK							
	MDCD	MDAP	MDDP	MSTA	(Not used)	MDMDP	MRTA	MRMA
6C	Y_WING::PINTXMASK							
	(Not used)							
6D	Y_WING::PINTXMASK							
	(Not used)							
6E	Y_WING::PINTXMASK							
	(Not used)							
6F	Y_WING::PINTXMASK							
	(Not used)				Mask INTD	Mask INTC	Mask INTB	Mask INTA

Byte27 = 8E & Byte34 = X0 & Byte28 = 67/77 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Y WING::OCBERRMASK (Not used)							
71	Y WING::OCBERRMASK (Not used)							
72	Y WING::OCBERRMASK (Not used)							
73	Y WING::OCBERRMASK (Not used)							
74	Y WING::PCIXCOMMAND (Not used)							
75	Y WING::PCIXCOMMAND (Not used)							
76	Y WING::PCIXCOMMAND (Not used)							
77	Y WING::PCIXCOMMAND (Not used)							
78	Y WING::PCIXCOMMAND (Not used)							
79	Y WING::PCIXCOMMAND (Not used)							
7A	Y WING::PCIXCOMMAND (Not used)							
7B	Y WING::PCIXCOMMAND (Not used)							
7C	Y WING::PCIXCOMMAND (Not used)							
7D	Y WING::PCIXCOMMAND (Not used)							
7E	Y WING::PCIXCOMMAND (Not used)							
7F	Y WING::PCIXCOMMAND (Not used)							

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'67' : PCI_CHK1B(Y_Wing))							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'77' : PCI_CHK1B(Y_Wing))							
2A	MPID							
2B	Y-Wing::INT_STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2C	Y-Wing::INT_STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2D	DHUB::MPA_STS							
	(Not used)	(Not used)	FCA CHK1 B 4MP	DRR CHK1 B 4MP	PCI CHK1 B Q	PCI CHK1 B M	FCA CHK1 B 8/4MP	DRR CHK1 B 8/4MP
2E	DHUB::MPA_STS							
	BSINT	CHK3 H SNMST	CHK3 H SNSLV	CHK1B FM	(Not used)	(Not used)	CHK1B XR	MPINT
2F	Y-Wing::SERR Status/PCI Error Status							
	SS	SS	SS	RCO	SCCO	USC	SCD	RSCE
30	Y-Wing::PCI Error Status							
	DCD	DAP	DDP	STA	RFU	DMDP	RTA	RMA
31	Y-Wing::OnChipBus Error Status							
	OPE	RFU	RFU	BL64E	BL32E	BL16E	BL8E	BL4E
32	Y-Wing::OnChipBus Error Status							
	RFU			RCOE	DNDE	BEE	RFU	

Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No.							
	‘0A’							
41	(Not used)							
42	Y_Wing::BIUOCBERRINFO2							
	(Not used)							
43	Y_Wing::BIUOCBERRINFO2							
	(Not used)							
44	Y_Wing::BIUOCBERRINFO2							
	Bus Error Info				Bus Error Cmd			
45	Y_Wing::BIUOCBERRINFO2							
	Bus Error Cmd				Bus Error Addr2			
46	Y_Wing::BIUOCBERRINFO1							
	Bus Error Addr1							
47	Y_Wing::BIUOCBERRINFO1							
	Bus Error Addr1							
48	Y_Wing::BIUOCBERRINFO1							
	Bus Error Addr1							
49	Y_Wing::BIUOCBERRINFO1							
	Bus Error Addr1							
4A	Y_Wing::SDERRADDR							
	ERRADD							
4B	Y_Wing::SDERRADDR							
	ERRADD						(Not used)	(Not used)
4C	Y_Wing::SDERRADDR							
	ERRADD							
4D	Y_Wing::SDERRADDR							
	ERRADD							
4E	Y_Wing::SDERRSYNHI							
	(Not used)							
4F	Y_Wing::SDERRSYNHI							
	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	Y Wing::SDERRSYNHI (Not used)							
51	Y Wing::SDERRSYNHI ES0HL							
52	Y Wing::SDERRSYNLO (Not used)							
53	Y Wing::SDERRSYNLO (Not used)							
54	Y Wing::SDERRSYNLO (Not used)							
55	Y Wing::SDERRSYNLO ES0LL							
56	Y Wing::GPIO DATA2							
	GPD31	GPD30	GPD29	GPD28	GPD27	GPD26	GPD25	GPD24
57	Y Wing::GPIO DATA2							
	GPD23	GPD22	GPD21	GPD20	GPD19	GPD18	GPD17	GPD16
58	Y Wing::GPIO DATA3							
	GPD47	GPD46	GPD45	GPD44	GPD43	GPD42	GPD41	GPD40
59	Y Wing::GPIO DATA3							
	GPD39	GPD38	GPD37	GPD36	GPD35	GPD34	GPD33	GPD32
5A	Y Wing::RFCSC RFCS							
5B	Y Wing::RFCSC RFCS							
5C	Y Wing::TFCSC TFCS							
5D	Y Wing::TFCSC TFCS							
5E	Y Wing::FM STATUS (Not used)							
5F	Y Wing::FM STATUS (Not used)							
							FM1 BSY	FM0 BSY

Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	Y_Wing::ERROR STATUS							
	(Not used)	FMWP ERR	FM WERR	SEQ CNT PERR	SEQ CNT ERR	SEQ WERR	SEQ PERR	SEQ PERR
61	Y_Wing::ERROR STATUS							
	YWING ERR	(Not used)	FMRD AERR	EMRD PERR	(Not used)	LCE2 ERR	LDAT PERR	LADR PERR
62	Y_Wing::HardRESETenable							
	(Not used)	(Not used)	(Not used)	SEQ CNT PERR EN	SEQ CNT ERR EN	SEQ WERR EN	SEQ RERR EN	SEQ PERR EN
63	Y_Wing::HardRESETenable							
	YWING ERR EN	(Not used)	FMRD AERR EN	FMRD PERR EN	(Not used)	LCE2 ERR EN	LDAT PERR EN	LADR PERR EN
64	Y_Wing::PLD CONTROL							
	DEBUG MD	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	FMWP REG
65	Y_Wing::PLD CONTROL							
	SEQ ERR MODE	BT OK	ERR CLK	(Not used)	BYPS CLKS	(Not used)	LRDY EN	RST REQ
66	Y_Wing::BadVAddr							
	BadVAddr							
67	Y_Wing::BadVAddr							
	BadVAddr							
68	Y_Wing::BadVAddr							
	BadVAddr							
69	Y_Wing::BadVAddr							
	BadVAddr							
6A	Y_Wing::BadVAddr							
	BadVAddr							
6B	Y_Wing::BadVAddr							
	BadVAddr							
6C	Y_Wing::BadVAddr							
	BadVAddr							
6D	Y_Wing::BadVAddr							
	BadVAddr							
6E	Y_Wing::STATUS							
	XX	CU(2:0)			(Not used)	FR	(Not used)	DS
6F	Y_Wing::STATUS							
	DS							

Byte27 = 8E & Byte34 = X0 & Byte28 = 68 (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	Y Wing::STATUS IM(7:0)							
71	Y Wing::STATUS							
	KX	SX	UX	KSU		ERL	EXL	IE
72	Y Wing::Reason							
	BD	(Not used)	CE		(Not used)			
73	Y Wing:: Reason (Not used)							
74	Y Wing:: Reason IP(7:0)							
75	Y Wing:: Reason							
	(Not used)	ExcCode					(Not used)	(Not used)
76	Y Wing::EPC EPC							
77	Y Wing::EPC EPC							
78	Y Wing::EPC EPC							
79	Y Wing::EPC EPC							
7A	Y Wing::EPC EPC							
7B	Y Wing::EPC EPC							
7C	Y Wing::EPC EPC							
7D	Y Wing::EPC EPC							
7E	Y Wing::PARITY ERROR Parity							
7F	Y Wing::CACHE ERROR							
	ER	EC	ED	ET	ES	EE	EB	(Not used)

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
27	Format (x'8')				Message (x'E')			
28	Subcode (x'68' : Y_Wing_CHK1B)							
29	MPID							
2A	Y-Wing::INT STATUS							
	CH16 INT STATUS	CH15 INT STATUS	CH14 INT STATUS	CH13 INT STATUS	CH12 INT STATUS	CH11 INT STATUS	CH10 INT STATUS	CH09 INT STATUS
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	Y-Wing::NMI STATUS							
	(Not used)					OnChipBus Parity Error	Ethernet#2 Parity Error	Ethernet#1 Parity Error
2D	Y-Wing::NMI STATUS							
	BCU Parity Error	SC memory un correctable Error	SC memory correctable Error	SC memory Parity Error	SDRAM un correctable Error	SDRAM correctable Error	BIU-OnChipBus Error	Outside NMI (MPHOLD)
2E	(Not used)							
2F	(Not used)							
30	(Not used)							
31	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '0C'							
41	DHUB::DRSTATS							
	END_64	EXEC_64	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
42	DHUB::DRSTATS							
	CHK2	CHK2A (DRR_CTL)	CHK2B (DRR_DT)	CHK2C (DRR_MICR O)	CHK2D (CHSN_CTL)	CHK2E (CNSN_DT)	CHK2F (TO/ABT)	CHK1B
43	DHUB::DRSTATS							
	POUSE	DSKTIMOU T	P0_ERRINT	P1_ERRINT	ST0_VALID	ST0_CRAB/C MA ERR	ST1_VALID	ST1_CRAB/ CMA ERR
44	DHUB::DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO_#3	MP NO_#2	MP NO_#1	MP NO_#0
45	DHUB::DRSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
46	DHUB::DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
47	DHUB::DRERRB							
	XRSEQ_EN CERR (M)	XRSEQ (S) EQPER (M)	XRSEQ_DEC ERR (M)	XRSEQ (S) TERR (M)	XRSEQ_HW ERR (M)	XRSEQ RDDTPER (M)	DTLCSEQ RDDTPER (M)	DTLCSEQ RDDTPER (S)
48	DHUB::DRERRB							
	XRSEQ_EN CERR (S)	XRSEQ (S) EQPER (S)	XRSEQ_DEC ERR (S)	XRSEQ (S) TERR (S)	XRSEQ_HW ERR (S)	XRSEQ RDDTPER (S)	SQD0M RDDTPER (M)	SQD0M RDDTPER (S)
49	DHUB::DRERRB							
	XRSEQCMP _CMPERR	DCMDSEQ_ RDDTPER (M)	DCMDSEQ_ RDDTPER (S)	SQD4M RDDTPER (M)	SQD4M RDDTPER (S)	SQD4N RDDTPER (M)	SQD4N RDDTPER (S)	DBD2 RDDTPER
4A	DHUB::DRERRB							
	SQDON RDDTPER (M)	SQDON RDDTPER (S)	SQD1 RDDTPER (M)	SQD1 RDDTPER (S)	SQD2 RDDTPER (S)	SQD2 RDDTPER (S)	SQD3 RDDTPER (S)	SQD3 RDDTPER (S)
4B	DHUB::DRERRB							
	SQDE0M RDDTPER (M)	SQDE0M RDDTPER (S)	SQDE0N RDDTPER (M)	SQDE0N RDDTPER (S)	DETDR RDDTPER	DETDR W/R_DTPER	C0 PLCNTRX RDDTPER	C0 PLCNTTX RDDTPER
4C	DHUB::DRERRB							
	S64T RDDTPER (M)	S64T RDDTPER (S)	DBD3 RDDTPER	DBD4M RDDTPER	DBD4N RDDTPER	DBD1M RDDTPER	DBD1N RDDTPER	ACGEN RDDTPER
4D	DHUB::DRERRB							
	DBDE1N RDDTPER	DBDE1M RDDTPER	DBDE4N RDDTPER	DBDE4M RDDTPER	S64RCNT RDDTPER	RDADRCNT M RDDTPER	RDADRCNT N RDDTPER	WTADRCN TM RDDTPER
4E	DHUB::DRERRB							
	WTADRCN TN RDDTPER	PLCNTD0M RDDTPER	PLCNTD0N RDDTPER	PLCNTD4M RDDTPER	PLCNTD4N RDDTPER	SBCNT1 RDDTPER	SBCNT3 RDDTPER	PLCNT5281 _RDDTPER
4F	DHUB::DRERRC							
	PLCNT5283 RDDTPER	RPLRCCHK RDDTPER	ST0CHK RDPER	ST1CHK RDPER	WLACHK PDPER	WSLRCCHK PDPER	LACHG PDPER	ADRCNT64 PDPER

Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB::DRERRC							
	FERDR_ W/R DTPER	FERDR_ RDPER	ERMDR_ W/R DTPER	ERMDR_ RDPER	DRMD0 PASSWORD	DRRERR_ RDPER	DTCNT_ RDPER	CMD64SEQ HWERR (S)
51	DHUB::DRERRC							
	WADB0 ADR2PER	WADB0 ADR3PER	WADB0_ WTPER	WADB0_ RDPER	DRMD0 ADR2PER	DRMD0 ADR3PER	DRMD0_ WTPER	DRMD0_ RDPER
52	DHUB::DRERRC							
	(Not used)	WADB0 XRBCERR	WADB0 XRADRERR	XRSEQ_BLK ACSERR S	XRSEQ_BLK ACSERR M	WADB0 LM RDDTPER	PRM0 RDPER	PRM1 RDPER
53	DHUB::DRERRC							
	CMD64SEQ _HWERR (M)	CMD64SEQ RDPER (M)	CMD64SEQ ENCERR (S)	PRM64 WTP ER	PRM64 RDP ER	RDB0 DTPE R	CMD64SEQ SEQPER (M)	CMD64SEQ _DECERR (M)
54	DHUB::DRERRC							
	(Not used)	(Not used)	(Not used)	CMD64SEQ SEQPER (S)	CMD64SEQC MP_CMPE R	CMD64SEQ DECERR (S)	CMD64SEQ ENCERR (M)	CMD64SEQ _RDPER (S)
55	DHUB::DRERRC							
	MSTSEQ ENCERR (M)	MSTSEQ SEQPER (M)	MSTSEQ DECERR (M)	MSTSEQ HWERR (M)	MSTSEQ RDPER (M)	(Not used)	(Not used)	(Not used)
56	DHUB::DRERRC							
	DBD3X RD PER	SQD3X RDPER (M)	SQD3X RDPER (S)	MSTSEQ ENCERR (S)	MSTSEQ SEQPER (S)	MSTSEQ DECERR (S)	MSTSEQ HWERR (S)	MSTSEQ RDPER (S)
57	DHUB::DRERRD							
	C0 STTCNT _RDDTPER	C0 WAITCN T_RDDTPER	C0 ACKTCN T_RDDTPER	C0 ENTTCN T_RDDTPER	C0 CHABT _RDDTPER (M)	C0 CHABT _RDDTPER (S)	C0 RXSEQ _RDDTPER (M)	C0 RXSEQ _RDDTPER (S)
58	DHUB::DRERRD							
	C0 SQP3 _RDDTPER (M)	C0 SQP3 _RDDTPER (S)	C0 TXSEQ _RDDTPER (M)	C0 TXSEQ _RDDTPER (S)	C0 TXXSEQ _RDDTPER (M)	C0 TXXSEQ _RDDTPER (S)	C0 TOKEICNT _RDDTPER	C0 DBP0 _RDDTPER
59	DHUB::DRERRD							
	C1 STTCNT _RDDTPER	C1 WAITCN T_RDDTPER	C1 ACKTCN T_RDDTPER	C1 ENTTCN T_RDDTPER	C1 CHABT _RDDTPER (M)	C1 CHABT _RDDTPER (S)	C1 RXSEQ _RDDTPER (M)	C1 RXSEQ _RDDTPER (S)
5A	DHUB::DRERRD							
	C1 SQP3 _RDDTPER (M)	C1 SQP3 _RDDTPER (S)	C1 TXSEQ _RDDTPER (M)	C1 TXSEQ _RDDTPER (S)	C1 TXXSEQ _RDDTPER (M)	C1 TXXSEQ _RDDTPER (S)	C1 TOKEICNT _RDDTPER	C1 DBP0 _RDDTPER
5B	DHUB::DRERRD							
	FERP0 FERDTPER	FERP1 FERDTPER	C1 DBP3 _RDDTPER	C1 DBP4 _RDDTPER	C1 DBP1 _RDDTPER	C0 DBP3 _RDDTPER	C0 DBP4 _RDDTPER	C0 DBP1 _RDDTPER
5C	DHUB::DRERRD							
	P0MD0 WTDTPER (S)	P1MD0 WTDTPER (S)	P0MD0 _RDDTPER	P0MD0 ADR2PER	P0MD0 ADR3PER	P1MD0 _RDDTPER	P1MD0 ADR2PER	P1MD0 ADR3PER
5D	DHUB::DRERRD							
	FERP0 _RDDTPER	FERP1 _RDDTPER	XRABT DECERR (S)	XRABT ENCERR (S)	XRABT GNTERR (S)	XRABT HWERR (S)	XRABT (S) EQPER (S)	XRABT _RDDTPER (S)
5E	DHUB::DRERRD							
	TDTCNT _RDDTPER	(Not used)	XRABT DECERR (M)	XRABT ENCERR (M)	XRABT GNTERR (M)	XRABT HWERR (M)	XRABT (S) EQPER (M)	XRABT _RDDTPER (M)
5F	DHUB::DRERRE							
	FERDT _RDDTPER	ERMDT _RDDTPER	DETD _RDDTPER	DBFE1 _RDDTPER	DBF0 _RDDTPER	DBF1 _RDDTPER	DBFE3 _RDDTPER	DBF3 _RDDTPER

Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB::DRERRE							
	DBF4 RDDTP̄ER	DBF5 RDDTP̄ER	SQF0 RDDTP̄ER (M)	SQF0 RDDTP̄ER (S)	SQF1 RDDTP̄ER (M)	SQF1 RDDTP̄ER (S)	SQF2 RDDTP̄ER (M)	SQF2 RDDTP̄ER (S)
61	DHUB::DRERRE							
	SQF3 RDDTP̄ER (M)	SQF3 RDDTP̄ER (S)	SQF4 RDDTP̄ER (M)	SQF4 RDDTP̄ER (S)	SQF5 RDDTP̄ER (M)	SQF5 RDDTP̄ER (S)	SQFE2 RDDTP̄ER (M)	SQFE2 RDDTP̄ER (S)
62	DHUB::DRERRE							
	WDTCHK RDDTP̄ER	RDTCHK RDDTP̄ER	DTMD0 ADR2P̄ER	DTMD0 ADR3P̄ER	DTMD0 RDDTP̄ER	DTMD0 WDTP̄ER (S)	SQF6 RDDTP̄ER (M)	SQF6 RDDTP̄ER (S)
63	DHUB::DRERRE							
	RXACFF RDDTP̄ER	ADRCNTF RDDTP̄ER	PLCNTF0 RDDTP̄ER	PLCNTF4 RDDTP̄ER	PLCNTF5 RDDTP̄ER	ST0CHKF RDDTP̄ER	ST1CHKF RDDTP̄ER	TSLRCCHK RDDTP̄ER
64	DHUB::DRERRE							
	DB1M C1 CRCT̄ERR	DB1M C1 UCRCT̄ERR	DB1M C1 ADR̄ERR	DB1M C1 ECC̄ERR	DB1N C1 CRCT̄ERR	DB1N C1 UCRCT̄ERR	DB1N C1 ADR̄ERR	DB1N C1 ECC̄ERR
65	DHUB::DRERRE							
	DBF3 C1 ECC̄ERR	(Not used)	(Not used)	C1 DSKCNT RDDTP̄ER	C0 DSKCNT RDDTP̄ER	PC1CHK1B MONI	YWINGERR MONI	QDTACHK1 B MONI
66	DHUB::DRERRE							
	C1 PLCNTRX RDDTP̄ER	C1 PLCNTTX RDDTP̄ER	(Not used)	(Not used)	(Not used)	DBF3 C1 CRCT̄ERR	DBF3 C1 UCRCT̄ERR	DBF3 C1 ADR̄ERR
67	(Not used)							
68	(Not used)							
69	(Not used)							
6A	(Not used)							
6B	(Not used)							
6C	(Not used)							
6D	(Not used)							
6E	(Not used)							
6F	(Not used)							

Byte27 = 8E & Byte34 = X0 & Byte28 = 6A/7A (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	(Not used)							
71	(Not used)							
72	(Not used)							
73	(Not used)							
74	(Not used)							
75	(Not used)							
76	(Not used)							
77	(Not used)							
78	(Not used)							
79	(Not used)							
7A	(Not used)							
7B	(Not used)							
7C	DHUB::PCIERR0/1							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	DHUB::PCIERR0/1							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ_ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ_ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	DHUB::PCIERR0/1							
	MSTSEQ_ ERR	RETRY_ OVR	XR_ARB_ ERR	XR_SLV_ ERR	XPTXBUF0_ ERR	XPCMDBUF ERR	XPRXBUF0_ ERR	MSTTXBUF 0 ERR
7F	DHUB::PCIERR0/1							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0_ ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'6A' : QDTA CHK1B(4MP))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INTB 8/4MP	DRR INTB 8/4MP	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP
2D	DHUB::QDTA DRMODEO							
	TREND EREN	DRRCHK1B EREN	PCICHK1B EREN	YER_EREN	(Not used)	DRBACS2	DRBACS1	DRBACS0
2E	DHUB::QDTA DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DR R MICRO)	CHK2D(CH SN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
2F	DHUB::QDTA DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO#3	MP NO#2	MP NO#1	MP NO#0
30	DHUB::QDTA DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
31	DHUB::QDTA DRERRC							
	SQDEOM R DDTPER (M)	SQDEOM R DDTPER (S)	SQDEON R DDTPER (M)	SQDEON R DDTPER (S)	DETDR RDDTPER	DETDR W/ R_DTPER	CO PLCNT RX_RDDTP_ER	CO PLCNT TX_RDDTP_ER
32	DHUB::QDTA DRERRE							
	DBF3_C1 ECCERR	(Not used)	(Not used)	C1_DSKCN T_RDDTPE R	C0_DSKCN T_RDDTPE R	PCICHK1B_MONI	YWINGERR_MONI	QDTACHK1B_MONI

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'7A' : QDTA-MICRO CHK1B(4MP))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP
2D	DHUB::QDTA DRMODEO							
	TREND_ER_EN	DRRCHK1B_EREN	PCICLK1B_EREN	YER_EREN	(Not used)	DRBACS2	DRBACS1	DRBACS0
2E	DHUB::QDTA DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DR R MICRO)	CHK2D(CH SN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
2F	DHUB::QDTA DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO#3	MP NO#2	MP NO#1	MP NO#0
30	DHUB::QDTA DRSTATS							
	ABORT	YWING_ER_R	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
31	DHUB::QDTA DRERRC							
	(Not used)	WADBO_XRBCERR	WADBO_XRADRERR	XRSEQ_BLKACSERR_S	XRSEQ_BLKACSERR_M	WADBO_LMRDDTPER	PRM0_RDPER	PRM1_RDPER
32	DHUB::QDTA DRERRC							
	CMD64SEQ_HWERR (M)	CMD64SEQ_RDPER (M)	CMD64SEQ_ENCERR (S)	PRM64_WTPER	PRM64_RDPER	RDBO_DTPER	CMD64SEQ_SEQPER (M)	CMD64SEQ_DECERR (M)

Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B) (1/5)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
03								
04								
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B) (2/5)

	0	1	2	3	4	5	6	7
40	FREE AREA FORMAT No. '0D'							
41	DHUB::DRSTATS							
	END_64	EXEC_64	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
42	DHUB::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DRR _MICRO)	CHK2D (CHSN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
43	DHUB::DRSTATS							
	POUSE	DSKTIMOU T	P0_ERRINT	P1_ERRINT	ST0_VALID	ST0_CRAB/C MA ERR	ST1_VALID	ST1_CRAB/ CMA ERR
44	DHUB::DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO_#3	MP NO_#2	MP NO_#1	MP NO_#0
45	DHUB::DRSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
46	DHUB::DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
47	DHUB::DRERRC							
	PLCNT5283 RDDTPER	RPLRCCHK RDDTPER	ST0CHK RDPER	ST1CHK RDPER	WLACHK PDPER	WSLRCCHK PDPER	LACHG PDPER	ADRCNT64 PDPER
48	DHUB::DRERRC							
	FERDR_ W/R DTPER	FERDR_ RDPER	ERMDR_ W/R_DTPER	ERMDR_ RDPER	DRMD0 PASSWORD	DRRERR_ RDPER	DTCNT_ RDPER	CMD64SEQ _HWERR(S)
49	DHUB::DRERRC							
	WADB0 ADR2PER	WADB0 ADR3PER	WADB0 WTPER	WADB0 RDPER	DRMD0 ADR2PER	DRMD0 ADR3PER	DRMD0 WTPER	DRMD0 RDPER
4A	DHUB::DRERRC							
	(Not used)	WADB0 XRBCERR	WADB0 XRADRERR	XRSEQ BLK ACSERR S	XRSEQ BLK ACSERR M	WADB0 LM RDDTPER	PRM0 RDPER	PRM1 RDPER
4B	DHUB::DRERRC							
	CMD64SEQ _HWERR (M)	CMD64SEQ RDPER (M)	CMD64SEQ ENCERR (S)	PRM64 WTP ER	PRM64 RDP ER	RDB0 DTPE R	CMD64SEQ SEQPER (M)	CMD64SEQ _DECERR (M)
4C	DHUB::DRERRC							
	(Not used)	(Not used)	(Not used)	CMD64SEQ SEQPER (S)	CMD64SEQC MP_CMPER R	CMD64SEQ DECERR (S)	CMD64SEQ ENCERR (M)	CMD64SEQ _RDPER (S)
4D	DHUB::DRERRC							
	MSTSEQ ENCERR (M)	MSTSEQ SEQPER (M)	MSTSEQ DECERR (M)	MSTSEQ HWERR (M)	MSTSEQ RDPER (M)	(Not used)	(Not used)	(Not used)
4E	DHUB::DRERRC							
	DBD3X RD PER	SQD3X RDPER (M)	SQD3X RDPER (S)	MSTSEQ ENCERR (S)	MSTSEQ SEQPER (S)	MSTSEQ DECERR (S)	MSTSEQ HWERR (S)	MSTSEQ RDPER (S)
4F	DHUB::DRERRD							
	C0 STTCNT _RDDTPER	C0 WAITCN T_RDDTPER	C0 ACKTCN T_RDDTPER	C0 ENTTCN T_RDDTPER	C0 CHABT RDDTPER (M)	C0 CHABT RDDTPER (S)	C0 RXSEQ RDDTPER (M)	C0 RXSEQ RDDTPER (S)

Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B) (3/5)

	0	1	2	3	4	5	6	7
50	DHUB::DRERRD							
	C0 SQP3 RDDTPER (M)	C0 SQP3 RDDTPER (S)	C0 TXSEQ RDDTPER (M)	C0 TXSEQ RDDTPER (S)	C0 TXXSEQ RDDTPER (M)	C0 TXXSEQ RDDTPER (S)	C0 TOKEIC NT RDDTPER	C0 DBP0 RDDTPER
51	DHUB::DRERRD							
	C1 STTCNT RDDTPER	C1 WAITCN T_RDDTPER	C1 ACKTCN T_RDDTPER	C1 ENTTCN T_RDDTPER	C1 CHABT RDDTPER (M)	C1 CHABT RDDTPER (S)	C1 RXSEQ RDDTPER (M)	C1 RXSEQ RDDTPER (S)
52	DHUB::DRERRD							
	C1 SQP3 RDDTPER (M)	C1 SQP3 RDDTPER (S)	C1 TXSEQ RDDTPER (M)	C1 TXSEQ RDDTPER (S)	C1 TXXSEQ RDDTPER (M)	C1 TXXSEQ RDDTPER (S)	C1 TOKEIC NT RDDTPER	C1 DBP0 RDDTPER
53	DHUB::DRERRD							
	FERP0 FERDTPER	FERP1 FERDTPER	C1 DBP3 RDDTPER	C1 DBP4 RDDTPER	C1 DBP1 RDDTPER	C0 DBP3 RDDTPER	C0 DBP4 RDDTPER	C0 DBP1 RDDTPER
54	DHUB::DRERRD							
	P0MD0 WTDTPER (S)	P1MD0 WTDTPER (S)	P0MD0 RDDTPER	P0MD0 ADR2PER	P0MD0 ADR3PER	P1MD0 RDDTPER	P1MD0 ADR2PER	P1MD0 ADR3PER
55	DHUB::DRERRD							
	FERP0 RDDTPER	FERP1 RDDTPER	XRABT DECERR (S)	XRABT ENCERR (S)	XRABT GNTERR (S)	XRABT HWERR (S)	XRABT (S) EQPER (S)	XRABT RDDTPER (S)
56	DHUB::DRERRD							
	TDTCNT RDDTPER	(Not used)	XRABT DECERR (M)	XRABT ENCERR (M)	XRABT GNTERR (M)	XRABT HWERR (M)	XRABT (S) EQPER (M)	XRABT RDDTPER (M)
57	DHUB::DRERRE							
	FERDT RDDTPER	ERMDT RDDTPER	DETD RDDTPER	DBFE1 RDDTPER	DBF0 RDDTPER	DBF1 RDDTPER	DBFE3 RDDTPER	DBF3 RDDTPER
58	DHUB::DRERRE							
	DBF4 RDDTPER	DBF5 RDDTPER	SQF0 RDDTPER (M)	SQF0 RDDTPER (S)	SQF1 RDDTPER (M)	SQF1 RDDTPER (S)	SQF2 RDDTPER (M)	SQF2 RDDTPER (S)
59	DHUB::DRERRE							
	SQF3 RDDTPER (M)	SQF3 RDDTPER (S)	SQF4 RDDTPER (M)	SQF4 RDDTPER (S)	SQF5 RDDTPER (M)	SQF5 RDDTPER (S)	SQFE2 RDDTPER (M)	SQFE2 RDDTPER (S)
5A	DHUB::DRERRE							
	WDTCHK RDDTPER	RDTCCHK RDDTPER	DTMD0 ADR2PER	DTMD0 ADR3PER	DTMD0 RDDTPER	DTMD0 WTDTPER (S)	SQF6 RDDTPER (M)	SQF6 RDDTPER (S)
5B	DHUB::DRERRE							
	RXACFF RDDTPER	ADRCNTF RDDTPER	PLCNTF0 RDDTPER	PLCNTF4 RDDTPER	PLCNTF5 RDDTPER	ST0CHKF RDDTPER	ST1CHKF RDDTPER	TSLRCCHK RDDTPER
5C	DHUB::DRERRE							
	DB1M C1 CRCTERR	DB1M C1 UCRCTERR	DB1M C1 ADRERR	DB1M C1 ECCERR	DB1N C1 CRCTERR	DB1N C1 UCRCTERR	DB1N C1 ADRERR	DB1N C1 ECCERR
5D	DHUB::DRERRE							
	C1_PLCNTR X RDDTPER	C1_PLCNTT X RDDTPER	(Not used)	(Not used)	(Not used)	DBF3 C1 CRCTERR	DBF3 C1 UCRCTERR	DBF3 C1 ADRERR
5E	DHUB::DRSTATS							
	END_64	EXEC_64	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5F	DHUB::DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DRR _MICRO)	CHK2D (CHSN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B

Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B) (4/5)

	0	1	2	3	4	5	6	7
60	DHUB::DRSTATS							
	POUSE	DSKTIMEOUT	P0_ERRINT	P1_ERRINT	ST0_VALID	ST0_CRAB/CMA ERR	ST1_VALID	ST1_CRAB/CMA ERR
61	DHUB::DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO_#3	MP NO_#2	MP NO_#1	MP NO_#0
62	DHUB::DRSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
63	DHUB::DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
64	DHUB::DRERRC							
	PLCNT5283_RDDTPER	RPLRCCHK_RDDTPER	ST0CHK_RDPER	ST1CHK_RDPER	WLACHK_PDPER	WSLRCCHK_PDPER	LACHG_PDPER	ADRCNT64_PDPER
65	DHUB::DRERRC							
	FERDR_W/R_DTPER	FERDR_RDPER	ERMDR_W/R_DTPER	ERMDR_RDPER	DRMD0_PASSWORD	DRRERR_RDPER	DTCNT_RDPER	CMD64SEQ_HWERR (S)
66	DHUB::DRERRC							
	WADB0_ADR2PER	WADB0_ADR3PER	WADB0_WTPER	WADB0_RDPER	DRMD0_ADR2PER	DRMD0_ADR3PER	DRMD0_WTPER	DRMD0_RDPER
67	DHUB::DRERRC							
	(Not used)	WADB0_XRBCERR	WADB0_XRADRERR	XRSEQ_BLK_ACSERR S	XRSEQ_BLK_ACSERR M	WADB0_LM_RDDTPER	PRM0_RDPER	PRM1_RDPER
68	DHUB::DRERRC							
	CMD64SEQ_HWERR (M)	CMD64SEQ_RDPER (M)	CMD64SEQ_ENCERR (S)	PRM64_WTPER	PRM64_RDPER	RDB0_DTPER	CMD64SEQ_SEQPER (M)	CMD64SEQ_DECERR (M)
69	DHUB::DRERRC							
	(Not used)	(Not used)	(Not used)	CMD64SEQ_SEQPER(S)	CMD64SEQ_MP_CMPPER R	CMD64SEQ_DECERR(S)	CMD64SEQ_ENCERR(M)	CMD64SEQ_RDPER(S)
6A	DHUB::DRERRC							
	MSTSEQ_ENCERR (M)	MSTSEQ_SEQPER (M)	MSTSEQ_DECERR (M)	MSTSEQ_HWERR (M)	MSTSEQ_RDPER (M)	(Not used)	(Not used)	(Not used)
6B	DHUB::DRERRC							
	DBD3X_RDPER	SQD3X_RDPER (M)	SQD3X_RDPER (S)	MSTSEQ_ENCERR (S)	MSTSEQ_SEQPER (S)	MSTSEQ_DECERR (S)	MSTSEQ_HWERR (S)	MSTSEQ_RDPER (S)
6C	DHUB::DRERRD							
	C0_STTCNT_RDDTPER	C0_WAITCNT_RDDTPER	C0_ACKTCNT_RDDTPER	C0_ENTTCNT_RDDTPER	C0_CHABT_RDDTPER (M)	C0_CHABT_RDDTPER (S)	C0_RXSEQ_RDDTPER (M)	C0_RXSEQ_RDDTPER (S)
6D	DHUB::DRERRD							
	C0_SQP3_RDDTPER (M)	C0_SQP3_RDDTPER (S)	C0_TXSEQ_RDDTPER (M)	C0_TXSEQ_RDDTPER (S)	C0_TXXSEQ_RDDTPER (M)	C0_TXXSEQ_RDDTPER (S)	C0_TOKEICNT_RDDTPER	C0_DBP0_RDDTPER
6E	DHUB::DRERRD							
	C1_STTCNT_RDDTPER	C1_WAITCNT_RDDTPER	C1_ACKTCNT_RDDTPER	C1_ENTTCNT_RDDTPER	C1_CHABT_RDDTPER (M)	C1_CHABT_RDDTPER (S)	C1_RXSEQ_RDDTPER (M)	C1_RXSEQ_RDDTPER (S)
6F	DHUB::DRERRD							
	C1_SQP3_RDDTPER (M)	C1_SQP3_RDDTPER (S)	C1_TXSEQ_RDDTPER (M)	C1_TXSEQ_RDDTPER (S)	C1_TXXSEQ_RDDTPER (M)	C1_TXXSEQ_RDDTPER (S)	C1_TOKEICNT_RDDTPER	C1_DBP0_RDDTPER

Byte27 = 8E & Byte34 = X0 & Byte28 = 7B (CHK1B) (5/5)

	0	1	2	3	4	5	6	7
70	DHUB::DRERRD							
	FERP0 FERDTP̄ER	FERP1 FERDTP̄ER	C1 DBP3 RDDTP̄ER	C1 DBP4 RDDTP̄ER	C1 DBP1 RDDTP̄ER	C0 DBP3 RDDTP̄ER	C0 DBP4 RDDTP̄ER	C0 DBP1 RDDTP̄ER
71	DHUB::DRERRD							
	P0MD0 WTDTP̄ER (S)	P1MD0 WTDTP̄ER (S)	P0MD0 RDDTP̄ER	P0MD0 ADR2P̄ER	P0MD0 ADR3P̄ER	P1MD0 RDDTP̄ER	P1MD0 ADR2P̄ER	P1MD0 ADR3P̄ER
72	DHUB::DRERRD							
	FERP0 RDDTP̄ER	FERP1 RDDTP̄ER	XRABT DECERR (S)	XRABT ENCERR (S)	XRABT GNTERR (S)	XRABT HWERR (S)	XRABT (S) EQPER (S)	XRABT RDDTP̄ER (S)
73	DHUB::DRERRD							
	TDTCNT RDDTP̄ER	(Not used)	XRABT DECERR (M)	XRABT ENCERR (M)	XRABT GNTERR (M)	XRABT HWERR (M)	XRABT (S) EQPER (M)	XRABT RDDTP̄ER (M)
74	DHUB::DRERRE							
	FERDT RDDTP̄ER	ERMDT RDDTP̄ER	DETD RDDTP̄ER	DBFE1 RDDTP̄ER	DBF0 RDDTP̄ER	DBF1 RDDTP̄ER	DBFE3 RDDTP̄ER	DBF3 RDDTP̄ER
75	DHUB::DRERRE							
	DBF4 RDDTP̄ER	DBF5 RDDTP̄ER	SQF0 RDDTP̄ER (M)	SQF0 RDDTP̄ER (S)	SQF1 RDDTP̄ER (M)	SQF1 RDDTP̄ER (S)	SQF2 RDDTP̄ER (M)	SQF2 RDDTP̄ER (S)
76	DHUB::DRERRE							
	SQF3 RDDTP̄ER (M)	SQF3 RDDTP̄ER (S)	SQF4 RDDTP̄ER (M)	SQF4 RDDTP̄ER (S)	SQF5 RDDTP̄ER (M)	SQF5 RDDTP̄ER (S)	SQFE2 RDDTP̄ER (M)	SQFE2 RDDTP̄ER (S)
77	DHUB::DRERRE							
	WDTCHK RDDTP̄ER	RDCHK RDDTP̄ER	DTMD0 ADR2P̄ER	DTMD0 ADR3P̄ER	DTMD0 RDDTP̄ER	DTMD0 WTDTP̄ER (S)	SQF6 RDDTP̄ER (M)	SQF6 RDDTP̄ER (S)
78	DHUB::DRERRE							
	RXACFF RDDTP̄ER	ADRCNTF RDDTP̄ER	PLCNTF0 RDDTP̄ER	PLCNTF4 RDDTP̄ER	PLCNTF5 RDDTP̄ER	ST0CHKF RDDTP̄ER	ST1CHKF RDDTP̄ER	TSLRCCHK RDDTP̄ER
79	DHUB::DRERRE							
	DB1M C1 CRCTERR	DB1M C1 UCRCTERR	DB1M C1 ADRERR	DB1M C1 ECCERR	DB1N C1 CRCTERR	DB1N C1 UCRCTERR	DB1N C1 ADRERR	DB1N C1 ECCERR
7A	DHUB::DRERRE							
	C1 PLCNTR X_RDDTP̄ER R	C1 PLCNTT X_RDDTP̄ER	(Not used)	(Not used)	(Not used)	DBF3 C1 CRCTERR	DBF3 C1 UCRCTERR	DBF3 C1 ADRERR
7B	DHUB:: (Not used)							
7C	DHUB::PCIERR0/1							
	TXPE	PERR_OUT	SERR_OUT	SERR_IN	PERR_IN	RXPE	TGTABT	MSTABT
7D	DHUB::PCIERR0/1							
	TGTRXBUF 1 ERR	TGTRXBUF2 ERR	TGTSEQ ERR	TGTTXBUF0 ERR	TGTTXBUF1 ERR	XR_MSTSEQ ERR	TGTTXBUF0 ERR	PXRXBUF0 ERR
7E	DHUB::PCIERR0/1							
	MSTSEQ ERR	RETRY OVR	XR ARB ERR	XR SLV ERR	XPTXBUF0 ERR	XPCMDBUF ERR	XPRXBUF0 ERR	MSTTXBUF 0 ERR
7F	DHUB::PCIERR0/1							
	MSTTXBUF 1 ERR	MSTTXBUF 2 ERR	MSTRXBUF 0 ERR	PXRXBUF0 ERR	MSTRXBUF 1 ERR	MSTRXBUF 2 ERR	(Not used)	BRG_ERR

Format 8, Message E (processor failure : CHK1B)

	0	1	2	3	4	5	6	7
28	Format (x'8')				Message (x'E')			
29	Subcode (x'7B' : QDTA-MICRO CHK1B(4MP))							
2A	MPID							
2B	Y-Wing::INT STATUS							
	CH08 INT STATUS	CH07 INT STATUS	CH06 INT STATUS	CH05 INT STATUS	CH04 INT STATUS	CH03 INT STATUS	CH02 INT STATUS	CH01 INT STATUS
2C	DHUB::MPA STS							
	(Not used)	(Not used)	FCA INT B 8/4MP	DRR INT B 8/4MP	(Not used)	(Not used)	FCA CHK2 4MP	DRR CHK2 4MP
2D	DHUB::QDTA DRMODEO							
	TREND_EREN	DRRCHK1B EREN	PCICHK1B EREN	YER_EREN	(Not used)	DRBACS2	DRBACS1	DRBACS0
2E	DHUB::QDTA DRSTATS							
	CHK2	CHK2A (DRR CTL)	CHK2B (DRR DT)	CHK2C(DR R MICRO)	CHK2D(CH SN CTL)	CHK2E (CNSN DT)	CHK2F (TO/ABT)	CHK1B
2F	DHUB::QDTA DRSTATS							
	REV NO_#3	REV NO_#2	REV NO_#1	REV NO_#0	MP NO#3	MP NO#2	MP NO#1	MP NO#0
30	DHUB::QDTA DRSTATS							
	ABORT	YWING_ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD-ERR	T64ERR
31	DHUB::QDTA DRERRC							
	(Not used)	WADBO XRBCERR	WADBO XRADRERR	XRSEQ BL KACSERR_S	XRSEQ BL KACSERR_M	WADBO L MRDDTPER	PRM0 RDPER	PRM1 RDPER
32	DHUB::QDTA DRERRE							
	CMD64SEQ_HWERR (M)	CMD64SEQ_RDPER (M)	CMD64SEQ_ENCERR (S)	PRM64 WTPER	PRM64 RDPER	RDBO DTPER	CMD64SEQ_SEQPER (M)	CMD64SEQ_DECERR (M)

Byte27 = 8E & Byte34 = X0 & Byte28 = F0 (WCHK1)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	Processor ID (*1)							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	SP no. 2 ¹	SP no. 2 ⁰	CTLR no.	DKU physical device no.				
25	Cylinder address (low order)							
26	Cylinder address (high order)				Head address			
27	Format (x'8')				Message (x'E')			
28	Subcode							
29	Not used							
31								
32	Module ID							
33	Routine ID							
34	PCB number				Message code (x'0')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							
40	Not used							
7F								

*1: Failed processor number for WCHK1

Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used ('00')							
29	DCN status							
	Force device busy	Channel connect request	Command chain execute	Path reserve	Stack status	Sense pending	Long Busy (Command process)	Processor connect
2A	DCN status							
	Single path mode	Gurant path mode	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	Garant path MP
2B	DCN flag							
	RST NTF/ RST ALG SSB PND	Pin Data pending	(RSV)	(RSV)	ODE pending	RSQ pending	SCI/SCE pending	PCH pending
2C	LCT initiation cause							
	0:Ready 1:Not ready	0:Disable 1:Enable	0: No SNS 1: 64SNS pending	0:Mount 1:Not mount	0: No RST ALG LPN 1: Exist	0: No SYS RST LPN 1: Exist	0:No stack 1:Exist	0: No wait SNS 1: Exist
2D	LCT initiation cause							
	0:No wait-RI 1:Exist	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	0:Others 1:DEV BSY JOB

Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (2/3)

	0	1	2	3	4	5	6	7
2E	Command interface flag							
	Pending exist	SCE(RSP)	(RSV)	(RSV)	ODE pending	RSQ pending	SCI pending	PCH pending
2F	Command code							
30	Internal SSB log number							
31								
32	Related reset detailed log number							
33								
34	PCB number				Message code (x'1')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)*							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40	Not used							
41	Not used							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN number							
57								
58	Initiation cause code							
59								
5A	Command interface							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (from LCT)							
5F								
60	wchklnpbitmap (from LCT)							
61								
62								
63								

* : This information is not fixed until DKC reports SSB to HOST.

Byte27 = 8E & Byte34:bit4-7=1 (Selective Reset) (3/3)

	0	1	2	3	4	5	6	7
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	EXITFLAG AREA							
6D								
6E								
6F								
70	DCN (wldcn1l)							
71								
72								
73								
74	DCN (wldcn2l)							
75								
76								
77								
78	RIW							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8E & Byte34.bit4-7=3 (Wait SENSE Timeout) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used ('00')							
29	LDEV#							
2A	Basic SSB				Basic SSB			
2B								
2C	24-byte format				32-byte format	Exception code		
2D		F/M						
2E		System error code				System error code		
2F								
30	Reset internal SSB number							
31								
32	Detail log number for RESET							
33								
34	PCB number				Message code (x'3')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)*							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

* : This information is not fixed until DKC reports SSB to HOST.

Byte27 = 8E & Byte34:bit4-7=3 (Wait SENSE Timeout) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41								
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50	LPN							
51								
52								
53								
54								
55								
56								
57								
58	Not used							
59								
5A								
5B								
5C								
5D								
5E								
5F								
60	Timeout SSB							
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02	Format Message							
03	PCB type		Processor ID					
04	System error code							
05								
06	JCB ID							
07								
08	REQ ID							
09								
0A	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0B	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0C	SEMI-SYNC	RCU SIM	0	0	RCU#			
0D	LDEV number							
0E								
0F	CDEV#							
10	RDEV#							
11	RCU LDEV number							
12								
13	Internal SSB log number							
14								
15	Related failure log number							
16								
17	Related SIM log number							
18								
19	Related SSB log number							
1A								
1B								
1C	(Reserved)							
1D	SCB							
1E	Basic SSB							
1F								
20								
21								
22	Count							
23	Device address							
24	Cylinder address (Low)							
25	Cylinder address (High)				Head address			
26	Format (x'8')				Message (x'E')			
27	Related internal SSB log number							
28								
29	Not used							
2A	Not used							
2B	Old PSW							
2C								
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Related RESET detail log number							
33								
34	PCB number				Message code (x'5')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)*							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

* : This information is not fixed until DKC reports SSB to HOST.

Byte27 = 8E & Byte34:bit4-7=5 (CHK1B Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN number							
57								
58	Initiation cause code							
59								
5A	Command interface							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (form LCT)							
5F								
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN							
71								
72								
73								
74								
75								
76								
77								
78	DKCMAIN program counter							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8E & Byte34.bit4-7=6 (DKC Internal Reset) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used							
29								
2A								
2B								
2C	Old PSW							
2D								
2E								
2F								
30	Internal SSB log number							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'6')			
35	SSID (Low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)*							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

* : This information is not fixed until DKC reports SSB to HOST.

Byte27 = 8E & Byte34:bit4-7=6 (DKC Internal Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN							
57								
58	Initiation cause code							
59								
5A	Command interface							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (form LCT)							
5F								
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN							
71								
72								
73								
74								
75								
76								
77								
78	DKCMAIN program counter							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8E & Byte34.bit4-7=7 (LCP Internal Reset) (1/2)

00	0	1	2	3	4	5	6	7
01	Time stamp							
02								
05								
06	Format Message							
07	PCB type		Processor ID					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	Internal SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used							
29	Not used							
2A	Internal Selective Reset cause							
2B								
2C	For Hotline	Old PSW			For INT SEL	LCP error code		
2D					RST			
2E						x'00'		
2F						x'00'		
30	Internal SSB log number							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'7')			
35	SSID (Low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type = DKU87I/x'EF8E' : LDEV type = DKU86I)*							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

* : This information is not fixed until DKC reports SSB to HOST.

Byte27 = 8E & Byte34:bit4-7=7 (LCP Internal Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN							
57								
58	Initiation cause code							
59								
5A	Command interface							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (form LCT)							
5F								
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN							
71								
72								
73								
74								
75								
76								
77								
78	DKCMAIN program counter							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8E & Byte34:bit4-7=8 (Force Reset) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Old PSW							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'8')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	This information is not fixed unit DKC reports SSB to HOST.							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 8E & Byte34:bit4-7=8 (Force Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (form LCT)							
5F								
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN							
71								
72								
73								
74								
75								
76								
77								
78	DKCMAIN program counter							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8E & Byte34.bit4-7=9 (Logical Inconsistency Reset) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'E')			
28	Not used (x'00')							
29								
2A								
2B								
2C	Old PSW (Interruption source address)							
2D								
2E								
2F								
30	Internal SSB log number (RESET)							
31								
32	Reset detail log number							
33								
34	PCB number				Message code (x'9')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type = DKU86I)							
37	This information is not fixed unit DKC reports SSB to HOST.							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 8E & Byte34:bit4-7=9 (Logical Inconsistency Reset) (2/2)

	0	1	2	3	4	5	6	7
40	Not used (x'00')							
41	Not used (x'00')							
42	Initialize interface information							
43								
44	Reset type code							
45								
46	1st reset type code							
47	Reset SSB type code							
48	Interrupt task code							
49	Self CHK1A difference code							
4A	Multi reset type code							
4B								
4C	Interrupted address							
4D								
4E								
4F								
50	Interrupted operation code							
51								
52								
53								
54	Current LDEV number							
55								
56	Current LPN							
57								
58	Initiation cause code (from LCT)							
59								
5A	Command interface (from LCT)							
5B	DSB (from LCT)							
5C	sys status							
5D	REQ ID (from LCT)							
5E	JCB ID (form LCT)							
5F								
60	wchklmpbitmap (from LCT)							
61								
62								
63								
64	jcb id							
65								
66	req id							
67	Job type code							
68	job status							
69	Executing processor number							
6A	Initiation cause code							
6B	Initiation detail code							
6C	Command code							
6D	Channel sequence							
6E	Reserved							
6F								
70	DCN							
71								
72								
73								
74								
75								
76								
77								
78	DKCMAIN program counter							
79								
7A								
7B								
7C	SLV-DCN (slvdcnr)							
7D								
7E								
7F								

Byte27 = 8F (Logical Inconsistency) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'8')				Message (x'F')			
28	Module ID							
29	Routine ID							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number				Message code (x'00')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF8E' : LDEV type=DKU87I / x'EF8E' : LDEV type =DKU86I)							
37	This information is not fixed unit DKC reports SSB to HOST.							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 8F (Logical Inconsistency) (2/2)

	0	1	2	3	4	5	6	7
40	Error information							
41								
42								
43								
44								
45								
46								
47								
48								
49								
4A								
4B								
4C								
4D								
4E								
4F								
50								
51								
52								
53								
54								
55								
56								
57								
58								
59								
5A								
5B								
5C								
5D								
5E								
5F								
60								
61								
62								
63								
64								
65								
66								
67								
68								
69								
6A								
6B								
6C								
6D								
6E								
6F								
70								
71								
72								
73								
74								
75								
76								
77								
78								
79								
7A								
7B								
7C								
7D								
7E								
7F								

Byte27 = 9F (Trace data/Log data) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'9')				Message (x'F')			
28	Module ID (*2)							
29	Routine ID (*2)							
2A	Error information							
2B								
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number				Message code (x'00')			
35	SSID (low) of self subsystem							
36	Symptom code (x'FF9F')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = 9F (Trace data/Log data) (2/2)

Diagram illustrating the structure of a 32-bit register (labeled 40 to 7F). The register is divided into four 8-bit segments. The top segment is labeled "Error information" and contains bits 0 through 7. The bottom segment is labeled "7F" and contains bits 24 through 31. The middle two segments are unlabeled.

*1.

- (1) The pair of system error code x'AE5A' (Drive not Ready) and x'A772' are reported even when one of the following processes has completed successfully. These indicate the PDEV spin down executed correctly and do not mean the error condition. Please ignore them.
 - PDEV replace
 - DKU Inline execution at Installation or at PDEV expansion.
- (2) System error code x'AE5B' (Unit Attention) is reported even after one of the following processes has completed successfully. It indicates the PDEV was reset correctly and does not mean the error condition. Please ignore it.
 - DKA replace
 - DKA expansion
 - PDEV expansion
 - DKA micro-program exchange
 - PDEV micro-program exchange

*2: The details information of every Module ID & Routine ID

- **Module ID & Routine ID=0xC3A5** : The Specified R-VOL of HRC is still Online from the HOST.
(Add Pair operation from the host was rejected)

Check the SSB.LOG on SVP of the specified MCU. SSB containing EC=C3A5 on the specified R-VOL number should be logged.

(1) Analyze the SSB.LOG

Refer to the case of Byte27=0X(X≠8/A) for the way of tracing an on-line host.

*3: System error code x'507F' is LOG which shows that the function of MODE277 is not effective. PS OFF/ON is required in order to confirm the function of MODE277.

*4: In case of Mode 449 ON at the primary subsystem of UR, if the secondary subsystem is powered off or the path between primary and secondary subsystem is blocked, error code DB00 might be displayed on primary subsystem.

These error codes, which are generated when the communication between primary and secondary subsystem fails, doesn't indicate any failures.

Please ignore them.

Byte27 = E0 & Byte08 = CE (HUR Service SIM) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Not used							
21								
22								
23								
24								
25								
26	Content/Format (x'CF')							
27	Format (x'E')				Message (x'0')			
28	Hardware information							
29								
2A								
2B								
2C								
2D								
2E								
2F								
30								
31								
32	Module ID							
33	Routine ID							
34	PCB				Message (x'0')			
35	SSID of self subsystem (low order)							
36	Symptom code							
37	(This information is not fixed until DKC reports SSB to HOST.)							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = E0 & Byte08 = CE (HUR Service SIM) (2/2)

	0	1	2	3	4	5	6	7
40	Internal Control information							
41								
42	LDEV# for SIM							
43								
44	Not used							
45	Mirror ID							
46	Remote LDEV#							
47								
48	Not used							
49	JNL Group Number							
4A	Internal Control information							
7D								
7E	SSB code for SIM (upper) (x'CE')							
7F	SSB code for SIM (lower)							

Byte27 = EC (Environment Monitor Detected DKC Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'9')				Message (x'C')			
28	Error detection code (*1)							
29	Error detection code (*2)							
2A	Error reason code (*3)							
2B	Reserved area							
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number (don't care)				Message code (don't care)			
35	SSID (low) of self subsystem (don't care)							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (don't care)							
3B								
3C	Not used							
3D	Cylinder address (don't care)							
3E								
3F	Head address (don't care)							

Byte27 = EC (Environment Monitor Detected DKC Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
7F								

*1: Error Detection Code

Code	Detection
10	DKCMN1
20	DKCMN2
30	SSVP
00	Others

*2: Error Component Code

Code	Component	Code	Component	Code	Component
10	Logic 1	D0	AC BOX-1 (3 Phase)	E2	AC BOX-1 (Single Phase 30A)
11	Logic 2	D1	AC BOX-2 (3 Phase)	E3	AC BOX-2 (Single Phase 30A)
A0	DKCMN1	E0	AC BOX-1 (Single Phase)		
A1	DKCMN2	E1	AC BOX-2 (Single Phase)		
A2	SSVP				
A3	Others				

*3: Error Reason Code (1/2)

Code	Reason
10	Abnormal temperature (over60°C) is detected.
11	Abnormal temperature (over60°C) is detected.
12	Abnormal temperature (over45°C) is detected.
13	Abnormal temperature (over45°C) is detected.
20	Voltage alarm has been detected.
31	Voltage for DKCMN memory has dropped below 80%.
40	PS × 1 box abnormality has been detected.
41	PS × 2 box abnormality has been detected.
50	Battery- × 1 abnormality has been detected.
51	Battery- × 2 abnormality has been detected.
52	Battery- × 3 abnormality has been detected.
60	Abnormal AC power occurred.

*3: Error Reason Code (2/2)

Code	Reason
70	Low rotation speed of FFAN11 has been detected when Error Detection Code 1 is 10.
70	Low rotation speed of FFAN21 has been detected when Error Detection Code 1 is 11.
71	Low rotation speed of FFAN12 has been detected when Error Detection Code 1 is 10.
71	Low rotation speed of FFAN22 has been detected when Error Detection Code 1 is 11.
72	Low rotation speed of RFAN11 has been detected when Error Detection Code 1 is 10.
72	Low rotation speed of RFAN21 has been detected when Error Detection Code 1 is 11.
73	Low rotation speed of RFAN12 has been detected when Error Detection Code 1 is 10.
73	Low rotation speed of RFAN22 has been detected when Error Detection Code 1 is 11.
81	JP for DKC ALARM remains.
85	JP1 for SVP remains.
90	Environment monitor register access error.
91	Environment monitor register BUSY error.
92	Ps control Register error.
93	CE INH Register error.
9A	DKCMN ready off.
A0	An environment monitors alarm disagreement.
A1	An environment monitors P/S On disagreement.
A2	An environment monitors P/S Off disagreement.
A3	EPO unmatched.
A4	Battery output unmatched.
A6	REMOTE signal unmatched.
F2	Cable error between PS P/L and DKCPNL.

Byte27 = EE (Environment Monitor Detected DKU Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'9')				Message (x'C')			
28	Error Detection code (*1)							
29	Error Component code (*2)							
2A	Error Reason code (*3)							
2B	Reserved area							
2C								
2D								
2E								
2F								
30								
31								
32								
33								
34	PCB number (don't care)				Message code (don't care)			
35	SSID (low) of self subsystem (don't care)							
36	Symptom code (x'FFEC')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information (don't care)							
3B								
3C	Not used							
3D	Cylinder address (don't care)							
3E								
3F	Head address (don't care)							

Byte27 = EE (Environment Monitor Detected DKU Failure) (2/2)

	0	1	2	3	4	5	6	7
40	Error Detection Code (*1)							
41	Error Component Code (*2)							
42	Error Reason Code (*3)							
43	Not used							
7F								

*1: Error Detection Code

Code	Detection	Code	Component
40	DKA0	80	FSWx00-L (HDU-0, CL1)
41	DKA1	82	FSWx00-U (HDU-0, CL2)
42	DKA2	90	FSWx01-L (HDU-1, CL1)
43	DKA3	92	FSWx01-U (HDU-1, CL2)
		A0	FSWx02-L (HDU-2, CL1)
		A2	FSWx02-U (HDU-2, CL2)
		B0	FSWx03-L (HDU-3, CL1)
		B2	FSWx03-U (HDU-3, CL2)

*2: Error Component Code

Code	Component	Code	Component
60	HDU-RK00	70	HDU-RK20
61	HDU-RK01	71	HDU-RK21
62	HDU-RK02	72	HDU-RK22
63	HDU-RK03	73	HDU-RK23
64	HDU-RK10	74	HDU-RK30
65	HDU-RK11	75	HDU-RK31
66	HDU-RK12	76	HDU-RK32
67	HDU-RK13	77	HDU-RK33

*3: Error Reason Code

Code	Reason
22	ACDC alarm has been detected.
4A	HDU MPS-0 abnormality has been detected.
4B	HDU MPS-1 abnormality has been detected.
4e	HDD-PL 48V Voltage fail 1(1) has been detected.
4f	HDD-PL 48V Voltage fail 1(2) has been detected.
72	FAN WARNING 0 has been detected.
73	FAN WARNING 1 has been detected.
98	PS ON abnormality (Cluster 1) has been detected.
99	PS ON abnormality (Cluster 2) has been detected.
94	FSW access error abnormality (Cluster 1) has been detected.
95	FSW access error abnormality (Cluster 2) has been detected.
A0	Cluster 1-DKA alarm disagreement.
A1	Cluster 2-DKA alarm disagreement.
A8	Cluster 1-FSW alarm disagreement.
A9	Cluster 2-FSW alarm disagreement.

Byte27 = F0 (Operation Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'E')			
28	Not used (x'00')							
29	Not used (x'00')							
2A	Not used (x'00')							
2B	Hardware level (*1)							
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Message code (x'0')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF0')							
37								
38	Log and message control (x'00')							
39	Program action code (x'1D')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = F0 (Operation Terminated) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Hardware level

Bit0 : Hardware level flag	
For bit0 = 0	For bit0 = 1
Bit 1 : Not used	Bit 1 : Not used
Bit 2 ~ 3 : SP number	Bit 2 ~ 3 : SP number
Bit 4 ~ 5 : CHLs per Cluster	Bit 4 ~ 5 : CHLs per Cluster
00 : 4	0000 : Parallel = 4, Serial = 0
01 : 8	0001 : Parallel = 8, Serial = 0
10 : Not used	0010 : Parallel = 4, Serial = 2
11 : Not used	0100 : Parallel = 4, Serial = 4
Bit 6 : NVS	0110 : Parallel = 0, Serial = 2
0 : Without NVS	1000 : Parallel = 0, Serial = 4
1 : With NVS	1010 : Parallel = 0, Serial = 8
Bit 7 : Not used	1100 : Parallel = 0, Serial = 8
Bit 8 ~ 10 : Cache size	Bit 8 : DUAL FRAME
000 : Non cache	0 : DUAL FRAME
001 : 256MB	1 : MODULAR POWER
010 : 512MB	Bit 9 ~ 11 : Cache size
011 : 768MB	000 : Non cache
100 : 1024MB	001 : 256MB
101 : 1280MB	010 : 512MB
110 : 1536MB	011 : 768MB
111 : Over 1536MB	100 : 1024MB
Bit11 ~ 13 : Cluster hardware level	101 : 1280MB
Bit14 ~ 15 : Cache/NVS hardware level	110 : 1536MB
	111 : Over 1536MB
	Bit12 ~ 13 : Cluster hardware level
	Bit14 ~ 15 : Cache/NVS hardware level

Byte27 = F1 (Micro-program Detected Cache Failure) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'I')			
28	Queue transition in progress (Side A)	Queue transition in progress (Side B)	Free SGCB queue operation in progress	Free SLCB queue operation in progress	Free GRPT queue operation in progress	Not used		
29	Not used							
	VDEV number							
2A	Queue type							
	VDEV number		Slot number					
2B	Queue number							
	Slot number							
2C	Queue number							
	Slot number							
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							

* : When byte 28, bit2-4 = 000, upper value is valid.

: When byte 28, bit2-4 ≠ 000, lower value is valid.

Byte27 = F1 (Micro-program Detected Cache Failure) (2/2)

	0	1	2	3	4	5	6	7
30	Not used (x'00')							
31	Module ID							
32	Routine ID							
33	PCB number				Message code (x'0')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF1')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used (x'00')							
3D	Cylinder address							
3E								
3F	Head address							
40								
	Error information							
7F								

Byte27 = F2 (CACHE CHK2)

00	Time stamp							
05								
06	Format Message							
07	Processor ID							
08	System Error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP REPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	Internal SSB Log Number							
17								
18	Related Failure Log Number							
19								
1A	Related SIM Log Number							
1B								
1C	Related SSB Log Number							
1D								
1E	(Reserved)							
1F	SCB							
20	Inf valid	Err Code						
21	Segment Number							
22	Filemark	ECM	ILI	Reserved	Sense Key			
23	Information Bytes							
24								
25								
26								
27	Format (x'F')				Message (x'2')			
28	Subcode (See following pages)							
29	Detail Information							
2A	(See following pages)							
2B								
2C								
2D	SSID for Other Subsystem (x'0000' for EDCC)							
2E								
2F	Maker Code							
30	Detail Information (See Following Pages.)							
31	Module ID							
32	Routine ID							
33	P/K ID				Errcode (don't care)			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF2')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration Information							
3B								
3C	RSV							
3D	Cylinder address							
3E								
3F	Head address							
40	Detail Information (See following pages)							
7F								

Byte27 = F2 (CACHE CHK2) Byte28-31 Detail

	0	1	2	3	4	5	6	7
28	Subcode							
	Error Detected: ‘1’ = P PATH ERR (DTA), ‘3’ = P PATH ERR (CSW), ‘5’ = C PATH ERR (CSW), ‘7’ = C PATH ERR (CMA), ‘8’ = CACHE BOARD ERR, ‘9’ = CACHE MEMORY ERR ‘B’ = C PATH ERR (between calibration execution)				CARB BOARD ERR ‘0’ = NO ‘1’ = YES	CMA BOARD ERR ‘0’ = NO ‘1’ = YES	CARB REG LOG ERR ‘0’ = OK ‘1’ = NG	CMA REG LOG ERR ‘0’ = OK ‘1’ = NG
29	Transfer Information							
	Transfer P/K Type and Master Path ‘0’ = ESCON I/F CHA ‘2’ = FICON I/F CHA ‘4’ = NAS I/F CHA ‘5’ = OPEN-FC I/F CHA ‘8’ = FIBRE I/F DKA				Transfer Mode ‘0’ = DMA0 ‘1’ = DMA1 ‘4’ = PMA or EXDMA ‘6’ = FCA0 ‘7’ = FCA1 ‘7’ = DRR			
2A	Act Mode							
	‘0’ = Normal Write, ‘1’ = Read ‘2’ = 2 Slave Write, ‘3’ = CtoC Cache Copy ‘4’ = CinC Cache Copy				(RSV)	(RSV)	(RSV)	(RSV)
2B	Using Path Code							
	P PATH# ‘0’ = P#0 ‘1’ = P#1	SLAVE#1 CACHE P/K# (‘0’ ~ ‘3’)	SLAVE#1 CMA# (‘0’ ~ ‘1’)	LOG PATH# ‘0’ = ORIGIN ‘1’ = OTHER	SLAVE#2 CACHE P/K# (‘0’ ~ ‘3’)		SLAVE#2 CMA# (‘0’ ~ ‘1’)	
2C	STATUS0 (SLAVE 1) byte0							
	STATUS0 ERR	STATUS0 CARB ERR	STATUS0 CMA ERR	STATUS0 CMA STATUS ERR	STATUS0 CARB UNINITIA L	STATUS0 CARB WARNING	STATUS0 CMA WARNING 0	STATUS0 CMA WARNING 1

30	STATUS0 (SLAVE 1) byte0							
	STATUS0 PATH ID# ('0' ~ 'F')				STATUS0 CMA UNINITIAL	STATUS0 Answer CACHE#		STATUS0 Answer CMA#

Byte27 = F2 (CACHE CHK2) Byte40-7F Detail

	0	1	2	3	4	5	6	7
40	STATUS0 (SLAVE#1) byte2							
	STATUS0 PXIP OUT DATA ERR	STATUS0 PXIP OUT SLRC ERR	STATUS0 PXAC OUT PERR	STATUS0 PXRAMO OUT PERR	(RSV)	STATUS0 PXRXF OUT DATA ERR	STATUS0 PXRXF OUT SLRC ERR	STATUS0 SXSW IN PERR
41	STATUS0 (SLAVE#1) byte3							
	STATUS0 PXTXR IN PEER	STATUS0 PXTXR OUT PERR	STATUS0 PX MIC ERR	STATUS0 PX TIME OUT	STATUS0 PX HW ERR	STATUS0 PX UNCRE	STATUS0 ARB ERR	STATUS0 CMA ERRINT
42	STATUS0 (SLAVE#1) byte4							
	STATUS0 CXOR IN PERR	STATUS0 CXIP OUT DATA ERR	STATUS0 CXIP OUT SLRC ERR	STATUS0 SXSR IN PERR	STATUS0 CX TIME OUT	STATUS0 CX HW ERR	STATUS0 CARB STATUS ID ERR	(RSV) INVLD COM
43	STATUS0 (SLAVE#1) byte5							
	STATUS0 BOARD DISABLE	STATUS0 MEM NOT RDY	STATUS0 MODULE DISABLE	STATUS0 MEM BACKUP MODE	STATUS0 DIAG UMNT	STATUS0 GCTLERR	STATUS0 REG CODE ER	STATUS0
44	STATUS0 (SLAVE#1) byte6							
	STATUS0 PKID PERR	STATUS0 GSLRCER / TOOUT	STATUS0 DT PKT ER / MCLKER	STATUS0 CMD PKT ER / PCLKER	STATUS0 MODULE ER / ABTERR	STATUS0 HCTL ER	STATUS0 MCTL ER	STATUS0 BRD ER
45	STATUS0 (SLAVE#1) byte7							
	STATUS0 Memory Access RETURN CRC CODE							
46	STATUS1 (SLAVE#2) byte0							
	STATUS1 ERR	STATUS1 CARB ERR	STATUS1 CMA ERR	STATUS1 CMA STATUS ERR	STATUS1 CARB UNINITIAL	STATUS1 CARB WARNING	STATUS1 CMA WARNING0	STATUS1 CMA WARNING1
47	STATUS1 (SLAVE#2) byte1							
	STATUS1 PATH ID# ('0' ~ 'F')				STATUS1 CMA UNINITIAL	STATUS1 Answer CACHE#		STATUS1 Answer CMA#
48	STATUS1 (SLAVE#2) byte2							
	STATUS1 PXIP OUT DATA ERR	STATUS1 PXIP OUT SLRC ERR	STATUS1 PXAC OUT PERR	STATUS1 PXRAMO OUT PERR	(RSV)	STATUS1 PXRXF OUT DATA ERR	STATUS1 PXRXF OUT SLRC ERR	STATUS1 SXSW IN PERR
49	STATUS1 (SLAVE#2) byte3							
	STATUS1 PXTXR IN PEER	STATUS1 PXTXR OUT PERR	STATUS1 PX MIC ERR	STATUS1 PX TIME OUT	STATUS1 PX HW ERR	STATUS1 PX UNCRE	STATUS1 ARB ERR	STATUS1 CMA ERRINT
4A	STATUS1 (SLAVE#2) byte4							
	STATUS1 CXOR IN PERR	STATUS1 CXIP OUT DATA ERR	STATUS1 CXIP OUT SLRC ERR	STATUS1 SXSR IN PERR	STATUS1 CX TIME OUT	STATUS1 CX HW ERR	STATUS1 CARB STATUS ID ERR	(RSV)
4B	STATUS1 (SLAVE#2) byte5							
	STATUS1 BOARD DISABLE	STATUS1 MEM NOT RDY	STATUS1 MODULE DISABLE	STATUS1 MEM BACKUP MODE	STATUS1 DIAG UMNT	STATUS1 GCTLERR	STATUS1 REG CODE ER	STATUS1 INVLD COM
4C	STATUS1 (SLAVE#2) byte6							
	STATUS1 PKID PERR	STATUS1 GSLRCER / TOOUT	STATUS1 DT PKT ER / MCLKER	STATUS1 CMD PKT ER / PCLKER	STATUS1 MODULE ER / ABTERR	STATUS1 HCTL ER	STATUS1 MCTL ER	STATUS1 BRD ER
4D	STATUS1 (SLAVE#2) byte7							
	STATUS0 Memory Access RETURN CRC CODE							
4E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
4F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = F2 (CACHE CHK2) Byte40-7F Detail

	0	1	2	3	4	5	6	7
50	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
51	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
52	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
53	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
54	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
55	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
56	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
57	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
58	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
59	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5A	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5B	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
5F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = F2 (CACHE CHK2) Byte40-7F Detail

	0	1	2	3	4	5	6	7
60	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
61	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
62	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
63	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
64	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
65	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
66	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
67	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
68	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
69	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6A	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6B	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
6F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = F2 (CACHE CHK2) Byte40-7F Detail

	0	1	2	3	4	5	6	7
70	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
71	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
72	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
73	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
74	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
75	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
76	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
77	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
78	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
79	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7A	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7B	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7C	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7D	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7E	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)
7F	Reserved							
	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)	(RSV)

Byte27 = F5 (Remote Copy Paths Removed/Restored) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'5')			
28	Reason code (*1)							
29	RCU device address							
2A	RCU Manufacture code / Factory code							
2B								
2C	RCU Sequence No.							
2D								
2E								
2F	MCU Manufacture code / Factory code							
30								
31	MCU Sequence No.							
32								
33								
34	SSID of self subsystem							
35								
36	Symptom code (x'38')							
37	Symptom code (x'87')							
38	Not used (x'00')							
39	Not used (x'00')							
3A	Configuration information							
3B								
3C	Not usde (x'00')							
3D	Not used (x'00')							
3E	Reference code (x'FFFB')							
3F								

Byte27 = F5 (Remote Copy Paths Removed/Restored) (2/2)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

*1: Reason code

x'01' : One or more remote copy paths removed unexpectedly.

x'02' : One or more remote copy paths restored.

x'03' : All remote copy paths removed unexpectedly.

Byte27 = F6 (CFW Access not Authorized) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'6')			
28	Reason code (x'01' : CFW ID unmatched)							
29	Not used							
2A								
2B								
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used							
31	Module ID							
32	Routine ID							
33	PCB number				Not used			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFF6')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not usde (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = F6 (CFW Access not Authorized) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte27 = FA (NVS Terminated) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'A')			
28	Reason code (x'02' : NVS failure)							
29	Not used							
2A								
2B								
2C								
2D	SSID of mate subsystem (x'0000' for EDCC)							
2E								
2F	Maker code & Manufacture code (x'00')							
30	Not used							
31	Module ID							
32	Routine ID							
33	PCB number				Error code (x'F')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFFA')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not usde (x'00')							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = FA (NVS Terminated) (2/2)

	0	1	2	3	4	5	6	7
40	Key cede (for reconnection)							
41	Format/Message (for reconnection)							
42	Module ID (for reconnection)							
43	Routine ID (for reconnection)							
44	Not used							
7F								

Byte27 = FB (HRC/HODM Pair suspend) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (low)							
26	Cylinder address (high)				Head address			
27	Format (x'F')				Message (x'B')			
28	R-Vol Suspended	R-Vol failed	Reason code (*1)					
29	RCU device address							
2A	RCU Manufacture code/Factory code							
2B								
2C								
2D	RCU Sequence No.							
2E								
2F	MCU Manufacture code/Factory code							
30								
31								
32	MCU Sequence No.							
33								
34	SSID of self subsystem							
35								
36	Symptom code (x'FE')							
37	Symptom code (same byte #28)							
38	Not used (x'00')							
39	Not used (x'00')							
3A	Configuration information							
3B								
3C	Not usde (x'00')							
3D	Not used (x'00')							
3E	Reference code (x'FFFB')							
3F								

Byte27 = FB (HRC/HODM Pair suspend) (2/2)

	0	1	2	3	4	5	6	7
40	Error information							
7F								

*1: Reason code

x'14'-x'2F' : Reserved

x'30' : Pair suspended. MCU device write error.

x'31' : Pair suspended. RCU subsystem error/or MCU subsystem error.

x'32' : Pair suspended. RCU device communication error.

x'33' : Pair suspended (Critical device status). All write command is rejected until the pair is re-established.

x'34' : Pair suspended. RCU device is not ready (intervention required).

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
01								
02								
03								
04								
05								
06	Format Message							
07	Processor ID							
08	System error code							
09								
0A	Reserved							
0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVPREPORT	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	PK information VLD	Reserved
0F	SEMI-SYNC	RCU SIM	Reserved					
10	LDEV#							
11								
12	CDEV#							
13	RDEV#							
14	JCB#							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	Reserved							
1F	SCB (x'35')							
20	Basic SSB							
21								
22								
23	Count							
24	Device address							
25	Cylinder address (Low)							
26	Cylinder address (High)				Head address			
27	Format (x'F')				Message (x'F')			
28	Subcode (See following pages)							
29	Hardware information (See following pages)							
2A								
2B								
2C								
2D								
2E								
2F								
30								
31	Module ID							
32	Routine ID							
33	PKID				Not used (x'0')			
34	SSID of self subsystem							
35								
36	Symptom code (x'FFFF')							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Not used							
3D	Cylinder address							
3E								
3F	Head address							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) (2/2)

	0	1	2	3	4	5	6	7
40	Hardware information (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(1) Byte28 = 01 : DXBF Correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'01' : DXBF Correctable error							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Not used (x'00')Common information 1 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(2) Byte28 = 02 : ECMA Warning

	0	1	2	3	4	5	6	7
28	Subcode : x'02' : ECMA Warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 2 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(3) Byte28 = 04 : SMA Warning

	0	1	2	3	4	5	6	7
28	Subcode : x'04' : SMA Warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Warning Occurred SM PCB# (Side A:x'00', Side B:x'01')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 3 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(4) Byte28 = 06/07/08/09 : Three SM DISABLE lines inconsistent

	0	1	2	3	4	5	6	7
28	Subcode : x'06' : Three SM (A) DISABLE lines inconsistent Subcode : x'07' : Three SM (B) DISABLE lines inconsistent							
29	MPID (REPORTED SSB)							
2A	Not used							
2B	EMPA:HOUT AB							
	Not used	SMDA OUTX(2)	SMDA OUTX(1)	SMDA OUTX(0)	Not used	SMDB OUTX(2)	SMDB OUTX(1)	SMDB OUTX(0)
2C	Not used							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	EMPA:HOUT AB							
	SMDA MV	SMDA X(2)	SMDA X(1)	SMDA X(0)	SMDB MV	SMDB X(2)	SMDB X(1)	SMDB X(0)
40	Common information 4 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(5) Byte28 = 10 : SMA slave error

	0	1	2	3	4	5	6	7
28	Subcode : x'10' : SMA slave error							
29	MPID (REPORTED SSB)							
2A	Scan slave MPID (Micro Information)							
2B	Type of SCAN (MP Status Read: '04', SCAN read: '02', SCAN write: '01')							
2C	Slave Error detected SM# (Side A:x'00', Side B:x'01')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Scan slave MPID							
40	Common information 5 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(6) Byte28 = 11 : MPA slave error

	0	1	2	3	4	5	6	7
28	Subcode : x'11' : MPA slave error							
29	MPID (REPORTED SSB)							
2A								
	Transfer ESMA (4: Side A, 3: Side B)							
2B								
	SSB reported P/K#							
2C	MPA:MPASTS (BIT35)							
	BSINT	CHK3 HSNMST	CHK3 HSNSLV	CHK1B FM	(Not used)	COM CHK1B	CHK1B XR	MPINT
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	MPA:SLVCHK3STS							
	Not used	Not used	Not used	Not used	Not used	Not used	LM CHK3	DSTAGE
40	Common information 6 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(7) Byte28 = 12 : SMA SCINT ERROR

	0	1	2	3	4	5	6	7
28	Subcode : x'12' : SMA SCINT ERROR							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Warning Occurred SM PCB# (Side A:x'00', Side B:x'01')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 7 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(8) Byte28 = 20 : FSW HBC BUS test

	0	1	2	3	4	5	6	7
28	Subcode : x'20' : FSW HBC BUS test							
29	MPID (REPORTED SSB)							
2A	LEDADRDT: HBC Bus Access address							
	LED ADDRESS REG							
2B	LEDADRDT: HBC Bus Access write data							
	LED DATA REG							
2C	LEDADRDT: HBC Bus Access write data							
	SERIAL RD DT							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	LEDCMD: HBC Bus Access command							
	LED CMD REG 0x01:WT,0x02:RD							
40	Common information 8 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(9) Byte28 = 40 : Abnormal DC Voltage CTL warning

	0	1	2	3	4	5	6	7
28	Subcode : x'40' : Abnormal DC Voltage CTL warning							
29	MPID (REPORTED SSB)							
2A	Register access result BYTE (00:No Error, 01>Error detected)							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 9 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(10) Byte28 = 41 : Abnormal CEMODE warning

	0	1	2	3	4	5	6	7
28	Subcode : x'41' : Abnormal CEMODE warning							
29	MPID (REPORTED SSB)							
2A	Register access result BYTE (00:No Error, 01:Error detected)							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 9 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(11) Byte28 = 80 : CHUB correctable error

	0	1	2	3	4	5	6	7
28	Subcode : x'80' : CHUB correctable error							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 10 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(12) Byte28 = 81 : DHUB DDR warning

	0	1	2	3	4	5	6	7
28	Subcode : x'81' : DHUB DDR warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 11 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(13) Byte28 = 82 : FCA warning

	0	1	2	3	4	5	6	7
28	Subcode : x'82' : FCA warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 12 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(14) Byte28 = 83 : DCMA warning

	0	1	2	3	4	5	6	7
28	Subcode : x'83' : DCMA warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 13 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(15) Byte28 = 84 : CARB memory warning

	0	1	2	3	4	5	6	7
28	Subcode : x'84' : CARB memory warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used x'00')							
40	Common information 14 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(16) Byte28 = 85 : PCI warning

	0	1	2	3	4	5	6	7
28	Subcode : x'85' : PCI warning							
29	MPID (REPORTED SSB)							
2A	Not used (x'00')							
2B	Not used (x'00')							
2C	LOG No. (L#)							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 15 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning : Error handling support) Hardware detail information

(17) Byte28 = B0 : CARB warning

	0	1	2	3	4	5	6	7
28	Subcode : x'B0' : CARB warning							
29	MPID (REPORTED SSB)							
2A	Register access result BYTE (00:No Error, 01:Error detected)							
2B	Used P-PATH# (00:P-PATH0, 01:P-PATH1)							
2C	Not used (x'00')							
2D	Not used (x'00')							
2E	Not used (x'00')							
2F	Not used (x'00')							
30	Not used (x'00')							
40	Common information 16 (See following pages)							
7F								

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type1 (1/4)

	0	1	2	3	4	5	6	7
40	BFC0 DXBF0 WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	BFC0 ECC1bERR 3	BFC0 ECC1bERR 2	BFC0 ECC1bERR 1	BFC0 ECC1bERR 0
41	BFC0 DXBF0 WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	BFC1 ECC1bERR 3	BFC1 ECC1bERR 2	BFC1 ECC1bERR 1	BFC1 ECC1bERR 0
42	BFC0 DXBF1 WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	BFC0 ECC1bERR 3	BFC0 ECC1bERR 2	BFC0 ECC1bERR 1	BFC0 ECC1bERR 0
43	BFC1 DXBF1 WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	BFC1 ECC1bERR 3	BFC1 ECC1bERR 2	BFC1 ECC1bERR 1	BFC1 ECC1bERR 0
44	DA0 DXBF WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	DXBF ECC1bERR 3	DXBF ECC1bERR 2	DXBF ECC1bERR 1	DXBF ECC1bERR 0
45	DA1 DXBF WARN							
	(Not used)	(Not used)	(Not used)	(Not used)	DXBF ECC1bERR 3	DXBF ECC1bERR 2	DXBF ECC1bERR 1	DXBF ECC1bERR 0
46	Not used							
47	Not used							
48	CHUB WARN 0							
	(Not used)	(Not used)	(Not used)	ROSC ECC1bERR	PMA ECC1bERR 3	PMA ECC1bERR 2	PMA ECC1bERR 1	PMA ECC1bERR 0
49	CHUB WARN 0							
	DMA ECC1bERR 7	DMA ECC1bERR 6	DMA ECC1bERR 5	DMA ECC1bERR 4	DMA ECC1bERR 3	DMA ECC1bERR 2	DMA ECC1bERR 1	DMA ECC1bERR 0
4A	CHUB WARN 0							
	BUF3 RD ECC1bERR	BUF3 WR ECC1bERR	BUF2 RD ECC1bERR	BUF2 WR ECC1bERR	BUF1 RD ECC1bERR	BUF1 WR ECC1bERR	BUF0 RD ECC1bERR	BUF0 WR ECC1bERR
4B	CHUB WARN 0							
	DXBF ECC1bERR 1	DXBF ECC1bERR 0	BFCTL1 RD ECC1bERR	BFCTL0 RD ECC1bERR	BRIDGE ECC1bERR 3	BRIDGE ECC1bERR 2	BRIDGE ECC1bERR 1	BRIDGE ECC1bERR 0
4C	Not used							
4D	Not used							
4E	Not used							
4F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type1 (2/4)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Not used							
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type1 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type1 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type2 (1/4)

	0	1	2	3	4	5	6	7
40	ECMA Threshold counter for the warning							
	Module group #0 - H							
41	ECMA Threshold counter for the warning							
	Module group #0 - L							
42	Not used							
43	Not used							
44	ECMA Threshold counter for the warning							
	Module group #2 - H							
45	ECMA Threshold counter for the warning							
	Module group #2 - L							
46	Not used							
47	Not used							
48	ECMA Threshold counter for the warning							
	Module group #4 - H							
49	ECMA Threshold counter for the warning							
	Module group #4 - L							
4A	Not used							
4B	Not used							
4C	ECMA Threshold counter for the warning							
	Module group #6 - H							
4D	ECMA Threshold counter for the warning							
	Module group #6 - L							
4E	Not used							
4F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type2 (2/4)

	0	1	2	3	4	5	6	7
50	ECMA Threshold counter for the warning							
	Module group #8 - H							
51	ECMA Threshold counter for the warning							
	Module group #8 - L							
52	Not used							
53	Not used							
54	ECMA Threshold counter for the warning							
	Module group #10 - H							
55	ECMA Threshold counter for the warning							
	Module group #10 - L							
56	Not used							
57	Not used							
58	ECMA Threshold counter for the warning							
	Module group #12 - H							
59	ECMA Threshold counter for the warning							
	Module group #12 - L							
5A	Not used							
5B	Not used							
5C	ECMA Threshold counter for the warning							
	Module group #14 - H							
5D	ECMA Threshold counter for the warning							
	Module group #14 - L							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type2 (3/4)

	0	1	2	3	4	5	6	7
60	ECMA:WARNING Register Information							
	WARNING Register Information							
61	Not used							
62	ECMA:ERRSTAT0 information							
	ERRLOC register MG#X-H							
63	ECMA:ERRSTAT0 information							
	ERRLOC register MG#X-H							
64	ECMA:ERRSTAT0 information							
	ERRLOC register MG#X-H							
65	ECMA:ERRSTAT1 information							
	ERRLOC register MG#X-H							
66	ECMA:ERRSTAT0 information							
	ERRLOC register MG#X-L							
67	ECMA: ERRSTAT0 information							
	ERRLOC register MG#X-L							
68	ECMA:ERRSTAT1 information							
	ERRLOC register MG#X-L							
69	ECMA:ERRSTAT1 information							
	ERRLOC register MG#X-L							
6A	ECMA:ERRSTAT0 information							
	ERRLOC register MG#XN-H							
6B	ECMA:ERRSTAT0 information							
	ERRLOC register MG#XN-H							
6C	ECMA:ERRSTAT1 information							
	ERRLOC register MG#XN-H							
6D	ECMA:ERRSTAT1 information							
	ERRLOC register MG#XN-H							
6E	ECMA:ERRSTAT0 information							
	ERRLOC register MG#XN-L							
6F	ECMA:ERRSTAT0 information							
	ERRLOC register MG#XN-L							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type2 (4/4)

	0	1	2	3	4	5	6	7
70	ECMA:ERRSTAT1 information ERRLOC register MG#XN- L							
71	ECMA:ERRSTAT1 information ERRLOC register MG#XN- L							
72	Not used							
73	Not used							
74	Warning Occurred CMM.NO CMM.NO							
75	Warning Occurred CMM.NO PK LOCATION							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type3 (1/4)

	0	1	2	3	4	5	6	7
40	Shared memory Threshold counter for the warning SMA-DIMM00							
41	Shared memory Threshold counter for the warning SMA-DIMM01							
42	Shared memory Threshold counter for the warning SMA-DIMM02							
43	Shared memory Threshold counter for the warning SMA-DIMM03							
44	Shared memory Threshold counter for the warning SMA-DIMM04							
45	Shared memory Threshold counter for the warning SMA-DIMM05							
46	Not used							
47	Not used							
48	Not used							
49	SMA:DIMM SIGNAL ERROR DIMM							
	(Not used)	(Not used)	DIMM05 control error	DIMM04 control error	DIMM03 control error	DIMM02 control error	DIMM01 control error	DIMM00 control error
4A	SMA:UNCOR ERROR DIMM							
	(Not used)	(Not used)	DIMM05 uncorrectable error	DIMM04 uncorrectable error	DIMM03 uncorrectable error	DIMM02 uncorrectable error	DIMM01 uncorrectable error	DIMM00 uncorrectabl e error
4B	SMA:COR ERROR DIMM							
	(Not used)	(Not used)	DIMM05 correctable error	DIMM04 correctable error	DIMM03 correctable error	DIMM02 correctable error	DIMM01 correctable error	DIMM00 correctable error
4C	SMA:COR ERROR DIMM00/01 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
4D	SMA:COR ERROR DIMM00/01 LOCATION							
	Correctable error in memory IC8 (for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)
4E	SMA:COR ERROR DIMM00/01 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
4F	SMA:COR ERROR DIMM00/01 LOCATION							
	Correctable error in memory IC8 for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type3 (2/4)

	0	1	2	3	4	5	6	7
50	SMA:COR ERROR DIMM02/03 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
51	SMA:COR ERROR DIMM02/03 LOCATION							
	Correctable error in memory IC8 (for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)
52	SMA:COR ERROR DIMM02/03 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
53	SMA:COR ERROR DIMM02/03 LOCATION							
	Correctable error in memory IC8 (for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)
54	SMA:COR ERROR DIMM04/05 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
55	SMA:COR ERROR DIMM04/05 LOCATION							
	Correctable error in memory IC8 (for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)
56	SMA:COR ERROR DIMM04/05 LOCATION							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	Correctable error in memory IC6 (for data)	Correctable error in memory IC7 (for data)	Correctable error in memory IC5 (for data)
57	SMA:COR ERROR DIMM04/05 LOCATION							
	Correctable error in memory IC8 (for data)	Correctable error in memory IC4 (for data)	Correctable error in memory IC9 (for data)	Correctable error in memory IC3 (for data)	Correctable error in memory IC10 (for data)	Correctable error in memory IC2 (for data)	Correctable error in memory IC11 (for data)	Correctable error in memory IC1 (for data)
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type3 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type3 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type4 (1/4)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Not used							
45	Not used							
46	Not used							
47	Not used							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	Not used							
4D	Not used							
4E	Not used							
4F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type4 (2/4)

	0	1	2	3	4	5	6	7
50	Threshold counter for the warning							
	MPID 32				MPID 33			
51	Threshold counter for the warning							
	MPID 34				MPID 35			
52	Threshold counter for the warning							
	MPID 36				MPID 37			
53	Threshold counter for the warning							
	MPID 38				MPID 39			
54	Threshold counter for the warning							
	MPID 40				MPID 41			
55	Threshold counter for the warning							
	MPID 42				MPID 43			
56	Threshold counter for the warning							
	MPID 44				MPID 45			
57	Threshold counter for the warning							
	MPID 46				MPID 47			
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type4 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type4 (4/4)

	0	1	2	3	4	5	6	7
70	Threshold counter for the warning							
	MPID 96				MPID 97			
71	Threshold counter for the warning							
	MPID 98				MPID 99			
72	Threshold counter for the warning							
	MPID 100				MPID 101			
73	Threshold counter for the warning							
	MPID 102				MPID 103			
74	Threshold counter for the warning							
	MPID 104				MPID 105			
75	Threshold counter for the warning							
	MPID 106				MPID 107			
76	Threshold counter for the warning							
	MPID 108				MPID 109			
77	Threshold counter for the warning							
	MPID 110				MPID 111			
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type5 (1/4)

	0	1	2	3	4	5	6	7
40	Threshold counter for the scan group							
	Scan path group A							
41	Threshold counter for the scan group							
	Scan path group B							
42	Not used							
43	Not used							
44	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#31	SINT MASK MPA#30	SINT MASK MPA#29	SINT MASK MPA#28	SINT MASK MPA#27	SINT MASK MPA#26	SINT MASK MPA#25	SINT MASK MPA#24
45	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#23	SINT MASK MPA#22	SINT MASK MPA#21	SINT MASK MPA#20	SINT MASK MPA#19	SINT MASK MPA#18	SINT MASK MPA#17	SINT MASK MPA#16
46	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#15	SINT MASK MPA#14	SINT MASK MPA#13	SINT MASK MPA#12	SINT MASK MPA#11	SINT MASK MPA#10	SINT MASK MPA#09	SINT MASK MPA#08
47	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#07	SINT MASK MPA#06	SINT MASK MPA#05	SINT MASK MPA#04	SINT MASK MPA#03	SINT MASK MPA#02	SINT MASK MPA#01	SINT MASK MPA#00
48	Not used							
49	Not used							
4A	SMA:MP STATUS TIMEOUT DATA							
	MPST_TODT							
4B	SMA:MP STATUS TIMEOUT DATA							
	MPST_TODT							
4C	SMA:VALID TIMER TIMEOUT DATA							
	VAL_TODT							
4D	SMA:VALID TIMER TIMEOUT DATA							
	VAL_TODT							
4E	SMA:SCINT TIMEOUT DATA							
	SC_TODT							
4F	SMA:SCINT TIMEOUT DATA							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type5 (2/4)

	0	1	2	3	4	5	6	7
50	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_ PERR MPA#31	SCCNT_ PERR MPA#30	SCCNT_ PERR MPA#29	SCCNT_ PERR MPA#28	SCCNT_ PERR MPA#27	SCCNT_ PERR MPA#26	SCCNT_ PERR MPA#25	SCCNT_ PERR MPA#24
51	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_ PERR MPA#23	SCCNT_ PERR MPA#22	SCCNT_ PERR MPA#21	SCCNT_ PERR MPA#20	SCCNT_ PERR MPA#19	SCCNT_ PERR MPA#18	SCCNT_ PERR MPA#17	SCCNT_ PERR MPA#16
52	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_ PERR MPA#15	SCCNT_ PERR MPA#14	SCCNT_ PERR MPA#13	SCCNT_ PERR MPA#12	SCCNT_ PERR MPA#11	SCCNT_ PERR MPA#10	SCCNT_ PERR MPA#09	SCCNT_ PERR MPA#08
53	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_ PERR MPA#07	SCCNT_ PERR MPA#06	SCCNT_ PERR MPA#05	SCCNT_ PERR MPA#04	SCCNT_ PERR MPA#03	SCCNT_ PERR MPA#02	SCCNT_ PERR MPA#01	SCCNT_ PERR MPA#00
54	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_ PERR MPA#31	VALCNT_ PERR MPA#30	VALCNT_ PERR MPA#29	VALCNT_ PERR MPA#28	VALCNT_ PERR MPA#27	VALCNT_ PERR MPA#26	VALCNT_ PERR MPA#25	VALCNT_ PERR MPA#24
55	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_ PERR MPA#23	VALCNT_ PERR MPA#22	VALCNT_ PERR MPA#21	VALCNT_ PERR MPA#20	VALCNT_ PERR MPA#19	VALCNT_ PERR MPA#18	VALCNT_ PERR MPA#17	VALCNT_ PERR MPA#16
56	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_ PERR MPA#15	VALCNT_ PERR MPA#14	VALCNT_ PERR MPA#13	VALCNT_ PERR MPA#12	VALCNT_ PERR MPA#11	VALCNT_ PERR MPA#10	VALCNT_ PERR MPA#09	VALCNT_ PERR MPA#08
57	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_ PERR MPA#07	VALCNT_ PERR MPA#06	VALCNT_ PERR MPA#05	VALCNT_ PERR MPA#04	VALCNT_ PERR MPA#03	VALCNT_ PERR MPA#02	VALCNT_ PERR MPA#01	VALCNT_ PERR MPA#00
58	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#31	SCSEQ_ERR MPA#30	SCSEQ_ERR MPA#29	SCSEQ_ERR MPA#28	SCSEQ_ERR MPA#27	SCSEQ_ERR MPA#26	SCSEQ_ERR MPA#25	SCSEQ_ERR MPA#24
59	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#23	SCSEQ_ERR MPA#22	SCSEQ_ERR MPA#21	SCSEQ_ERR MPA#20	SCSEQ_ERR MPA#19	SCSEQ_ERR MPA#18	SCSEQ_ERR MPA#17	SCSEQ_ERR MPA#16
5A	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#15	SCSEQ_ERR MPA#14	SCSEQ_ERR MPA#13	SCSEQ_ERR MPA#12	SCSEQ_ERR MPA#11	SCSEQ_ERR MPA#10	SCSEQ_ERR MPA#09	SCSEQ_ERR MPA#08
5B	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#07	SCSEQ_ERR MPA#06	SCSEQ_ERR MPA#05	SCSEQ_ERR MPA#04	SCSEQ_ERR MPA#03	SCSEQ_ERR MPA#02	SCSEQ_ERR MPA#01	SCSEQ_ERR MPA#00
5C	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ERR MPA#31	SINT_OUT_ERR MPA#30	SINT_OUT_ERR MPA#29	SINT_OUT_ERR MPA#28	SINT_OUT_ERR MPA#27	SINT_OUT_ERR MPA#26	SINT_OUT_ERR MPA#25	SINT_OUT_ERR MPA#24
5D	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ERR MPA#23	SINT_OUT_ERR MPA#22	SINT_OUT_ERR MPA#21	SINT_OUT_ERR MPA#20	SINT_OUT_ERR MPA#19	SINT_OUT_ERR MPA#18	SINT_OUT_ERR MPA#17	SINT_OUT_ERR MPA#16
5E	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ERR MPA#15	SINT_OUT_ERR MPA#14	SINT_OUT_ERR MPA#13	SINT_OUT_ERR MPA#12	SINT_OUT_ERR MPA#11	SINT_OUT_ERR MPA#10	SINT_OUT_ERR MPA#09	SINT_OUT_ERR MPA#08
5F	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ERR MPA#07	SINT_OUT_ERR MPA#06	SINT_OUT_ERR MPA#05	SINT_OUT_ERR MPA#04	SINT_OUT_ERR MPA#03	SINT_OUT_ERR MPA#02	SINT_OUT_ERR MPA#01	SINT_OUT_ERR MPA#00

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type5 (3/4)

	0	1	2	3	4	5	6	7
60	SMA:SCINT SIGNAL							
	SINT SIG MPA#31	SINT SIG MPA#30	SINT SIG MPA#29	SINT SIG MPA#28	SINT SIG MPA#27	SINT SIG MPA#26	SINT SIG MPA#25	SINT SIG MPA#24
61	SMA:SCINT SIGNAL							
	SINT SIG MPA#23	SINT SIG MPA#22	SINT SIG MPA#21	SINT SIG MPA#20	SINT SIG MPA#19	SINT SIG MPA#18	SINT SIG MPA#17	SINT SIG MPA#16
62	SMA:SCINT SIGNAL							
	SINT SIG MPA#15	SINT SIG MPA#14	SINT SIG MPA#13	SINT SIG MPA#12	SINT SIG MPA#11	SINT SIG MPA#10	SINT SIG MPA#09	SINT SIG MPA#08
63	SMA:SCINT SIGNAL							
	SINT SIG MPA#07	SINT SIG MPA#06	SINT SIG MPA#05	SINT SIG MPA#04	SINT SIG MPA#03	SINT SIG MPA#02	SINT SIG MPA#01	SINT SIG MPA#00
64	Not used							
65	SMA:PATH0 ERROR STATUS							
	ALL LOCK BUSY	MEMORY NOT READY	BACKUP BUSY	ALL LOCK ERROR	MEMORY DISCONNECT BUSY	ILLEGAL ADDRESS ERROR	DIMM INV ERROR	ILLEGAL LOCK ERROR
66	SMA:PATH0 ERROR STATUS							
	ASC TO ERROR	LOCK TO ERROR	(Not used)	DSTG ADDRESS ERROR	PRB ERROR	CHECK CODE ERROR	PATH ANY ERROR	PATH BOD ERROR
67	SMA:PATH0 ERROR STATUS							
	CORRECT ABLE ERROR	UNCORRECT ABLE ERROR	I/O REG PARITY ERROR	SMB ERROR	(Not used)	SM PS DOWN	MCTL SIG ERROR	CMN BOD ERROR
68	SMA:PATH0 RXB ERROR							
	RXB CMD PARITY ERROR	RXB ADR PARITY ERROR	RXB WDT PARITY ERROR	RX TO ERROR	SYNC0 UNDER ERROR	SYNC1 UNDER ERROR	SYNC0 OVR	SYNC1 OVR
69	SMA:PATH0 RXB ERROR							
	LRC ERROR	LRC COMPERE ERROR	LOCK COMMAND ERROR	R/W COMMAND ERROR	ENT ON ERROR	ENT OFF ERROR	RPTR ERROR	NEXT RPTR ERROR
6A	SMA:PATH0 RXB ERROR							
	RX CNT ERROR	PCNT0 ERROR	RX SEQ ERROR	(Not used)	REQ TO ERROR	(Not used)	(Not used)	(Not used)
6B	SMA:PATH0 RXB ERROR							
	CMD PGEN ERROR	ADR PGEN ERROR	WDT PGEN ERROR	(Not used)	RXB PERR	(Not used)	(Not used)	(Not used)
6C	SMA:PATH0 PRB ERROR							
	IR PID ERROR	IRB PARITY ERROR	(Not used)	(Not used)	PR PID PARITY ERROR	PRB PARITY ERROR	PRCC PARITY ERROR	(Not used)
6D	SMA:PATH0 PRB ERROR							
	PCNT1 ERROR	PRB CNT ERROR	TX SEQ ERROR	PID ERROR	PRB TO ERROR	EINT ERROR	(Not used)	(Not used)
6E	SMA:PATH0 PRB ERROR							
	MEM BKUP BSY	PATH BKUP BSY	DBL ALLK ERROR	ALLK MPID ERROR	DIMM INV ERROR	SM AREA ERROR	(Not used)	MPA OUT ERROR
6F	SMA:PATH0 PRB ERROR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type5 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	SMA:PATH1 ERROR STATUS							
	ALL LOCK BUSY	MEMORY NOT READY	BACKUP BUSY	ALL LOCK ERROR	MEMORY DISCONNECT BUSY	ILLEGAL ADDRESS ERROR	ALL LOCK BUSY	MEMORY NOT READY
72	SMA:PATH1 ERROR STATUS							
	ASC TO ERROR	LOCK TO ERROR	(Not used)	DSTG ADDRESS ERROR	PRB ERROR	CHECK CODE ERROR	ASC TO ERROR	LOCK TO ERROR
73	SMA:PATH1 ERROR STATUS							
	CORRECTABLE ERROR	UNCORRECTABLE ERROR	I/O REG PARITY ERROR	SMB ERROR	(Not used)	SM PS DOWN	CORRECTABLE ERROR	UNCORRECTABLE ERROR
74	SMA:PATH1 RXB ERROR							
	RXB CMD PARITY ERROR	RXB ADR PARITY ERROR	RXB WDT PARITY ERROR	RX TO ERROR	SYNC0 UNDER ERROR	SYNC1 UNDER ERROR	RXB CMD PARITY ERROR	RXB ADR PARITY ERROR
75	SMA:PATH1 RXB ERROR							
	LRC ERROR	LRC COMPERE ERROR	LOCK COMMAND ERROR	R/W COMMAND ERROR	ENT ON ERROR	ENT OFF ERROR	LRC ERROR	LRC COMPERE ERROR
76	SMA:PATH1 RXB ERROR							
	RX CNT ERROR	PCNT0 ERROR	RX SEQ ERROR	(Not used)	REQ TO ERROR	(Not used)	RX CNT ERROR	PCNT0 ERROR
77	SMA:PATH1 RXB ERROR							
	CMD PGEN ERROR	ADR PGEN ERROR	WDT PGEN ERROR	(Not used)	RXB PERR	(Not used)	CMD PGEN ERROR	ADR PGEN ERROR
78	SMA:PATH1 PRB ERROR							
	IR PID ERROR	IRB PARITY ERROR	(Not used)	(Not used)	PR PID PARITY ERROR	PRB PARITY ERROR	IR PID ERROR	IRB PARITY ERROR
79	SMA:PATH1 PRB ERROR							
	PCNT1 ERROR	PRB CNT ERROR	TX SEQ ERROR	PID ERROR	PRB TO ERROR	EINT ERROR	PCNT1 ERROR	PRB CNT ERROR
7A	SMA:PATH1 PRB ERROR							
	MEM P BKU BSY	PATH BKUP BSY	DBL ALLK ERROR	ALLK MPID ERROR	DIMM INV ERROR	SM AREA ERROR	MEM BKUP BSY	PATH BKUP BSY
7B	SMA:PATH1 PRB ERROR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7C	SMA:BOARD ERROR STATUS							
	WDB ERROR	MWB ERROR	MRB ERROR	RDB ERROR	RWB ERROR	MARB ERROR	MV SEQ ERROR	MSEQ ERROR
7D	SMA:BOARD ERROR STATUS							
	RD SEQ ERROR	RTN SEQ ERROR	REF CTL ERROR	LQ ARB ERROR	LQ ERROR	IWDB ERROR	IWB ERROR	IRDB ERROR
7E	SMA:BOARD ERROR STATUS							
	IRB ERROR	IARB ERROR	IMV SEQ ERROR	ISEQ ERROR	TIMER ERROR	SC SEQ ERROR	BC REQ ERROR	SLOT ERROR
7F	SMA:BOARD ERROR STATUS							
	PD ADR ERROR	CLK ERROR	LK WAIT TO	LK PRT ERROR	LADR PERR	PID ERROR	FIFO ERROR	DLL CTL ERROR

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type6 (1/4)

	0	1	2	3	4	5	6	7
40	MPA:SLVCHK3STS							
	XMST0	XMST1	XMST2	SMST3	CHK3 SLVA	CHK3 SLVB	CHK3 SLVC	CHK3 SLVD
41	MPA:LM SLVCHK3							
	(Not used)	(Not used)	XR CMP	XR SMRX	XR SMTX	XR RCV	XR FIFO	XR PCI
42	MPA:LM SLVCHK3							
	A0 CTLER	A1 CTLER	B0 CTLER	B1 CTLER	C0 CTLER	C1 CTLER	D0 CTLER	D1 CTLER
43	MPA:LM SLVCHK3							
	A0 DTER	A1 DTER	B0 DTER	B1 DTER	C0 DTER	C1 DTER	D0 DTER	D1 DTER
44	MPA:LM SLVCHK3							
	A0 TIMEOUT	A1 TIMEOUT	B0 TIMEOUT	B1 TIMEOUT	C0 TIMEOUT	C1 TIMEOUT	D0 TIMEOUT	D1 TIMEOUT
45	MPA:SEQDS1TM							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	TOUT	TPER
46	MPA:SEQDSO							
	DTPER(3)	DTPER(2)	DTPER(1)	DTPER(0)	XPERR	XXPER	YPERR	YYPERR
47	MPA:REGDS2							
	CRCERR	LRCERR	X,XXPTERR	Y,YYPTERR	MMO PER(3)	MMO PER(2)	MMO PER(1)	MMO PER(0)
48	MPA:REGDS2							
	MPASRS ERR3	MPASRS ERR2	MPASRS ERR1	MPASRS ERR0	SMASRS ERR3	SMASRS ERR2	SMASRS ERR1	SMASRS ERR0
49	MPA:CNTDS2							
	(Not used)	(Not used)	(Not used)	(Not used)	NPLCNT PER(1)	NPLCNT PER(0)	CPLCNT PER(1)	CPLCNT PER(0)
4A	MPA:REGDSAC							
	BUF(3)	BUF(2)	BUF(1)	BUF(0)	XPT	XXPT	YPT	YYPT
4B	MPA:REGDSBD							
	BUF(3)	BUF(2)	BUF(1)	BUF(0)	XPT	XXPT	YPT	YYPT
4C	MPA:SCANA REGS1							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
4D	MPA:SCANA REGS1							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
4E	MPA:SCANA REGS6							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
4F	MPA:SCANA REGS6							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type6 (2/4)

	0	1	2	3	4	5	6	7
50	MPA:SCANA SEQSITM							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	TIMECNT	TIMEOUT
51	MPA:SCANA REGS0							
	MPSTS ERR3	MPSTS ERR2	MPSTS ERR1	MPSTS ERR0	PORTDT	PORTDT	PORTDT	PORTDT
52	MPA:SCANA REGS7							
	(Not used)	CRCPER	LRCPCR	STERR	RDELRC	WRNLRC ERR	RDNLRC ERR	WRELRC ERR
53	MPA:SCANA REGS7							
	SCDT ERR3	SCDT ERR2	SCDT ERR1	SCDT ERR0	ERDT ERR3	ERDT ERR2	ERDT ERR1	ERDT ERR0
54	MPA:SCANA REGS7							
	INTDT2 ERR3	INTDT2 ERR2	INTDT2 ERR1	INTDT2 ERR0	INTDT ERR3	INTDT ERR2	INTDT ERR1	INTDT ERR0
55	MPA:SCANB REGS1							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
56	MPA:SCANB REGS1							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
57	MPA:SCANB REGS6							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
58	MPA:SCANB REGS6							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
59	MPA:SCANB SEQSITM							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	TIMECNT	TIMEOUT
5A	MPA:SCANB REGS0							
	MPSTS ERR3	MPSTS ERR2	MPSTS ERR1	MPSTS ERR0	PORTDT	PORTDT	PORTDT	PORTDT
5B	MPA:SCANB REGS7							
	(Not used)	CRCPER	LRCPCR	STERR	RDELRC	WRNLRC ERR	RDNLRC ERR	WRELRC ERR
5C	MPA:SCANB REGS7							
	SCDT ERR3	SCDT ERR2	SCDT ERR1	SCDT ERR0	ERDT ERR3	ERDT ERR2	ERDT ERR1	ERDT ERR0
5D	MPA:SCANB REGS7							
	INTDT2 ERR3	INTDT2 ERR2	INTDT2 ERR1	INTDT2 ERR0	INTDT ERR3	INTDT ERR2	INTDT ERR1	INTDT ERR0
5E	MPA:SCANC REGS1							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
5F	MPA:SCANC REGS1							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type6 (3/4)

	0	1	2	3	4	5	6	7
60	MPA:SCANC REGS6							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
61	MPA:SCANC REGS6							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
62	MPA:SCANC SEQS1TM							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	TIMECNT	TIMEOUT
63	MPA:SCANC REGS0							
	MPSTS ERR3	MPSTS ERR2	MPSTS ERR1	MPSTS ERR0	PORTDT	PORTDT	PORTDT	PORTDT
64	MPA:SCANC REGS7							
	(Not used)	CRCPER	LRCPCR	STERR	RDELRC	WRNLRC ERR	RDNLRC ERR	WRELRC ERR
65	MPA:SCANC REGS7							
	SCDT ERR3	SCDT ERR2	SCDT ERR1	SCDT ERR0	ERDT ERR3	ERDT ERR2	ERDT ERR1	ERDT ERR0
66	MPA:SCANC REGS7							
	INTDT2 ERR3	INTDT2 ERR2	INTDT2 ERR1	INTDT2 ERR0	INTDT ERR3	INTDT ERR2	INTDT ERR1	INTDT ERR0
67	MPA:SCANC REGS1							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
68	MPA:SCANC REGS1							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
69	MPA:SCAND REGS6							
	MMO FLG0	MMO FLG1	MMO FLG2	MMO FLG3	XRTER	XXPTER	YRTER	YYPTER
6A	MPA:SCAND REGS6							
	DTRDY X	DTRDY XX	REGRDY Y	REGRDY YY	MMO ER3	MMO ER2	MMO ER1	MMO ER0
6B	MPA:SCAND SEQS1TM							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	TIMECNT	TIMEOUT
6C	MPA:SCAND REGS0							
	MPSTS ERR3	MPSTS ERR2	MPSTS ERR1	MPSTS ERR0	PORTDT	PORTDT	PORTDT	PORTDT
6D	MPA:SCAND REGS7							
	(Not used)	CRCPER	LRCPCR	STERR	RDELRC	WRNLRC ERR	RDNLRC ERR	WRELRC ERR
6E	MPA:SCAND REGS7							
	SCDT ERR3	SCDT ERR2	SCDT ERR1	SCDT ERR0	ERDT ERR3	ERDT ERR2	ERDT ERR1	ERDT ERR0
6F	MPA:SCAND REGS7							
	INTDT2 ERR3	INTDT2 ERR2	INTDT2 ERR1	INTDT2 ERR0	INTDT ERR3	INTDT ERR2	INTDT ERR1	INTDT ERR0

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type6 (4/4)

	0	1	2	3	4	5	6	7
70	MPA:MST0 ERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	MARBL	MARBR	MARBCMPE	MARBCMPE
71	MPA:MST0 ERRSTS							
	MSTCM PERR	MSTCM PERR	MIFCM PERR	MIFCM PERR	MIFSE QRCHK	MIFSE QLCHK	MSTSE QRCHK	MSTSE QLCHK
72	MPA:MST0 ERRSTS							
	MSTCNT	MIFRX CHK3E	TXMST CHK3E	MXRIF CHK3E	MIFTX CHK3E	SEQ TMTOUT	SEQTM CHK3E	(Not used)
73	MPA:MST0 ERRSTS							
	NADR CINTER	NADR CINTER	CADR CINTER	CADR CINTER	XRER3	XRER2	XRER1	XRER0
74	MPA:MST1 ERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	MARBL	MARBR	MARBCMPE	MARBCMPE
75	MPA:MST1 ERRSTS							
	MSTCM PERR	MSTCM PERR	MIFCM PERR	MIFCM PERR	MIFSE QRCHK	MIFSE QRCHK	MSTSE QRCHK	MSTSE QRCHK
76	MPA:MST1 ERRSTS							
	MSTCNT	MIFRX CHK3E	TXMST CHK3E	MXRIF CHK3E	MIFTX CHK3E	SEQ TMTOUT	SEQTM CHK3E	(Not used)
77	MPA:MST1 ERRSTS							
	NADR CINTER	NADR CINTER	CADR CINTER	CADR CINTER	XRER3	XRER2	XRER1	XRER0
78	MPA:MST2 ERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	MARBL	MARBR	MARBCMPE	MARBCMPE
79	MPA:MST2 ERRSTS							
	MSTCM PERR	MSTCM PERR	MIFCM PERR	MIFCM PERR	MIFSE QRCHK	MIFSE QRCHK	MSTSE QRCHK	MSTSE QRCHK
7A	MPA:MST2 ERRSTS							
	MSTCNT	MIFRX CHK3E	TXMST CHK3E	MXRIF CHK3E	MIFTX CHK3E	SEQ TMTOUT	SEQTM CHK3E	(Not used)
7B	MPA:MST2 ERRSTS							
	NADR CINTER	NADR CINTER	CADR CINTER	CADR CINTER	XRER3	XRER2	XRER1	XRER0
7C	MPA:MST3 ERRSTS							
	(Not used)	(Not used)	(Not used)	(Not used)	MARBL	MARBR	MARBCMPE	MARBCMPE
7D	MPA:MST3 ERRSTS							
	MSTCM PERR	MSTCM PERR	MIFCM PERR	MIFCM PERR	MIFSE QRCHK	MIFSE QRCHK	MSTSE QRCHK	MSTSE QRCHK
7E	MPA:MST3 ERRSTS							
	MSTCNT	MIFRX CHK3E	TXMST CHK3E	MXRIF CHK3E	MIFTX CHK3E	SEQ TMTOUT	SEQTM CHK3E	(Not used)
7F	MPA:MST3 ERRSTS							
	NADR CINTER	NADR CINTER	CADR CINTER	CADR CINTER	XRER3	XRER2	XRER1	XRER0

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type7 (1/4)

	0	1	2	3	4	5	6	7
40	Threshold counter Side A							
41	Threshold counter Side B							
42	Not used							
43	Not used							
44	Not used							
45	SMA:SMA CONTROL							
	(Not used)	(Not used)	DIMM PTY INV	DIMM TGL INH	LOCK MODE01	LOCK MODE00	BCSC WRN MODE	DUAL TIME EN
46	SMA:SMA CONTROL							
	DBG SIG	DBG SIG SEL	(Not used)	AC FMT	SCINT PULS EN	(Not used)	EINT SHRT PULS	ENT OFF EN
47	SMA:SMA CONTROL							
	SMPS DWN EN	CLK CHK EN	ECC ADR MSK	DIAG EN	DQS CTL EN	PWR DWN EN	DT RVS INH	REF MODE
48	Not used							
49	SMA:SMA STATUS							
	(Not used)	(Not used)	SLOT 01	SLOT 00	SLOT 08	(Not used)	DISC GRNT	PWR DWN MODE
4A	SMA:SMA STATUS							
	DISC MODE	SELF REF	SMPS DWN	MACK UP	TIM EN	ALL LOCK	MEM RDY	SM EN
4B	SMA:SMA STATUS							
	PK VER03	PK VER02	PK VER01	PK VER00	LSI VER03	LSI VER02	LSI VER01	LSI VER00
4C	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#31	SINT MASK MPA#30	SINT MASK MPA#29	SINT MASK MPA#28	SINT MASK MPA#27	SINT MASK MPA#26	SINT MASK MPA#25	SINT MASK MPA#31
4D	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#23	SINT MASK MPA#22	SINT MASK MPA#21	SINT MASK MPA#20	SINT MASK MPA#19	SINT MASK MPA#18	SINT MASK MPA#17	SINT MASK MPA#23
4E	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#15	SINT MASK MPA#14	SINT MASK MPA#13	SINT MASK MPA#12	SINT MASK MPA#11	SINT MASK MPA#10	SINT MASK MPA#09	SINT MASK MPA#15
4F	SMA:SCINT OUTPUT MASK							
	SINT MASK MPA#07	SINT MASK MPA#06	SINT MASK MPA#05	SINT MASK MPA#04	SINT MASK MPA#03	SINT MASK MPA#02	SINT MASK MPA#01	SINT MASK MPA#07

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type7 (2/4)

	0	1	2	3	4	5	6	7
50	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 00	BC REG0 MASK bit 01	BC REG0 MASK bit 02	BC REG0 MASK bit 03	BC REG0 MASK bit 04	BC REG0 MASK bit 05	BC REG0 MASK bit 06	BC REG0 MASK bit 07
51	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 10	BC REG0 MASK bit 11	BC REG0 MASK bit 12	BC REG0 MASK bit 13	BC REG0 MASK bit 14	BC REG0 MASK bit 15	BC REG0 MASK bit 16	BC REG0 MASK bit 17
52	SMA:MP STATUS TIMEOUT DATA							
	MPST_TODT							
53	SMA:MP STATUS TIMEOUT DATA							
	MPST_TODT							
54	SMA:VALID TIMER TIMEOUT DATA							
	VAL_TODT							
55	SMA:VALID TIMER TIMEOUT DATA							
	VAL_TODT							
56	SMA:SCINT TIMEOUT DATA							
	SC_TODT							
57	SMA:SCINT TIMEOUT DATA							
	SC_TODT							
58	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_PERR_MPA#31	SCCNT_PERR_MPA#30	SCCNT_PERR_MPA#29	SCCNT_PERR_MPA#28	SCCNT_PERR_MPA#27	SCCNT_PERR_MPA#31	SCCNT_PERR_MPA#30	SCCNT_PERR_MPA#29
59	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_PERR_MPA#23	SCCNT_PERR_MPA#22	SCCNT_PERR_MPA#21	SCCNT_PERR_MPA#20	SCCNT_PERR_MPA#19	SCCNT_PERR_MPA#23	SCCNT_PERR_MPA#22	SCCNT_PERR_MPA#21
5A	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_PERR_MPA#15	SCCNT_PERR_MPA#14	SCCNT_PERR_MPA#13	SCCNT_PERR_MPA#12	SCCNT_PERR_MPA#11	SCCNT_PERR_MPA#15	SCCNT_PERR_MPA#14	SCCNT_PERR_MPA#13
5B	SMA:SCAN COUNTER PARITY ERROR							
	SCCNT_PERR_MPA#07	SCCNT_PERR_MPA#06	SCCNT_PERR_MPA#05	SCCNT_PERR_MPA#04	SCCNT_PERR_MPA#03	SCCNT_PERR_MPA#07	SCCNT_PERR_MPA#06	SCCNT_PERR_MPA#05
5C	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_PERR_MPA#31	VALCNT_PERR_MPA#30	VALCNT_PERR_MPA#29	VALCNT_PERR_MPA#28	VALCNT_PERR_MPA#27	VALCNT_PERR_MPA#31	VALCNT_PERR_MPA#30	VALCNT_PERR_MPA#29
5D	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_PERR_MPA#23	VALCNT_PERR_MPA#22	VALCNT_PERR_MPA#21	VALCNT_PERR_MPA#20	VALCNT_PERR_MPA#19	VALCNT_PERR_MPA#23	VALCNT_PERR_MPA#22	VALCNT_PERR_MPA#21
5E	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_PERR_MPA#15	VALCNT_PERR_MPA#14	VALCNT_PERR_MPA#13	VALCNT_PERR_MPA#12	VALCNT_PERR_MPA#11	VALCNT_PERR_MPA#15	VALCNT_PERR_MPA#14	VALCNT_PERR_MPA#13
5F	SMA:VALID COUNTER PARITY ERROR							
	VALCNT_PERR_MPA#07	VALCNT_PERR_MPA#06	VALCNT_PERR_MPA#05	VALCNT_PERR_MPA#04	VALCNT_PERR_MPA#03	VALCNT_PERR_MPA#07	VALCNT_PERR_MPA#06	VALCNT_PERR_MPA#05

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type7 (3/4)

	0	1	2	3	4	5	6	7
60	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#31	SCSEQ_ERR MPA#30	SCSEQ_ERR MPA#29	SCSEQ_ERR MPA#28	SCSEQ_ERR MPA#27	SCSEQ_ERR MPA#31	SCSEQ_ERR MPA#30	SCSEQ_ERR MPA#29
61	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#23	SCSEQ_ERR MPA#22	SCSEQ_ERR MPA#21	SCSEQ_ERR MPA#20	SCSEQ_ERR MPA#19	SCSEQ_ERR MPA#23	SCSEQ_ERR MPA#22	SCSEQ_ERR MPA#21
62	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#15	SCSEQ_ERR MPA#14	SCSEQ_ERR MPA#13	SCSEQ_ERR MPA#12	SCSEQ_ERR MPA#11	SCSEQ_ERR MPA#15	SCSEQ_ERR MPA#14	SCSEQ_ERR MPA#13
63	SMA:SCAN SEQUENCER ERROR							
	SCSEQ_ERR MPA#07	SCSEQ_ERR MPA#06	SCSEQ_ERR MPA#05	SCSEQ_ERR MPA#04	SCSEQ_ERR MPA#03	SCSEQ_ERR MPA#07	SCSEQ_ERR MPA#06	SCSEQ_ERR MPA#05
64	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ ERR MPA#31	SINT_OUT_ ERR MPA#30	SINT_OUT_ ERR MPA#29	SINT_OUT_ ERR MPA#28	SINT_OUT_ ERR MPA#27	SINT_OUT_ ERR MPA#31	SINT_OUT_ ERR MPA#30	SINT_OUT_ ERR MPA#29
65	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ ERR MPA#23	SINT_OUT_ ERR MPA#22	SINT_OUT_ ERR MPA#21	SINT_OUT_ ERR MPA#20	SINT_OUT_ ERR MPA#19	SINT_OUT_ ERR MPA#23	SINT_OUT_ ERR MPA#22	SINT_OUT_ ERR MPA#21
66	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ ERR MPA#15	SINT_OUT_ ERR MPA#14	SINT_OUT_ ERR MPA#13	SINT_OUT_ ERR MPA#12	SINT_OUT_ ERR MPA#11	SINT_OUT_ ERR MPA#15	SINT_OUT_ ERR MPA#14	SINT_OUT_ ERR MPA#13
67	SMA:SCINT OUTPUT ERROR							
	SINT_OUT_ ERR MPA#07	SINT_OUT_ ERR MPA#06	SINT_OUT_ ERR MPA#05	SINT_OUT_ ERR MPA#04	SINT_OUT_ ERR MPA#03	SINT_OUT_ ERR MPA#07	SINT_OUT_ ERR MPA#06	SINT_OUT_ ERR MPA#05
68	SMA:SCINT SIGNAL							
	SINT_SIG MPA#31	SINT_SIG MPA#30	SINT_SIG MPA#29	SINT_SIG MPA#28	SINT_SIG MPA#27	SINT_SIG MPA#26	SINT_SIG MPA#31	SINT_SIG MPA#30
69	SMA:SCINT SIGNAL							
	SINT_SIG MPA#23	SINT_SIG MPA#22	SINT_SIG MPA#21	SINT_SIG MPA#20	SINT_SIG MPA#19	SINT_SIG MPA#18	SINT_SIG MPA#23	SINT_SIG MPA#22
6A	SMA:SCINT SIGNAL							
	SINT_SIG MPA#15	SINT_SIG MPA#14	SINT_SIG MPA#13	SINT_SIG MPA#12	SINT_SIG MPA#11	SINT_SIG MPA#10	SINT_SIG MPA#15	SINT_SIG MPA#14
6B	SMA:SCINT SIGNAL							
	SINT_SIG MPA#07	SINT_SIG MPA#06	SINT_SIG MPA#05	SINT_SIG MPA#04	SINT_SIG MPA#03	SINT_SIG MPA#02	SINT_SIG MPA#07	SINT_SIG MPA#06
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type7 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type8 (1/4)

	0	1	2	3	4	5	6	7
40	Threshold counter Side A							
41	Threshold counter Side B							
42	Threshold counter Side C							
43	Threshold counter Side D							
44	Not used							
45	SMA:SMA CONTROL							
	DBG SIG	(Not used)	(Not used)	(Not used)	(Not used)	BSSW CHK EN	ENT WAIT EN	DBG SIG
46	SMA:SMA CONTROL							
	DBG SIG	(Not used)	(Not used)	(Not used)	(Not used)	BSSW CHK EN	ENT WAIT EN	DBG SIG
47	SMA:SMA CONTROL							
	SMPSDWN EN	CLK CHK EN	ECC ADR MSK	DIAG EN	DUMP MODE	PWR DWN EN	DT RVS INH	SMPSDWN EN
48	Not used							
49	SMA:SMA STATUS							
	(Not used)	(Not used)	SLOT 01	SLOT 00	SLOT 08	(Not used)	DISC GRNT	PWR DWN MODE
4A	SMA:SMA STATUS							
	DISC MODE	SELF REF	SMPS DWN	MACK UP	TIM EN	ALL LOCK	MEM RDY	SM EN
4B	SMA:SMA STATUS							
	PK VER03	PK VER02	PK VER01	PK VER00	LSI VER03	LSI VER02	LSI VER01	LSI VER00
4C	SMA:SCINT OUTPUT MASK							
	SC MASK MPA#0	SC MASK MPA#1	SC MASK MPA#2	SC MASK MPA#3	SC MASK MPA#4	SC MASK MPA#5	SC MASK MPA#6	SC MASK MPA#0
4D	SMA:SCINT OUTPUT MASK							
	SC MASK MPA#8	SC MASK MPA#9	SC MASK MPA#A	SC MASK MPA#B	SC MASK MPA#C	SC MASK MPA#D	SC MASK MPA#E	SC MASK MPA#8
4E	SMA:SCINT OUTPUT MASK							
	SC MASK MPA#0	SC MASK MPA#1	SC MASK MPA#2	SC MASK MPA#3	SC MASK MPA#4	SC MASK MPA#5	SC MASK MPA#6	SC MASK MPA#0
4F	SMA:SCINT OUTPUT MASK							
	SC MASK MPA#8	SC MASK MPA#9	SC MASK MPA#A	SC MASK MPA#B	SC MASK MPA#C	SC MASK MPA#D	SC MASK MPA#E	SC MASK MPA#8

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type8 (2/4)

	0	1	2	3	4	5	6	7
50	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 00	BC REG0 MASK bit 01	BC REG0 MASK bit 02	BC REG0 MASK bit 03	BC REG0 MASK bit 04	BC REG0 MASK bit 05	BC REG0 MASK bit 06	BC REG0 MASK bit 07
51	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 10	BC REG0 MASK bit 11	BC REG0 MASK bit 12	BC REG0 MASK bit 13	BC REG0 MASK bit 14	BC REG0 MASK bit 15	BC REG0 MASK bit 16	BC REG0 MASK bit 17
52	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 20	BC REG0 MASK bit 21	BC REG0 MASK bit 22	BC REG0 MASK bit 23	BC REG0 MASK bit 24	BC REG0 MASK bit 25	BC REG0 MASK bit 26	BC REG0 MASK bit 27
53	SMA:BCAST REG0 MASK							
	BC REG0 MASK bit 30	BC REG0 MASK bit 31	BC REG0 MASK bit 32	BC REG0 MASK bit 33	BC REG0 MASK bit 34	BC REG0 MASK bit 35	BC REG0 MASK bit 36	BC REG0 MASK bit 37
54	SMA:BCAST REG1 MASK							
	BC REG1 MASK bit 00	BC REG1 MASK bit 01	BC REG1 MASK bit 02	BC REG1 MASK bit 03	BC REG1 MASK bit 04	BC REG1 MASK bit 00	BC REG1 MASK bit 01	BC REG1 MASK bit 02
55	SMA:BCAST REG1 MASK							
	BC REG1 MASK bit 10	BC REG1 MASK bit 11	BC REG1 MASK bit 12	BC REG1 MASK bit 13	BC REG1 MASK bit 14	BC REG1 MASK bit 10	BC REG1 MASK bit 11	BC REG1 MASK bit 12
56	SMA:BCAST REG1 MASK							
	BC REG1 MASK bit 20	BC REG1 MASK bit 21	BC REG1 MASK bit 22	BC REG1 MASK bit 23	BC REG1 MASK bit 24	BC REG1 MASK bit 20	BC REG1 MASK bit 21	BC REG1 MASK bit 22
57	SMA:BCAST REG1 MASK							
	BC REG1 MASK bit 30	BC REG1 MASK bit 31	BC REG1 MASK bit 32	BC REG1 MASK bit 33	BC REG1 MASK bit 34	BC REG1 MASK bit 30	BC REG1 MASK bit 31	BC REG1 MASK bit 32
58	SMA:BCAST REG2 MASK							
	BC REG2 MASK bit 00	BC REG2 MASK bit 01	BC REG2 MASK bit 02	BC REG2 MASK bit 03	BC REG2 MASK bit 04	BC REG2 MASK bit 00	BC REG2 MASK bit 01	BC REG2 MASK bit 02
59	SMA:BCAST REG2 MASK							
	BC REG2 MASK bit 10	BC REG2 MASK bit 11	BC REG2 MASK bit 12	BC REG2 MASK bit 13	BC REG2 MASK bit 14	BC REG2 MASK bit 10	BC REG2 MASK bit 11	BC REG2 MASK bit 12
5A	SMA:BCAST REG2 MASK							
	BC REG2 MASK bit 20	BC REG2 MASK bit 21	BC REG2 MASK bit 22	BC REG2 MASK bit 23	BC REG2 MASK bit 24	BC REG2 MASK bit 20	BC REG2 MASK bit 21	BC REG2 MASK bit 22
5B	SMA:BCAST REG2 MASK							
	BC REG2 MASK bit 30	BC REG2 MASK bit 31	BC REG2 MASK bit 32	BC REG2 MASK bit 33	BC REG2 MASK bit 34	BC REG2 MASK bit 30	BC REG2 MASK bit 31	BC REG2 MASK bit 32
5C	SMA:BCAST REG3 MASK							
	BC REG3 MASK bit 00	BC REG3 MASK bit 01	BC REG3 MASK bit 02	BC REG3 MASK bit 03	BC REG3 MASK bit 04	BC REG3 MASK bit 00	BC REG3 MASK bit 01	BC REG3 MASK bit 02
5D	SMA:BCAST REG3 MASK							
	BC REG3 MASK bit 10	BC REG3 MASK bit 11	BC REG3 MASK bit 12	BC REG3 MASK bit 13	BC REG3 MASK bit 14	BC REG3 MASK bit 10	BC REG3 MASK bit 11	BC REG3 MASK bit 12
5E	SMA:BCAST REG3 MASK							
	BC REG3 MASK bit 20	BC REG3 MASK bit 21	BC REG3 MASK bit 22	BC REG3 MASK bit 23	BC REG3 MASK bit 24	BC REG3 MASK bit 20	BC REG3 MASK bit 21	BC REG3 MASK bit 22
5F	SMA:BCAST REG3 MASK							
	BC REG3 MASK bit 30	BC REG3 MASK bit 31	BC REG3 MASK bit 32	BC REG3 MASK bit 33	BC REG3 MASK bit 34	BC REG3 MASK bit 30	BC REG3 MASK bit 31	BC REG3 MASK bit 32

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type8 (3/4)

	0	1	2	3	4	5	6	7
60	SMA:BCAST REG4 MASK							
	BC REG4 MASK bit 00	BC REG4 MASK bit 01	BC REG4 MASK bit 02	BC REG4 MASK bit 03	BC REG4 MASK bit 04	BC REG4 MASK bit 00	BC REG4 MASK bit 01	BC REG4 MASK bit 02
61	SMA:BCAST REG4 MASK							
	BC REG4 MASK bit 10	BC REG4 MASK bit 11	BC REG4 MASK bit 12	BC REG4 MASK bit 13	BC REG4 MASK bit 14	BC REG4 MASK bit 10	BC REG4 MASK bit 11	BC REG4 MASK bit 12
62	SMA:BCAST REG4 MASK							
	BC REG4 MASK bit 20	BC REG4 MASK bit 21	BC REG4 MASK bit 22	BC REG4 MASK bit 23	BC REG4 MASK bit 24	BC REG4 MASK bit 20	BC REG4 MASK bit 21	BC REG4 MASK bit 22
63	SMA:BCAST REG4 MASK							
	BC REG4 MASK bit 30	BC REG4 MASK bit 31	BC REG4 MASK bit 32	BC REG4 MASK bit 33	BC REG4 MASK bit 34	BC REG4 MASK bit 30	BC REG4 MASK bit 31	BC REG4 MASK bit 32
64	SMA:BCAST REG5 MASK							
	BC REG5 MASK bit 00	BC REG5 MASK bit 01	BC REG5 MASK bit 02	BC REG5 MASK bit 03	BC REG5 MASK bit 04	BC REG5 MASK bit 00	BC REG5 MASK bit 01	BC REG5 MASK bit 02
65	SMA:BCAST REG5 MASK							
	BC REG5 MASK bit 10	BC REG5 MASK bit 11	BC REG5 MASK bit 12	BC REG5 MASK bit 13	BC REG5 MASK bit 14	BC REG5 MASK bit 10	BC REG5 MASK bit 11	BC REG5 MASK bit 12
66	SMA:BCAST REG5 MASK							
	BC REG5 MASK bit 20	BC REG5 MASK bit 21	BC REG5 MASK bit 22	BC REG5 MASK bit 23	BC REG5 MASK bit 24	BC REG5 MASK bit 20	BC REG5 MASK bit 21	BC REG5 MASK bit 22
67	SMA:BCAST REG5 MASK							
	BC REG5 MASK bit 30	BC REG5 MASK bit 31	BC REG5 MASK bit 32	BC REG5 MASK bit 33	BC REG5 MASK bit 34	BC REG5 MASK bit 30	BC REG5 MASK bit 31	BC REG5 MASK bit 32
68	SMA:BCAST REG6 MASK							
	BC REG6 MASK bit 00	BC REG6 MASK bit 01	BC REG6 MASK bit 02	BC REG6 MASK bit 03	BC REG6 MASK bit 04	BC REG6 MASK bit 00	BC REG6 MASK bit 01	BC REG6 MASK bit 02
69	SMA:BCAST REG6 MASK							
	BC REG6 MASK bit 10	BC REG6 MASK bit 11	BC REG6 MASK bit 12	BC REG6 MASK bit 13	BC REG6 MASK bit 14	BC REG6 MASK bit 10	BC REG6 MASK bit 11	BC REG6 MASK bit 12
6A	SMA:BCAST REG6 MASK							
	BC REG6 MASK bit 20	BC REG6 MASK bit 21	BC REG6 MASK bit 22	BC REG6 MASK bit 23	BC REG6 MASK bit 24	BC REG6 MASK bit 20	BC REG6 MASK bit 21	BC REG6 MASK bit 22
6B	SMA:BCAST REG6 MASK							
	BC REG6 MASK bit 30	BC REG6 MASK bit 31	BC REG6 MASK bit 32	BC REG6 MASK bit 33	BC REG6 MASK bit 34	BC REG6 MASK bit 30	BC REG6 MASK bit 31	BC REG6 MASK bit 32
6C	SMA:BCAST REG7 MASK							
	BC REG7 MASK bit 00	BC REG7 MASK bit 01	BC REG7 MASK bit 02	BC REG7 MASK bit 03	BC REG7 MASK bit 04	BC REG7 MASK bit 00	BC REG7 MASK bit 01	BC REG7 MASK bit 02
6D	SMA:BCAST REG7 MASK							
	BC REG7 MASK bit 10	BC REG7 MASK bit 11	BC REG7 MASK bit 12	BC REG7 MASK bit 13	BC REG7 MASK bit 14	BC REG7 MASK bit 10	BC REG7 MASK bit 11	BC REG7 MASK bit 12
6E	SMA:BCAST REG7 MASK							
	BC REG7 MASK bit 20	BC REG7 MASK bit 21	BC REG7 MASK bit 22	BC REG7 MASK bit 23	BC REG7 MASK bit 24	BC REG7 MASK bit 20	BC REG7 MASK bit 21	BC REG7 MASK bit 22
6F	SMA:BCAST REG7 MASK							
	BC REG7 MASK bit 30	BC REG7 MASK bit 31	BC REG7 MASK bit 32	BC REG7 MASK bit 33	BC REG7 MASK bit 34	BC REG7 MASK bit 30	BC REG7 MASK bit 31	BC REG7 MASK bit 32

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type8 (4/4)

	0	1	2	3	4	5	6	7
70	SMA:BCINT OUTPUT ERROR							
	BINT_OUT_ ERR MPA#0	BINT_OUT_ ERR MPA#1	BINT_OUT_ ERR MPA#2	BINT_OUT_ ERR MPA#3	BINT_OUT_ ERR MPA#4	BINT_OUT_ ERR MPA#5	BINT_OUT_ ERR MPA#6	
71	SMA:BCINT OUTPUT ERROR							
	BINT_OUT_ ERR MPA#8	BINT_OUT_ ERR MPA#9	BINT_OUT_ ERR MPA#A	BINT_OUT_ ERR MPA#B	BINT_OUT_ ERR MPA#C	BINT_OUT_ ERR MPA#D	BINT_OUT_ ERR MPA#E	
72	SMA:BCINT OUTPUT ERROR							
	BINT_OUT_ ERR MPA#0	BINT_OUT_ ERR MPA#1	BINT_OUT_ ERR MPA#2	BINT_OUT_ ERR MPA#3	BINT_OUT_ ERR MPA#4	BINT_OUT_ ERR MPA#5	BINT_OUT_ ERR MPA#6	BINT_OUT_ ERR MPA#7
73	SMA:BCINT OUTPUT ERROR							
	BINT_OUT_ ERR MPA#8	BINT_OUT_ ERR MPA#9	BINT_OUT_ ERR MPA#A	BINT_OUT_ ERR MPA#B	BINT_OUT_ ERR MPA#C	BINT_OUT_ ERR MPA#D	BINT_OUT_ ERR MPA#E	BINT_OUT_ ERR MPA#F
74	SMA:BCINT SIGNAL							
	BINT_SIG MPA#0	BINT_SIG MPA#1	BINT_SIG MPA#2	BINT_SIG MPA#3	BINT_SIG MPA#4	BINT_SIG MPA#5	BINT_SIG MPA#6	BINT_SIG MPA#7
75	SMA:BCINT SIGNAL							
	BINT_SIG MPA#8	BINT_SIG MPA#9	BINT_SIG MPA#A	BINT_SIG MPA#B	BINT_SIG MPA#C	BINT_SIG MPA#D	BINT_SIG MPA#E	BINT_SIG MPA#F
76	SMA:BCINT SIGNAL							
	BINT_SIG MPA#0	BINT_SIG MPA#1	BINT_SIG MPA#2	BINT_SIG MPA#3	BINT_SIG MPA#4	BINT_SIG MPA#5	BINT_SIG MPA#6	BINT_SIG MPA#7
77	SMA:BCINT SIGNAL							
	BINT_SIG MPA#8	BINT_SIG MPA#9	BINT_SIG MPA#A	BINT_SIG MPA#B	BINT_SIG MPA#C	BINT_SIG MPA#D	BINT_SIG MPA#E	BINT_SIG MPA#F
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type9 (1/4)

	0	1	2	3	4	5	6	7
40	FCA:LEDCLT							
	(Not used)	DKUFORCE0	DKUFORCE1	DKUFORCE2	DKUFORCE3	DKUFORCE4	DKUOUTERR MSK	DKUERR CLR
41	FCA:LEDCLT							
	DKUACDCON 0	DKUACDCON 1	DKUBATON 0	DKUBATON 1	(Not used)	(Not used)	(Not used)	(Not used)
42	FCA:LEDSTS							
	LED ALARM 0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
43	FCA:LEDSTS							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY
44	FCA:LEDADRDT (After PK_STAT Reg. Read : F3)							
	LED ADDRESS REG							
45	FCA:LEDADRDT (After PK_STAT Reg. Read : F3)							
	LED DATA REG							
46	FCA:LEDADRDT (After PK_STAT Reg. Read : F3)							
	SERIAL RD DT							
47	FCA:LEDCMD (After PK_STAT Reg. Read : F3)							
	LED CMD REG 0x01:WT,0x02:RD							
48	FCA:LEDSTS (After PK_STAT Reg. Read : F3)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
49	FCA:LEDSTS (After PK_STAT Reg. Read : F3)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY
4A	FCA:LEDADRDT (After HBUS_ERR Reg. Read : F8)							
	LED ADDRESS REG							
4B	FCA:LEDADRDT (After HBUS_ERR Reg. Read : F8)							
	LED DATA REG							
4C	FCA:LEDADRDT (After HBUS_ERR Reg. Read : F8)							
	SERIAL RD DT							
4D	FCA:LEDCMD (After HBUS_ERR Reg. Read : F8)							
	LED CMD REG 0x01:WT,0x02:RD							
4E	FCA:LEDSTS (After HBUS_ERR Reg. Read : F8)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
4F	FCA:LEDSTS (After HBUS_ERR Reg. Read : F8)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type9 (2/4)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	FCA:LEDADRDT (After PK_STAT Reg. Read : D3) LED ADDRESS REG							
55	FCA:LEDADRDT (After PK_STAT Reg. Read : D3) LED DATA REG							
56	FCA:LEDADRDT (After PK_STAT Reg. Read : D3) SERIAL RD DT							
57	FCA:LEDCMD (After PK_STAT Reg. Read : D3) LED CMD REG 0x01:WT,0x02:RD							
58	FCA:LEDSTS (After PK_STAT Reg. Read : D3)							
	LED ALARM 0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
59	FCA:LEDSTS (After PK_STAT Reg. Read : D3)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY
5A	FCA:LEDADRDT (After HBUS_ERR Reg. Read : D8) LED ADDRESS REG							
5B	FCA:LEDADRDT (After HBUS_ERR Reg. Read : D8) LED DATA REG							
5C	FCA:LEDADRDT (After HBUS_ERR Reg. Read : D8) SERIAL RD DT							
5D	FCA:LEDCMD (After HBUS_ERR Reg. Read : D8) LED CMD REG 0x01:WT,0x02:RD							
5E	FCA:LEDSTS (After HBUS_ERR Reg. Read : D8)							
	LED ALARM 0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
5F	FCA:LEDSTS (After HBUS_ERR Reg. Read : D8)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type9 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	FCA:LEDADRDT (After PK_STAT Reg. Read : B3) LED ADDRESS REG							
65	FCA:LEDADRDT (After PK_STAT Reg. Read : B3) LED DATA REG							
66	FCA:LEDADRDT (After PK_STAT Reg. Read : B3) SERIAL RD DT							
67	FCA:LEDCMD (After PK_STAT Reg. Read : B3) LED CMD REG 0x01:WT,0x02:RD							
68	FCA:LEDSTS (After PK_STAT Reg. Read : B3)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	LED ALARM0	LED ALARM1
69	FCA:LEDSTS (After PK_STAT Reg. Read : B3)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY
6A	FCA:LEDADRDT (After HBUS_ERR Reg. Read : B8) LED ADDRESS REG							
6B	FCA:LEDADRDT (After HBUS_ERR Reg. Read : B8) LED DATA REG							
6C	FCA:LEDADRDT (After HBUS_ERR Reg. Read : B8) SERIAL RD DT							
6D	FCA:LEDCMD (After HBUS_ERR Reg. Read : B8) LED CMD REG 0x01:WT,0x02:RD							
6E	FCA:LEDSTS (After HBUS_ERR Reg. Read : B8)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
6F	FCA:LEDSTS (After HBUS_ERR Reg. Read : B8)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type9 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	FCA:LEDADRDT (After PK_STAT Reg. Read : 93) LED ADDRESS REG							
75	FCA:LEDADRDT (After PK_STAT Reg. Read : 93) LED DATA REG							
76	FCA:LEDADRDT (After PK_STAT Reg. Read : 93) SERIAL RD DT							
77	FCA:LEDCMD (After PK_STAT Reg. Read : 93) LED CMD REG 0x01:WT,0x02:RD							
78	FCA:LEDSTS (After PK_STAT Reg. Read : 93)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
79	FCA:LEDSTS (After PK_STAT Reg. Read : 93)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY
7A	FCA:LEDADRDT (After HBUS_ERR Reg. Read : 98) LED ADDRESS REG							
7B	FCA:LEDADRDT (After HBUS_ERR Reg. Read : 98) LED DATA REG							
7C	FCA:LEDADRDT (After HBUS_ERR Reg. Read : 98) SERIAL RD DT							
7D	FCA:LEDCMD (After HBUS_ERR Reg. Read : 98) LED CMD REG 0x01:WT,0x02:RD							
7E	FCA:LEDSTS (After HBUS_ERR Reg. Read : 98)							
	LED ALARM0	LED ALARM1	DKU ACDC ON 0 MONITOR	DKU ACDC ON 1 MONITOR	DKU BATT ON 0 MONITOR	DKU BATT ON 1 MONITOR	(Not used)	(Not used)
7F	FCA:LEDSTS (After HBUS_ERR Reg. Read : 98)							
	(Not used)	STS	ACK ERROR	STS PARITY ERROR	RD DT PARITY ERROR	DKU ERROR	RD BUSY	WT BUSY

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type10 (1/4)

	0	1	2	3	4	5	6	7
40	Not used							
41	Not used							
42	Not used							
43	Not used							
44	Threshold counter for the warning MIX-1A							
45	Threshold counter for the warning CHA-1B							
46	Not used							
47	Not used							
48	Threshold counter for the warning CSW-1C							
49	Not used							
4A	Threshold counter for the warning CACHE-1C							
4B	Not used							
4C	Threshold counter for the warning SM-1C							
4D	Not used							
4E	Not used							
4F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type10 (2/4)

	0	1	2	3	4	5	6	7
50	Not used							
51	Not used							
52	Not used							
53	Not used							
54	Threshold counter for the warning MIX-2F							
55	Threshold counter for the warning CHA-2E							
56	Not used							
57	Not used							
58	Threshold counter for the warning CSW-2D							
59	Not used							
5A	Not used							
5B	Threshold counter for the warning CACHE-2D							
5C	Threshold counter for the warning SM-2D							
5D	Not used							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type10 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	MIX-1A Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
65	CHA-1B Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
66	Not used							
67	Not used							
68	CSW-1C Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
69	Not used							
6A	CHA-1C Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
6B	Not used							
6C	SM-1C Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type10 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	MIX-2F Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
75	CHA-2E Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
76	Not used							
77	Not used							
78	CSW-2D Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
79	Not used							
7A	Not used							
7B	CACHE-2D Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
7C	SM-2D Voltage CTL Reg. Information							
	(Not used)	(Not used)	(Not used)	(Not used)	CEMODE	(Not used)	DC CTL UP (+5%)	DC CTL UP (-5%)
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type11 (1/4)

	0	1	2	3	4	5	6	7
40	BRIDGE0WARNSTS(BRIDGE0 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	BGRM ECCS ERR
41	BRIDGE1WARNSTS(BRIDGE1 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	BGRM ECCS ERROR
42	BRIDGE2WARNSTS(BRIDGE2 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	BGRM ECCS ERROR
43	BRIDGE3WARNSTS(BRIDGE3 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	BGRM ECCS ERROR
44	BFCTL0WARNSTS(BFC0 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	BF01 ECC SERR	BF00 ECC SERR	BF11 ECC SERR	BF10 ECC SERR
45	BFCTL1WARNSTS(BFC1 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	BF01 ECC SERR	BF00 ECC SERR	BF11 ECC SERR	BF10 ECC SERR
46	D0WARNING(DMA0 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	WRNEBF1	WRNEBF0
47	D1WARNING(DMA1 MEMORY)							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	WRNEBF1	WRNEBF0
48	RRB ERROR LOG(BUF0(RRB) MEMORY)							
	RRBSEQ ENCERR	RRBSEQ DECERR	(Not used)	(Not used)	RRBECC ADR1 ERR	RRBECC CIRCUIT ERR	RRBECC UNC ERR	RRBECC CRCT ERR
49	PWB ERROR LOG(BUF0(PWB) MEMORY)							
	PWBSEQ ENCERR	PWBSEQ DECERR	(Not used)	(Not used)	PWBECC ADR1 ERR	PWBECC CIRCUIT ERR	PWBECC UNC ERR	PWBECC CRCT ERR
4A	RRB ERROR LOG(BUF1(RRB) MEMORY)							
	RRBSEQ ENCERR	RRBSEQ DECERR	(Not used)	(Not used)	RRBECC ADR1 ERR	RRBECC CIRCUIT ERR	RRBECC UNC ERR	RRBECC CRCT ERR
4B	PWB ERROR LOG(BUF1(PWB) MEMORY)							
	PWBSEQ ENCERR	PWBSEQ DECERR	(Not used)	(Not used)	PWBECC ADR1 ERR	PWBECC CIRCUIT ERR	PWBECC UNC ERR	PWBECC CRCT ERR
4C	RRB ERROR LOG(BUF2(RRB) MEMORY)							
	RRBSEQ ENCERR	RRBSEQ DECERR	(Not used)	(Not used)	RRBECC ADR1 ERR	RRBECC CIRCUIT ERR	RRBECC UNC ERR	RRBECC CRCT ERR
4D	PWB ERROR LOG(BUF2(PWB) MEMORY)							
	PWBSEQ ENCERR	PWBSEQ DECERR	(Not used)	(Not used)	PWBECC ADR1 ERR	PWBECC CIRCUIT ERR	PWBECC UNC ERR	PWBECC CRCT ERR
4E	RRB ERROR LOG(BUF3(RRB) MEMORY)							
	RRBSEQ ENCERR	RRBSEQ DECERR	(Not used)	(Not used)	RRBECC ADR1 ERR	RRBECC CIRCUIT ERR	RRBECC UNC ERR	RRBECC CRCT ERR
4F	PWB ERROR LOG(BUF3(PWB) MEMORY)							
	PWBSEQ ENCERR	PWBSEQ DECERR	(Not used)	(Not used)	PWBECC ADR1 ERR	PWBECC CIRCUIT ERR	PWBECC UNC ERR	PWBECC CRCT ERR

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type11 (2/4)

	0	1	2	3	4	5	6	7
50	CHUB WARN 0							
	(Not used)	(Not used)	(Not used)	ROSC ECC1bERR	(Not used)	(Not used)	(Not used)	(Not used)
51	CHUB WARN 0							
	DMA ECC1bERR 7	DMA ECC1bERR 6	DMA ECC1bERR 5	DMA ECC1bERR 4	DMA ECC1bERR 3	DMA ECC1bERR 2	DMA ECC1bERR 1	DMA ECC1bERR 0
52	CHUB WARN 0							
	BUF3 RD ECC1bERR	BUF3 WR ECC1bERR	BUF2 RD ECC1bERR	BUF2 WR ECC1bERR	BUF1 RD ECC1bERR	BUF1 WR ECC1bERR	BUF0 RD ECC1bERR	BUF0 WR ECC1bERR
53	CHUB WARN 0							
	DXBF ECC1bERR 1	DXBF ECC1bERR 0	BFCTL1 RD ECC1bERR	BFCTL0 RD ECC1bERR	BRIDGE ECC1bERR 3	BRIDGE ECC1bERR 2	BRIDGE ECC1bERR 1	BRIDGE ECC1bERR 0
54	CHUB WARN 1							
	(Not used)	(Not used)	BFCTL1 WR ECC1bERR	BFCTL0 WR ECC1bERR	(Not used)	(Not used)	(Not used)	(Not used)
55	Not used							
56	Not used							
57	Not used							
58	Not used							
59	Not used							
5A	Not used							
5B	Not used							
5C	Not used							
5D	Not used							
5E	Not used							
5F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type11 (3/4)

	0	1	2	3	4	5	6	7
60	Not used							
61	Not used							
62	Not used							
63	Not used							
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type11 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type12 (1/4)

	0	1	2	3	4	5	6	7
40	DRR:DRSTATS							
	END 64	EXEC 64	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
41	DRR:DRSTATS							
	CHK2	CHK2A DRR CTL	CHK2B DRR DT	CHK2C DRR MICRO	CHK2D CHSN CTL	CHK2E CNSN DT	CHK2F TO/ABT	CHK1B
42	DRR:DRSTATS							
	POUSE	DSKTIMOUT	P0 ERRINT	P1 ERRINT	ST0 VALID	ST0 CRAB/CMA ERR	ST1 VALID	ST1 CRAB/CMA ERR
43	DRR:DRSTATS							
	REV NO #3	REV NO #2	REV NO #1	REV NO #0	MP NO #3	MP NO #2	MP NO #1	MP NO #0
44	DRR:DRSTATS							
	P0 ABT ERR	P0 ENT TO	P0 ACK TO	P0 ST TO	P1 ABT ERR	P1 ENT TO	P1 ACK TO	P1 ST TO
45	DRR:DRSTATS							
	ABORT	YWING ERR	(Not used)	(Not used)	IMP-ERR Warning	ROSC-ERR Warning	PASWD- ERR	T64ERR
46	DRR:DRSTATS							
	CMDSEQ15	CMDSEQ14	CMDSEQ13	CMDSEQ12	CMDSEQ11	CMDSEQ10	CMDSEQ9	CMDSEQ8
47	DRR:DRSTATS							
	CMDSEQ7	CMDSEQ6	CMDSEQ5	CMDSEQ4	CMDSEQ3	CMDSEQ2	CMDSEQ1	CMDSEQ0
48	DRR:CRCTMONI							
	CMDSEQ7	CMDSEQ6	CMDSEQ5	CMDSEQ4	CMDSEQ3	CMDSEQ2	CMDSEQ1	CMDSEQ0
49	DRR:TPRMA0							
	Total length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h,16kB=0420h							
4A	DRR:TPRMA0							
	Total length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h,16kB=0420h							
4B	DRR:TPRMA0							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
4C	DRR:TPRMA0							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
4D	DRR:TPRMA0							
	ACO CACHE DIAG	(Not used)	(Not used)	ACO CACHE SLRC CHK	AC1 CACHE SLRC CHK	(Not used)	(Not used)	AC1 CACHE DIAG
4E	DRR:TPRMA0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	DRR:TPRMA0							
	SLV0 ADR0							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type12 (2/4)

	0	1	2	3	4	5	6	7
50	DDR:TPRMA0 SLV1 ADR0							
51	DDR:TPRMA2							
	VERIFY N	VERIFY M	MRCF	2WT	TRMODE			
52	DDR:TPRMA2							
53	DDR:TPRMA2							
54	DDR:TPRMA2							
55	DDR:TPRMA2							
	DATA VALID0	DATA VALID1	DATA VALID2	DATA VALID3	DATA VALID4	DATA VALID5	DATA VALID6	DATA VALID7
56	DDR:TPRMA2							
	DATA VALID8	DATA VALID9	DATA VALID10	DATA VALID11	DATA VALID12	DATA VALID13	(Not used)	(Not used)
57	DDR:TPRMA2							
	Offset							
58	DDR:TPRMA2							
	Offset							
59	DDR:TPRMB0							
	Total length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h,16kB=0420h							
5A	DDR:TPRMB0							
	Total length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h,16kB=0420h							
5B	DDR:TPRMB0							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
5C	DDR:TPRMB0							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
5D	DDR:TPRMB0							
	ACO CACHE DIAG	(Not used)	(Not used)	ACO CACHE SLRC CHK	AC1 CACHE SLRC CHK	(Not used)	(Not used)	AC1 CACHE DIAG
5E	DDR:TPRMB0							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5F	DDR:TPRMB0							
	SLV0 ADR0							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type12 (3/4)

	0	1	2	3	4	5	6	7
60	DDR:TPRMB1 SLV1 ADR0							
61	DDR:TPRMB2 VERIFY N VERIFY M MRCF 2WT TRMODE							
62	DDR:TPRMB2 PARAMETOR 0							
63	DDR:TPRMB2 PARAMETOR 1							
64	DDR:TPRMB2 PARAMETOR 2							
65	DDR:TPRMB2							
	DATA VALID0	DATA VALID1	DATA VALID2	DATA VALID3	DATA VALID4	DATA VALID5	DATA VALID6	DATA VALID7
66	DDR:TPRMB2							
	DATA VALID8	DATA VALID9	DATA VALID10	DATA VALID11	DATA VALID12	DATA VALID13	(Not used)	(Not used)
67	DDR:TPRMB2 Offset							
68	DDR:TPRMB2 Offset							
69	DDR:DRMODE0							
	CHK2 INTEN	CHK2A EREN	CHK2B EREN	CHK2C EREN	CHK2D EREN	CHK2E EREN	CHK2F EREN	(Not used)
6A	DDR:DRMODE0							
	TREND EREN	DDRCHK1B EREN	PCICLK1B EREN	YER ERN	(Not used)	DRBACS2	DRBACS1	DRBACS0
6B	DDR:DRMODE0							
	NOMST64	(Not used)	SP ACS	SM ACS	(Not used)	(Not used)	ECCINH N	ECCINH M
6C	DDR:DRMODE0							
	P1REQ7	P1REQ6	P1REQ5	P1REQ4	P1REQ3	P1REQ2	P1REQ1	P1REQ0
6D	DDR:DRMODE0							
	P0REQ7	P0REQ6	P0REQ5	P0REQ4	P0REQ3	P0REQ2	P0REQ1	P0REQ0
6E	DDR:DRMODE0							
	(Not used)	ENTTIM	(Not used)	ENTCNTCINC	ENTCNTCTOC	ENCNTRD	ENCNTDWT	ENCNTWT
6F	DDR:DRMODE0							
	NOPGENMD	(Not used)	ABORT	(Not used)	ENCHSNA BORT	(Not used)	(Not used)	(Not used)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type12 (4/4)

	0	1	2	3	4	5	6	7
70	DRR:DRMODE0							
	FIXPLMD	TABORT	(Not used)	(Not used)	PKID3	PKID2	PKID1	PKID0
71	DRR:DRMODE7							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
72	DRR:DRMODE7							
	Packet length Eg)16B=000h,528B=0021h,1056B=0042,1584B=0063h,2112B=0084h							
73	DRR:DTSTATS							
	DTNERR:STATUS							
74	DRR:DTSTATS							
	DTNERR:STATUS							
75	DRR:DTSTATS							
	DTNERR:STATUS							
76	DRR:DTSTATS							
	DTNERR:STATUS							
77	DRR:DTSTATS							
	DTNERR:STATUS							
78	DRR:DTMODE0							
	CHK2 EN	CHK2A EN DTN CTL	CHK2B EN DTN DT	CHK2C EN DTN MICR0	CHK2D EN CHSN STL	CHK2E EN SNSN DT	CHK2F EN TO/ABT	(Not used)
79	DRR:DTMODE0							
	CHK2 EN	CHK2A EN DTN CTL	CHK2B EN DTN DT	CHK2C EN DTN MICR0	CHK2D EN CHSN STL	CHK2E EN SNSN DT	CHK2F EN TO/ABT	YER EREN
7A	DRR:DTMODE0							
	ECCINH	DTBACS0	DTBACS1	DTBACS2	TQUADM	HALFMD	QUADM	WAITMD
7B	DRR:DTMODE0							
	P0REQ7	P0REQ6	P0REQ5	P0REQ4	P0REQ3	P0REQ2	P0REQ1	P0REQ0
7C	DRR:DTMODE0							
	P1REQ7	P1REQ6	P1REQ5	P1REQ4	P1REQ3	P1REQ2	P1REQ1	P1REQ0
7D	DRR:DTMODE0							
	ENTTIM CLRON	ENTCNT CLRON	ENTTIM	ENICNICNC	ENICNICIOC	ENTCNTRD	ENTCNIDWT	ENTCNTWT
7E	DRR:DTMODE0							
	(Not used)	DTB ACS	ENCHK2 CLRON	ENOUTDIAG	ENCTLCHK2 OLDMD	ENDTCHK2 OLDMD	ENTERALWAY SMD	CHSNABORT
7F	DRR:DTMODE0							
	ABORT	EN450PFLG	EN450FLG	ENR4CACHE	EN450DTF	EN450DT	ENMSKSCK	ENCUDGMD

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type13 (1/4)

	0	1	2	3	4	5	6	7
40	FCA:RAMRDT LOG0							
	ECC RAM RD DT LOG BYTE00-03							
41	FCA:RAMRDT LOG0							
	ECC RAM RD DT LOG BYTE00-03							
42	FCA:RAMRDT LOG0							
	ECC RAM RD DT LOG BYTE00-03							
43	FCA:RAMRDT LOG0							
	ECC RAM RD DT LOG BYTE00-03							
44	FCA:RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE00-07							
45	FCA:RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE00-07							
46	FCA:RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE00-07							
47	FCA:RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE00-07							
48	FCA:RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE00-11							
49	FCA:RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE00-11							
4A	FCA:RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE00-11							
4B	FCA:RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE00-11							
4C	FCA:RAMRDT LOG3							
	ECC RAM RD DT LOG BYTE00-15							
4D	FCA:RAMRDT LOG3							
	ECC RAM RD DT LOG BYTE00-15							
4E	FCA:RAMRDT LOG3							
	ECC RAM RD DT LOG BYTE00-15							
4F	FCA:RAMRDT LOG3							
	ECC RAM RD DT LOG BYTE00-15							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type13 (2/4)

	0	1	2	3	4	5	6	7
50	FCA:RAMRECC LOG							
	FCA:CR RAMRDT LOG0							
51	FCA:RAMRA LOG							
	ECC RAM RD ADR LOG							
52	FCA:SYND LOG							
	ECC SYNDROME LOG							
53	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE00-03)							
54	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE00-03)							
55	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE00-03)							
56	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE00-03)							
57	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE04-07)							
58	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE04-07)							
59	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE04-07)							
5A	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE04-07)							
5B	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE08-11)							
5C	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE08-11)							
5D	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE08-11)							
5E	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE08-11)							
5F	FCA:CR RAMRDT LOG0							
	ECC RAM RD DT LOG (BYTE12-15)							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type13 (3/4)

	0	1	2	3	4	5	6	7
60	FCA:CR RAMRDT LOG3							
	ECC RAM RD DT LOG (BYTE12-15)							
61	FCA:CR RAMRDT LOG3							
	ECC RAM RD DT LOG (BYTE12-15)							
62	FCA:CR RAMRDT LOG3							
	ECC RAM RD DT LOG (BYTE12-15)							
63	FCA:CR RAMRECC LOG							
	ECC RAM RD ECC LOG							
64	FCA:CR RAMRA LOG							
	ECC RAM RD ECC LOG							
65	FCA:CR SYND LOG							
	ECC RAM RD ECC LOG							
66	FCA:ECCRAMRDTLOG0							
	ECC RAM RD DT LOG BYTE 00-03							
67	FCA:ECCRAMRDTLOG0							
	ECC RAM RD DT LOG BYTE 00-03							
68	FCA:ECCRAMRDTLOG0							
	ECC RAM RD DT LOG BYTE 00-03							
69	FCA:ECCRAMRDTLOG0							
	ECC RAM RD DT LOG BYTE 00-03							
6A	FCA:CR RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE 04-07							
6B	FCA:CR RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE 04-07							
6C	FCA:CR RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE 04-07							
6D	FCA:CR RAMRDT LOG1							
	ECC RAM RD DT LOG BYTE 04-07							
6E	FCA:CR RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE 08-11							
6F	FCA:CR RAMRDT LOG2							
	ECC RAM RD DT LOG BYTE 08-11							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type13 (4/4)

	0	1	2	3	4	5	6	7
70	FCA:ECCRAMRDTLOG2 ECC RAM RD DT LOG BYTE 08-11							
71	FCA:ECCRAMRDTLOG2 ECC RAM RD DT LOG BYTE 08-11							
72	FCA:ECCRAMRDTLOG3 ECC RAM RD DT LOG BYTE 12-15							
73	FCA:ECCRAMRDTLOG3 ECC RAM RD DT LOG BYTE 12-15							
74	FCA:ECCRAMRDTLOG3 ECC RAM RD DT LOG BYTE 12-15							
75	FCA:ECCRAMRDTLOG3 ECC RAM RD DT LOG BYTE 12-15							
76	FCA:ECCRAMRECCLOG ECC RAM RD ECC LOG							
77	FCA:ECCRAMRALOG ECC RAMRA LOG							
78	FCA:ECCRAMSYNDLOG ECC SYNDROME LOG							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type14 (1/4)

	0	1	2	3	4	5	6	7
40	DCMA:RAMECCERR							
	FIFO0 RAM CORRECT	FIFO0 RAMUNCRE	FIFO0 RAM ADRCRRECT	FIFO0 RAM ECCIR	FIFO1 RAM CORRECT	FIFO1 RAM UNCRE	FIFO1 RAM ADRCRRECT	FIFO1 RAM ECCIR
41	DCMA:RAMECCERR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
42	DCMA:FIFO0 Collect Error CNT For FIFO0 FIFOECC Collect Error detected Counter							
43	DCMA:FIFO0 Collect Error CNT For FIFO1 FIFOECC Collect Error detected Counter							
44	DCMA:FIFOECCCNT (Collectable Count Threshold) Collectable Count Threshold							
45	DCMA:OTHER ERR							
	RTNCRC0 ERR	RTNCRC1 ERR	FIFO0 CORCNTOV	FIFO1 CORCNTOV	(Not used)	(Not used)	LKEYACC ERR	ENT OFF
46	Not used							
47	Not used							
48	Not used							
49	Not used							
4A	Not used							
4B	Not used							
4C	Not used							
4D	Not used							
4E	Not used							
4F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type14 (2/4)

	0	1	2	3	4	5	6	7
50	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
51	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
52	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
53	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
54	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
55	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
56	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
57	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
58	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
59	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5A	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5B	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5C	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5D	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5E	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							
5F	FIFO0 RAMDATA0							
	Error Data when FIFO ECC Error detected							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type14 (3/4)

	0	1	2	3	4	5	6	7
60	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
61	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
62	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
63	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
64	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
65	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
66	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
67	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
68	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
69	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6A	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6B	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6C	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6D	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6E	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							
6F	FIFO1 RAMDATA0							
	Error Data when FIFO ECC Error detected							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type14 (4/4)

	0	1	2	3	4	5	6	7
70	FIFO0 PARITY							
	Parity Data when FIFO ECC Error detected							
71	FIFO0 PARITY							
	Parity Data when FIFO ECC Error detected							
72	FIFO0 RAMECC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
73	FIFO0 RAMECC							
	ECC Data when FIFO ECC Error detected							
74	FIFO0 ECCSYD							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
75	FIFO0 ECCSYD							
	Syndrome Data when FIFO ECC Error detected							
76	FIFO0 RAMADR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
77	FIFO0 RAMADR							
	RAM address Data when FIFO ECC Error detected							
78	FIFO1 PARITY							
	Parity Data when FIFO ECC Error detected							
79	FIFO1 PARITY							
	Parity Data when FIFO ECC Error detected							
7A	FIFO1 RAMECC							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7B	FIFO1 RAMECC							
	ECC Data when FIFO ECC Error detected							
7C	FIFO1 ECCSYD							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7D	FIFO1 ECCSYD							
	Syndrome Data when FIFO ECC Error detected							
7E	FIFO1 RAMADR							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
7F	FIFO1 RAMADR							
	RAM address Data when FIFO ECC Error detected							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type15 (1/4)

	0	1	2	3	4	5	6	7
40	FIFOWAR: FIFO Collectable Error detected Reg.							
	PXRAM1 CRCTERR	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
41	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
42	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
43	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
44	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
45	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
46	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
47	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
48	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
49	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4A	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4B	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4D	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4E	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4F	FIFOWAR: FIFO Collectable Error detected Reg.							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type15 (2/4)

	0	1	2	3	4	5	6	7
50	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
51	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
52	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
53	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
54	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
55	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
56	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
57	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
58	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
59	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5A	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5B	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5C	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5D	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5E	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							
5F	RAM1RDTLOGLT: Data Value when FIFO0 Error Occurred							
	PX RAM1 RDTLOGLT							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type15 (3/4)

	0	1	2	3	4	5	6	7
60	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	RAMECC LOGLT(8)
61	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	RAMECC LOGLT(7)	RAMECC LOGLT(6)	RAMECC LOGLT(5)	RAMECC LOGLT(4)	RAMECC LOGLT(3)	RAMECC LOGLT(2)	RAMECC LOGLT(1)	RAMECC LOGLT(0)
62	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
63	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	RAMRA LOGLT(7)	RAMRA LOGLT(6)	RAMRA LOGLT(5)	RAMRA LOGLT(4)	RAMRA LOGLT(3)	RAMRA LOGLT(2)	RAMRA LOGLT(1)	RAMRA LOGLT(0)
64	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
65	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	SYND LOGLT(7)	SYND LOGLT(6)	SYND LOGLT(5)	SYND LOGLT(4)	SYND LOGLT(3)	SYND LOGLT(2)	SYND LOGLT(1)	SYND LOGLT(0)
66	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
67	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
68	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
69	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6A	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6B	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6C	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6D	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6E	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
6F	RAM1 LOGDT1: Data Value when FIFO1 Error Occurred							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type15 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type16 (1/4)

	0	1	2	3	4	5	6	7
40	PCI:PCI WARN0							
	PCI0 TGTTXH CRCT	PCI0 TGTTXL CRCT	PCI0 TGTRXH CRCT	PCI0 TGTRXL CRCT	PCI0 MSTTXH CRCT	PCI0 MSTTXL CRCT	PCI0 MSTRXH CRCT	PCI0 MSTRXL CRCT
41	PCI:PCI WARN0							
	PCI10 TGTTXH CRCT	PCI10 TGTTXL CRCT	PCI10 TGTRXH CRCT	PCI10 TGTRXL CRCT	PCI10 MSTTXH CRCT	PCI10 MSTTXL CRCT	PCI10 MSTRXH CRCT	PCI10 MSTRXL CRCT
42	PCI:PCI WARN0							
	PCI11 TGTTXH CRCT	PCI11 TGTTXL CRCT	PCI11 TGTRXH CRCT	PCI11 TGTRXL CRCT	PCI11 MSTTXH CRCT	PCI11 MSTTXL CRCT	PCI11 MSTRXH CRCT	PCI11 MSTRXL CRCT
43	PCI:PCI WARN0							
	PCI12 TGTTXH CRCT	PCI12 TGTTXL CRCT	PCI12 TGTRXH CRCT	PCI12 TGTRXL CRCT	PCI12 MSTTXH CRCT	PCI12 MSTTXL CRCT	PCI12 MSTRXH CRCT	PCI12 MSTRXL CRCT
44	PCI:LOG0							
	MSTRXECC LOGGADH0	MSTRXECC LOGGADH7	MSTRXECC LOGGADH6	MSTRXECC LOGGADH5	MSTRXECC LOGGADH4	MSTRXECC LOGGADH3	MSTRXECC LOGGADH2	MSTRXECC LOGGADH1
45	PCI:LOG0							
	MSTRXECC LOGGADH0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
46	PCI:LOG0							
	MSTRXECC LOGGADL8	MSTRXECC LOGGADL7	MSTRXECC LOGGADL6	MSTRXECC LOGGADL5	MSTRXECC LOGGADL4	MSTRXECC LOGGADL3	MSTRXECC LOGGADL2	MSTRXECC LOGGADL1
47	PCI:LOG0							
	MSTRXECC LOGGADL0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
48	PCI:LOG1							
	MSTRXECC LOGGBEH8	MSTRXECC LOGGBEH7	MSTRXECC LOGGBEH6	MSTRXECC LOGGBEH5	MSTRXECC LOGGBEH4	MSTRXECC LOGGBEH3	MSTRXECC LOGGBEH2	MSTRXECC LOGGBEH1
49	PCI:LOG1							
	MSTRXECC LOGGBEH0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4A	PCI:LOG1							
	MSTRXECC LOGGBEL8	MSTRXECC LOGGBEL7	MSTRXECC LOGGBEL6	MSTRXECC LOGGBEL5	MSTRXECC LOGGBEL4	MSTRXECC LOGGBEL3	MSTRXECC LOGGBEL2	MSTRXECC LOGGBEL1
4B	PCI:LOG1							
	MSTRXECC LOGGBEL0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
4C	PCI:LOG2							
	MSTRXECC LOGGDTH35	MSTRXECC LOGGDTH34	MSTRXECC LOGGDTH33	MSTRXECC LOGGDTH32	MSTRXECC LOGGDTH31	MSTRXECC LOGGDTH30	MSTRXECC LOGGDTH29	MSTRXECC LOGGDTH28
4D	PCI:LOG2							
	MSTRXECC LOGGDTH27	MSTRXECC LOGGDTH26	MSTRXECC LOGGDTH25	MSTRXECC LOGGDTH24	MSTRXECC LOGGDTH23	MSTRXECC LOGGDTH22	MSTRXECC LOGGDTH21	MSTRXECC LOGGDTH20
4E	PCI:LOG2							
	MSTRXECC LOGGDTH19	MSTRXECC LOGGDTH18	MSTRXECC LOGGDTH17	MSTRXECC LOGGDTH16	MSTRXECC LOGGDTH15	MSTRXECC LOGGDTH14	MSTRXECC LOGGDTH13	MSTRXECC LOGGDTH12
4F	PCI:LOG2							
	MSTRXECC LOGGDTH11	MSTRXECC LOGGDTH10	MSTRXECC LOGGDTH09	MSTRXECC LOGGDTH08	MSTRXECC LOGGDTH07	MSTRXECC LOGGDTH06	MSTRXECC LOGGDTH05	MSTRXECC LOGGDTH04

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type16 (2/4)

	0	1	2	3	4	5	6	7
50	PCI:LOG3							
	MSTRXECC LOGGDTH03	MSTRXECC LOGGDTH02	MSTRXECC LOGGDTH01	MSTRXECC LOGGDTH00	(Not used)	(Not used)	(Not used)	(Not used)
51	PCI:LOG3							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
52	PCI:LOG3							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
53	PCI:LOG3							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
54	PCI:LOG4							
	MSTRXECC LOGGDTL35	MSTRXECC LOGGDTL34	MSTRXECC LOGGDTL33	MSTRXECC LOGGDTL32	MSTRXECC LOGGDTL31	MSTRXECC LOGGDTL30	MSTRXECC LOGGDTL29	MSTRXECC LOGGDTL28
55	PCI:LOG4							
	MSTRXECC LOGGDTL27	MSTRXECC LOGGDTL26	MSTRXECC LOGGDTL25	MSTRXECC LOGGDTL24	MSTRXECC LOGGDTL23	MSTRXECC LOGGDTL22	MSTRXECC LOGGDTL21	MSTRXECC LOGGDTL20
56	PCI:LOG4							
	MSTRXECC LOGGDTL19	MSTRXECC LOGGDTL18	MSTRXECC LOGGDTL17	MSTRXECC LOGGDTL16	MSTRXECC LOGGDTL15	MSTRXECC LOGGDTL14	MSTRXECC LOGGDTL13	MSTRXECC LOGGDTL12
57	PCI:LOG4							
	MSTRXECC LOGGDTL11	MSTRXECC LOGGDTL10	MSTRXECC LOGGDTL09	MSTRXECC LOGGDTL08	MSTRXECC LOGGDTL07	MSTRXECC LOGGDTL06	MSTRXECC LOGGDTL05	MSTRXECC LOGGDTL04
58	PCI:LOG5							
	MSTRXECC LOGGDTL03	MSTRXECC LOGGDTL02	MSTRXECC LOGGDTL01	MSTRXECC LOGGDTL00	(Not used)	(Not used)	(Not used)	(Not used)
59	PCI:LOG5							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5A	PCI:LOG5							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5B	PCI:LOG5							
	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5C	PCI:LOG6							
	MSTRXECC LOGECH8	MSTRXECC LOGECH7	MSTRXECC LOGECH6	MSTRXECC LOGECH5	MSTRXECC LOGECH4	MSTRXECC LOGECH3	MSTRXECC LOGECH2	MSTRXECC LOGECH1
5D	PCI:LOG6							
	MSTRXECC LOGECH0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
5E	PCI:LOG6							
	MSTRXECC LOGECL8	MSTRXECC LOGECL7	MSTRXECC LOGECL6	MSTRXECC LOGECL5	MSTRXECC LOGECL4	MSTRXECC LOGECL3	MSTRXECC LOGECL2	MSTRXECC LOGECL1
5F	PCI:LOG6							
	MSTRXECC LOGECL0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type16 (3/4)

	0	1	2	3	4	5	6	7
60	PCI:LOG7							
	MSTRXECC LOGSYNH8	MSTRXECC LOGSYNH7	MSTRXECC LOGSYNH6	MSTRXECC LOGSYNH5	MSTRXECC LOGSYNH4	MSTRXECC LOGSYNH3	MSTRXECC LOGSYNH2	MSTRXECC LOGSYNH8
61	PCI:LOG7							
	MSTRXECC LOGSYNH0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	MSTRXECC LOGSYNH0
62	PCI:LOG7							
	MSTRXECC LOGSYNL8	MSTRXECC LOGSYNL7	MSTRXECC LOGSYNL6	MSTRXECC LOGSYNL5	MSTRXECC LOGSYNL4	MSTRXECC LOGSYNL3	MSTRXECC LOGSYNL2	MSTRXECC LOGSYNL8
63	PCI:LOG7							
	MSTRXECC LOGSYNL0	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)	(Not used)
64	Not used							
65	Not used							
66	Not used							
67	Not used							
68	Not used							
69	Not used							
6A	Not used							
6B	Not used							
6C	Not used							
6D	Not used							
6E	Not used							
6F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type16 (4/4)

	0	1	2	3	4	5	6	7
70	Not used							
71	Not used							
72	Not used							
73	Not used							
74	Not used							
75	Not used							
76	Not used							
77	Not used							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type17 (1/4)

	0	1	2	3	4	5	6	7
40	CARB:P0ERR0							
	P0ERR0							
41	CARB:P0ERR0							
	P0ERR0							
42	CARB:P0ERR0							
	P0ERR0							
43	CARB:P0ERR0							
	P0ERR0							
44	CARB:P0ERR0/P0ERR1							
	P0ERR0							P0ERR1
45	CARB:P0ERR1							
	P0ERR1							
46	CARB:P0ERR1							
	P0ERR1							
47	CARB:PIERR0							
	PIERR0							
48	CARB:PIERR0							
	PIERR0							
49	CARB:PIERR0							
	PIERR0							
4A	CARB:PIERR0							
	PIERR0							
4B	CARB:PIERR0/PIERR1							
	PIERR0							PIERR1
4C	CARB:PIERR1							
	PIERR1							
4D	CARB:PIERR1							
	PIERR1							
4E	CARB:P2ERR0							
	P2ERR0							
4F	CARB:P2ERR0							
	P2ERR0							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type17 (2/4)

	0	1	2	3	4	5	6	7
50	CARB:P2ERR0							
	P2ERR0							
51	CARB:P2ERR0							
	P2ERR0							
52	CARB:P2ERR0/P2ERR1							
	P2ERR0						P2ERR1	
53	CARB:P2ERR1							
	P2ERR1							
54	CARB:P2ERR1							
	P2ERR1							
55	CARB:P3ERR0							
	P3ERR0							
56	CARB:P3ERR0							
	P3ERR0							
57	CARB:P3ERR0							
	P3ERR0							
58	CARB:P3ERR0							
	P3ERR0							
59	CARB:P3ERR0/P3ERR1							
	P3ERR0						P3ERR1	
5A	CARB:P3ERR1							
	P3ERR1							
5B	CARB:P3ERR1							
	P3ERR1							
5C	CARB:P5ERR0							
	P5ERR0							
5D	CARB:P5ERR0							
	P5ERR0							
5E	CARB:P5ERR0							
	P5ERR0							
5F	CARB:P5ERR0							
	P5ERR0							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type17 (3/4)

	0	1	2	3	4	5	6	7
60	CARB:P5ERR0/P5ERR1							
	P5ERR0							P5ERR1
61	CARB:P5ERR1							
	P5ERR1							
62	CARB:P5ERR1							
	P5ERR1							
63	CARB:P6ERR0							
	P6ERR0							
64	CARB:P6ERR0							
	P5ERR0							
65	CARB:P6ERR0							
	P6ERR0							
66	CARB:P6ERR0							
	P6ERR0							
67	CARB:P6ERR0/P6ERR1							
	P6ERR0							P6ERR1
68	CARB:P6ERR1							
	P6ERR1							
69	CARB:P6ERR1							
	P6ERR1							
6A	CARB:P7ERR0							
	P7ERR0							
6B	CARB:P7ERR0							
	P7ERR0							
6C	CARB:P7ERR0							
	P7ERR0							
6D	CARB:P7ERR0							
	P7ERR0							
6E	CARB:P7ERR0							
	P7ERR0							P7ERR1
6F	CARB:P7ERR1							
	P7ERR1							

Byte27 = FF (Cache Memory/Shared Memory/SM Side/Scan/LED BUS/Abnormal MODE/ CARB error/Correctable error/Warning) Byte 40-7F Common detail information : Type17 (4/4)

	0	1	2	3	4	5	6	7
70	CARB:P7ERR1							
	P7ERR1							
71	CARB:P8ERR0							
	P7ERR1							
72	CARB:P8ERR0							
	P8ERR0							
73	CARB:P8ERR0							
	P8ERR0							
74	CARB:P8ERR0							
	P8ERR0							
75	CARB:P8ERR0/P8ERR1							
	P8ERR0							
76	CARB:P8ERR1							
	P8ERR1							
77	CARB:P8ERR1							
	P8ERR1							
78	Not used							
79	Not used							
7A	Not used							
7B	Not used							
7C	Not used							
7D	Not used							
7E	Not used							
7F	Not used							

Byte0D : Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'3')			
27	Operation byte of LR/LRE command parameter							
28	Auxiliary byte of LR/LRE command parameter							
29	Byte 2 for LR/LRE command							
2A	Reserved							
2B								
2C	Search parameter of LR command (CCHHR)							
2D	or							
2E	Record ID of re-execution command (R)							
2F								
30	Sector number to re-execute the LR command							
31	TLF for LR/LRE command							
32								
33	Operation byte = 0x3F : LRE External operation byte others : Reserved							
34	SSID (Low) of self subsystem							
35								
36	Exception code (x'08C1')							
37								
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Not used (x'01')							
3D	Cylinder address							
3E								
3F	Head address							

Byte0D : Bit1 = 1 & Byte36-37 = 08C1 (Machine Condition Exception) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte0D : Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception: Incomplete Domain) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type							
26	Content (x'6')				Format (x'4')			
27	Operation byte of LR/LRE command parameter							
28	Extension operation byte of LR/LRE command parameter							
29	Head address of extent							
2A	(DX command parameter)							
2B								
2C								
2D								
2E	Path group ID							
2F	(byte 1 to 7 of the ID by the last Set Path Group ID command)							
30								
31								
32								
33								
34	SSID (Low) of self subsystem							
35								
36	Exception code (x'0212')							
37								
38	Log and message control (x'05')							
39	Program action code (x'10')							
3A	Configuration information							
3B								
3C	Not used (x'01')							
3D	Cylinder address							
3E								
3F	Head address							

Byte0D : Bit1 = 1 & Byte36-37 = 0212 (Command Sequence Exception: Incomplete Domain) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
54								
55								
56								
57	Module ID (detected error)							
	Routine ID (detected error)							
7F	Not used							

Byte0D : Bit1 = 1 & Byte36-37 = 4XXX (Data exception) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type (x'24' : DKU87I-3)							
26	Content				Format (x'1')			
27	Sector number (*1)							
28	Cylinder address (*1)							
29								
2A	Head address (*1)							
2B								
2C	Record number (*1)							
2D	Error displacement (*1)							
2E								
2F	Drive serial number (x'0C') +							
30	(x'24') +							
31	(DKU sequence number)							
32								
33								
34	SSID of self subsystem							
35								
36	Exception class (x'4')				Message (*2)			
37	Correction flag (*3)							
38	Log and message control (x'08')							
39	Program action code (x'91')							
3A	Configuration information							
3B								
3C	Not used (x'01')							
3D	Cylinder address							
3E								
3F	Head address							

Byte0D : Bit1 = 1 & Byte36-37 = 4XXX (Data exception) (2/3)

	0	1	2	3	4	5	6	7
40	Causal Key code							
41	Causal Format/Message							
42	Causal Module ID							
43	Causal Routine ID							
44	VDEV number, slot number							
45								
46								
47								
48	Slot status							
49								
4A								
4B								
4C	Staging bit map/Dirty bit map							
4D								
4E								
4F								
50	Current sub block number							
51	EXIT flag (*4)							
52	Slot failure information (*5)							
53	Obtained information flat (*6)							
54	Head sub block number for HIT/MISS decision + Remain sub block count							
55	(when byte36 is x'40/41/42/43') / PACC in CBUF (when byte36 is x'45')							
56	Not used (when byte36 is x'40/41/42/43') /							
57	PAHF in CBUF (when byte36 is x'45')							
58	Not used (when byte36 is x'40/41/42/43') /							
59	Current CC in JCB (when byte36 is x'45')							
5A	Not used (when byte36 is x'40/41/42/43') /							
5B	Current HH in JCB (when byte36 is x'45')							
5C								
	Reserved							
7F								

*1: Not determined for occurrence in HA or RO

*2: Contents of message

x'0' : Data check in HA field
 x'1' : Data check in C field
 x'2' : Data check in K field
 x'3' : Data check in D field
 x'4' : Missing SYNC byte in HA field
 x'5' : Missing SYNC byte in C field (PA error)
 x'6' : Missing SYNC byte in K field
 x'7' : Missing SYNC byte in D field
 x'8' : Not used
 x'9' : Missing AM during retry
 x'A' to x'F' : Not used

*3: Contents of correction flag

Bits 0 and 1 : ECC correction bit
 10 : Correctable (Recovered)
 11 : Uncorrectable
 Bit 2 : offset active
 Bits 3 to 7 : Not used

Byte0D : Bit1 = 1 & Byte36-37 = 4XXX (Data exception) (3/3)

*4: EXIT flag

- x'08' : CHL I/F CHK2 time over
- x'0D' : EOF
- x'0E' : DSB = 0E requested
- x'0F' : Logical error
- x'1E' : Make sense only
- x'1F' : Logical error (make sense only)
- x'30' : Selective reset requested
- x'40' : 'Staging' requested
- x'41' : 'Waiting free segment' requested
- x'42' : 'Time over of waiting for staging' requested
- x'43' : 'Staging for PA mismatch' requested
- x'45' : 'Destaging' requested
- x'46' : 'High priority destaging' requested
- x'47' : 'Pseud-through process' requested
- x'4B' : 'DSB = 4E retry' requested (data transfer)
- x'4E' : 'DSB = 4E retry' requested (DSB = 4E retry)
- x'4F' : STG requested (Read or STG in progress)

*5: Slot failure

- x'80' : Sector write error
- x'40' : Sector read error
- x'20' : ECC/LRC error
- x'10' : Incomplete data
- x'08' : No decision

*6: Obtained information flag

- bit0-1 : Block type
 - 00 : Data block
 - 01 : HA/RO block
 - 10 : Bit map block
- bit2-3 : Side
 - 00 : Cache write side
 - 01 : NVS write side
 - 10 : Cache read side
- bit4 : Segment
 - 0 : No segment
 - 1 : Segment exist
- bit5-7 : Reserved

Byte36 = 6F (Subsystem Information) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Command overrun threshold exceeded							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Unused (0)	Format (x'1')			
27	Data overrun threshold exceeded							
28	Seek count							
29								
2A	Drive serial number		(x'0C') +					
2B			(x'24') +					
2C			(DKU sequence number)					
2D								
2E								
2F	DKC serial number		(x'0C') +					
30			(x'24') +					
31			(x'00') +					
32			(DKC sequence number : high) +					
33			(DKC sequence number : low)					
34	SSID of self subsystem							
35								
36	Exception code Y : 0 : Statistics/RRBL command, 2 : Channel data overrun							
37	(x'6FXY') X : For serial CHL : LCP#, For parallel CHL : CHL#							
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Read/search byte count							
3D								
3E								
3F								

Byte36 = 6F (Subsystem Information) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 0 (FPC Detected

Failure : DSP) (1/2)

00	0	1	2	3	4	5	6	7
05	Time stamp							
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('111' : FPC Detected Failure)			0				
28	Not used							
29	Not used							
2A	SCSI command code (See SSBLOG05-3830)							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ : RDEV number							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 0 (FPC Detected

Failure : DSP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'00')							
41	Not used							
42	CDB							
43								
44								
45								
4B								
4C	(x'AB')							
4D	(x'CD')							
4E	CMD BLK number							
4F	Data transfer end flag							
50	I/O status							
51								
52	PDEV status							
53								
54	LOOP status							
55								
56								
57								
58	Frame Manager status							
59								
5A								
5B								
5C	TL status							
5D								
5E								
5F								
60	IO issued status 1							
61								
62	IO issued status 2							
63								
64	Not used							
65								
66								
67								
68	JOB require info							
69								
6A								
6B								
6C	Data transfer status							
6D								
6E								
6F								
70	Not used							
71								
72								
73								
7F								

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 1 (FPC Detected

Failure : FBP) (1/2)

00	0	1	2	3	4	5	6	7
05	Time stamp							
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('111' : FPC Detected Failure)			0				
28	Internal Failure Part							
29								
2A	Not used							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'DYZZ') Y : CDEV number ZZ : RDEV number							
37								
38	Log and message control (x'00')							
39	Program action code (x'00')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 7 & Byte40 : Bit7 = 1 (FPC Detected)

Failure : FBP) (2/2)

	0	1	2	3	4	5	6	7
40	SSB edit format form (x'01')							
41	Not used							
42								
43								
44	ERQ index							
45								
46								
47								
48	IMQ index							
49								
4A								
4B								
4C	IO request information							
4D								
4E								
4F								
50	FPC Config/Control register							
51								
52								
53								
54	FPC Status register							
55								
56								
57								
58	FM Configuration register							
59								
5A								
5B								
5C	FM Status register							
5D								
5E								
5F								
60	FM Link Status 1 register							
61								
62								
63								
64	FM Link Status 2 register							
65								
66								
67								
68	FCA register 1							
69								
6A								
6B								
6C	FCA register 2							
6D								
6E								
6F								
70	FPC PCI register							
71								
72	AL_PA							
73								
74	FPC Config Command							
75								
76								
77								
78	Fibre status							
79								
7A								
7B								
7C	Fibre error count							
7D								
7E								
7F								

SCSI command code

Code	Command name
00	TEST UNIT READY
03	REQUEST SENSE
04	FORMAT UNIT
07	REASSIGN BLOCKS
12	INQUIRY
15	MODE SELECT
1A	MODE SENSE
1B	START/STOP UNIT
1C	RECEIVE DIAGNOSTIC RESULT
1D	SEND DIAGNOSTIC
28	READ (EXTEND)
2A	WRITE (EXTEND)
2E	WRITE AND VERIFY
3B	WRITE BUFFER

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 0 (Drive Detected Failure : DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('000' : Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command code							
2B	Not used							
2C	Module ID							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'EYZZ') Y : CDEV number ZZ : RDEV number							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36 : Bit0-3 = D & Byte26 : Bit4-7 = 1 & Byte27 : Bit0-2 = 0 (Drive Detected Failure : DSP) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41								
42	CDB							
43								
4B	Not used							
4C								
53	SCSI extend sense data							
54								
73	Error LBA number							
74								
75								
76								
77	Logical LBA number							
78								
79								
7A								
7B	Physical LBA number							
7C								
7D								
7E								
7F								

Byte36 : Bit0-3 = E & Byte2C : Bit0-7 ≠ 5X/6X (Drive Detected Failure : DSP) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format ID							
07	PCB type		MP number					
08	System error code							
09	(Return Code : Low 2 Byte)							
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPT	ECKD32	SRAID	RCOPY	SEPARATE	HST REP	HST REP	EXPLS
0E	LDEV#VL	PDEV#VL	SSB#VL	LOG#VL	SIM#VL	RSSB#VL	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06')							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	0	Format (x'1')			
27	Type code ('000' : Driver Detected Failure)			0	Sense key			
28	Additional Sense Code							
29	Additional Sense Code Qualifier							
2A	SCSI command code							
2B	Not used							
2C	Module ID (≠ 5X/6X)							
2D	Routine ID							
2E	Not used							
2F	Drive serial number (x'0C24') + (DKU sequence number)							
30	This information is not fixed until DKC reports SSB to HOST.							
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'EYZZ') Y : CDEV number ZZ : RDEV number							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36 : Bit0-3 = E & Byte2C : Bit0-7 ≠ 5X/6X (Drive Detected Failure : DSP) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
41								
42	CDB							
43								
4B	Not used							
4C								
53	SCSI extend sense data							
54								
73	Error LBA number							
74								
75								
76								
77	Logical LBA number							
78								
79								
7A								
7B	Physical LBA number							
7C								
7D								
7E								
7F								

Byte36 = E0 & Byte2C = 5X/6X & Byte20 : Bit3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used	Format (x'1')			
27	Type code (b'000')			Not used	Not used			
28	Ready	Enable	SSB pending	Not used				
29	Not used							
2A	M.M. RSV	PIN VOL	Write INH	Not used				
2B	Not used							
2C	Module ID (*1)							
2D	Routine ID (*1)							
2E	Not used							
2F	Drive serial number (x'0C') +							
30	(x'24') +							
31	(DKU sequence number)							
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'E000')							
37								
38	Log and message control (x'08')							
39	Program action code (x'92')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36 = E0 & Byte2C = 5X/6X & Byte20 : Bit3 = 1 (LDEV blockade/Pin VOL detected/Write inhibited) (2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

*1: Module ID-Routine ID

x'5011/501C/5202'

: LDEV blockade

x'501A/501B'

: Pin VOL detected

x'6605/6697/6705/6784/6805/6853/6909/6953' : Write inhibited

Byte36-37 = E210 & Byte 2C = 5X/6X & Byte20 : Bit1 = 1 (Intervention requested : LDEV not ready)

(1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB							
21								
22	Storage control type (x'06' : Cache DKC)							
23	Count							
24	Device address							
25	Device type							
26	Unit flag	DEV ADR V	TRK ADR V	Not used	Format (x'2')			
27	Type code (b'000')			Not used	Not used			
28	Ready	Enable	SSB PEND	0	0	0	0	0
29	x'00'							
2A	M.M RSV	PIN VOL	0	0	0	0	0	0
2B	Host type	DKC type	0	0	DKU type			
2C	Module ID•Routine ID							
2D	(x'501C/5202' : LDEV not ready)							
2E	Command code							
2F	Drive serial number (x'0C') +							
30	(x'24') +							
31	(DKU sequence number)							
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (x'E210')							
37								
38	Log and message control (x'01')							
39	Program action code (x'02')							
3A	Configuration information							
3B								
3C	Message code							
3D	Cylinder address							
3E								
3F	Head address							

Byte36-37 = E210 & Byte 2C = 5X/6X & Byte20 : Bit1 = 1 (Intervention requested : LDEV not ready)

(2/2)

	0	1	2	3	4	5	6	7
40	Not used							
7F								

Byte26 : Bit4-7 = F & Byte3C = F1 (DKC SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB*							
21								
22	Storage control type (x'06' : Cache DKC)*							
23	Count (x'00')*							
24	Device address*							
25	Device type*							
26	Content (*1)				Format (x'F')			
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Failure level (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)	'0000000'						
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C') +							
30	(x'24') +							
31	(x'00') +							
32	(DKC sequence number : high) +							
33	(DKC sequence number : low)*							
34	SSID of self subsystem							
35								
36	Exception code (See "Reference code" table)							
37								
38	Log and message control (*15)*							
39	Program action code (x'10')*							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26 : Bit4-7 = F & Byte3C = F1 (DKC SIM) (2/3)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR *
3B	32Byte SSB (0)	0	0	0	0	Extended path	Storage path number	*
3C	SIM message code (x'F1')							
3D	Affected SSID*							
3E								
3F	Not used							
40								
	Not used							
7F								

*: This information is not fixed until DKC reports SIM to HOST.

*1: Content

bit0 : Serial number valid (0:Invalid, 1:Valid)
 bit1 : Device address valid (0:Invalid, 1:Valid)
 bit2 : Track address valid (0:Invalid, 1:Valid)
 bit3 : Not used (0)

*2: Range subject to the error

x'0' : Not used
 x'1' : Error range not known
 x'2' : SP error
 x'3' : CHL-I error
 x'4' : Not affect performance
 x'5 ~ F' : Not used

*3: Range affected by repair

x'0' : Not used
 x'1' : Repair range not known
 x'2' : Repair dose not affect performance
 x'3' : SP disabled during repair
 x'4' : SSID disabled during repair
 x'5 ~ F' : Not used

*4: Failure level

00 : Service Alert
 01 : Moderate Alert
 10 : serious Alert
 11 : Acute Alert

*5: Failure SPs

0 : 1SP
 1 : 2SP

*6: SPs in SSID

0 : 2SP
 1 : 4SP

*7: Failure SSIDs

0 : 1SSID
 1 : 2SSID

*8: Affected SSIDs

0 : 1SSID
 1 : 2SSID

*9: Status

0 : Reduction
 1 : Stop

*10: Resent SIM

0 : Unresent SIM
 1 : Resent SIM

Byte26 : Bit4-7 = F & Byte3C = F1 (DKC SIM) (3/3)

*11: Hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1536MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per cluster

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 4

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 4

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1536MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code.

This byte is byte 2 of the isolation code.

*13: SSB ID

0 : SVP

1 : SP

*14: SVP ID

0 : SVP1

1 : SVP2

*15: Log and message control

bit 0-2 : Not used

bit 3 : Log mode (0)

bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

Byte26 : Bit4-7 = F & Byte3C = F2 (CACHE SIM) (1/3)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB*							
21								
22	Storage control type (x'06' : Cache DKC)*							
23	Count (x'00')*							
24	Device address*							
25	Device type*							
26	Content (*1)				Format (x'F')			
27	SIM sense record ID (x'E0')							
28	Range subject to the error (*2)				Range affected by repair (*3)			
29	Failure level (*4)		Failure SPs (*5)	SPs in SSID (*6)	Failure SSIDs (*7)	Affected SSIDs (*8)	Status (*9)	0
2A	Resent SIM (*10)	'0000000'						
2B	Hardware level (*11)							
2C								
2D	Isolation code (*12)							
2E	SSB ID (*13)	SIM sequence number						SVP ID (*14)
2F	DKC serial number (x'0C') +							
30	(x'24') +							
31	(x'00') +							
32	(DKC sequence number : high) +							
33	(DKC sequence number : low)*							
34	SSID of self subsystem							
35								
36	Exception code (See “Reference code” table)							
37								
38	Log and message control (*15)*							
39	Program action code (x'10')*							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26 : Bit4-7 = F & Byte3C = F2 (CACHE SIM) (2/3)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR *
3B	32Byte SSB (0)	0	0	0	0	Extended path	Storage path number	*
3C	SIM message code (x'F1')							
3D	Affected SSID*							
3E								
3F	Not used*							
40								
	Not used							
7F								

*: This information is not fixed until DKC reports SIM to HOST.

*1: Content

bit0 : Serial number valid (0:Invalid, 1:Valid)
 bit1 : Device address valid (0:Invalid, 1:Valid)
 bit2 : Track address valid (0:Invalid, 1:Valid)
 bit3 : Not used (0)

*2: Range subject to the error

x'0' : Not used
 x'1' : Error range not known
 x'2' : SP error
 x'3' : CHL-I error
 x'4' : Not affect performance
 x'5 ~ F' : Not used

*3: Range affected by repair

x'0' : Not used
 x'1' : Repair range not known
 x'2' : Repair dose not affect performance
 x'3' : SP disabled during repair
 x'4' : SSID disabled during repair
 x'5 ~ F' : Not used

*4: Failure level

00 : Service Alert
 01 : Moderate Alert
 10 : serious Alert
 11 : Acute Alert

*5: Failure SPs

0 : 1SP
 1 : 2SP

*6: SPs in SSID

0 : 2SP
 1 : 4SP

*7: Failure SSIDs

0 : 1SSID
 1 : 2SSID

*8: Affected SSIDs

0 : 1SSID
 1 : 2SSID

*9: Status

0 : Reduction
 1 : Stop

*10: Resent SIM

0 : Unresent SIM
 1 : Resent SIM

Byte26 : Bit4-7 = F & Byte3C = F2 (CACHE SIM) (3/3)

*11: Hardware level

① When bit 0 is 0.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs PER CLUSTER

00 : 4

01 : 8

10 : Not used

10 : Not used

11 : Not used

Bit 6 : NVS

0 : Without NVS

1 : With NVS

Bit 8-10 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1536MB

Bit 11-13 : Cluster hardware level ('000')

Bit 14-15 : Cache/NVS hardware level ('000')

② When bit 0 is 1.

Bit 1 : Not used

Bit 2-3 : SP number

Bit 4-5 : CHLs per cluster

0000 : Parallel = 4, serial = 0

0001 : Parallel = 8, serial = 0

0010 : Parallel = 4, serial = 2

0100 : Parallel = 4, serial = 4

0110 : Parallel = 0, serial = 2

1000 : Parallel = 0, serial = 4

1010 : Parallel = 0, serial = 8

1100 : Parallel = 0, serial = 6

Bit 8 : Dual frame

0 : Dual frame

1 : Single frame

Bit 9-11 : Cache size

000 : Non cache

001 : 256MB

010 : 512MB

011 : 768MB

100 : 1024MB

101 : 1280MB

110 : 1536MB

111 : Over 1536MB

Bit 12-13 : Cluster hardware level ('00')

Bit 14-15 : Cache/NVS hardware level ('00')

*12: Isolation code

Byte 2D ≠ x'00' : Byte 36 and 37 are a symptom code.

Byte 2D = x'00' : Byte 36 and 37 are a symptom code.

This byte is byte 2 of the isolation code.

*13: SSB ID

0 : SVP

1 : SP

*14: SVP ID

0 : SVP1

1 : SVP2

*15: Log and message control

bit 0-2 : Not used

bit 3 : Log mode (0)

bit 4-5 : Logging action

00 : Don't log

01 : Unconditional log

10 : Log only once

11 : Log only if persistent

bit 6-7 : Operator message

00 : No message

01 : Unconditional message

10 : Send only once

11 : Send only if persistent

Byte26 : Bit4-7 = F & Byte3C = FE (Device SIM) (1/2)

	0	1	2	3	4	5	6	7
00 05	Time stamp							
06	Format Message							
07	PCB type		Processor ID (Note)					
08 09	System error code							
0A 0B	JCB ID							
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB*							
21								
22	Storage control type (x'06' : Cache DKC)*							
23	Count (x'00')*							
24	Device address*							
25	Device type*							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number					SVP ID (*3)	
28	Device exception (x'1')				Service message (x'1' : Action, x'C' : No action)			
29	Failure level (*4)		'000000'					
2A	Resent SIM (*5)	'0000000'						
2B	Exception class FRU list code (x'80')							
2C	Hardware and microcode corequisite indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (x'05')							
2F	Drive serial number* (x'0C') + (x'24') + (DKU sequence number)							
30								
31								
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (See "Reference code" table)							
37								
38	Log and message control (*6)*							
39	Program action code (x'10')*							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26 : Bit4-7 = F & Byte3C = FE (Device SIM) (2/2)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR *
3B	32Byte SSB (0)	0	0	0	0	Extended path	Storage path number	*
3C	SIM message code (x'FE')							
3D	Affected SSID*							
3E								
3F	Not used*							
40								
	Not used							
7F								

*: This information is not fixed until DKC reports SIM to HOST.

*1: Content

bit0 : Serial number valid (0:Invalid, 1:Valid)
 bit1 : Device address valid (0:Invalid, 1:Valid)
 bit2 : Track address valid (0:Invalid, 1:Valid)
 bit3 : Not used (0)

*2: SSB ID

0 : SVP
 1 : SP

*3: SVP ID

0 : SVP1
 1 : SVP2

*4: Failure level

00 : Service Alert
 01 : Moderate Alert
 10 : serious Alert
 11 : Acute Alert

*5: Resent SIM

0 : Unresent SIM
 1 : Resent SIM

*6: Log and message control

bit 0-2 : Not used
 bit 3 : Log mode (0)
 bit 4-5 : Logging action
 00 : Don't log
 01 : Unconditional log
 10 : Log only once
 11 : Log only if persistent

bit 6-7 : Operator message

00 : No message
 01 : Unconditional message
 10 : Send only once
 11 : Send only if persistent

Byte26 : Bit4-7 = F & Byte3C = FF (Media SIM) (1/2)

	0	1	2	3	4	5	6	7
00	Time stamp							
05								
06	Format Message							
07	PCB type		Processor ID (Note)					
08	System error code							
09								
0A	JCB ID							
0B								
0C	REQ ID							
0D	24CMPAT	ECKD32	SCSI-RAID	RCOPY	SEPARATE	Host report	SVP report	Force log
0E	LDEV#VLD	PDEV#VLD	SSB#VLD	LOG#VLD	SIM#VLD	RSSB#VLD	0	0
0F	SEMI-SYNC	RCU SIM	0	0	RCU#			
10	LDEV number							
11								
12	CDEV#							
13	RDEV#							
14	RCU LDEV number							
15								
16	SSB log number							
17								
18	Related failure log number							
19								
1A	Related SIM log number							
1B								
1C	Related SSB log number							
1D								
1E	(Reserved)							
1F	SCB							
20	Basic SSB*							
21								
22	Storage control type (x'06' : Cache DKC)*							
23	Count (x'00')*							
24	Device address*							
25	Device type*							
26	Content (*1)				Format (x'F')			
27	SSB ID (*2)	SIM sequence number					SVP ID (*3)	
28	Device exception (x'1')				Service message (x'1')			
29	Failure level (*4)		Media maintenance procedure number (Correctable : x'07', Uncorrectable : x'05')					
2A	Resent SIM (*5)	'0000000'						
2B	Exception class FRU list code (x'80')							
2C	Hardware and microcode corequisite indicator (x'01')							
2D	Device address (See reference code)							
2E	Action flag (Correctable : x'87', Uncorrectable : x'85')							
2F	Drive serial number* (x'0C') +							
30	(x'24') +							
31	(DKU sequence number)							
32								
33								
34	SSID of self subsystem							
35								
36	Exception code (See "Reference code" table)							
37								
38	Log and message control (*6)*							
39	Program action code (x'10')*							

*: This information is not fixed until DKC reports SIM to HOST.

Byte26 : Bit4-7 = F & Byte3C = FF (Media SIM) (2/2)

	0	1	2	3	4	5	6	7
3A	Dual frame	EDCC mode	0	0	NONSYNC operation	Serial channel	Report output	Permanent path ERR *
3B	32Byte SSB (0)	0	0	0	0	Extended path	Storage path number	*
3C	SIM message code (x'FF')							
3D	Cylinder address*							
3E								
3F	Head address*							
40								
	Not used							
7F								

*: This information is not fixed until DKC reports SIM to HOST.

*1: Content

bit0 : Serial number valid (0:Invalid, 1:Valid)
 bit1 : Device address valid (0:Invalid, 1:Valid)
 bit2 : Track address valid (0:Invalid, 1:Valid)
 bit3 : Not used (0)

*2: SSB ID

0 : SVP
 1 : SP

*3: SVP ID

0 : SVP1
 1 : SVP2

*4: Failure level

00 : Service Alert
 01 : Moderate Alert
 10 : serious Alert
 11 : Acute Alert

*5: Resent SIM

0 : Unresent SIM
 1 : Resent SIM

*6: Log and message control

bit 0-2 : Not used
 bit 3 : Log mode (0)
 bit 4-5 : Logging action
 00 : Don't log
 01 : Unconditional log
 10 : Log only once
 11 : Log only if persistent

bit 6-7 : Operator message
 00 : No message
 01 : Unconditional message
 10 : Send only once
 11 : Send only if persistent