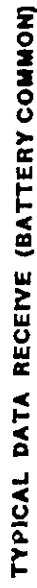


KEYING VOLTAGE =
±65 VDC TYPICAL
MARK=NEG CURRENT
SPACE=POS CURRENT



**LOOP CURRENT MUST BE
EXTERNALLY LIMITED TO
100 MA MAX (60 MA TYP)
AND ARC SUPPRESSION TO
LESS THAN 350 VOLTS**

NOTE:
KEYING VOLTAGE =
+130VDC TYPICAL
(REF: KEYS POSITIVE)



KEYING VOLTAGE=
-130VDC TYPICAL
(REF: KEYS NEGATIVE)

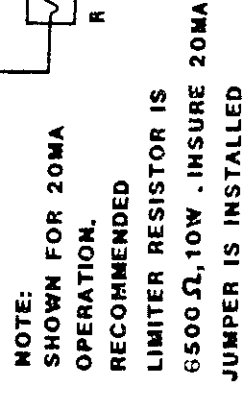


Figure 2-12. Typical High Level Applications

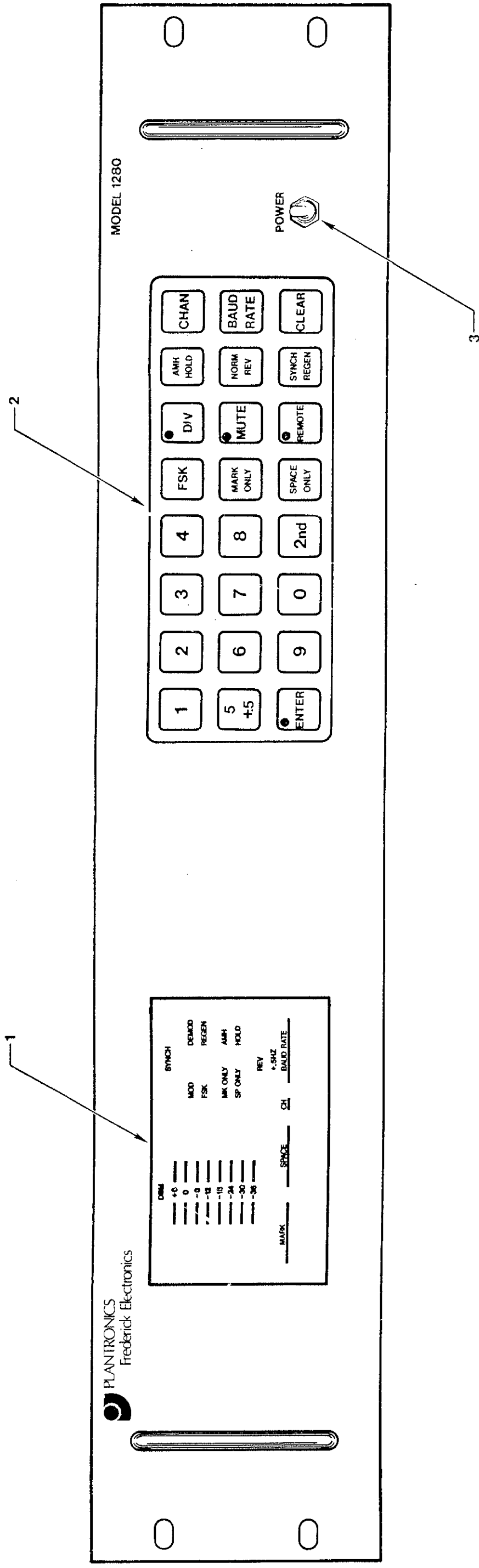


Figure 3-1. Model 1280 Front Panel

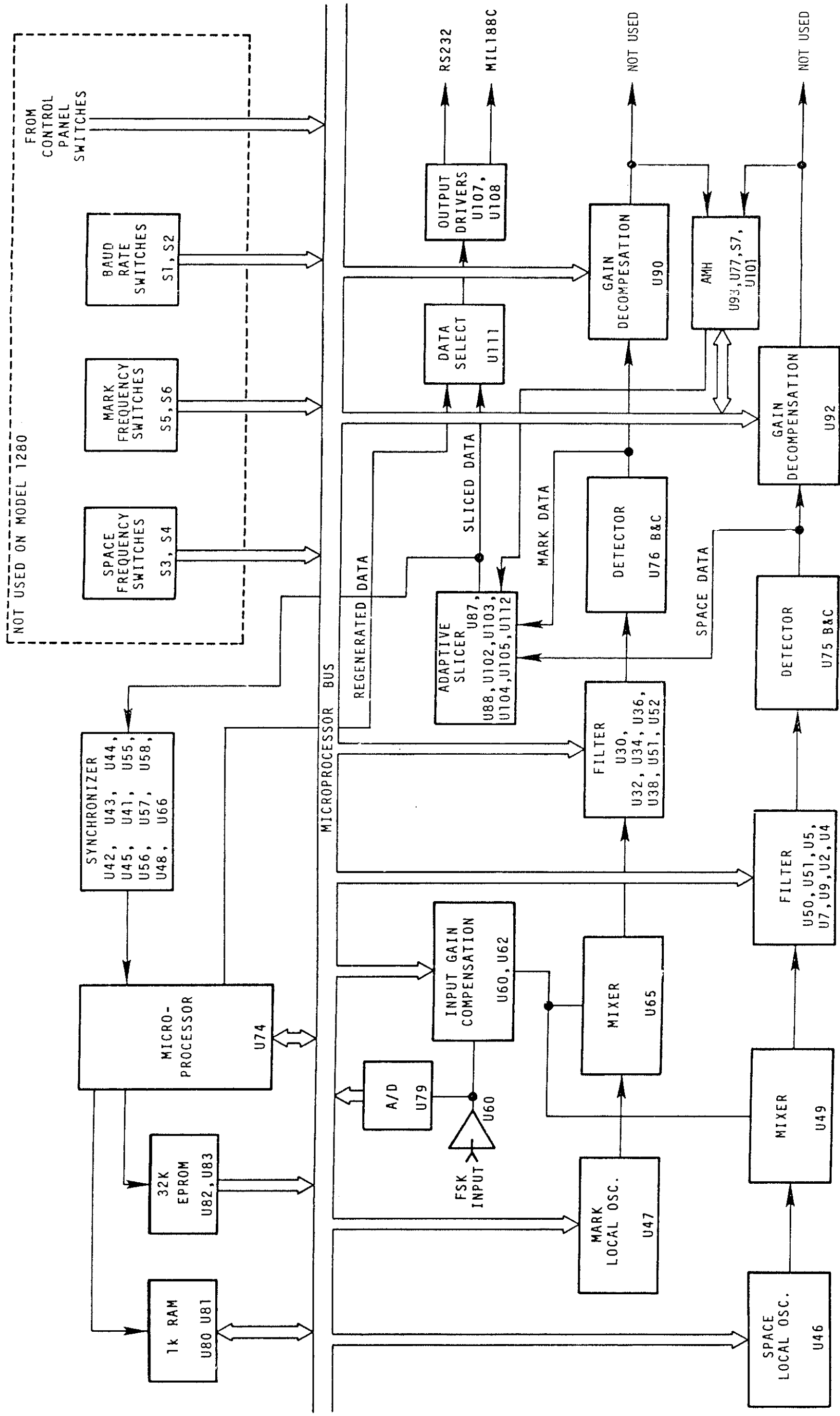


Figure 4-2. Demodulator Block Diagram

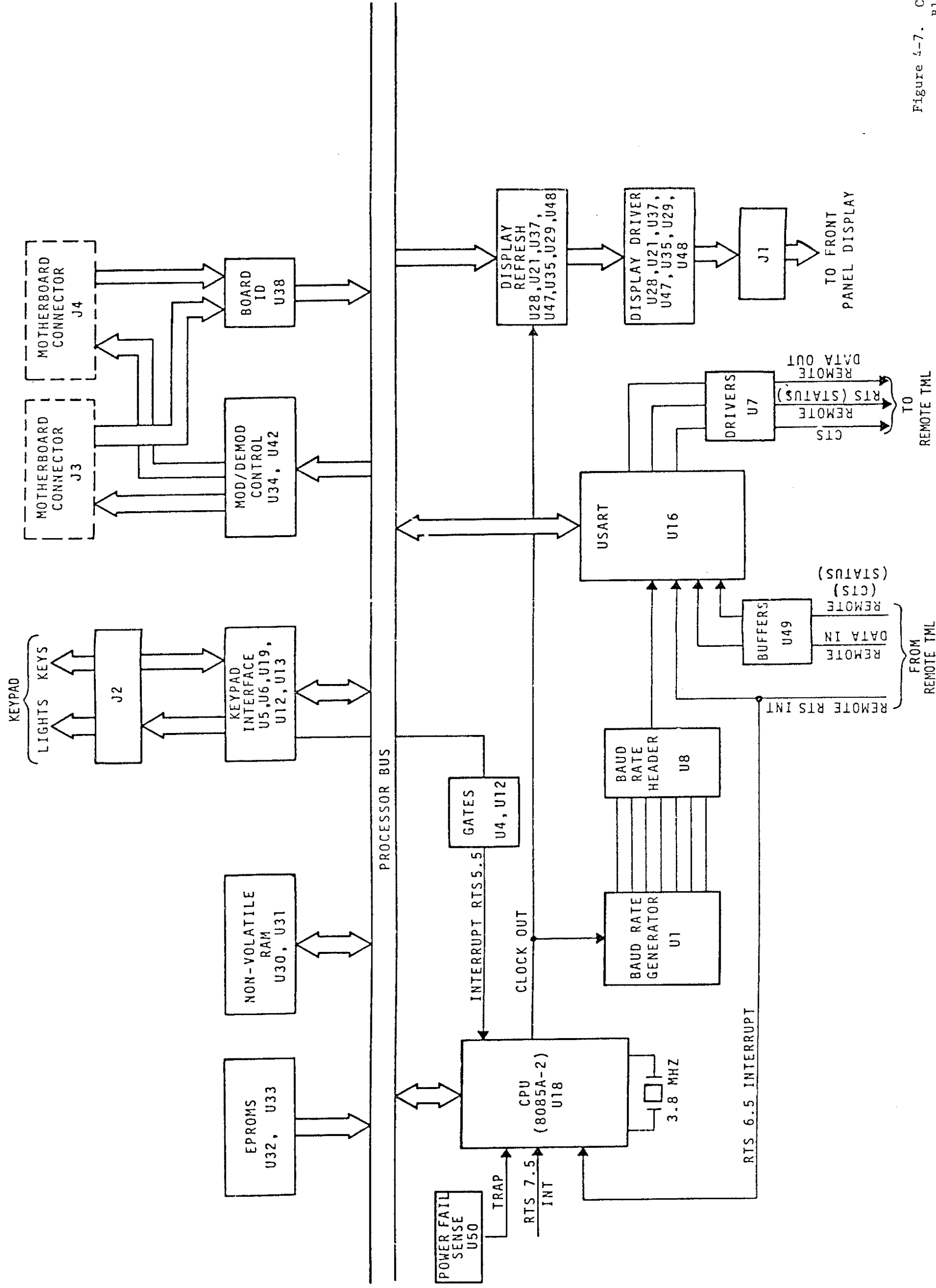


Figure 4-7. Control Board Block Diagram

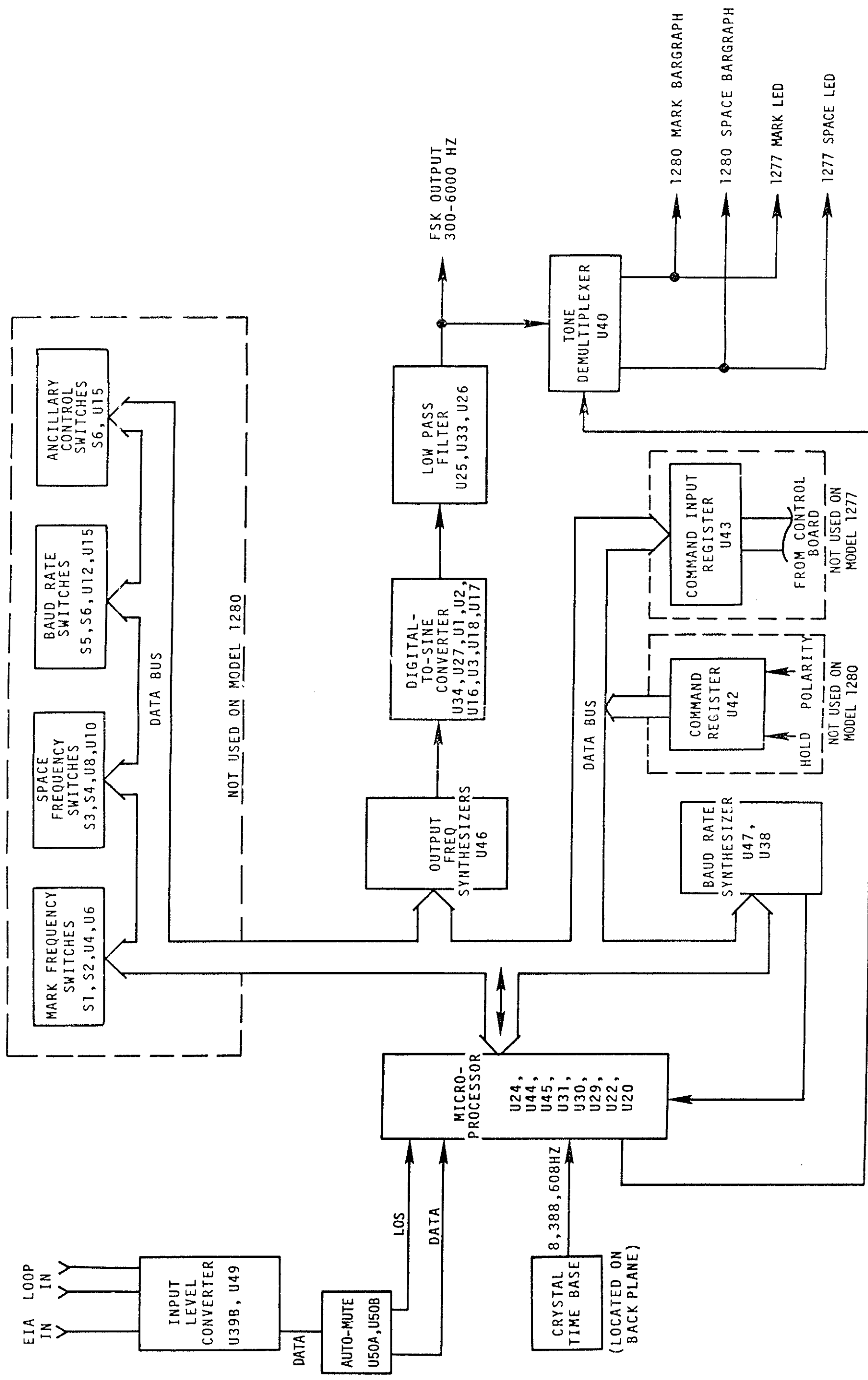
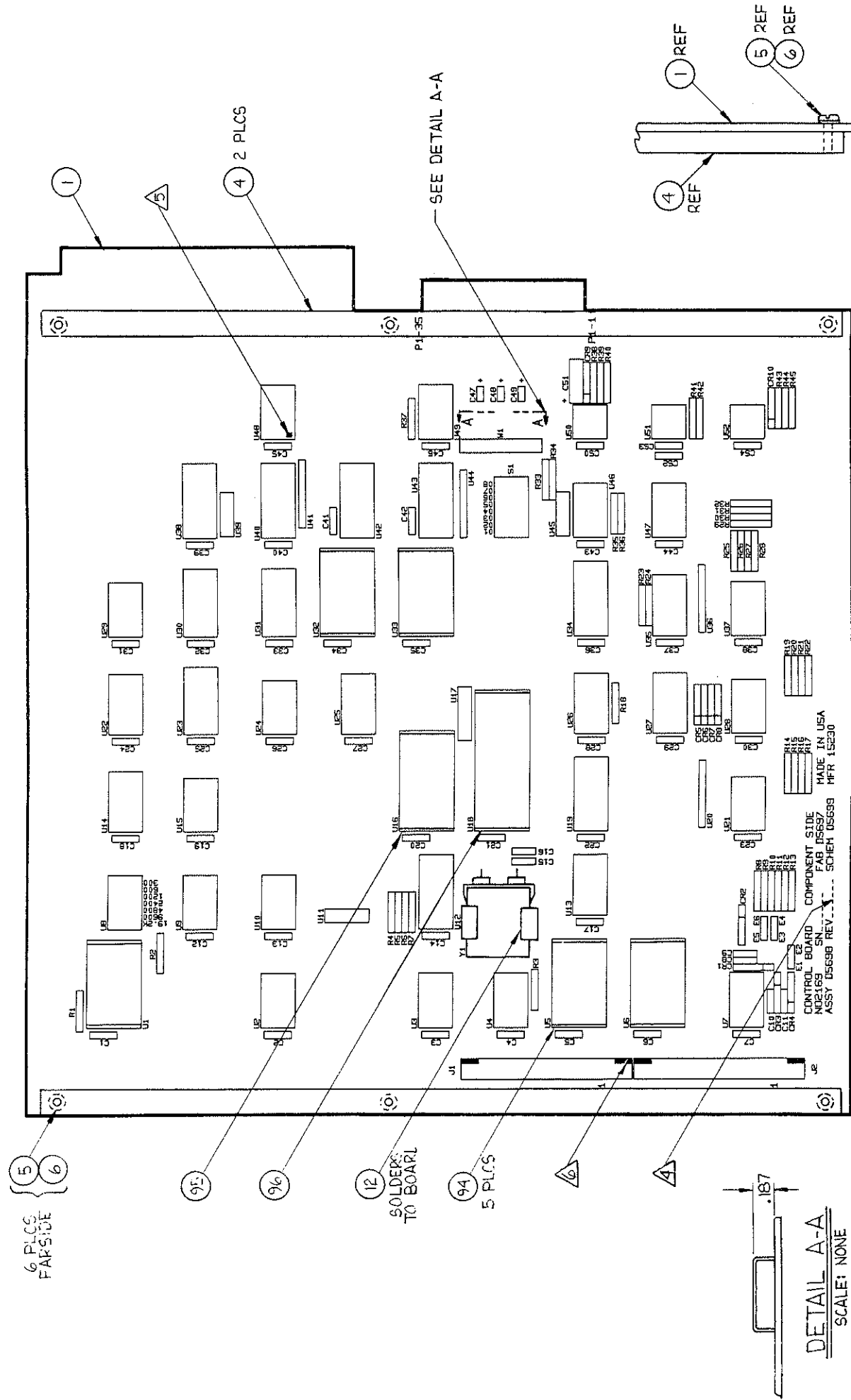


Figure 4-5. Modulator Block Diagram



NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE DOCUMENTS:

P.C. BOARD-N02169
FABRICATION DWG-D5697
SCHEMATIC DWG-D5699

2. REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY
AND DO NOT NECESSARILY APPEAR ON PARTS.

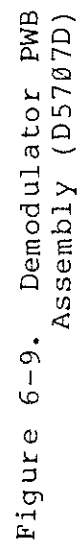
3. SOLDER USING SN60 OR SN63 PER QQ-5-571.

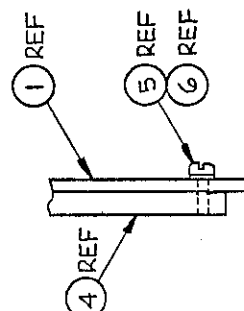
△ MARK CURRENT REVISION LEVEL IN 12 HIGH CHARACTERS
USING WHITE EPOXY INK, APPROXIMATELY WHERE SHOWN.

△ PIN 1 ORIENTATION TYPICAL, DENOTED BY SQUARE PAD ON PWB.

△ INSTALL J1 AND J2 WITH KEYING TABS IN TOWARD BOARD.

Figure 6-8. Control PWB
Assembly (D5698D)





6. DO NOT INSTALL U45; MAINTAIN POSITION FOR FUTURE USE.

Figure 6-10. Modulator PWB Assembly (D5725D)