

NOKIA

DX 200

BSC3i S10.5

BSC3i, Jumper Settings for the Plug-in Units

Site Documents

BSC3018_P

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Summary of changes

Changes between document issues are cumulative. Therefore, the latest document issue contains all changes made to previous issues.

Changes made between issue 2–1 and 2–0

The following sections have been updated: *WDW18-S P01091*, *HWAT-A C08950*, *MO91 drive P01090*.

Issue 2-0

The following chapters have been updated: *AC25-A*, *CL2TG-S/ CL3TG-S*, *ET2E-S*, *ET2E-SC*, *HWAT-A*, *MBIF-B*, and *SERO-A*, *SWCOP-A*.

Issue 1-0

This is the first issue of DN01154825.

1

About this manual

The *BSC3i, Jumper settings for plug-in units* provides the basic information needed for setting the various jumpers of the plug-in units in the BSC3i. The settings are referred to as standard when no changes are required, and alternative when the application delivered may need reconfiguration.

How to use this manual

This document provides the setting information, in a dedicated chapter, for those plug-in units which have jumpers:

Where to find more

For more information on the hardware settings (strappings), see the *Plug-in Unit Description* of the unit concerned.

For information on the grounding of the BSC3i equipment, see document *DX 200 Grounding Principles of PCM Trunk Circuit Cables*.

Typographic conventions

Table 1 presents the conventions used in this description.

Table 1. Typographic conventions.

<i>Emphasized font</i>	Indicates a reference to a manual, chapter or section, for example: See section <i>Installation</i> .
	Indicates a word or phrase that is emphasized, for example: <i>The CPU</i> .

2

Overview of jumper settings for the plug-in units

Before setting the various jumpers of the plug-in units in the BSC3i, the user is advised to read the information presented below.

Presentation of the settings

The standard and alternative jumpers of the plug-in units are presented in tables except if the plug-in unit has factory settings. The standard settings are illustrated in the jumper group location maps. For the standard jumpers, the table indicates the jumper group number, the connection required and a note about the connection. For the alternative settings, the meaning of the connection (like the plug-in unit location), the required connection and a note about the connection are listed. Each alternative jumper group is preceded by a title describing it.

In the column "Connection", a hyphen (-) is used between the pins of the jumper group that are to be connected on the plug-in unit. The pins are symbolized by consecutive numbers. If there is only a hyphen and no numbers in the column, no connection is made. The numbers of the jumper group/s are also given for the alternative settings.

The column "Note" indicates the alternative selected with the jumper concerned. If the jumper group is relevant only in connection with plug-in unit testing, the column is blank. The alternatives are listed in the plug-in unit descriptions.

If the plug-in unit has a jumper group which is strapped for interchangeability, the setting alternatives are presented in a form that differs from the presentation of the other tables.

In the interchangeability table,

- X = the corresponding connection is made
- blank = the corresponding connection is not made.

Implementation of the jumper settings

A hardware setting (strapping) is made by means of a jumper (marked by a rectangle).

The materials required: Jumper code 15291 00089

Selection of settings

Settings are often called only as jumper settings, or when they can clearly be defined they are divided to two groups:

- standard settings
- alternative settings.

The standard settings do not depend on external factors so they are always set in the same way regardless of the equipment position.

The alternative settings depend on external factors, such as the track position, the parameter to be selected, and the operating environment.

The plug-in units with both standard and alternative settings are a mixture of the cases in the two previous chapters.

Presentation of the read-only memory circuits

The following information is given about the read-only memory circuits:

- location of read-only memory circuit/s on the plug-in unit
- installation direction of read-only memory circuit/s (direction of the notch of circuit/s)
- setting of read-only memory circuit/s in the socket (sockets of circuit/s are indicated with broken line)
- the following can be read inside the circuit/s depending on the case:
- EPROM or PROM; either marking HIGH or LOW on 8-bit read-only memory circuits (not on 16-bit circuits)
- base addresses of circuits (for some plug-in units, only the beginning of the base address of the circuit, e.g. 60 = 60000H, A0 = A0000H, is presented) or the name of the program on the circuit + indexes of circuits.

This document provides the setting information, in a dedicated chapter, for those plug-in units which have jumpers:

- AC25-A
- AS7-B
- CL3TG
- CP710-A
- ET2A
- ET2E-S

- ET2E-SC
- HWAT-A
- MBIF-B
- PCU-B
- SERO-A
- SWCOP-A
- SW64B
- MO91
- WDW18-S/WDW36.

The following plug-in units have no jumper settings to be set by the user:

- ESB20: The switch and the pin header on the board are only for testing purposes.
- PSC1-S
- PSC6-A

3 AC25-A C08944

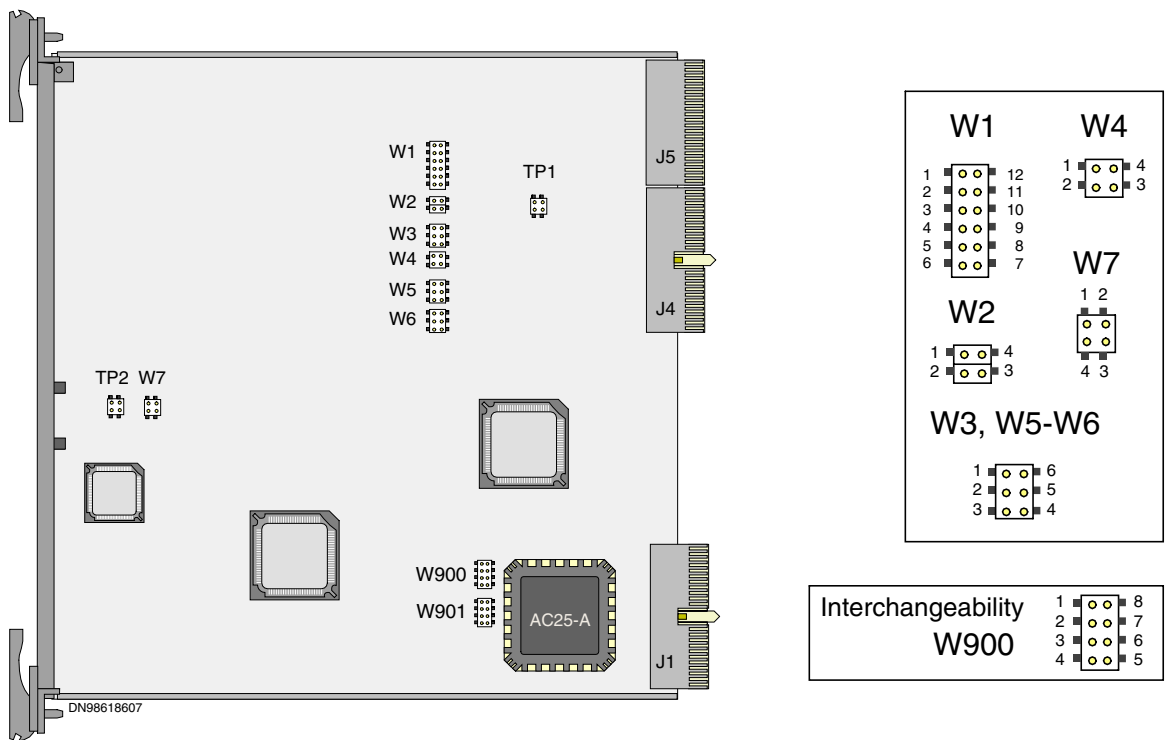


Figure 1. Jumpers and read-only memory circuits of the plug-in unit AC25-A.

Alternative settings on jumper groups W1 through W7 depend on the application as presented in the table below.

Jumper group W900 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Jumper groups W901 and W7 are used for testing purposes. No jumpers set on these.

Alternative settings

Table 2. Alternative settings of AC25-A.

Meaning	Jumper	Setting
<i>SELECTION OF EXTERNAL INTERFACE TYPE</i>		
<i>X.21 interface in use (Channel 2)</i>	W1;1 - 2 W1;3 - 4 W1;5 - 6 W1;7 - 8 W1;9 - 10 W1;11 - 12 W3;1 - 2 W3; 5 - 6 W6;1 - 6	ON ON ON OFF OFF OFF ON ON ON
Selection of bit timing:		
DCE (Bit timing of the output data from AC25-A)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	OFF ON ON ON
DTE (Bit timing of the output data from serial interface)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	ON OFF OFF OFF
<i>V.24 interface in use (Channel 1) (Default setting)</i>	W1;1 - 2 W1;3 - 4 W1;5 - 6 W1;7 - 8 W1;9 - 10 W1;11 - 12 W3;1 - 2 W3;5 - 6 W6;1 - 6	OFF OFF OFF ON ON ON ON ON ON
Selection of bit timing:		
DTE (Bit timing of the output data from serial interface) (Default setting)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	ON OFF OFF OFF

Table 2. Alternative settings of AC25-A. (Continued)

Meaning	Jumper	Setting
DCE (Bit timing of the output data from AC25-A)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	OFF ON ON ON
<i>Limited V.24 interface in use (Channel 2)</i>	W1;1 - 2 W1;3 - 4 W1;5 - 6 W1;7 - 8 W1;9 - 10 W1;11 - 12 W2;1 - 4 W4;ALL W6;1 - 6	ON ON ON OFF OFF OFF ON OFF ON
Selection of bit timing:		
DTE (Bit timing of the output data from the serial interface)	W3;1 - 2	ON
DCE (Bit timing of the output data from AC25-A)	W3;2 - 3	ON
DCD-circuit control:		
Serial interface control	W3;5 - 6	ON
Forced control into active state (ON state)	W3;4 - 5	OFF
<i>V.35 interface in use (Channel 1)</i>	W1;1 - 2 W1;3 - 4 W1;5 - 6 W1;7 - 8 W1;9 - 10 W1;11 - 12 W3;1 - 2 W3;5 - 6 W6;2 - 5	OFF OFF OFF ON ON ON ON ON ON
V.35 interface bit timing:		
DTE (Bit timing of the output data from serial interface)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	ON OFF OFF OFF

Table 2. Alternative settings of AC25-A. (Continued)

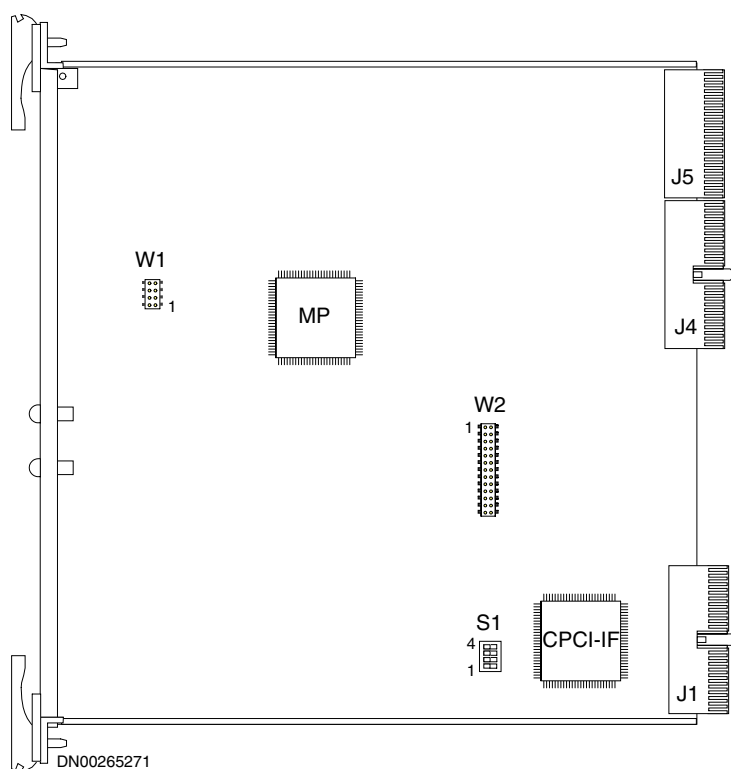
Meaning	Jumper	Setting
DCE (Bit timing of the output data from AC25-A)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	OFF ON ON ON
V.36 interface in use (Channel 1)	W1;1 - 2 W1;3 - 4 W1;5 - 6 W1;7 - 8 W1;9 - 10 W1;11 - 12 W3;1 - 2 W3;5 - 6 W6;3 - 4	OFF OFF OFF ON ON ON ON ON ON
V.36 interface bit timing:		
DTE (Bit timing of the output data from serial interface)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	ON OFF OFF OFF
DCE (Bit timing of the output data from AC25-A)	W2;1 - 4 W2;2 - 3 W4;1 - 4 W4;2 - 3	OFF ON ON ON
UART INTERFACE CONTROL		
1) UART interface is not in use (Default setting)	W5;1 - 6	ON
2) UART interface is in use	W5;1 - 6	OFF
Bit timing of UART interface:		
Bit timing of output data from AC25-A	W5;2 - 5 W5;3 - 4	ON OFF
Bit timing of output data from serial interface	W5;2 - 5 W5;3 - 4	OFF ON

Interchangeability code settings (W900)

Table 3. Interchangeability code settings of AC25-A (W900).

ICC code	W900 settings			
	1 - 8	2 - 7	3 - 6	4 - 5
A	ON	ON	ON	ON
B	OFF	ON	ON	ON
C	ON	OFF	ON	ON
D	OFF	OFF	ON	ON
E	ON	ON	OFF	ON
F	OFF	ON	OFF	ON
G	ON	OFF	OFF	ON
H	OFF	OFF	OFF	ON
J	ON	ON	ON	OFF
K	OFF	ON	ON	OFF
L	ON	OFF	ON	OFF
M	OFF	OFF	ON	OFF
N	ON	ON	OFF	OFF
P	OFF	ON	OFF	OFF
R	ON	OFF	OFF	OFF

4 AS7-B C81550



CPCI-IF = CPCI-interface

Figure 2. Jumpers and switch of the plug-in unit AS7-B.

There are no standard nor alternative jumpers to be set on the plug-in unit.

Jumper group W1 is a JTAG connector and jumper group W2 is a PRTA connector. No jumpers set on these.

Switch S1 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Interchangeability code settings (S1)

Note

Switch 1 = MSB and switch 4 = LSB.

ON = the switch is on the left; OFF = the switch is on the right.

Table 4. Setting the interchangeability code (S1).

ICC Code	Switch (S1) settings			
	Switch 1 (MSB)	Switch 2	Switch 3	Switch 4 (LSB)
A	OFF	OFF	OFF	OFF
B	OFF	OFF	OFF	ON
C	OFF	OFF	ON	OFF
D	OFF	OFF	ON	ON
E	OFF	ON	OFF	OFF
F	OFF	ON	OFF	ON
G	OFF	ON	ON	OFF
H	OFF	ON	ON	ON
J	ON	OFF	OFF	OFF
K	ON	OFF	OFF	ON
L	ON	OFF	ON	OFF
M	ON	OFF	ON	ON
N	ON	ON	OFF	OFF
P	ON	ON	OFF	ON
R	ON	ON	ON	OFF

5 CL2TG-S C08825/CL3TG-S C08827

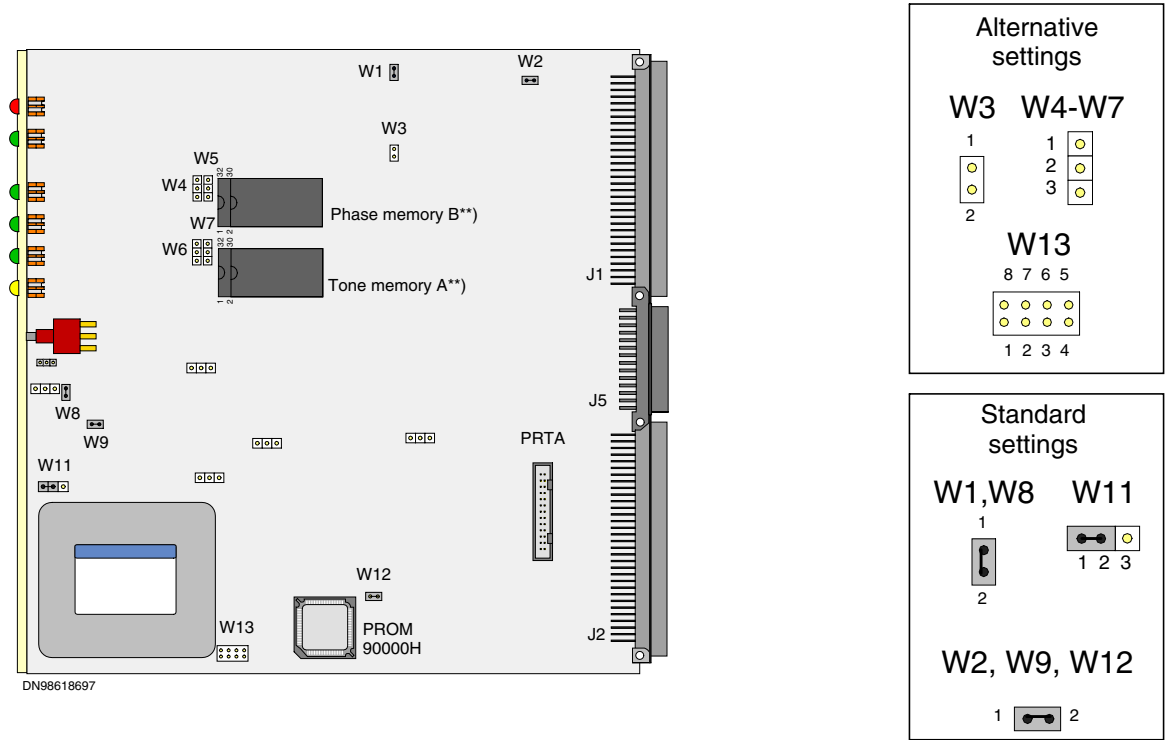


Figure 3. Jumper pins, standard jumpers and EPROM circuits in the CL2TG-S/CL3TG-S.

Standard settings are presented in the table below, and the jumpers must be set as shown during normal operation.

Alternative settings on jumper groups W3, W4, W5, W6 and W7 depend on the application as presented in the table below.

Jumper group W13 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

- There are two different sizes of read-only memory circuits for phase memory and tone generator memory: 28-pin (256 kbit) and 32-pin (1 Mbit / 2 Mbit / 4 Mbit).
- The PRTA (Processor Test Adaptor) W10 is a connector which can be used for testing and servicing.

Standard settings (W1, W2, W8, W9, W11, W12)

Table 5. Standard settings of CL2TG-S/CL3TG-S.

Jumper	Setting	Meaning
W1;1 - 2	ON	For testing purposes
W2;1 - 2	ON	For testing purposes
W8;1 - 2	ON	For testing purposes
W9;1 - 2	ON	For testing purposes
W11;1 - 2	ON	For testing purposes
W12;1 - 2	ON	For testing purposes

Alternative settings (W3, W4, W5, W6, W7)

Table 6. Alternative settings of CL2TG-S/CL3TG-S (W3, W4, W5, W6, W7).

Meaning	Jumper	Setting
TON-XXX product: Tone generator memory 1 Mbit / Phase memory 1 Mbit	W3;1 - 2	ON
TON-MYAICC B	W4;2 - 3	ON
TON-SEAICC C	W5;1 - 2	ON
TON-SEBICC B	W6;1 - 2	ON
TON-GBAICC B	W7;2 - 3	ON
TON-GBBICC A		
TON-LKAICC B		
TON-THAICC B		
TON-THBICC B		
Tone generator memory 2 Mbit or 4 Mbit / Phase memory 1 Mbit	W3;1 - 2	ON
Other TON products and newer ICCs than listed above	W4;2 - 3	ON
	W5;1 - 2	ON
	W6;2 - 3	ON
	W7;2 - 3	ON

ICC = Interchangeability code

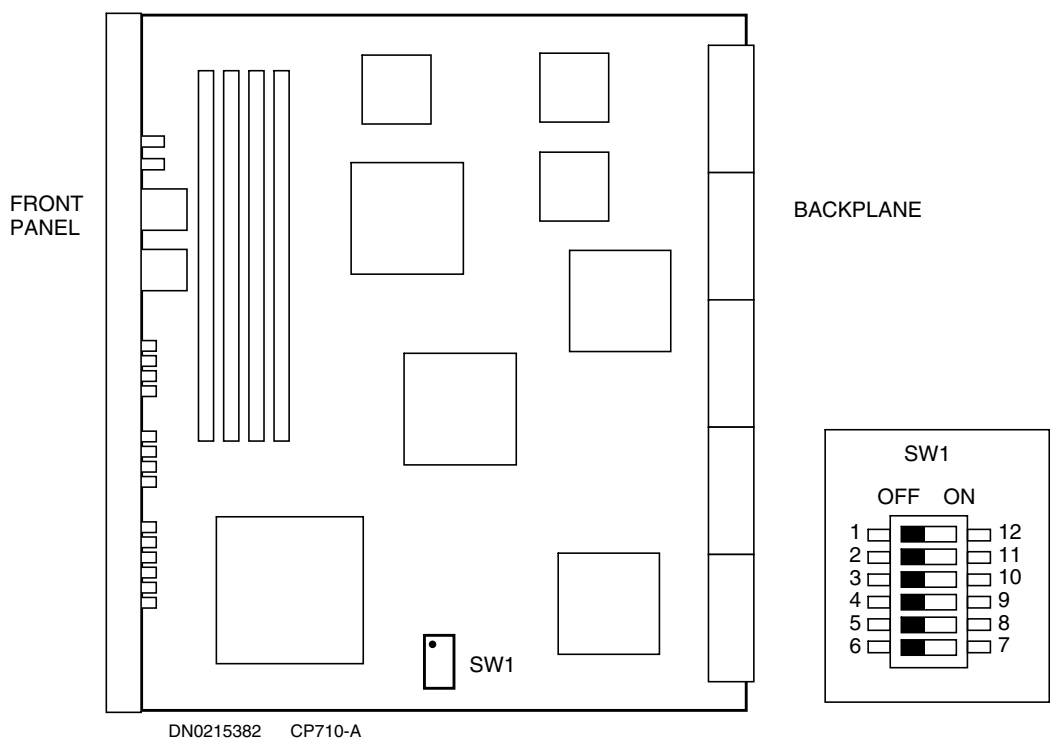
Interchangeability code settings (W13)

Table 7. Interchangeability code settings of CL2TG-S/CL3TG-S (W13).

ICC code	W13 settings			
	1 - 8	2 - 7	3 - 6	4 - 5
A	ON	ON	ON	ON
B	OFF	ON	ON	ON
C	ON	OFF	ON	ON
D	OFF	OFF	ON	ON
E	ON	ON	OFF	ON
F	OFF	ON	OFF	ON
G	ON	OFF	OFF	ON
H	OFF	OFF	OFF	ON
J	ON	ON	ON	OFF
K	OFF	ON	ON	OFF
L	ON	OFF	ON	OFF
M	OFF	OFF	ON	OFF
N	ON	ON	OFF	OFF
P	OFF	ON	OFF	OFF
R	ON	OFF	OFF	OFF

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CP710-A, C101180



Alternative DIP-switch settings are presented in the tables below.

Figure 4. Jumpers of the central processor CP710-A

Switch SW1 settings

Table 8. SW1 switch settings.

Switch	Setting	Meaning
1-12	OFF	Interchangeability code bit 3, read by software. See ICC code settings below.
2-11	OFF	Interchangeability code bit 2, read by software. See ICC code settings below.
3-10	OFF	Interchangeability code bit 1, read by software. See ICC code settings below.
4-9	OFF	Interchangeability code bit 0, read by software. See ICC code settings below.
5-8	OFF	Processor speedstep mode OFF = High performance mode. Default.
6-7	OFF	Message Bus Interface (MBIF) usage OFF = MBIF in use. Default ON = MBIF not in use

Interchangeability code settings (SW1)

Table 9. Interchangeability (ICC) code settings (SW1).

ICC code	Switch setting			
	Switch 1-12 (MSB)	Switch 2-11	Switch 3-10	Switch 4-9 (LSB)
A	OFF	OFF	OFF	OFF
B	OFF	OFF	OFF	ON
C	OFF	OFF	ON	OFF
D	OFF	OFF	ON	ON
E	OFF	ON	OFF	OFF
F	OFF	ON	OFF	ON
G	OFF	ON	ON	OFF
H	OFF	ON	ON	ON
J	ON	OFF	OFF	OFF
K	ON	OFF	OFF	ON
L	ON	OFF	ON	OFF

Table 9. Interchangeability (ICC) code settings (SW1). (Continued)

ICC code	Switch setting			
M	ON	OFF	ON	ON
N	ON	ON	OFF	OFF
P	ON	ON	OFF	ON
R	ON	ON	ON	OFF

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ET2A C08781

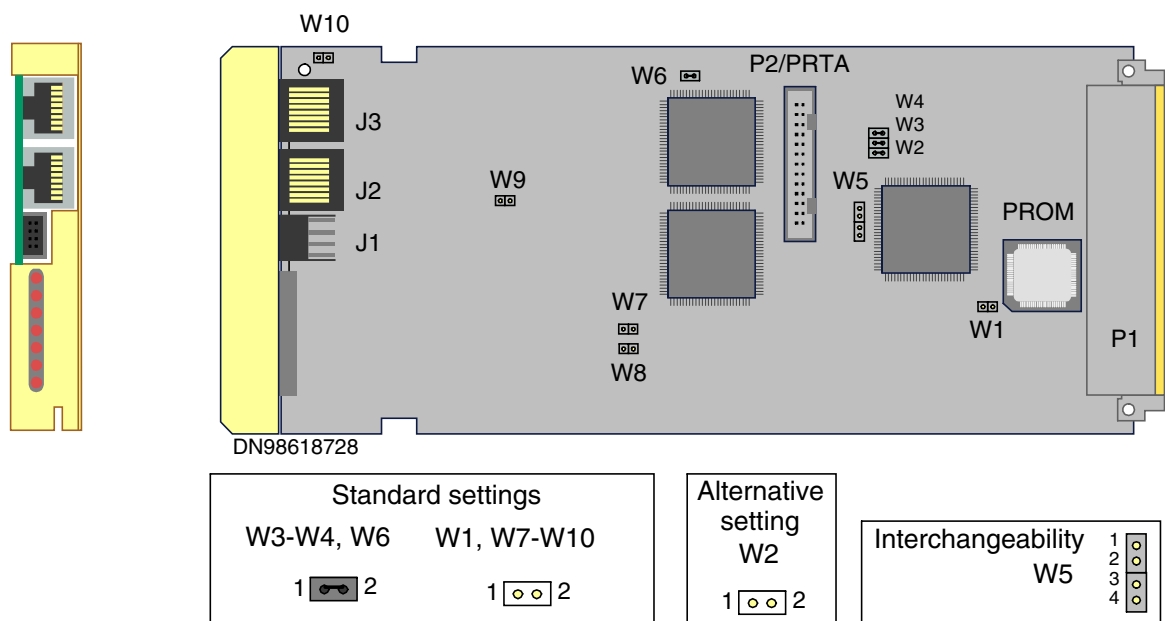


Figure 5. Jumper groups and EPROM circuits of plug-in unit ET2A.

Standard settings are presented in the table below, and the jumpers W1 through W10 must be set as shown in normal operation.

There are no alternative settings available on the plug-in unit.

Jumper group W5 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Standard settings (W1-W4, W6-W10)

Table 10. Standard settings of ET2A (W1-W4, W6-W10).

Jumper	Setting	Meaning
W1;1 - 2	OFF	Clock oscillator of processor connected (for testing)
W2;1 - 2	ON	Jumper readable by program; not in use
W3;1 - 2	ON	Clock oscillator for processor connected (for testing)
W4;1 - 2	ON	Processor RESET signal connected (for testing)
W6;1 - 2	ON	6.176 MHz signal connected to interface circuit
W7;1 - 2	OFF	-5 V (pin 2) and ground (pin 1) for testing
W8;1 - 2	OFF	+5 V (pin 1) and ground (pin 2) for testing
W9;1 - 2	OFF	Ground potential for testing
W10;1 - 2	OFF	Overvoltage ground of T1 interface separated from digital ground

Interchangeability code settings (W5)

The currently valid interchangeability code is set at the factory.

Table 11. Interchangeability code settings of ET2A (W5)

ICC code	W5 setting	
	1 - 2	3 - 4
A, E, J or N	ON	ON
B, F, K or P	OFF	ON
C, G, L or R	ON	OFF
D, H, M or S	OFF	OFF

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ET2E-S C08901

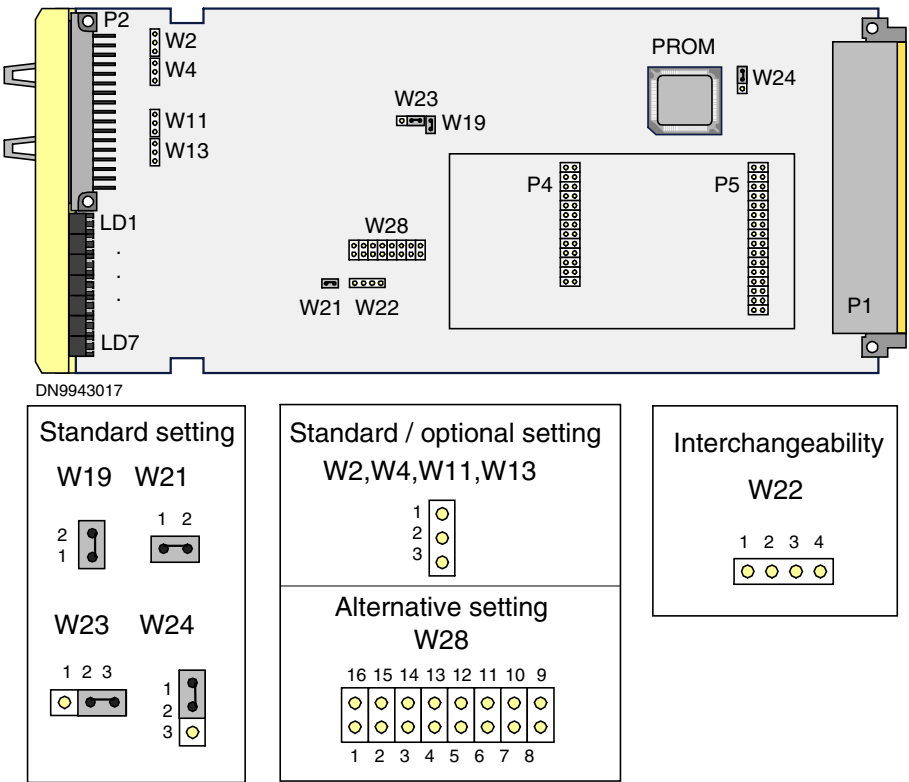


Figure 6. Jumpers and EPROM circuits of plug-in unit ET2E-S.

Standard settings are presented in the table below, and the jumpers W2, W4, W11, W13, W19, W21, W23, and W24 must be set as shown during normal operation.

Optional settings for grounding the cable sheaths (W2, W4, W11 and W13) are available for some applications if required.

Alternative settings are presented in the table below, and the setting of jumpers on W28 depends on the application. All the setting alternatives are presented.

Jumper group W22 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

A daughter board ET2DV (C 23200), implementing the V.11 interface of the ET2E and the through-connection of Sa bits, can be equipped to connectors P4 and P5 located on the plug-in unit.

Standard settings

The standard jumper settings for ET2E-S are described in the table below.

Table 12. Standard settings of ET2E-S.

Jumper	Setting	Meaning
W2;1 - 2	OFF	The plug-in unit ground is not connected with ET interface cables in incoming or outgoing transmission direction. These are default settings, but in some applications these can be changed if required; See <i>Optional grounding settings</i> below.
W2;2 - 3	OFF	
W4;1 - 2	OFF	
W4;2 - 3	OFF	
W11;1 - 2	OFF	
W11;2 - 3	OFF	
W13;1 - 2	OFF	
W13;2 - 3	OFF	
W19;1 - 2	ON	20 MHz clock connected to CPU
W21;1 - 2	ON	Watchdog reset enabled
W23;2 - 3	ON	256 kbyte of RAM memory selected for code area
W24;1 - 2	ON	Timer 0 / 1.25 MHz clock

Alternative settings

Table 13. Alternative settings of ET2E-S.

Meaning	Jumper	Setting
<i>Selection of line code</i>		
Double frame mode on circuit 0	W28;1 - 16	ON
CRC4 mode on circuit 0 (Default setting)	W28;1 - 16	OFF
Double frame mode on circuit 1	W28;2 - 15	ON
CRC4 mode on circuit 1 (Default setting)	W28;2 - 15	OFF
<i>Selection of function mode (program can bypass the settings concerned)</i>		

Table 13. Alternative settings of ET2E-S. (Continued)

Meaning	Jumper	Setting
ET2E in transparent mode	W28;4 - 13 W28;5 - 12	OFF OFF
ET2E in through-connection mode of Sa bits	W28;4 - 13 W28;5 - 12	ON OFF
ET2E in normal mose (Default setting)	W28;4 - 13 W28;5 - 12	OFF ON
ET2E in V.11 mode	W28;4 - 13 W28;5 - 12	ON ON
Bits 5 and 6 of the odd T0 in use (Default setting)	W28;3 - 14	ON
Bits 5, 6, 7 and 8 of the odd T0 in use	W28;3 - 14	OFF
<i>Supervision of the far-end error ratio (10e-6); T0/B4-bit</i>		
An alarm is sent to the far end	W28;7 - 10	ON
No alarm is sent to the far end (Default setting)	W28;7 - 10	OFF
<i>Transmission redundancy</i>		
Transmission redundancy in use	W28;8 - 9	ON
Transmission redundancy not in use (Default setting)	W28;8 - 9	OFF

Optional grounding settings

Table 14. Optional settings for grounding the cable sheaths of ET2E-S (W2, W4, W11 and W13).

Jumper group	Settin	Selection criteria	Notes
W2;ALL	OFF	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is separated from the plug-in unit ground	Default 1)
W2;2 - 3	ON	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is galvanically connected to the plug-in unit ground	1)
W2;1 - 2	ON	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is capacitively connected to the plug-in unit ground	1)
W4; ALL	OFF	The cable sheath of the PCM trunk circuit 0 in the incoming direction is separated from the plug-in unit ground	Default 1)

Table 14. Optional settings for grounding the cable sheaths of ET2E-S (W2, W4, W11 and W13). (Continued)

Jumper group	Setting	Selection criteria	Notes
W4;2 - 3	ON	The cable sheath of the PCM trunk circuit 0 in the incoming direction is galvanically connected to the plug-in unit ground	1)
W4;1 - 2	1-2	The cable sheath of the PCM trunk circuit 0 in the incoming direction is capacitively connected to the plug-in unit ground	1)
W11;ALL	OFF	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is separated from the plug-in unit ground	Default 1)
W11;2 - 3	ON	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is galvanically connected to the plug-in unit ground	1)
W11;1 - 2	ON	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is capacitively connected to the plug-in unit ground	1)
W13;ALL	OFF	The cable sheath of the PCM trunk circuit 1 in the incoming direction is separated from the plug-in unit ground	Default 1)
W13;2 - 3	ON	The cable sheath of the PCM trunk circuit 1 in the incoming direction is galvanically connected to the plug-in unit ground	1)
W13;1 - 2	ON	The cable sheath of the PCM trunk circuit 1 in the incoming direction is capacitively connected to the plug-in unit ground	1)

1) The default settings that are factory settings (no jumpers set) are normally applicable (M92 and M98 mechanics) as shown in *Standard settings* above. In the i-series network elements (M98) these settings have no meaning. In those arrangements where PCM trunk cables are brought to the front panels of ET units (e.g. BSC, BSC2, BSC3i and TCSM2 applications) these settings can be changed if required. More information on grounding alternatives is available in the document: *Grounding of External Interface Cables*.

Interchangeability code settings

Table 15. Interchangeability code settings of ET2E-S (W22).

ICC code	W22 setting	
	1 - 2	3 - 4
A, E, J or N	ON	ON
B, F, K or P	OFF	ON
C, G, L or R	ON	OFF
D, H, M or S	OFF	OFF

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ET2E-SC C08902

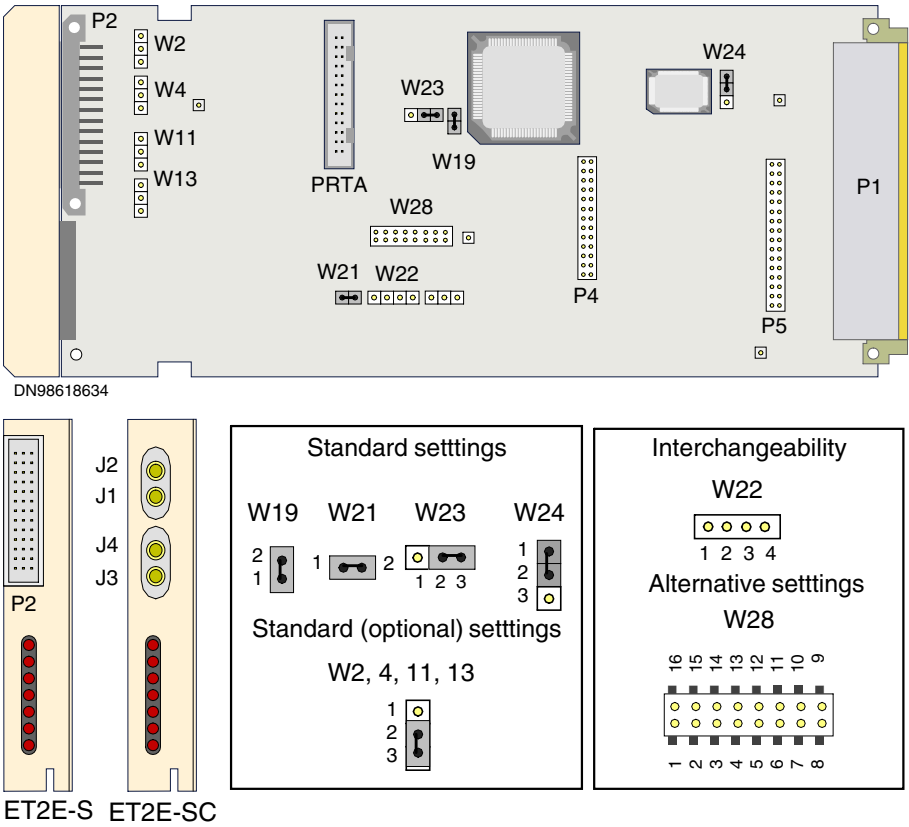


Figure 7. Jumpers and EPROM circuits of plug-in unit ET2E-SC.

Standard settings are presented in the table below, and the jumpers W2, W4, W11, W13, W19, W21, W23, and W24 must be set as shown in normal operation.

Alternative settings are presented in the table below, and the setting of jumpers on W28 depends on the application. All the setting alternatives are presented.

Jumper group W22 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

A daughter board ET2DV (C 23200), implementing the V.11 interface of the ET2E and the through-connection of Sa bits, can be equipped to connectors P4 and P5 located on the plug-in unit.

Standard settings (W2, W3, W11, W12, W19, W21, W23, W24)

Table 16. Standard settings of ET2E-SC (W2, W3, W11, W12, W19, W21, W23, W24).

Jumper	Setting	Meaning
W2;2 - 3	ON	The plug-in unit ground is connected galvanically to the cable sheath of the external interface cables in incoming and outgoing direction 1)
W4;2 - 3	ON	
W11;2 - 3	ON	
W13;2 - 3	ON	
W19;1 - 2	ON	20 MHz clock connected to CPU
W21;1 - 2	ON	Watchdog reset enabled
W23;2 - 3	ON	256 kbyte of RAM memory selected for code area
W24;1 - 2	ON	Timer 0 / 1.25 MHz clock

1) In the M92 and M98 network elements these are default settings, but in some applications these can be changed if required; see *Optional grounding settings* below.

Alternative settings (W28)

Table 17. Alternative settings of ET2E-SC (W28).

Meaning	Jumper	Setting
<i>Selection of line code:</i>		
Double frame mode on circuit 0	W28;1 - 16	ON
CRC4 mode on circuit 0 (Default setting)	W28;1 - 16	OFF
Double frame mode on circuit 1	W28;2 - 15	ON
CRC4 mode on circuit 1 (Default setting)	W28;2 - 15	OFF
<i>Selection of function mode (program can bypass the settings concerned):</i>		
ET2E in transparent mode	W28;4 - 13	OFF
	W28;5 - 12	OFF
ET2E in through-connection mode of Sa bits	W28;4 - 13	ON
	W28;5 - 12	OFF

Table 17. Alternative settings of ET2E-SC (W28). (Continued)

Meaning	Jumper	Setting
ET2E in normal mode (Default setting)	W28;4 - 13 W28;5 - 12	OFF ON
ET2E in V.11 mode:	W28;4 - 13 W28;5 - 12	ON ON
1) Bits 5 and 6 of the odd T0 in use (Default setting)	W28;3 - 14	ON
2) Bits 5, 6, 7 and 8 of the odd T0 in use	W28;3 - 14	OFF
<i>Supervision of the far-end error ratio (10e-6); T0/B4-bit:</i>		
An alarm is sent to the far end	W28;7 - 10	ON
No alarm is sent to the far end (Default setting)	W28;7 - 10	OFF
<i>Transmission redundancy:</i>		
Transmission redundancy in use	W28;8 - 9	ON
Transmission redundancy not in use (Default setting)	W28;8 - 9	OFF

Optional grounding settings (W2, W4, W11, W13)

Table 18. Optional settings for grounding the cable sheaths of ET2E-SC (W2, W4, W11 and W13).

Jumper group	Setting	Selection criteria	Notes
W2;ALL	OFF	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is separated from the plug-in unit ground	1)
W2;2 - 3	ON	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is galvanically connected to the plug-in unit ground	Default 1)
W2;1 - 2	ON	The cable sheath of the PCM trunk circuit 0 in the outgoing direction is capacitively connected to the plug-in unit ground	1)
W4; ALL	OFF	The cable sheath of the PCM trunk circuit 0 in the incoming direction is separated from the plug-in unit ground	1)
W4;2 - 3	ON	The cable sheath of the PCM trunk circuit 0 in the incoming direction is galvanically connected to the plug-in unit ground	Default 1)
W4;1 - 2	1-2	The cable sheath of the PCM trunk circuit 0 in the incoming direction is capacitively connected to the plug-in unit ground	1)

Table 18. Optional settings for grounding the cable sheaths of ET2E-SC (W2, W4, W11 and W13). (Continued)

Jumper group	Setting	Selection criteria	Notes
W11;ALL	OFF	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is separated from the plug-in unit ground	1)
W11;2 - 3	ON	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is galvanically connected to the plug-in unit ground	Default 1)
W11;1 - 2	ON	The cable sheath of the PCM trunk circuit 1 in the outgoing direction is capacitively connected to the plug-in unit ground	1)
W13;ALL	OFF	The cable sheath of the PCM trunk circuit 1 in the incoming direction is separated from the plug-in unit ground	1)
W13;2 - 3	ON	The cable sheath of the PCM trunk circuit 1 in the incoming direction is galvanically connected to the plug-in unit ground	Default 1)
W13;1 - 2	ON	The cable sheath of the PCM trunk circuit 1 in the incoming direction is capacitively connected to the plug-in unit ground	1)

1) The default settings that are factory settings are normally applicable (M92 and M98 mechanics) as shown in *Standard settings* above. However, these settings can be changed as described in this table if required. More information on grounding alternatives is available in the document: *Grounding of External Interface Cables*.

Interchangeability code settings (W22)

Table 19. Interchangeability code settings of ET2E-SC (W22).

ICC code	W22 setting	
	1 - 2	3 - 4
A, E, J or N	ON	ON
B, F, K or P	OFF	ON
C, G, L or R	ON	OFF
D, H, M or S	OFF	OFF

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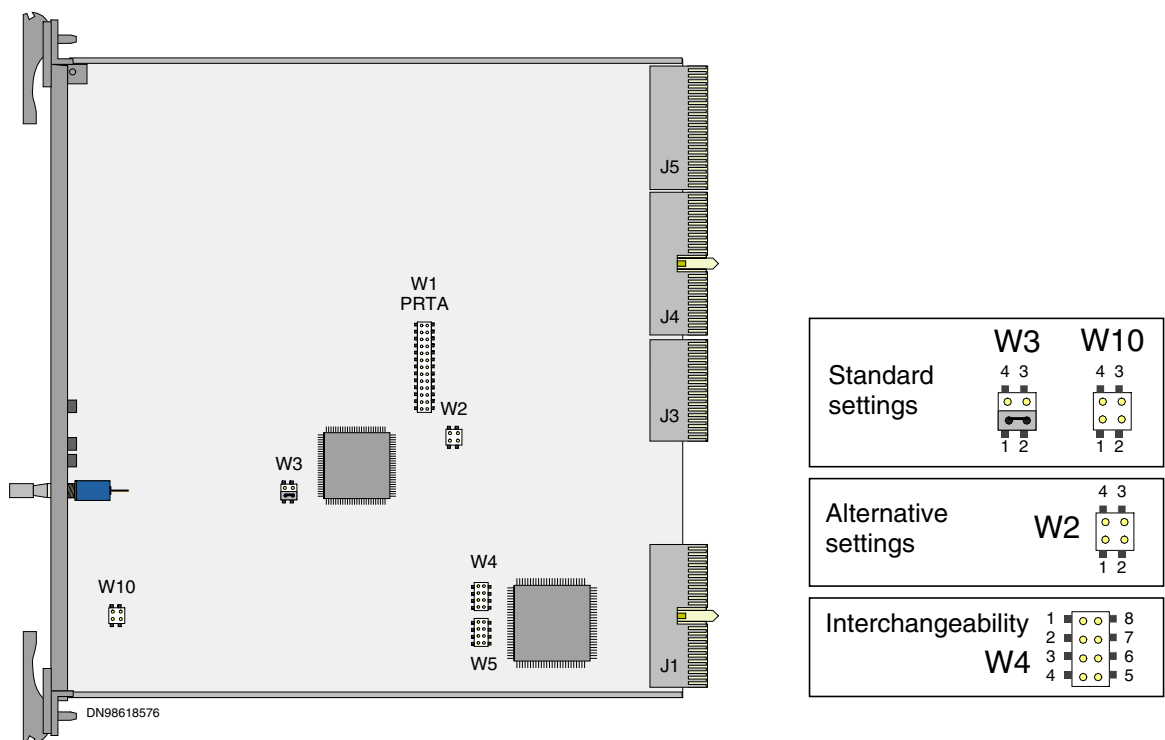


Figure 8. Jumpers of the plug-in unit HWAT-A.

Standard settings are presented in the table below, and the jumpers W3 and W10 must be set as shown during normal operation.

Alternative settings on jumper groups W2 depend on the application as presented in the table below.

Jumper group W4 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Connectors which do not require jumpers: W1 Service and testing connector PRTA, W5 Test connector for PCI bridge circuits.

Standard settings (W3, W10)

Table 20. Standard settings of HWAT-A (W3, W10).

Jumper	Setting	Meaning
W3;1 - 2	ON	Clock signal connected to CPU
W10;1 - 2	OFF	Oscillator for forced start-up counter, running
W10;3 - 4	OFF	

Alternative settings (W2)

Table 21. Alternative settings of HWAT-A (W2).

Meaning	Jumper	Setting
<i>Branching of SBUS (Supervision Bus)</i>		
SBUS is not branched Cabinets in one row; for example HLRI, Compact MSCi, SGSN, SRRi, CPS, CDS, HSS, IPT Gateway, and BSC3i	W2;2 - 3	ON
SBUS branched Cabinets in several rows; for example MSCi, Transit MSCi Cabinets in one row; for example MSS, GCS	W2;2 - 3	OFF

Interchangeability code settings (W4)

Table 22. Interchangeability code settings of HWAT-A (W4).

ICC code	W4 settings			
	1 - 8	2 - 7	3 - 6	4 - 5
A	ON	ON	ON	ON
B	OFF	ON	ON	ON
C	ON	OFF	ON	ON
D	OFF	OFF	ON	ON
E	ON	ON	OFF	ON
F	OFF	ON	OFF	ON
G	ON	OFF	OFF	ON
H	OFF	OFF	OFF	ON
J	ON	ON	ON	OFF

Table 22. Interchangeability code settings of HWAT-A (W4). (Continued)

ICC code	W4 settings			
K	OFF	ON	ON	OFF
L	ON	OFF	ON	OFF
M	OFF	OFF	ON	OFF
N	ON	ON	OFF	OFF
P	OFF	ON	OFF	OFF
R	ON	OFF	OFF	OFF
S	OFF	OFF	OFF	OFF

11 MBIF-B C74920

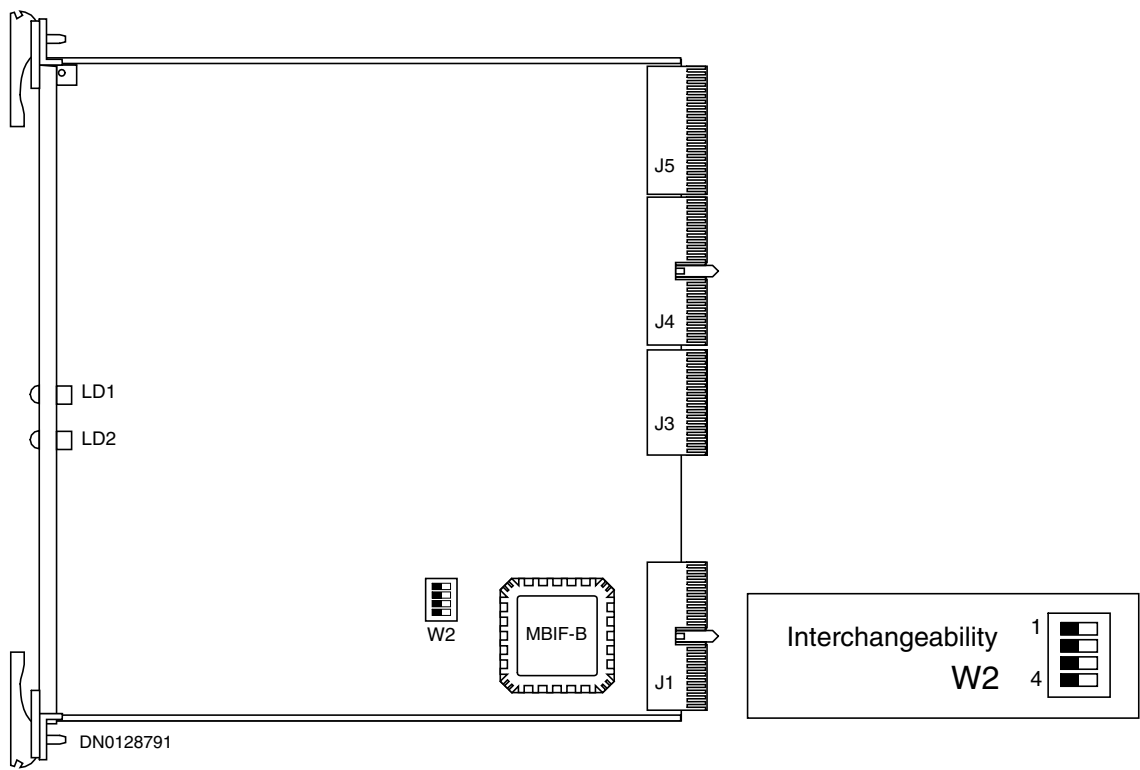


Figure 9. Switch W2, LED indicators and backplane connectors.

DIP-switch W2 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

There are no standard nor alternative settings available on the plug-in unit.

The other jumper groups are used only for tests or other special purposes.

Interchangeability code settings (W2)

Interchangeability is coded with four bits using a DIP-switch W2. Interchangeability codes are described in the table below.

Note

Switch 1 = MSB (the most significant bit) and switch 4 = LSB (the least significant bit).

OFF = the switch is on the left; ON = the switch is on the right.

Table 23. Interchangeability code settings of MBIF-B (W2).

ICC code	Switch (W2) settings			
	Switch 1 (MSB)	Switch 2	Switch 3	Switch 4 (LSB)
A	OFF	OFF	OFF	OFF
B	OFF	OFF	OFF	ON
C	OFF	OFF	ON	OFF
D	OFF	OFF	ON	ON
E	OFF	ON	OFF	OFF
F	OFF	ON	OFF	ON
G	OFF	ON	ON	OFF
H	OFF	ON	ON	ON
J	ON	OFF	OFF	OFF
K	ON	OFF	OFF	ON
L	ON	OFF	ON	OFF
M	ON	OFF	ON	ON
N	ON	ON	OFF	OFF
P	ON	ON	OFF	ON
R	ON	ON	ON	OFF
Not defined	ON	ON	ON	ON

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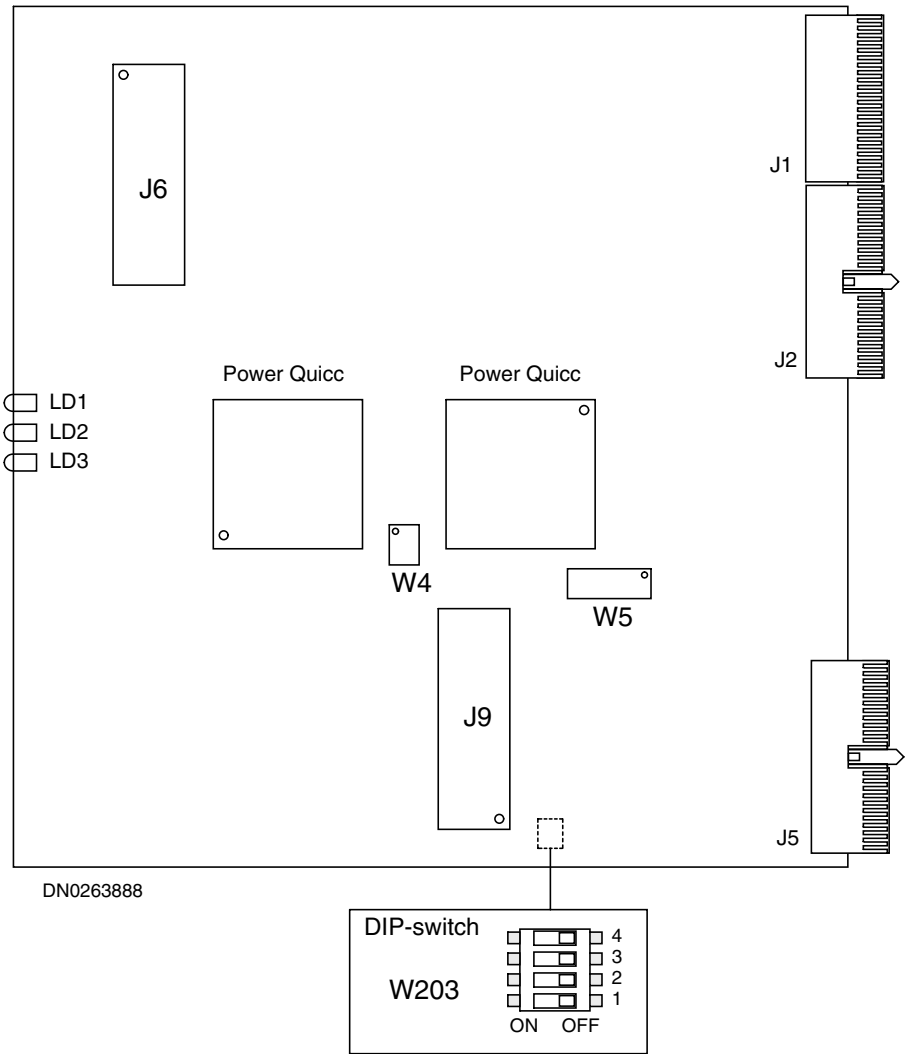


Figure 10. Jumper groups, LED indicators and the DIP-switch group of the PCU-B

Note

In the figure above, the circuit board of the PCU-B is presented primary side up. The DIP-switch group used for selecting interchangeability code (W203) is located on the secondary side of the circuit board of the PCU-B, and the location of the DIP-switch is indicated with a dash line. The detailed presentation of the DIP-switch shows the group as it is seen on the secondary side.

Standard settings (W4, W5)

Jumper groups W4 and W5 is used for testing. In normal operation, the jumper groups have no settings (see the table below).

Table 24. Standard settings of PCU-B (W4, W5).

Jumper	Setting	Meaning
W4: ALL	OFF	For testing purposes
W5: ALL	OFF	For testing purposes

Interchangeability code settings (W203)

A 4-switch DIP-switch group (W203) is used for setting the interchangeability code of the plug-in unit, when required. (see the table below).

Table 25. Setting the interchangeability code (W203).

ICC code	Jumper setting W301			
	Switch 1 (MSB)	Switch 2	Switch 3	Switch 4 (LSB)
A	OFF	OFF	OFF	OFF
B	OFF	OFF	OFF	ON
C	OFF	OFF	ON	OFF
D	OFF	OFF	ON	ON
E	OFF	ON	OFF	OFF
F	OFF	ON	OFF	ON
G	OFF	ON	ON	OFF
H	OFF	ON	ON	ON
J	ON	OFF	OFF	OFF

Table 25. Setting the interchangeability code (W203). (Continued)

ICC code	Jumper setting W301			
K	ON	OFF	OFF	ON
L	ON	OFF	ON	OFF
M	ON	OFF	ON	ON
N	ON	ON	OFF	OFF
P	ON	ON	OFF	ON
R	ON	ON	ON	OFF

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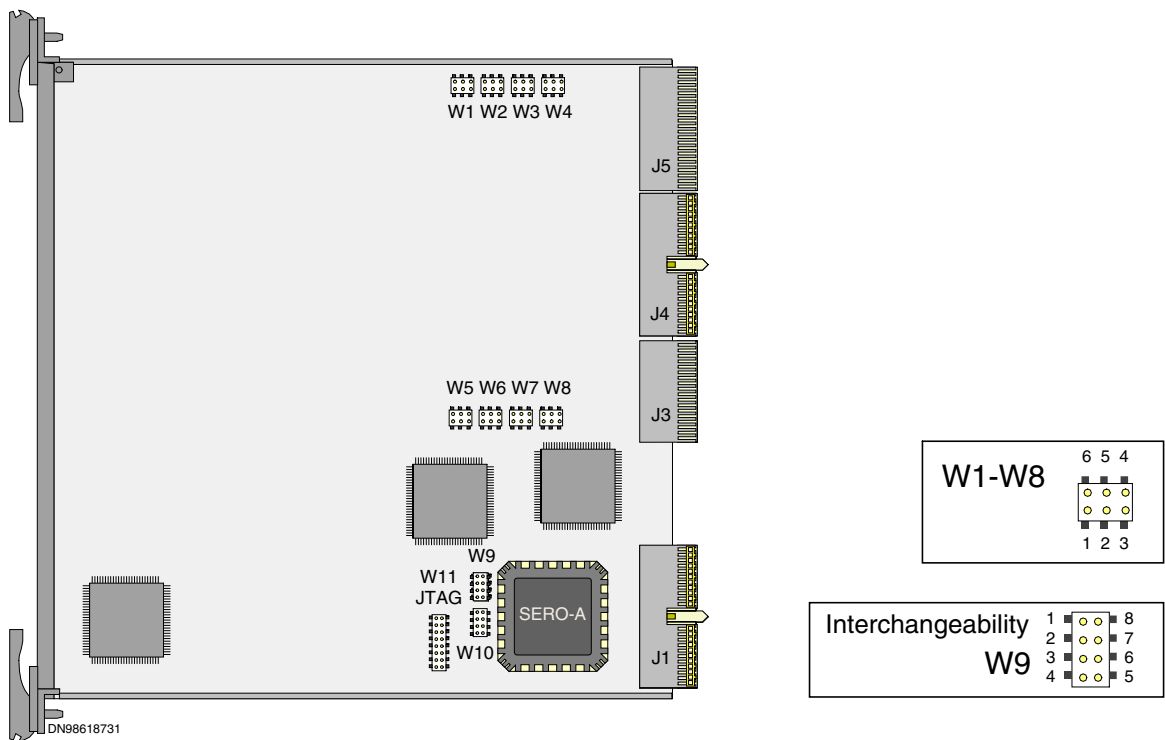


Figure 11. Jumpers of the plug-in unit SERO-A.

Alternative settings on jumper groups W1 through W8 depend on the application as presented in the table below.

Jumper group W9 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Jumper group W11 is a PRTA connector and W10 a SPI connector. No jumpers on these during normal operation.

Alternative settings

Table 26. Alternative setting of SERO-A.

Meaning	Jumper	Setting
<i>Channel 0 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W3;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W3;1 - 2	ON
<i>Channel 1 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W3;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W3;5 - 6	ON
<i>Channel 2 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W1;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W1;1 - 2	ON
<i>Channel 3 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W1;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W1;5 - 6	ON
<i>Channel 4 (V.28 or V.11)</i>		
V.28 in use: CTS (Clear To Send) control comes from an external device	W4;2 - 3	ON
V.28 in use: External CTS control not used, set to state 0 (Default setting)	W4;1 - 2	ON
V.11 in use	W4;1 - 2	ON
<i>Channel 5 (V.28 or V.11)</i>		
V.28 in use: CTS (Clear To Send) control comes from an external device	W4;4 - 5	ON
V.28 in use: External CTS control not used, set to state 0 (Default setting)	W4;5 - 6	ON
V.11 in use	W4;5 - 6	ON
<i>Channel 6 (V.28 or V.11)</i>		
V.28 in use: CTS (Clear To Send) comes from an external device	W2;2 - 3	ON
V.28 in use: External CTS not used, set to state 0 (Default setting)	W2;1 - 2	ON
V.11 in use	W2;1 - 2	ON
<i>Channel 7 (V.28 or V.11)</i>		
V.28 in use: CTS (Clear To Send) control comes from an external device	W2;4 - 5	ON
V.28 in use: External CTS control not used, set to state 0 (Default setting)	W2;5 - 6	ON

Table 26. Alternative setting of SERO-A. (Continued)

Meaning	Jumper	Setting
V.11 in use	W2;5 - 6	ON
<i>Channel 8 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W8;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W8;1 - 2	ON
<i>Channel 9 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W8;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W8;5 - 6	ON
<i>Channel 10 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W7;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W7;1 - 2	ON
<i>Channel 11 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W7;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W7;5 - 6	ON
<i>Channel 12 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W5;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W5;1 - 2	ON
<i>Channel 13 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W5;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W5;5 - 6	ON
<i>Channel 14 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W6;2 - 3	ON
External CTS control not used, set to state 0 (Default setting)	W6;1 - 2	ON
<i>Channel 15 (V.28)</i>		
CTS (Clear To Send) control comes from an external device	W6;4 - 5	ON
External CTS control not used, set to state 0 (Default setting)	W6;5 - 6	ON

Interchangeability settings (W9)

The interchangeability code selection (jumper group W9)

Table 27. Interchangeability code settings of SERO-A (W9).

ICC code	W9 settings			
	1 - 8	2 - 7	3 - 6	4 - 5
A	ON	ON	ON	ON
B	OFF	ON	ON	ON
C	ON	OFF	ON	ON
D	OFF	OFF	ON	ON
E	ON	ON	OFF	ON
F	OFF	ON	OFF	ON
G	ON	OFF	OFF	ON
H	OFF	OFF	OFF	ON
J	ON	ON	ON	OFF
K	OFF	ON	ON	OFF
L	ON	OFF	ON	OFF
M	OFF	OFF	ON	OFF
N	ON	ON	OFF	OFF
P	OFF	ON	OFF	OFF
R	ON	OFF	OFF	OFF

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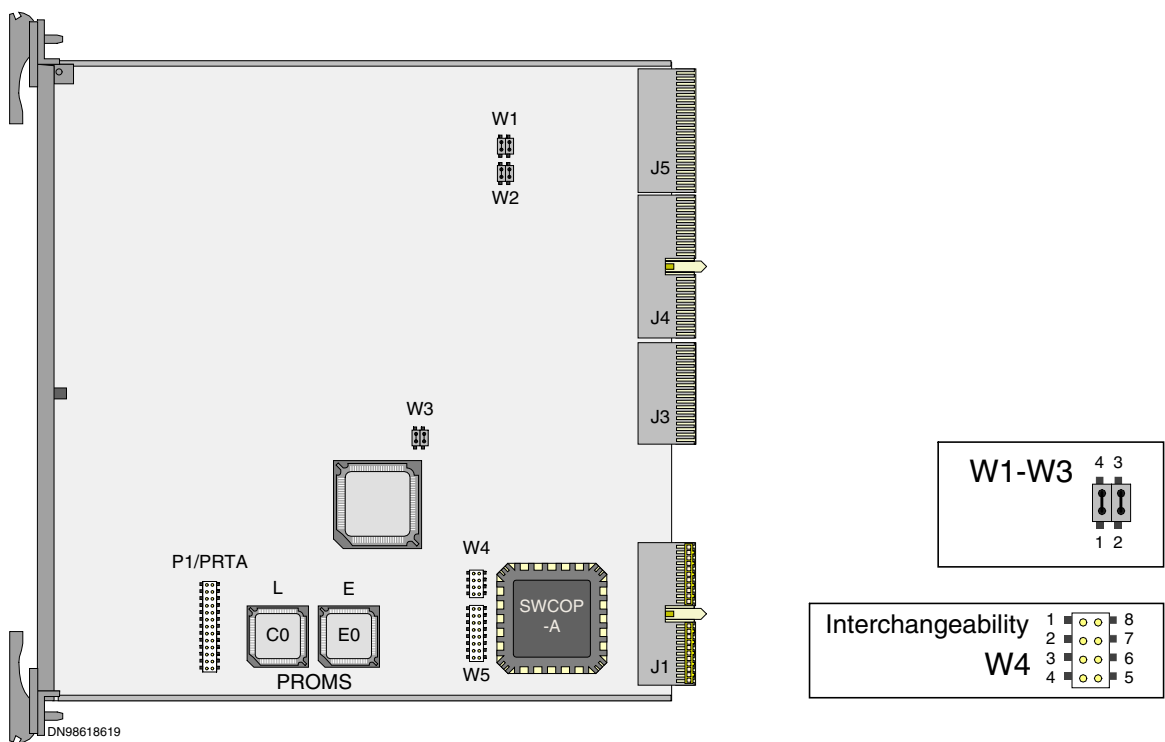


Figure 12. Jumpers and read-only memory circuits of the plug-in unit SWCOP-A.

Jumper group W5 has no settings. P1 is a test connector (PRTA) and has no settings.

The table *Standard settings* below presents the jumpers W1, W2 and W3 which must be set as shown during normal operation.

Jumper group W4 is used for setting the interchangeability code of the plug-in unit, when required, see the table below.

Standard settings (W1, W2, W3)

Table 28. Standard settings of SWCOP-A (W1, W2, W3).

Jumper	Setting	Meaning
W1;1 - 4	ON	For testing (_16 MHz clock signal for PLL circuit)
W1;2 - 3	ON	
W2;1 - 4	ON	For testing (8 MHz clock)
W2;2 - 3	ON	For testing (32 MHz clock)
W3;1 - 4	ON	For testing(16 MHz clock)
W3;2 - 3	ON	For testing (30 MHz clock signal to CPU)
		For testing (_RESET signal to CPU)

Interchangeability code settings (W4)

Table 29. Interchangeability code settings of SWCOP-A (W4).

ICC code	W4 settings			
	1 - 8	2 - 7	3 - 6	4 - 5
A	ON	ON	ON	ON
B	OFF	ON	ON	ON
C	ON	OFF	ON	ON
D	OFF	OFF	ON	ON
E	ON	ON	OFF	ON
F	OFF	ON	OFF	ON
G	ON	OFF	OFF	ON
H	OFF	OFF	OFF	ON
J	ON	ON	ON	OFF
K	OFF	ON	ON	OFF
L	ON	OFF	ON	OFF
M	OFF	OFF	ON	OFF
N	ON	ON	OFF	OFF
P	OFF	ON	OFF	OFF
R	ON	OFF	OFF	OFF

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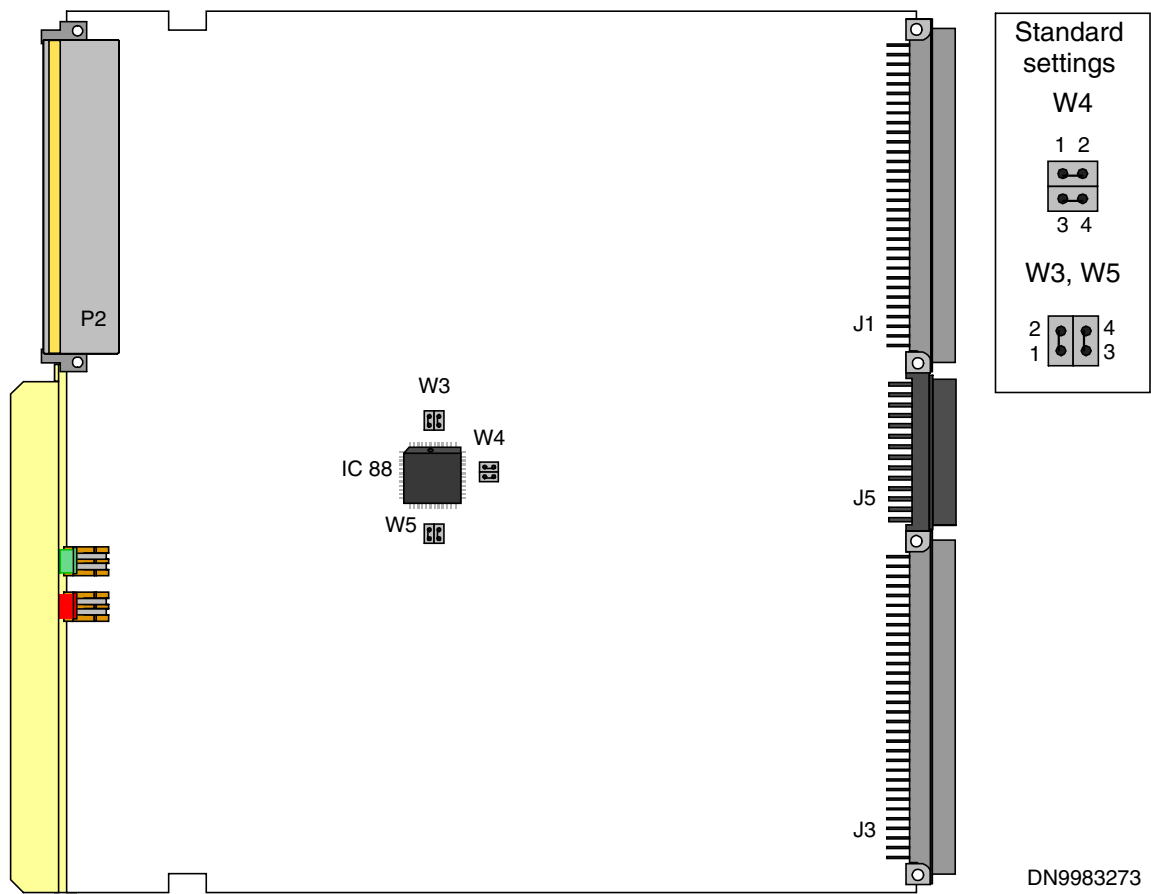


Figure 13. Jumpers of plug-in unit of SW64B.

Standard settings

Table 30. Standard settings.

Jumper group	Jumper
W3	1-2 3-4
W4	1-2 3-4
W5	1-2 3-4

16 MO91 drive P01090

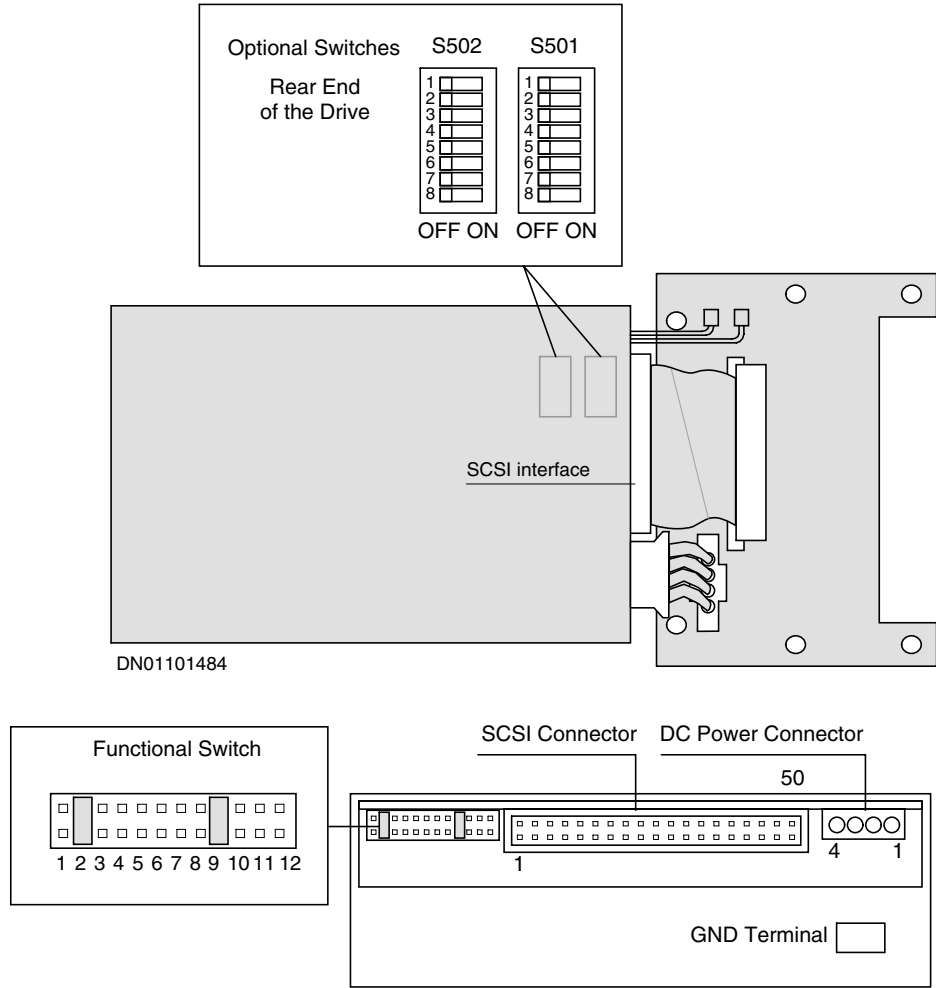


Figure 14. Jumpers of MO91 (Sony SMO-F561).

A magneto-optical unit FDU is composed of:

The MO91 (P01090) is mounted on an adapter board ODAD-A (C100640) or ODP-U-A Optical Device Plug-in Unit (C101810).

Standard settings

Table 31. Standard jumper settings of the MO91 (Functional Switch).

Jumper	Meaning	Default
1	SCSI ID2. Address = 2. Default.	OFF
2	SCSI ID1. Address = 2. Default.	ON
3	SCSI ID0. Address = 2. Default.	OFF
4	Disables SCSI parity checking. Default.	OFF
5	Disables write back caching. Default.	OFF
6	Disables auto spin-up. Default.	OFF
7	Forces verify for write command. Default.	OFF
8	Disables manual eject operation. Default.	OFF
9	Enables Ultra SCSI. Default.	ON
10	Device type. Default.	OFF
11	Enables termination. Default.	OFF
12	Terminator power. Default.	OFF

Table 32. Optional Dip Switch Assignments of the MO91.

Optional Switch	Switch number	Description	Default
S501	1	Reserved for internal use. Do not turn on.	OFF
	2	Reserved for internal use. Do not turn on.	OFF
	3	Disables Command Eject.	OFF
	4	Reserved for internal use. Do not turn on.	OFF
	5	Enable Library I/F Mode. Mode will be valid.	OFF
	6	Reserved for internal use. Do not turn on.	OFF
	7	Disables Write Cache.	OFF
	8	Disables Auto Spin-up upon cartridge loading.	OFF
S502	1	Reserved for internal use. Do not turn on.	OFF

Table 32. Optional Dip Switch Assignments of the MO91. (Continued)

Optional Switch	Switch number	Description	Default
	2	Reserved for internal use. Do not turn on.	OFF
	3	Reserved for internal use. Do not turn on.	OFF
	4	Reserved for internal use. Do not turn on.	OFF
	5	Disables SCAM selection.	OFF
	6	Reserved for internal use. Do not turn on.	OFF
	7	Enables Ultra SCSI.	OFF
	8	Enables Write Cache for Write and Verify Command	OFF

17 WDW18-S P01091, WDW36 P01096

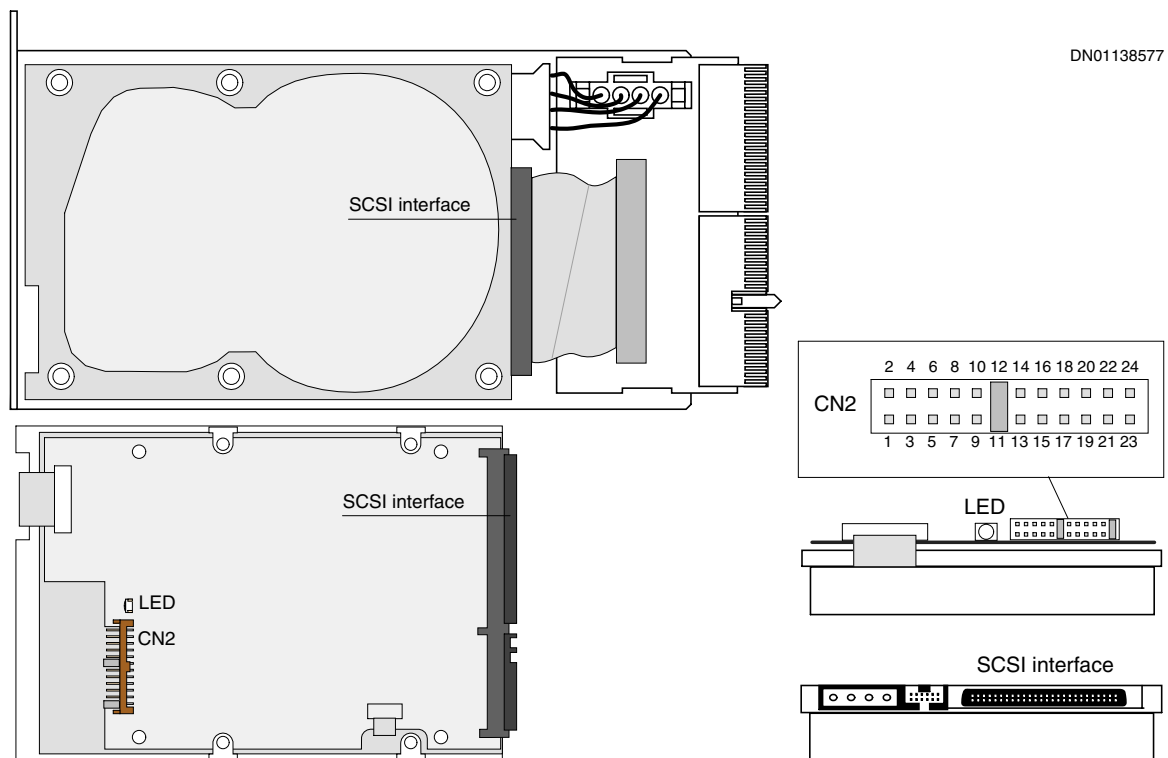


Figure 15. Jumpers of WDW18-S (Fujitsu MAN3184MP) and WDW36 (Fujitsu MAP3367NP). The cooling profile is not shown.

A hard disk drive WDU (HDU) is composed of:

The WDW18-S (P01091) or WDW36 (P01096) is mounted on an adapter board HDAD2-A (C71770) or HDP-U Hard Disk Plug-in Unit (C100720).

Standard settings of WDW18-S and WDW36 (CN2)

Table 33. Terminal settings, CN2 (WDW18-S and WDW36).

Jumper group	Jumper	Meaning
CN2 Terminal setting	1-2, OFF	(A0) SCSI address = 0. Default.
	3-4, OFF	(A1) SCSI address = 0. Default.
	5-6, OFF	(A2) SCSI address = 0. Default.
	7-8, OFF	(A3) SCSI address = 0. Default.
	9-10, OFF	Write protection, no protection. Default.
	11-12, ON	Spin-up immediately after power on. Default.
	13-14, OFF	SCSI bus width; 8/16 bit selectable. Default.
	15-16, OFF	SCSI interface operation mode (SE/LVD). Default.
	17-18, OFF	Spindle synchronization signal. Do not connect.
	19-20, OFF	IDD reset signal. Do not connect.
	21-22, OFF	Output signal connector to external LED. Do not connect.
	23-24, OFF	Terminal power supply is off. Default.