1. STANDARD CABLES

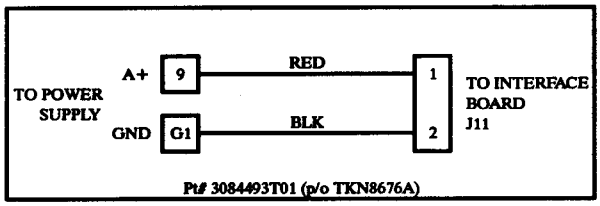


Figure 1. Power Cable

Power Cable		
Pin Number		Signal
Power Supply	J11	
9	1	A+
G1	2	GND

Table 1

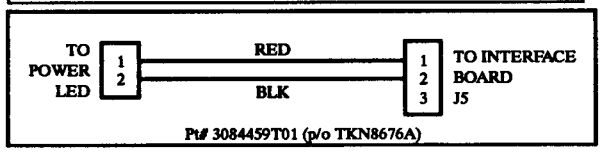


Figure 2. Power LED Cable

Power LED Cable		
Pin Number		Signal
Power LED	J5	
1	1	PWR_LED_A
2	2	PWR_LED_K
	3	NC

Table 2

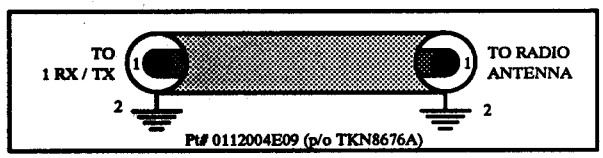


Figure 3. RF Cable

RF Coaxial Cable		
Pin Number		Signal
1 RX	Radio	
1	1	RF
2	2	ANA_GND

Table 3

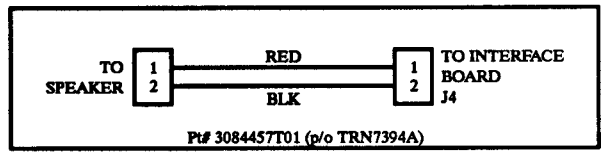


Figure 4. Speaker Cable

Speaker Cable		
Pin Number		Signal
Speaker	J4	
1	1	SPKR_HI
2	2	SPKR_LO

Table 4

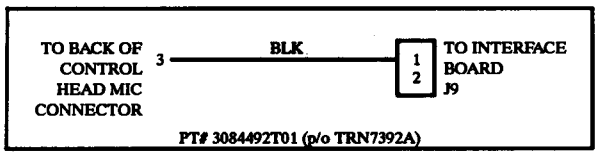


Figure 5. Monitor Cable

Monitor Cable		
Pin Number		Signal
Control Head	J9	
3	1	HUB
	2	NC

Table 5

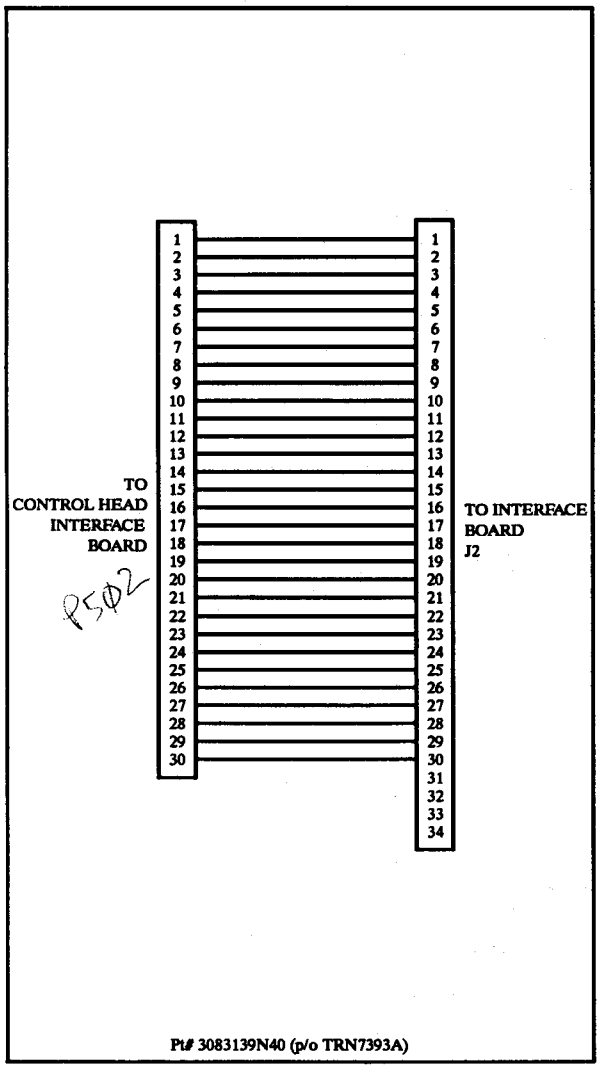


Figure 6. Control Head Cable

Control Head Cable		
Pin Number		Signal
Control Head	J2	
1	1	VSENSE1
2	2	NC
3	3	NC
4	4	VSENSE2
5	5	ANA_GND
6	6	SPARE2
7	7	SPARE1
8	8	ANA_GND
9	9	BUS+
10	10	BUS-
11	11	A+
12	12	SWB+
13	13	BUSY
14	14	SWB+
15	15	IGN
16	16	RESET
17	17	VO3
18	18	VI3
19	19	$\overline{\text{HUB}}$
20	20	VI1
21	21	VO1
22	22	VO2
23	23	SNET_KID
24	24	$\overline{\text{SNET\_WE}}$
25	25	$\overline{\text{PTT}}$
26	26	MIC_LO
27	27	SNET_KEY
28	28	HUB
29	29	NC
30	30	CTRL_HD_MIC
	31	NC
	32	NC
	33	NC
	34	NC

Table 6

$\overline{\text{Overbar}}$ denotes active low signal

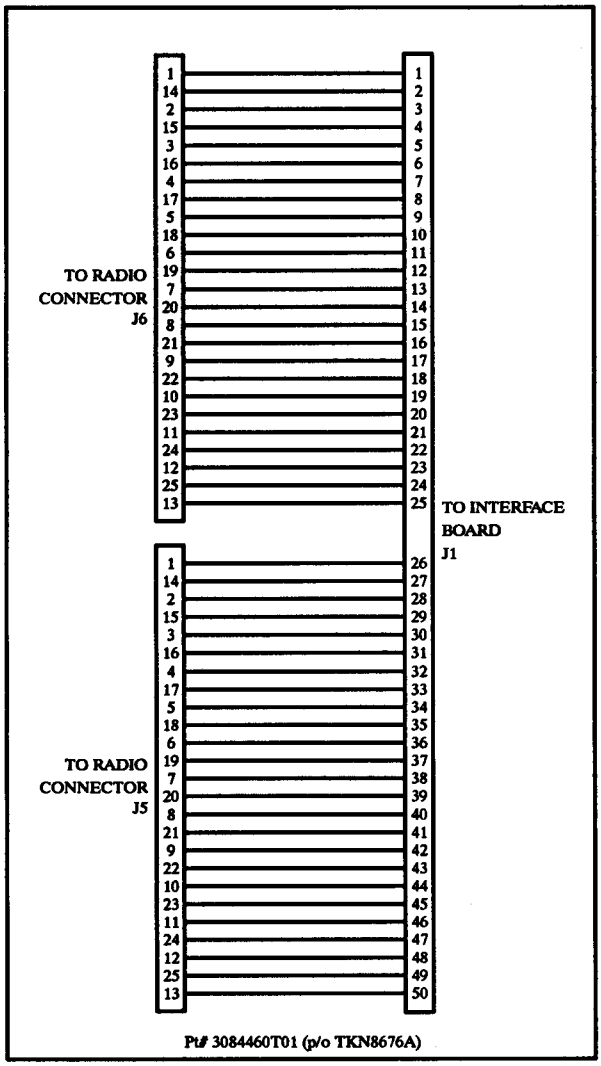


Figure 7. Radio Cable

Radio Cable			
Pin Number			Signal
Radio		J1	
J6	J5		
1		1	$\overline{\text{PTT}}$
14		2	NC
2		3	NC
15		4	RSSI
3		5	NC
16		6	$\overline{\text{SNET\_WE}}$
4		7	SNET_KEY
17		8	SNET_KID
5		9	NC
18		10	DIG_GND (NC)†
6		11	NC
19		12	NC
7		13	AUX_RX_AUD
20		14	NC
8		15	AUX_TX_AUD
21		16	NC
9		17	DET_AUD
22		18	SWB+
10		19	ANA_GND (NC)†
23		20	$\overline{\text{CHAN\_ACT}}$
11		21	MIC_LO (NC)†
24		22	SKPR_LO
12		23	RAD_MIC
25		24	SPKR_HI
13		25	NC
	1	26	NC
	14	27	BUS–
	2	28	FLT_AUD_SHL (NC)†
	15	29	RSSI
	3	30	IGN
	16	31	VO1
	4	32	NC
	17	33	VO2
	5	34	BUS+
	18	35	DIG_GND (NC)†
	6	36	FILT_AUD
	19	37	RESET
	7	38	AUX_RX_AUD
	20	39	NC
	8	40	AUX_TX_AUD
	21	41	BUS_SHLD (NC)†
	9	42	DET_AUD
	22	43	SWB+
	10	44	ANA_GND (NC)†
	23	45	BUSY
	11	46	MIC_LO (NC)†
	24	47	SPKR_LO
	12	48	RAD_MIC
	25	49	SPKR_HI
	13	50	EMER

Table 7

$\overline{\text{Overbar}}$ denotes active low signal
† Denotes TRC signal name

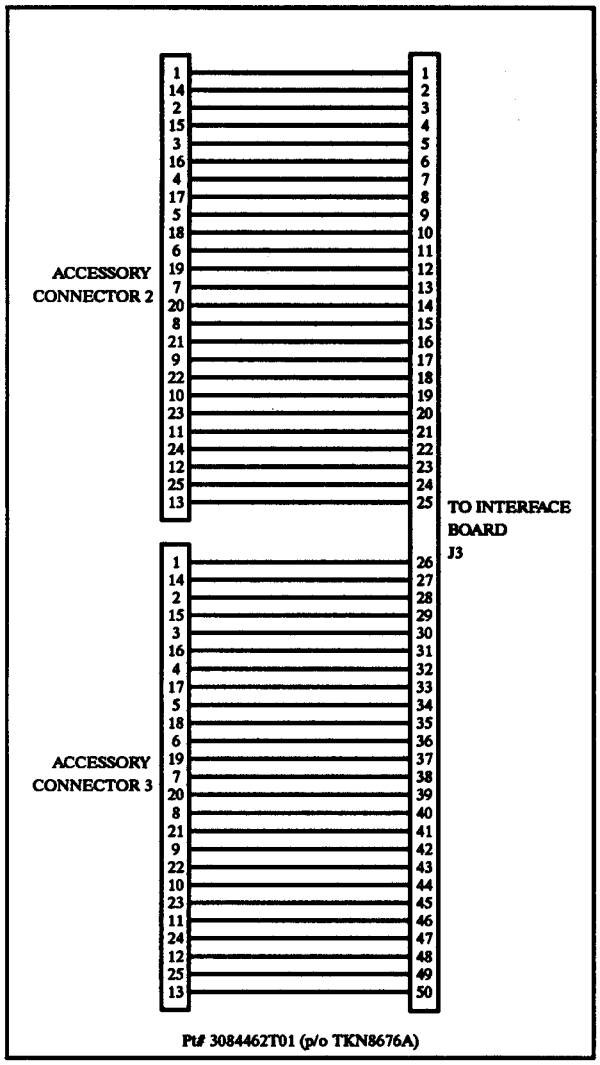


Figure 8. Accessory Cable

Accessory Cable			
Pin Number			
Accessory		J3	Signal
2	3		
1		1	RX+ (LINE1+)†
14		2	DGT_RX+ (SPKR_HI)†
2		3	AUD_SHLD
15		4	DGT_RX- (SPKR_LO)†
3		5	TX+ (LINE2+)†
16		6	RX- (LINE1-)†
4		7	MRTI_RX_AUD
17		8	MRTI_TX_AUD
5		9	A+
18		10	VO3
6		11	BUSY
19		12	BUS-
7		13	BUS+
20		14	RESET
8		15	$\overline{\text{MRTI\_AUD\_CTRL}}$
21		16	5V
9		17	VO1
22		18	VI3
10		19	$\overline{\text{PTT}}$
23		20	VO2
11		21	TX- (LINE2-)†
24		22	$\overline{\text{MONITOR}}$
12		23	DIG_GND
25		24	NC
13		25	VI1
	1	26	PS_AUD_IN
	14	27	NC
	2	28	ANA_GND
	15	29	NC
	3	30	AUX_RX_AUD
	16	31	PS_AUD_OUT
	4	32	DET_AUD
	17	33	AUX_TX_AUD
	5	34	A+
	18	35	SWB+
	6	36	BUSY
	19	37	BUS-
	7	38	BUS+
	20	39	RESET
	8	40	BUS_SHLD
	21	41	NC
	9	42	VSENSE1
	22	43	SNET_LED
	10	44	VSENSE1_SHLD
	23	45	VSENSE2
	11	46	NC
	24	47	VSENSE2_SHLD
	12	48	DIG_GND
	25	49	NC
	13	50	EMER

Table 8

$\overline{\text{Overbar}}$ denotes active low signal
† denotes TRC signal name

2. OPTIONAL CABLES

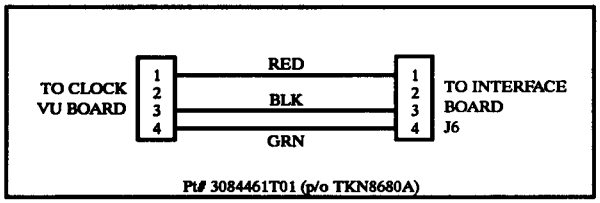


Figure 9. Clock/VU Cable

Clock/VU Cable		
Pin Number		**Signal**
Clock/VU	**J6**	
1	1	VU_AUD
2	2	NC
3	3	ANA_GND
4	4	A+

Table 9

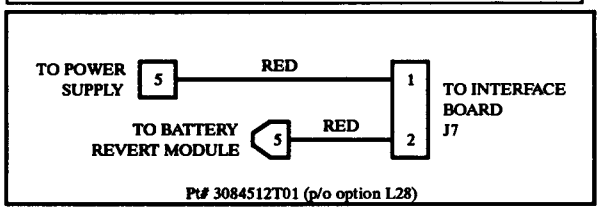


Figure 10. Battery Revert Cable

Battery Revert Sense Cable			
Pin Number			**Signal**
Power Supply	**Battery Revert**	**J7**	
5		1	SENSE
	5	2	SENSE

Table 10

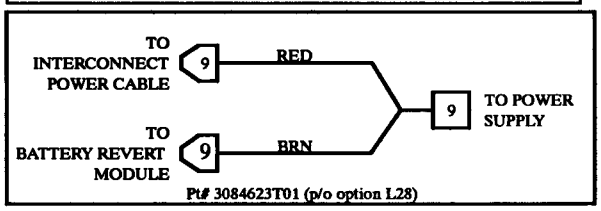


Figure 11. Battery Power Cable

Battery Power Cable			
Pin Number			**Signal**
Power Supply	**Battery Revert**	**Interface Power**	
9	9	9	A+

Table 11

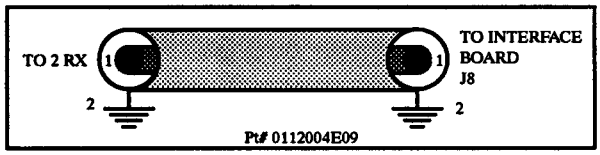


Figure 12. Discriminator Audio Cable

Discriminator Coaxial Cable		
Pin Number		**Signal**
2 RX	**J8**	
1	1	DET_AUD
2	2	ANA_GND

Table 12

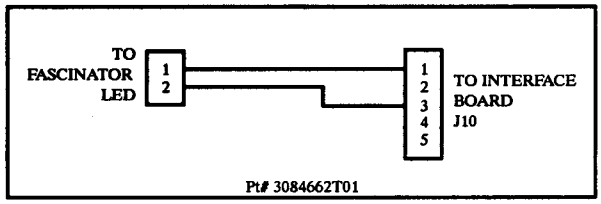


Figure 13. Fascinator LED Cable

Fascinator LED Cable		
Pin Number		**Signal**
Fascinator	**J10**	
1	1	SNET_LED_A
	2	NC
2	3	DIG_GND
	4	NC
	5	NC

Table 13

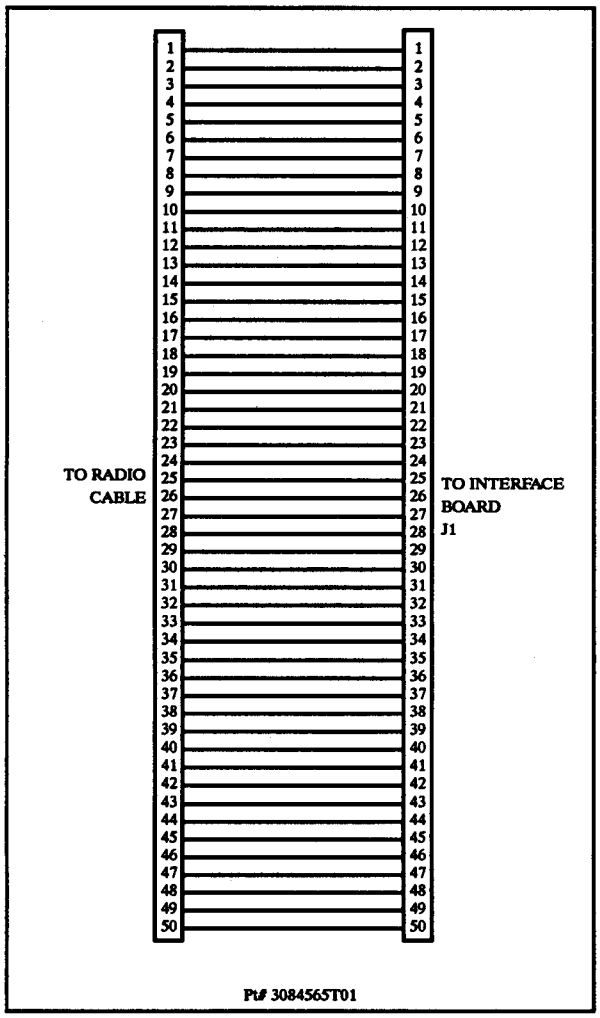


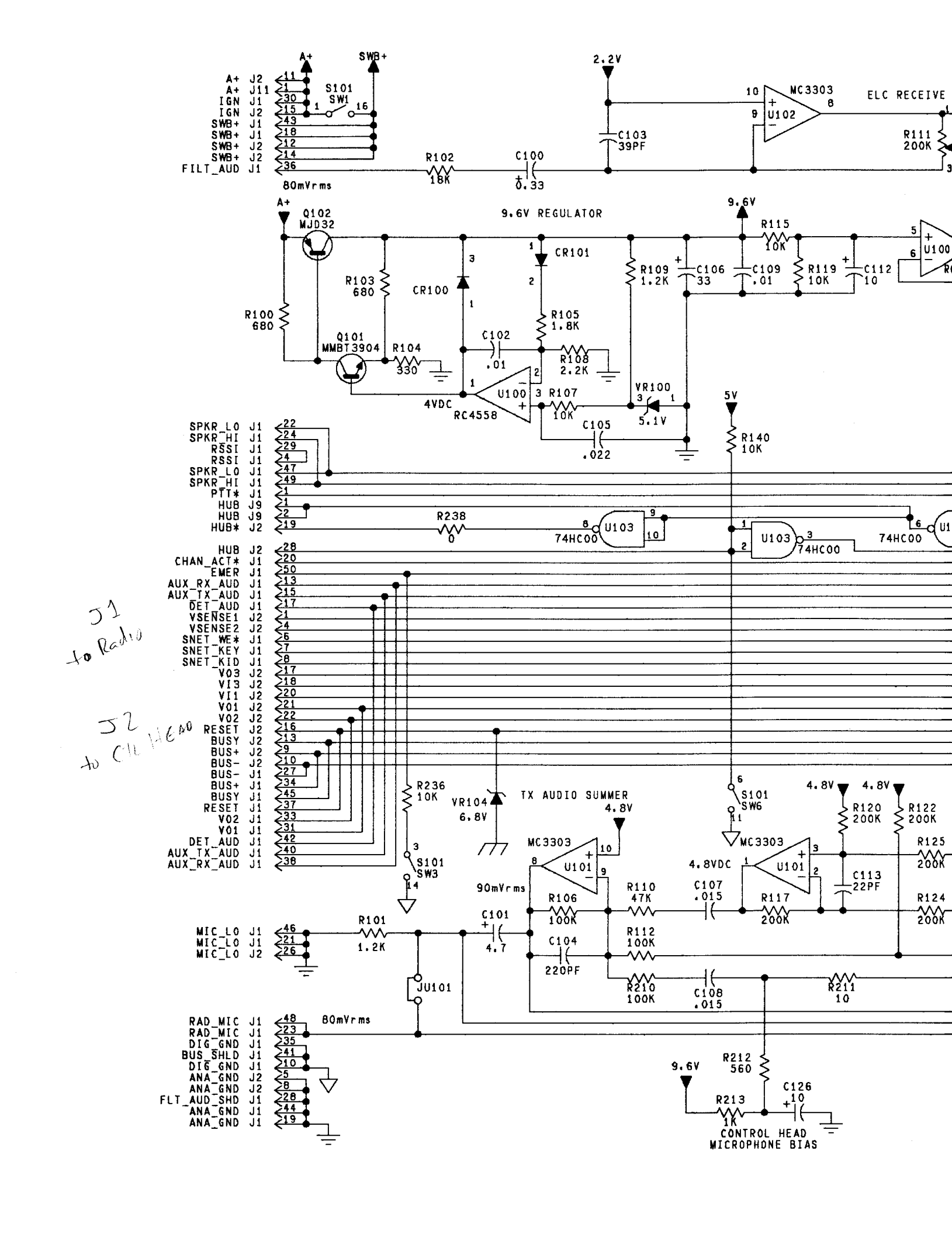
Figure 14. Service Cable

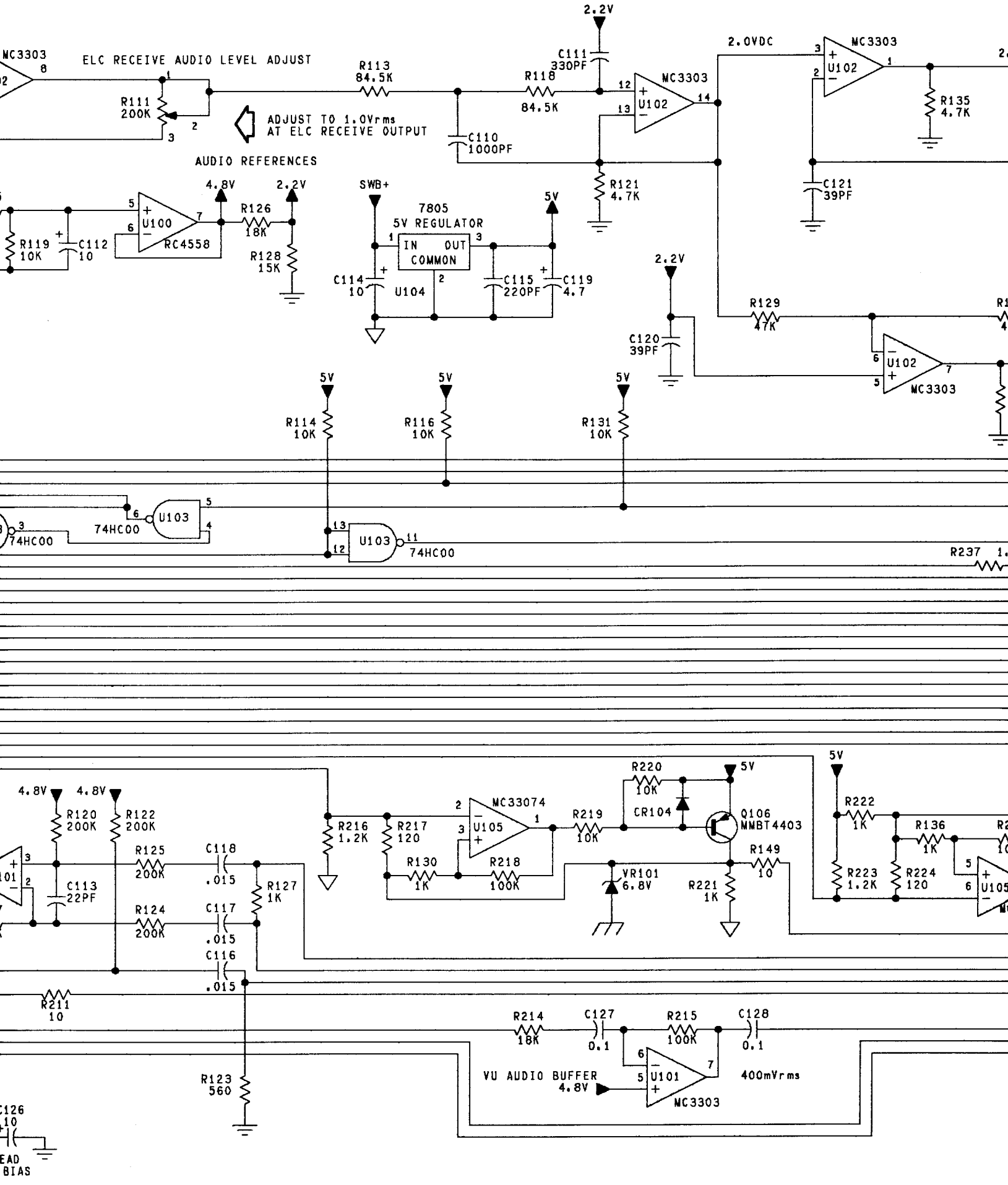
Extends Radio Cable on pg 3

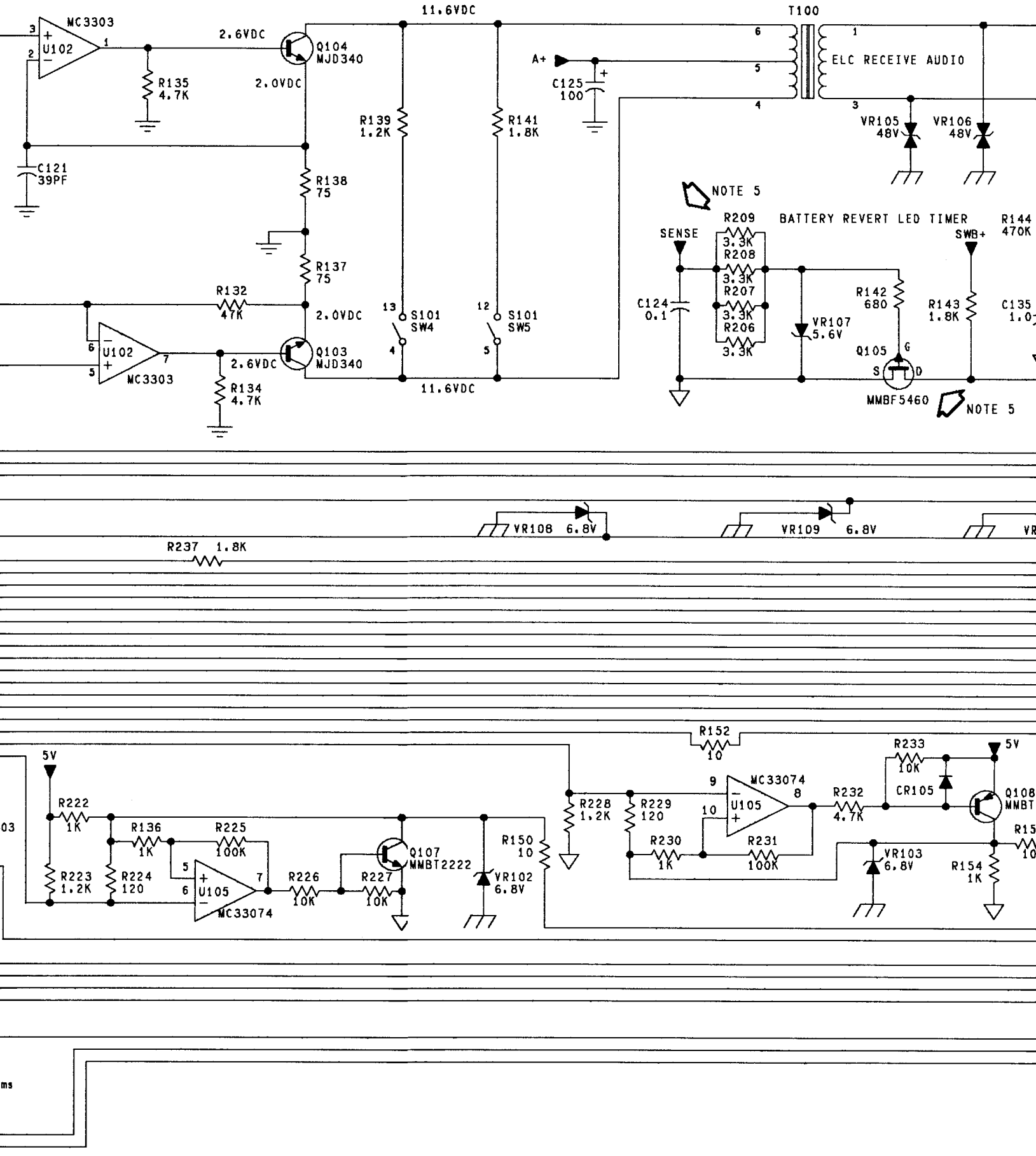
Service Cable		
Pin Number		Signal
Radio Cable	J1	
1	1	$\overline{\text{PTT}}$
2	2	NC
3	3	NC
4	4	RSSI
5	5	NC
6	6	$\overline{\text{SNET\_WE}}$
7	7	SNET_KEY
8	8	SNET_KID
9	9	NC
10	10	DIG_GND (NC)†
11	11	NC
12	12	NC
13	13	AUX_RX_AUD
14	14	NC
15	15	AUX_TX_AUD
16	16	NC
17	17	DET_AUD
18	18	SWB+
19	19	ANA_GND (NC)†
20	20	$\overline{\text{CHAN\_ACT}}$
21	21	MIC_LO (NC)†
22	22	SKPR_LO
23	23	RAD_MIC
24	24	SPKR_HI
25	25	NC
26	26	NC
27	27	BUS-
28	28	FLT_AUD_SHLD (NC)†
29	29	RSSI
30	30	IGN
31	31	VO1
32	32	NC
33	33	VO2
34	34	BUS+
35	35	DIG_GND (NC)†
36	36	FILT_AUD
37	37	RESET
38	38	AUX_RX_AUD
39	39	NC
40	40	AUX_TX_AUD
41	41	BUS_SHLD (NC)†
42	42	DET_AUD
43	43	SWB+
44	44	ANA_GND (NC)†
45	45	BUSY
46	46	MIC_LO (NC)†
47	47	SPKR_LO
48	48	RAD_MIC
49	49	SPKR_HI
50	50	EMER

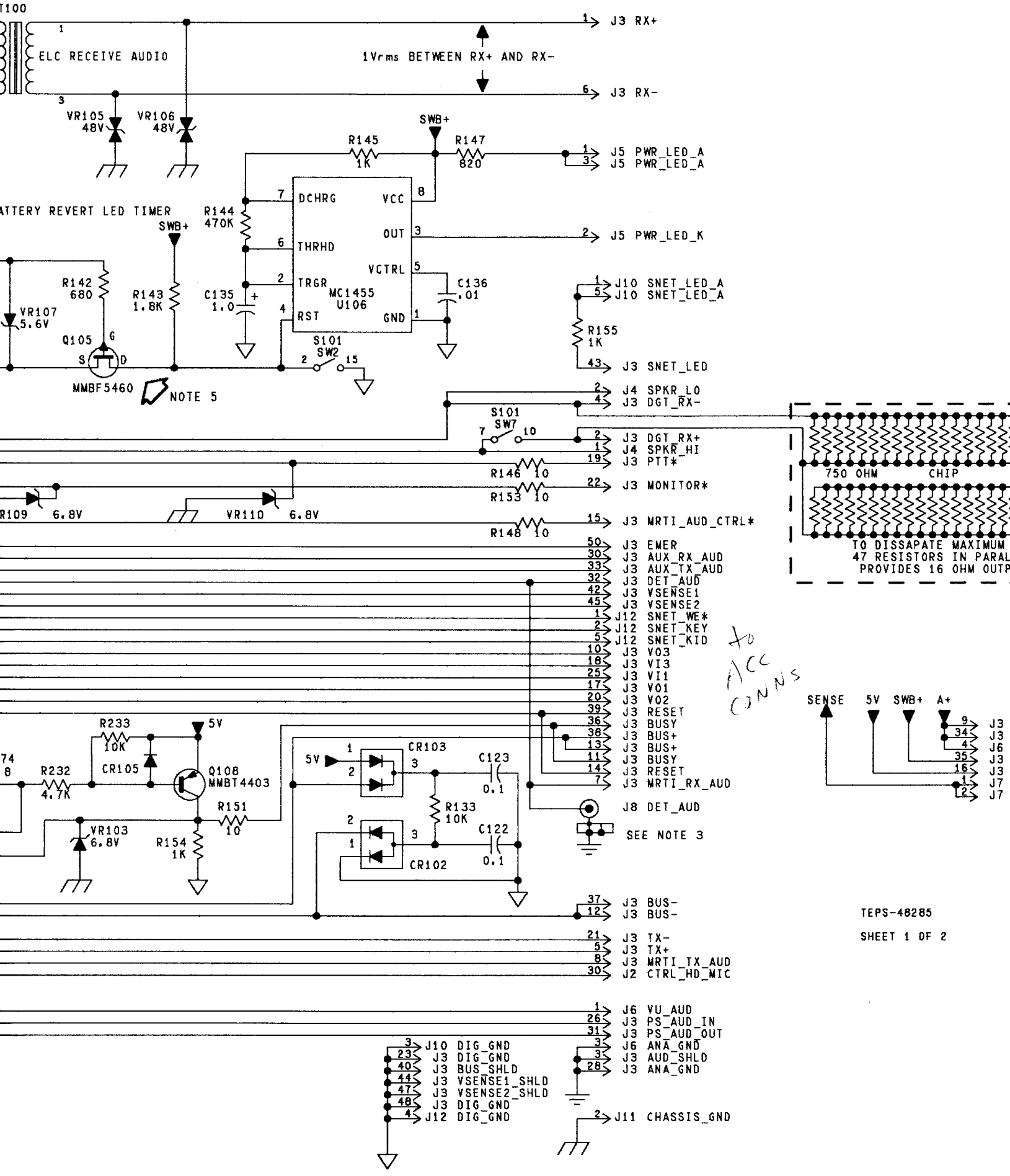
Table 14

$\overline{\text{Overbar}}$ denotes active low signal
† Denotes TRC signal


A+
SWB+
2.2V
A+ J2
A+ J11
IGN J1
IGN J2
SWB+ J1
SWB+ J1
SWB+ J2
SWB+ J2
FILT_AUD J1
S101
SW1
R102
18K
C100
0.33
80mVrms
C103
39PF
MC3303
U102
ELC RECEIVE
R111
200K
9.6V REGULATOR
Q102
MJD32
Q101
MMBT3904
R100
680
R103
680
R104
330
CR100
CR101
R105
1.8K
C102
.01
R108
2.2K
U100
4VDC
RC4558
R107
10K
VR100
5.1V
C105
.022
R109
1.2K
C106
33
C109
.01
R119
10K
C112
10
R115
10K
9.6V
5V
R140
10K
SPKR_LO J1
SPKR_HI J1
RSSI J1
RSSI J1
SPKR_LO J1
SPKR_HI J1
PTT* J1
HUB J9
HUB J9
HUB* J2
R238
0
U103
74HC00
HUB J2
CHAN_ACT* J1
EMER J1
AUX_RX_AUD J1
AUX_TX_AUD J1
DET_AUD J1
VSENSE1 J2
VSENSE2 J2
SNET_WE* J1
SNET_KEY J1
SNET_KID J1
V03 J2
VI3 J2
VI1 J2
V01 J2
V02 J2
RESET J2
BUSY J2
BUS+ J2
BUS- J2
BUS- J1
BUS+ J1
BUSY J1
RESET J1
V02 J1
V01 J1
DET_AUD J1
AUX_TX_AUD J1
AUX_RX_AUD J1
J1
to Radio
J2
to CH HEAD
R236
10K
S101
SW3
VR104
6.8V
TX AUDIO SUMMER
4.8V
MC3303
U101
90mVrms
R106
100K
R110
47K
C107
.015
R112
100K
C104
220PF
R210
100K
C108
.015
R101
1.2K
C101
4.7
JU101
MIC_LO J1
MIC_LO J1
MIC_LO J2
RAD_MIC J1
RAD_MIC J1
DIG_GND J1
BUS_SHLD J1
DIG_GND J1
ANA_GND J2
ANA_GND J2
FLT_AUD_SHD J1
ANA_GND J1
ANA_GND J1
80mVrms
S101
SW6
4.8VDC
R117
200K
C113
22PF
R120
200K
R122
200K
R125
200K
R124
200K
R211
10
R212
560
R213
1K
C126
10
9.6V
CONTROL HEAD
MICROPHONE BIAS


ELC RECEIVE AUDIO LEVEL ADJUST
R111 200K
R113 84.5K
R118 84.5K
C111 330PF
C110 1000PF
ADJUST TO 1.0Vrms AT ELC RECEIVE OUTPUT
AUDIO REFERENCES
2.2V
2.0VDC
MC3303
U102
R135 4.7K
C121 39PF
R121 4.7K
4.8V
R126 18K
R128 15K
U100
RC4558
R119 10K
C112 10
SWB+
7805
5V REGULATOR
IN OUT COMMON
U104
C114 10
C115 220PF
C119 4.7
5V
R129 47K
C120 39PF
R114 10K
R116 10K
R131 10K
74HC00
U103
R237
4.8V
R120 200K
R122 200K
R125 200K
R124 200K
C113 22PF
C118 .015
C117 .015
C116 .015
R127 1K
R211 10
R123 560
R216 1.2K
R217 120
R130 1K
R218 100K
MC33074
U105
R219 10K
R220 10K
CR104
Q106 MMBT4403
VR101 6.8V
R221 1K
R149 10
R222 1K
R223 1.2K
R136 1K
R224 120
R214 18K
C127 0.1
R215 100K
C128 0.1
VU AUDIO BUFFER
400mVrms
U101
C126 10
EAD BIAS


MC3303
U102
2.6VDC
Q104
MJD340
11.6VDC
T100
R135
4.7K
2.0VDC
R139
1.2K
R141
1.8K
A+
C125
100
ELC RECEIVE AUDIO
VR105
48V
VR106
48V
C121
39PF
R138
75
NOTE 5
R209
BATTERY REVERT LED TIMER
R144
470K
SENSE
3.3K
R208
3.3K
R207
3.3K
R206
3.3K
SWB+
R137
75
R132
47K
C124
0.1
R142
680
R143
1.8K
C135
1.0
13
S101
SW4
12
S101
SW5
VR107
5.6V
Q105
G
S
D
MMBF5460
NOTE 5
U102
MC3303
2.6VDC
Q103
MJD340
R134
4.7K
11.6VDC
VR108
6.8V
VR109
6.8V
R237
1.8K
5V
R222
1K
R136
1K
R225
100K
R223
1.2K
R224
120
U105
MC33074
R226
10K
R227
10K
Q107
MMBT2222
R150
10
VR102
6.8V
R152
10
R228
1.2K
R229
120
R230
1K
R231
100K
MC33074
U105
R232
4.7K
R233
10K
CR105
5V
Q108
MMBT
VR103
6.8V
R154
1K
R15
10


T100
ELC RECEIVE AUDIO
1Vrms BETWEEN RX+ AND RX-
J3 RX+
J3 RX-
VR105 48V
VR106 48V
SWB+
R145 1K
R147 820
J5 PWR_LED_A
J5 PWR_LED_A
ATTERY REVERT LED TIMER
R144 470K
DCHRG
VCC
OUT
THRHD
VCTRL
TRGR
RST
GND
MC1455 U106
C135 1.0
C136 .01
J5 PWR_LED_K
R142 680
R143 1.8K
VR107 5.6V
Q105
MMBF5460
NOTE 5
S101 SW2
J10 SNET_LED_A
J10 SNET_LED_A
R155 1K
J3 SNET_LED
J4 SPKR_LO
J3 DGT_RX-
S101 SW7
J3 DGT_RX+
J4 SPKR_HI
J3 PTT*
R146 10
J3 MONITOR*
R153 10
750 OHM
CHIP
TO DISSAPATE MAXIMUM
47 RESISTORS IN PARAL
PROVIDES 16 OHM OUTP
R109 6.8V
VR110 6.8V
J3 MRTI_AUD_CTRL*
R148 10
J3 EMER
J3 AUX_RX_AUD
J3 AUX_TX_AUD
J3 DET_AUD
J3 VSENSE1
J3 VSENSE2
J12 SNET_WE*
J12 SNET_KEY
J12 SNET_KID
J3 VO3
J3 VI3
J3 VI1
J3 VO1
J3 VO2
J3 RESET
J3 BUSY
J3 BUS+
J3 BUS+
J3 BUSY
J3 RESET
J3 MRTI_RX_AUD
J8 DET_AUD
SEE NOTE 3
to ACC CONNS
SENSE
5V
SWB+
A+
R233 10K
5V
CR105
Q108 MMBT4403
R232 4.7K
R151 10
VR103 6.8V
R154 1K
CR103
CR102
C123 0.1
C122 0.1
R133 10K
J3 BUS-
J3 BUS-
J3 TX-
J3 TX+
J3 MRTI_TX_AUD
J2 CTRL_HD_MIC
J6 VU_AUD
J3 PS_AUD_IN
J3 PS_AUD_OUT
J10 DIG_GND
J3 DIG_GND
J3 BUS_SHLD
J3 VSENSE1_SHLD
J3 VSENSE2_SHLD
J3 DIG_GND
J12 DIG_GND
J6 ANA_GND
J3 AUD_SHLD
J3 ANA_GND
J11 CHASSIS_GND
TEPS-48285
SHEET 1 OF 2

AUDIO INTERFACE BOARD

MODEL TRN7391A
SCHEMATIC DIAGRAM, CIRCUIT BOARD DETAIL
AND PARTS LIST

J3 RX+

J3 RX-

J5 PWR_LED_A
J5 PWR_LED_A

J5 PWR_LED_K

J10 SNET_LED_A
J10 SNET_LED_A

5

J3 SNET_LED

J4 SPKR_LO
J3 DGT_RX-

J3 DGT_RX+
J4 SPKR_HI
J3 PTT*

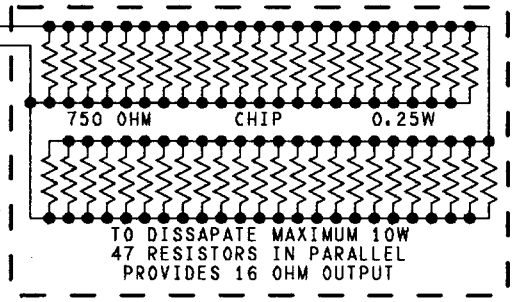
J3 MONITOR*

J3 MRTI_AUD_CTRL*

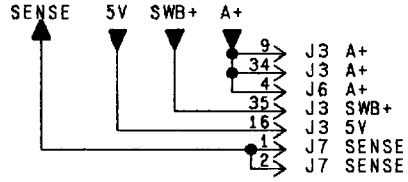
J3 EMER
J3 AUX_RX_AUD
J3 AUX_TX_AUD
J3 DET_AUD
J3 VSENSE1
J3 VSENSE2
J12 SNET_WE*
J12 SNET_KEY
J12 SNET_KID
J3 V03
J3 V13
J3 V11
J3 V01
J3 V02
J3 RESET
J3 BUSY
J3 BUS+
J3 BUS+
J3 BUSY
J3 RESET
J3 MRTI_RX_AUD

J8 DET_AUD

SEE NOTE 3



to ACC CONNS



J3 BUS-
J3 BUS-

J3 TX-
J3 TX+
J3 MRTI_TX_AUD
J2 CTRL_HD_MIC

J6 VU_AUD
J3 PS_AUD_IN
J3 PS_AUD_OUT
J6 ANA_GND
J3 AUD_SHLD
J3 ANA_GND

J11 CHASSIS_GND

TEPS-48285

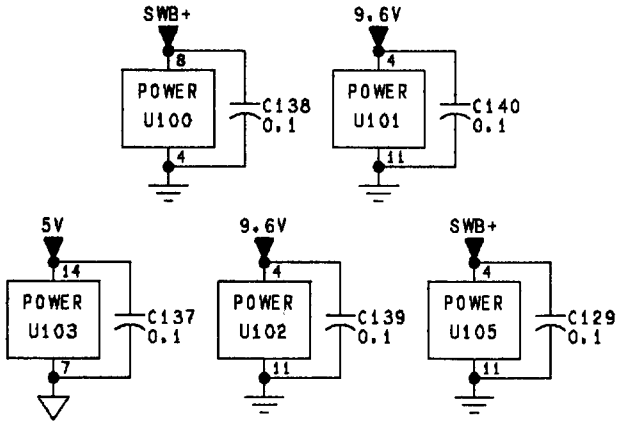
SHEET 1 OF 2

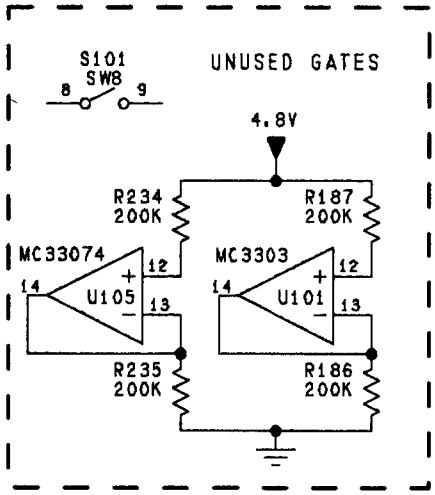
AUDIO INTERFACE BOARD

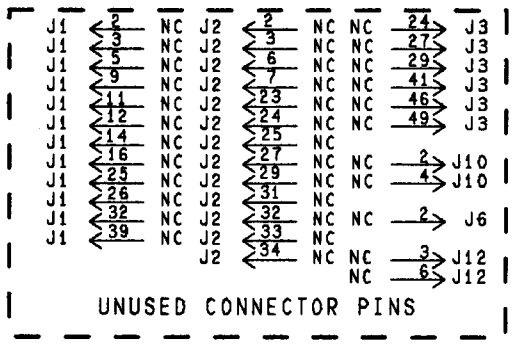
MODEL TRN7391
SCHEMATIC DIAGRAM, CIRCUIT BOARD DETAIL AND PARTS LIST

SWITCH	ON (CLOSED)
SW1	*FRONT PANEL PWR SWITCH
SW2	*STANDARD
SW3	*EMERGENCY ACTIVE HIGH
SW4	*600 OHM IMPEDANCE
SW5	900 OHM IMPEDANCE
SW6	OPTION L73
SW7	OPTION L791

SWITCH NOTES:
1. AN ASTERISK (*) INDICATES DEFA
2. SWITCHE SW8 IS UNUSED.
3. SW1, SW6, AND SW7 MUST BE ON F

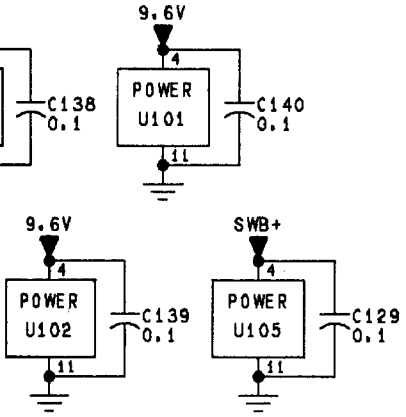






SWITCH	ON (CLOSED)	OFF (OPEN)
SW1	*FRONT PANEL PWR SWITCH INACTIVE	FRONT PANEL PWR SWITCH ACTIVE
SW2	*STANDARD	OPTION L28
SW3	*EMERGENCY ACTIVE HIGH	EMERGENCY ACTIVE LOW
SW4	*600 OHM IMPEDANCE	900 OHM IMPEDANCE
SW5	900 OHM IMPEDANCE	*600 OHM IMPEDANCE
SW6	OPTION L73	*STANDARD
SW7	OPTION L791	*STANDARD

SWITCH NOTES:
1. AN ASTERISK (*) INDICATES DEFAULT FACTORY SETTING.
2. SWITCHE SW8 IS UNUSED.
3. SW1, SW6, AND SW7 MUST BE ON FOR OPTION L791.

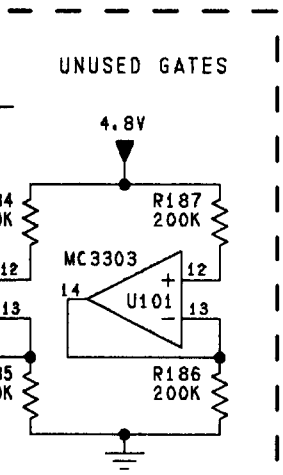


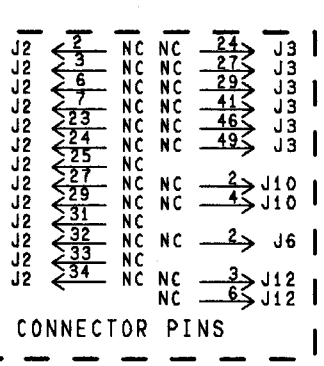
JUMPER	IN	OUT
JU101	*STANDARD	L938

JUMPER NOTES:
1. AN ASTERISK (*) INDICATES DEFAULT FACTORY SETTING.

SCHEMATIC NOTES:
1. UNLESS SPECIFIED OTHERWISE, ALL RESISTOR VALUES ARE IN OHMS AND ALL CAPACITOR VALUES ARE IN MICROFARADS.
2. AN ASTERISK (*) FOLLOWING A SIGNAL NAME INDICATES AN ACTIVE LOW SIGNAL.
3. CONTINGENCY PART.
4. ALL VOLTAGE MEASUREMENTS TAKEN USING 1kHz AT 60% FSD
5. BATTERY REVERT LED TIMER VOLTAGES:

POWER SOURCE	SENSE		Vd	
	MIN	MAX	MIN	MAX
AC	-33.0	-6.0	0.0	0.4
DC	0.45	0.60	1.0	3.0





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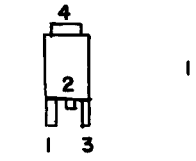
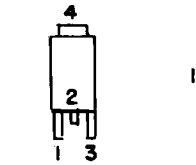
SHEET 2 OF 2

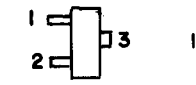
LEGEND	
	JUMPER IN
	JUMPER OUT
	COAX CONNECTOR
	DIGITAL GROUND
	ANALOG GROUND
	CHASSIS GROUND



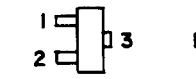
I.BASE, 2,4. COLL
Q102



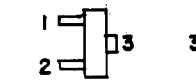
I.BASE, 2,4. COL
Q103,Q



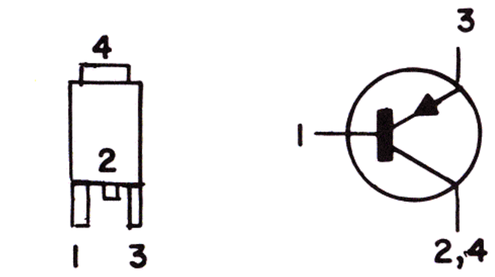
I.BASE, 2.EM
Q101 (MMBT3904



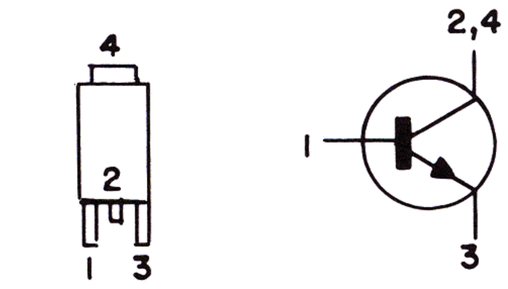
I.BASE, 2.EM
Q106,Q



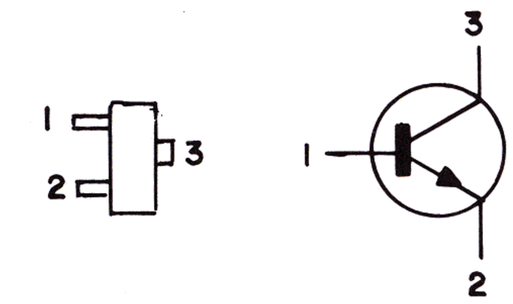
I.DRAIN, 2.
Q10



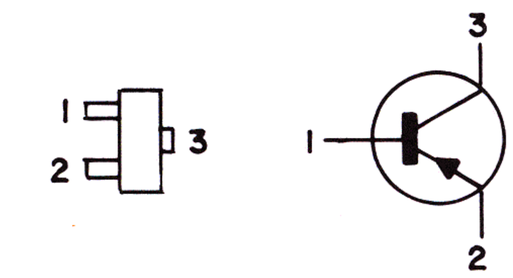
1.BASE, 2,4. COLLECTOR, 3.EMITTER
Q102 (MJD32)



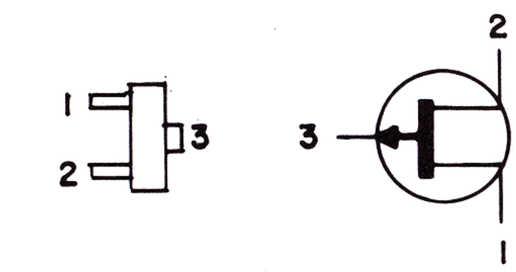
1. BASE, 2,4. COLLECTOR 3. EMITTER
Q103, Q104 (MJD340)



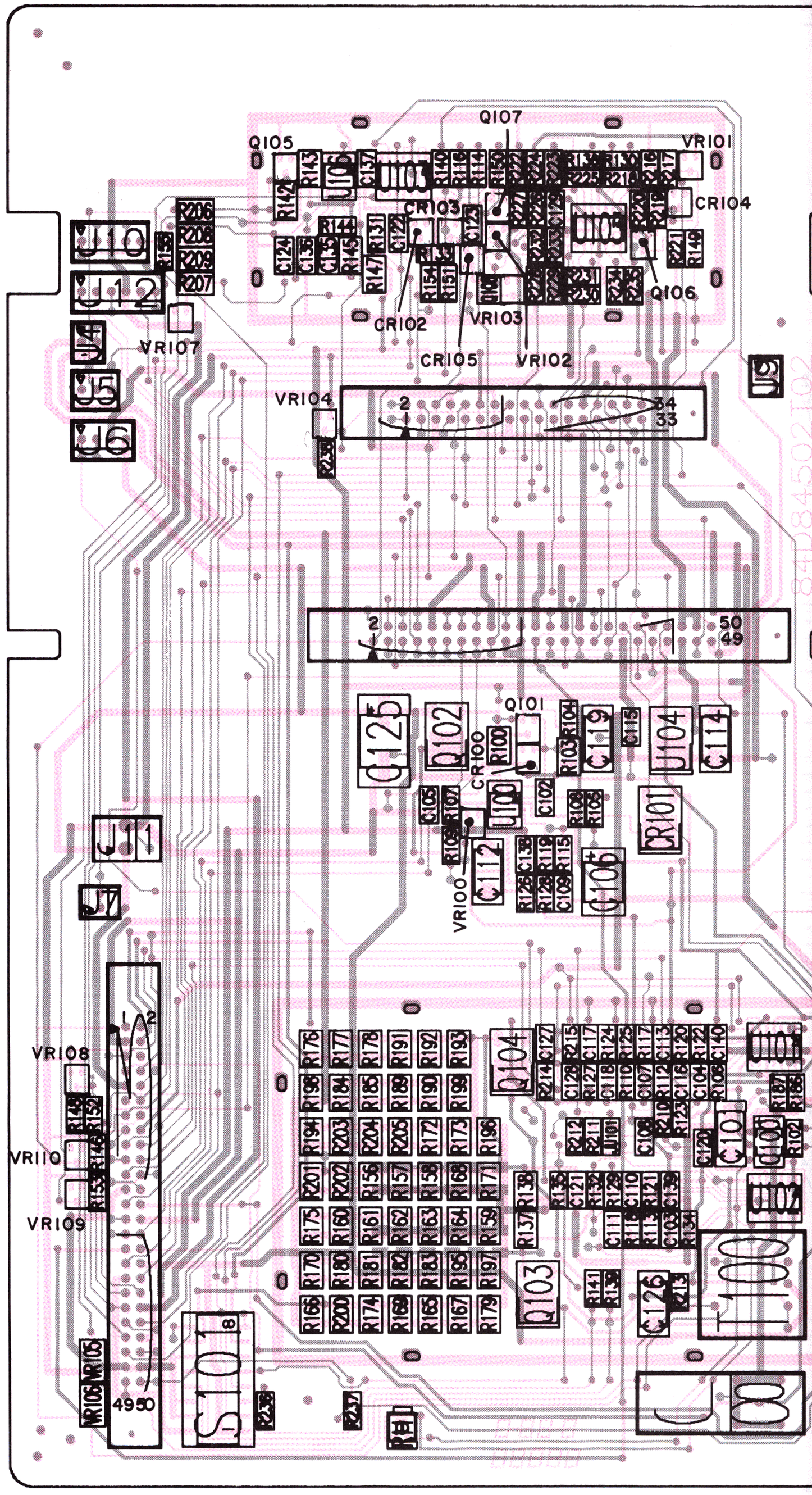
1. BASE, 2. EMITTER, 3. COLLECTOR
Q101 (MMBT3904) Q107 (MMBT2222)



1. BASE, 2. EMITTER, 3. COLLECTOR
Q106, Q108 (MMBT4403)



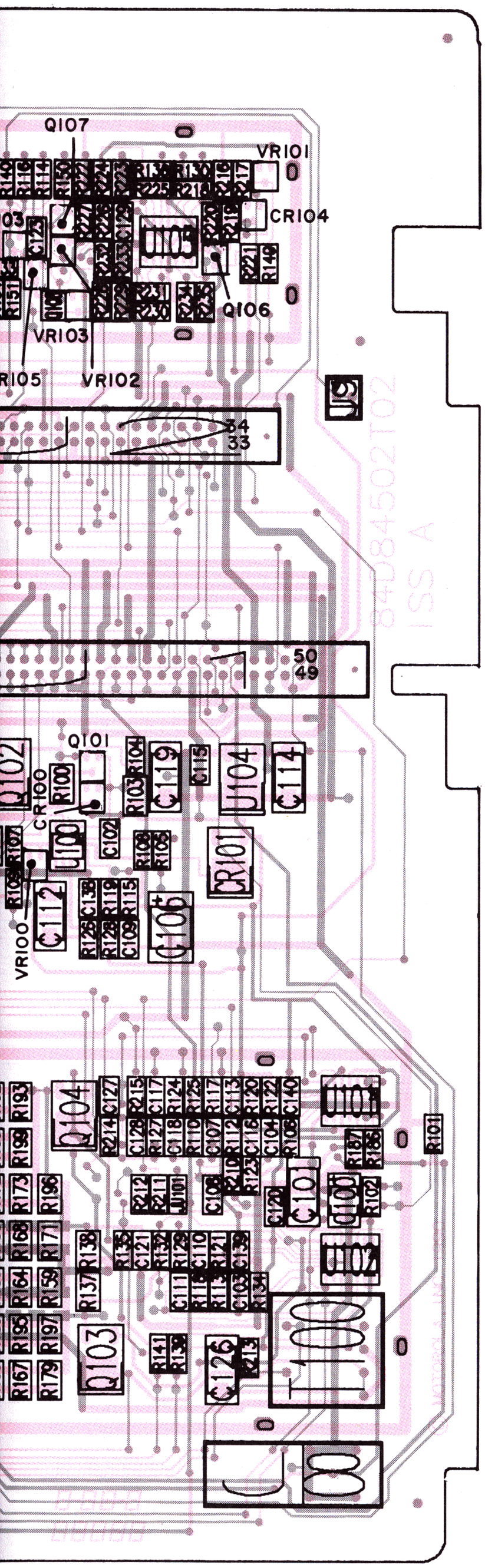
1. DRAIN, 2. SOURCE, 3. GATE
Q105 (MMBF5460)



SOLDER SIDE CEPS-48298

COMPONENT SIDE CEPS-48299

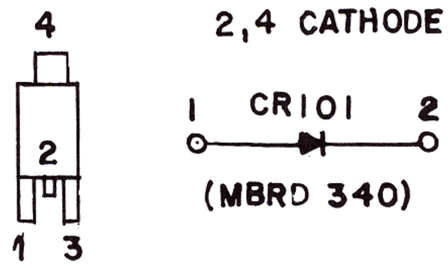
OVERLAY CEPS-48300

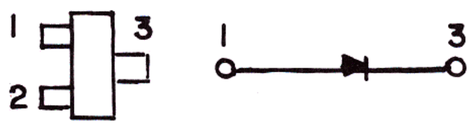


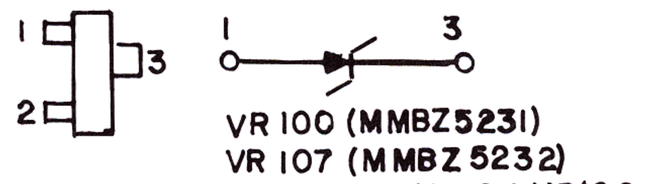
SOLDER SIDE CEPS-48298-A

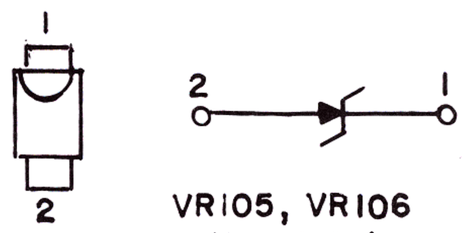
COMPONENT SIDE CEPS-48299-A

OVERLAY CEPS-48300-A









TRN7391A Audio Interface Board PL-11756-O

REF. SYMBOL	PART NO.	DESCRIPTION
		capacitor, fixed: pF ±5%; 50V unless otherwise stated
C100	2380090M15	0.33uF ±20
C101	2311049A08	1uF ±10%; 35V
C102	2113741B45	10000
C103	2113740B39	39
C104	2113740B67	560
C105	2113741B53	22000
C106	2380090M11	33 ±20; 25V
C107,108	2113741B49	15000
C109	2113741B45	10000
C110	2113740B73	1000
C111	2113740B61	330
C112	2311049A45	10uF ±10; 35V
C113	2113740B33	22
C114	2311049A45	10uF ±10; 35V
C115	2113740B56	200
C116 thru 118	2113741B49	15000
C119	2311049A15	4.7uF ±10%;35V
C120,121	2113740B39	39
C122 thru 124	2113741B69	100000
C125	2380090M25	100uF; 25V
C135	2311049A08	1uF ±10%; 35V
C136	2113741B45	10000
C137 thru 140	2113741B69	0.1uF
		diode: (see note)
CR100	4813833C10	.1A; 70V
CR101	4813833A03	Schottky type
CR102	4813833C03	dual 70V 'A1X' comm ano
CR103	4813833C04	dial 70V 'A4X' comm cath
		connector:
J1	2882505T15	plug: 50-contact
J2	2882505T04	plug: 34-contact
J3	2882505T15	plug: 50-contact
J4	2884324M07	plug: 2-contact
J5	2884324M08	plug: 3-contact
J6	2884324M09	plug: 4-contact
J7	2884324M07	plug: 2-contact
J8	0984393T01	receptacle, uhf
J9	2884324M07	plug: 2-contact
J10	2884324M10	plug: 5-contact
J11	2880004T02	plug: 2-contact
		jumper:
JU101,102	0611077A01	resistor, 0 ohm
JU104	0611077A01	resistor, 0 ohm
		transistor: (see note)
Q101	4813824A10	NPN
Q102	4813822A09	PNP
Q103,104	4813822A07	NPN
Q105	4813823A09	FET
		resistor, fixed: ±5%; 1/8W unless otherwise stated
R100	0611072A45	680; 1/4W
R101	0611077A76	1200
R102	0611077B06	20K
R103	0611072A45	680; 1/4W
R104	0611077A62	470
R105	0611077A80	2200
R106	0611077B23	47K
R107	0611077A98	10K
R108	0611077A82	2400
R109	0611077A76	1200
R110	0611077B15	3300
R111	1884355T14	10K
R112	0611077B23	47K
R113	0611077G81	100K
R114thru 116	0611077A98	10K
R117	0611077B30	100K
R118	0611077G81	100K
R119	0611077A98	10K
R120	0611077B30	100K
R121	0611077A90	5600
R122 thru 125	0611077B30	100K
R126	0611077B05	330K
R127	0611077A74	1000
R128	0611077B03	270K
R129	0611077B15	3300
R130	0611077A74	1000
R131	0611077A98	10K
R132	0611077B15	3300
R133	0611077A98	10K
R134	0611077A90	5600
R135	0611077A90	5600
R136	0611077A74	1000
R137,138	0611072A22	100
R139	0611077A76	1200
R140	0611077A68	820
R141	0611077A80	2200
R142	0611072A45	680; 1/4W
R143	0611072A55	1500
R144	0611077B39	68K
R145	0611072A49	820; 1/4W
R146	0611077A66	680
R147	0611072A47	750; 1/4W
R148 thru 153	0611077A66	680
R155	0611077A74	1000
R156 thru 185	0611072A46	750
R186,187	0611077B30	100K
R188	0611077A98	10K
R189 thru 205	0611072A46	750
R206 thru 209	0611072A61	3000; 1/4W
S101	4083706T01	switch, rotary
T100	2584422T01	transformer
U100	5113817A11	
U101,102	5113819A04	
U103	5113805A01	
U104	5113816G04	

	DESCRIPTION (PL-11756-O)
	resistor, fixed: ±5%; 1/8W (cont.)
	unless otherwise stated
15	680; 1/4W
32	330
30	1800
23	100K
08	10K
32	2200
76	1200
15	47K
14	variable: 200K
23	100K
31	84.5K ±1%
08	10K
30	200K
31	84.5K ±1%
08	10K
30	200K
00	4700
30	200K
05	18K
74	1000
03	15K
15	47K
74	1000
08	10K
15	47K
08	10K
00	4700
00	4700
74	1000
32	75; 1/4W
76	1200
38	560
30	1800
15	680; 1/4W
55	1800; 1/4W
39	470K
49	1000; 1/4W
36	470
17	820; 1/4W
36	470
74	1000
16	750; 1/4W
30	200K
08	10K
16	750; 1/4W
31	3300; 1/4W
	switch:
01	surface mount dip
	transformer:
01	line isolation
	integrated circuit: (see note)
11	RC4558
04	MC3303DR2
01	quad 2-input NAND
04	+5V regulator

REF. SYMBOL	PART NO.	DESCRIPTION (PL-11756-O)
		integrated circuit: (see note) (cont.)
U106	5113815G02	timer
		voltage regulator: (see note)
VR100	4813830A15	5.6V
VR101 thru 106	4884572T01	48V
VR107	4813830A15	5.6V
		non-referenced items:
	5483865R01	LABEL, bar code; 1/4"
	5484246T01	LABEL. bar code

NOTE: For optimum performance, diodes, transistors and integrated circuits must be ordered by Motorola part numbers.