

BUS SYSTEM TEST PROCEDURES

2-WIRE NO. 1 ELECTRONIC SWITCHING SYSTEM

1. GENERAL

1.001 This addendum supplements Section 231-148-301, Issue 2.

1.002 This addendum is issued for the following reasons:

- (a) To change the waveform requirements for the peripheral units bus test.
- (b) To update the T-CPD input message for use with CTX-5, Issue 7, CTX-6, Issue 7 and later generic programs.

2. CHANGES TO SECTION

ISSUE 1 CHANGES

2.001 On Page 6, change 3.03 (4) as follows:

(4) The dual trace display must meet the following requirements: (See Fig. 5.)

- (a) The address pulse at the output of the cable receiver pack must have a duration between 0.25 microseconds and 0.75 microseconds.
- (b) At least 0.25 microseconds of the address pulse must be coincident with the enable window.
- (c) Address pulse rise and fall times are not critical as long as they do not exist in adjacent windows.

2.002 On Page 8, replace Fig. 5 with the following figure:

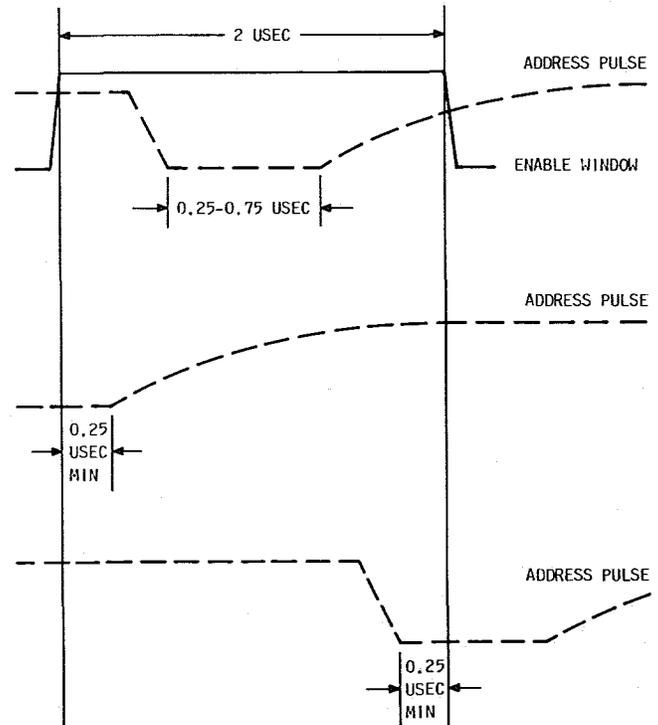


Fig. 5—Peripheral Units Bus Test Waveforms

2.003 On Pages 10, 11, and 12, replace 3.05 (5), 3.06 (5), and 3.07 (5) with the following paragraph.

(5) To set the OL3 flip-flop so that maintenance orders will be sent on the bus, type one of the following two messages.

- (a) For CTX-5, Issue 7, CTX-6, Issue 7 and later generic programs

T-CPD-W000017W.

- (b) For earlier generic programs

T-CPD-000017.

System response should be OK.

NOTICE

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