

JUNCTOR REASSIGNMENT PROCEDURES

NO. 2 AND NO. 2B ELECTRONIC SWITCHING SYSTEMS

CONTENTS	PAGE	CONTENTS	PAGE
1. GENERAL	2	REASSIGNMENT TEST	43
JUNCTOR GROUPING FRAME (JGF)	2		
NETWORK JUNCTOR SUBGROUP ASSIGNMENT	5	FIGURES	
CIRCUIT JUNCTOR SUBGROUP ASSIGNMENT	6	CONTENTS	PAGE
2. JUNCTOR ASSIGNMENT PROCESSING	7	1. Junctor Grouping Frame	3
ASSIGNMENT PROGRAM INPUT	7	2. Physical Realization of Junctor Grouping Frame	4
ASSIGNMENT PROGRAM OUTPUT	8	3. No. 2 ESS Network Block Diagram	5
JUNCTOR REASSIGNMENT PROGRAM (JASINT)	12	4. Terminations of Subgroups on the JGF	7
3. METHOD OF DIAGNOSING CIRCUIT JUNCTORS USING NEW TRANSLATIONS	14	5. Reversal Property of Slip	7
4. JUNCTOR REASSIGNMENT PROCEDURES	35	6. Processing of Junctor Assignments	8
5. TROUBLE RECOVERY	40	7. UTJF Locations of Circuit Junctors	9
6. REVERTING (BACKING-UP) PROCEDURE	40	8. JGF Vertical Definitions	10
7. EFFECTS OF SEQUENCE MESSAGES ON REMOVE AND RESTORE MESSAGES	42	9. Junctor Group Size Definitions	11
8. WRONG SECTOR SPECIFIED ON A PRIORITY INPUT MESSAGE (MP JP ___)	43	10. Peripheral Unit Assignment Table	12
9. HOW TO ABORT THE JUNCTOR		11. General Record Information	13
		12. Input Data Printout	15
		13. Input to the ODA Junctor Grouping Translator	17

NOTICE

Not for use or disclosure outside the
Bell System except under written agreement

CONTENTS	PAGE
14. Junctor Group Size Definitions . . .	19
15. JGF Plug-Jack Interconnection Pattern for Telephone Company Reference . . .	21
16. JFG Plug-Jack Interconnection Pattern for Installer's Use (Shown for Sector 0 Only)	23
17. Junctor Circuit Cable Running List . .	25
18. Switchboard Cable Connecting Table .	27
19. Junctor Capacity Evaluation and Pattern Analysis	29
20. Flow Chart of Junctor Reassignment Procedure	33

1. GENERAL

1.01 This section gives the procedures for making junctor reassignment changes using the maintenance teletypewriter (TTY) for the No. 2 and No. 2B Electronic Switching Systems (ESS). A junctor reassignment is required during office growth when new networks are added.

1.02 This section is reissued to include information for junctor capacity evaluation and pattern analysis, No. 2B ESS, and other changes since the last issue.

1.03 All network junctor terminals in No. 2 and No. 2B ESS are cabled to a peripheral frame called Junctor Grouping Frame (JGF). The JGF is used to provide cross-connections to form the required internetwork and intranetwork junctor groups.

1.04 A junctor may be either a wire junctor or a circuit junctor, depending on the type of talking path it completes. Trunk-to-trunk, trunk-to-line, and line-to-trunk connections require wire junctors, while line-to-line connections must have a

junctor circuit inserted in the junctor path to provide talking battery and supervision.

JUNCTOR GROUPING FRAME (JGF)

1.05 Junctor terminations of the LTNs as well as junctor circuits, are cabled in subgroups of eight to the JGF. These subgroups are associated via plugs and jacks on the JGF to form connections between or within networks (Fig. 1).

1.06 A JGF has the capacity for terminating 2560 junctors (tip and ring pairs). Each frame is divided into 10 vertical files which must be mounted on the frame in pairs (Fig. 1). A file will provide terminations for one LTN (512 tip and ring pairs) or 256 junctor circuits. Any vertical file can have either LTN or junctor circuit appearances, but not a combination of the two.

1.07 The JGF has eight shelves, each vertical file having four plugs and four jacks on each shelf (Fig. 2). Each network contains four plug-ended and four jack-ended subgroups on each shelf. There are four circuit junctor subgroups per shelf (each subgroup consisting of a plug-jack pair), per fully equipped circuit junctor file. Each plug is equipped with a 5-foot long cord (referred to as a patch cord) that allows any network to be interconnected to any other network. There may be up to 40-vertical files (10 per frame) in a No. 2 and No. 2B ESS office. Vertical files 16, 17, 18, and 19 on JGF 1 are designated as extension files so that plugs which originate on the first files can terminate on the last files. Wire junctor subgroups are formed by connecting the plug-ended subgroups of one network into the jack-ended subgroup of itself or another network (Fig. 3). Circuit junctor subgroups are formed by:

- (a) Connecting a plug-ended subgroup of one network into the jack-end of a circuit junctor subgroup (Fig. 3)
- (b) Connecting the plug-end of the same circuit junctor subgroup into a jack-ended subgroup of the second network (Fig. 3).

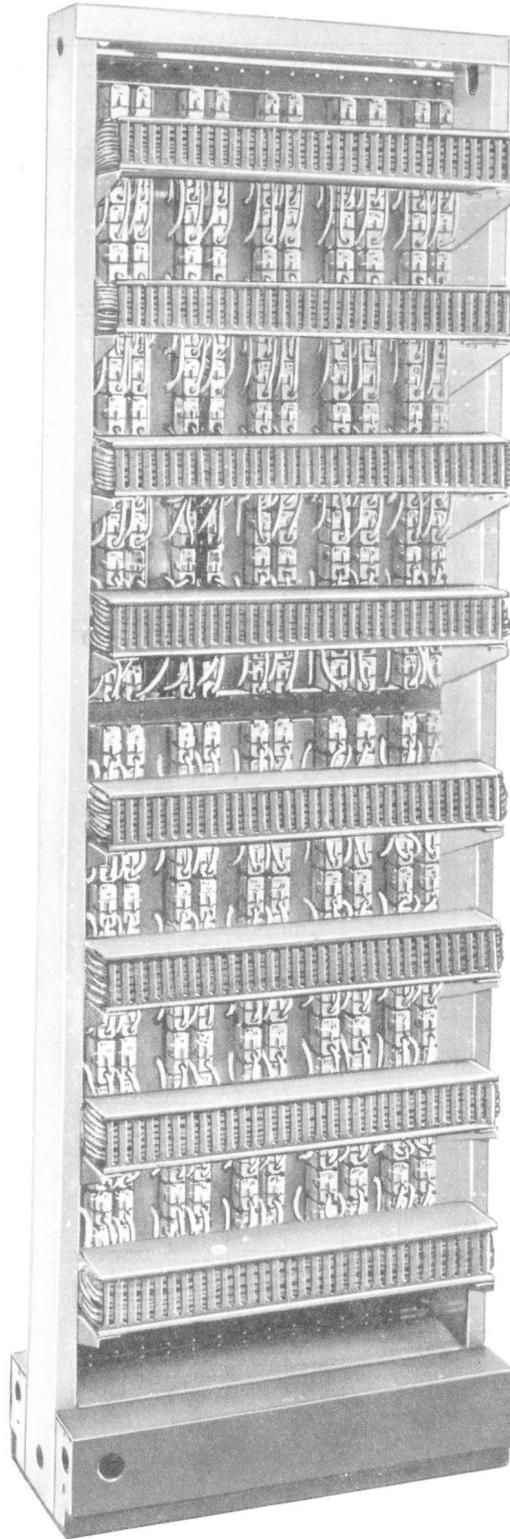


Fig. 1—Juncctor Grouping Frame

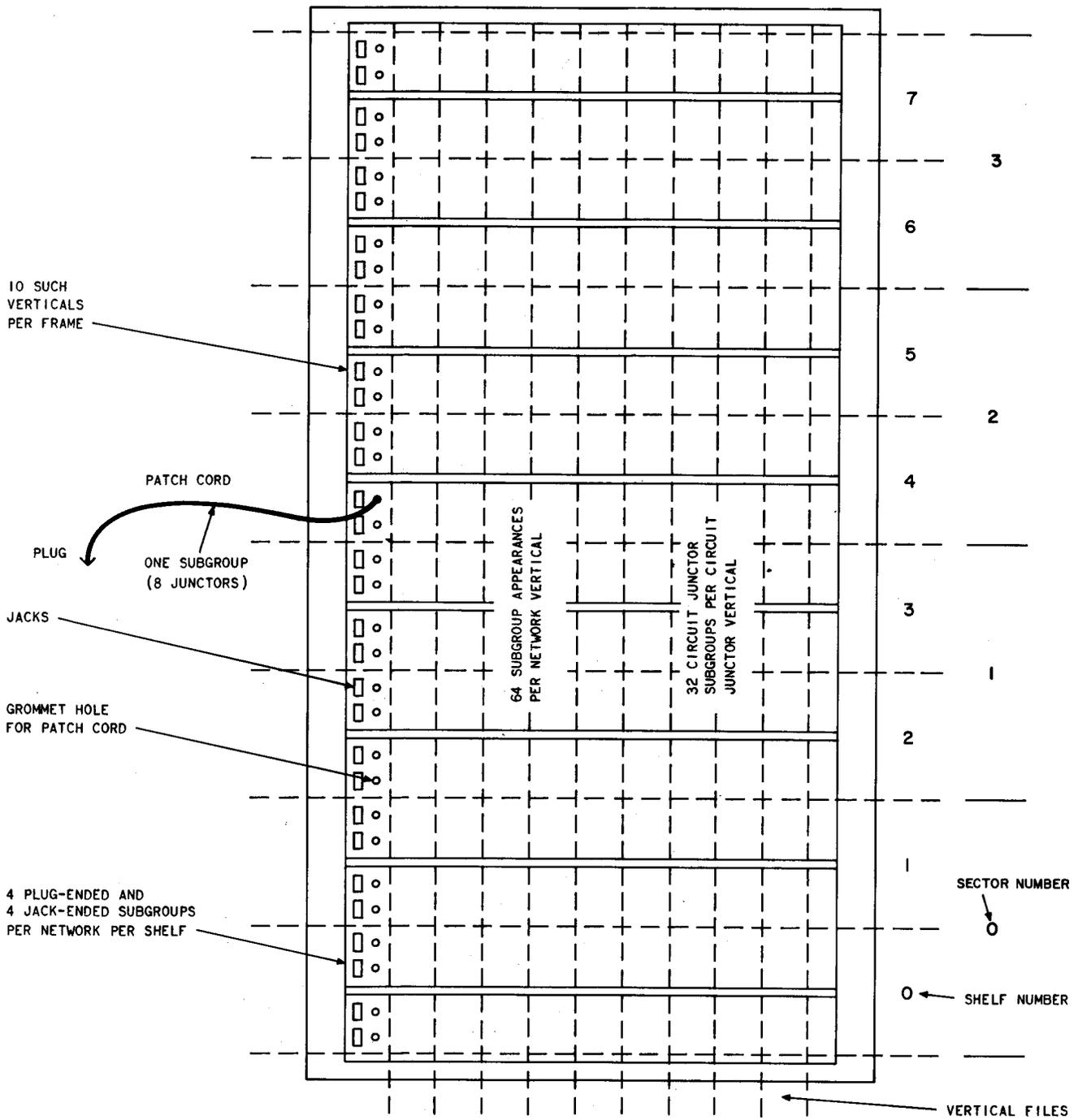


Fig. 2—Physical Realization of Junctor Grouping Frame

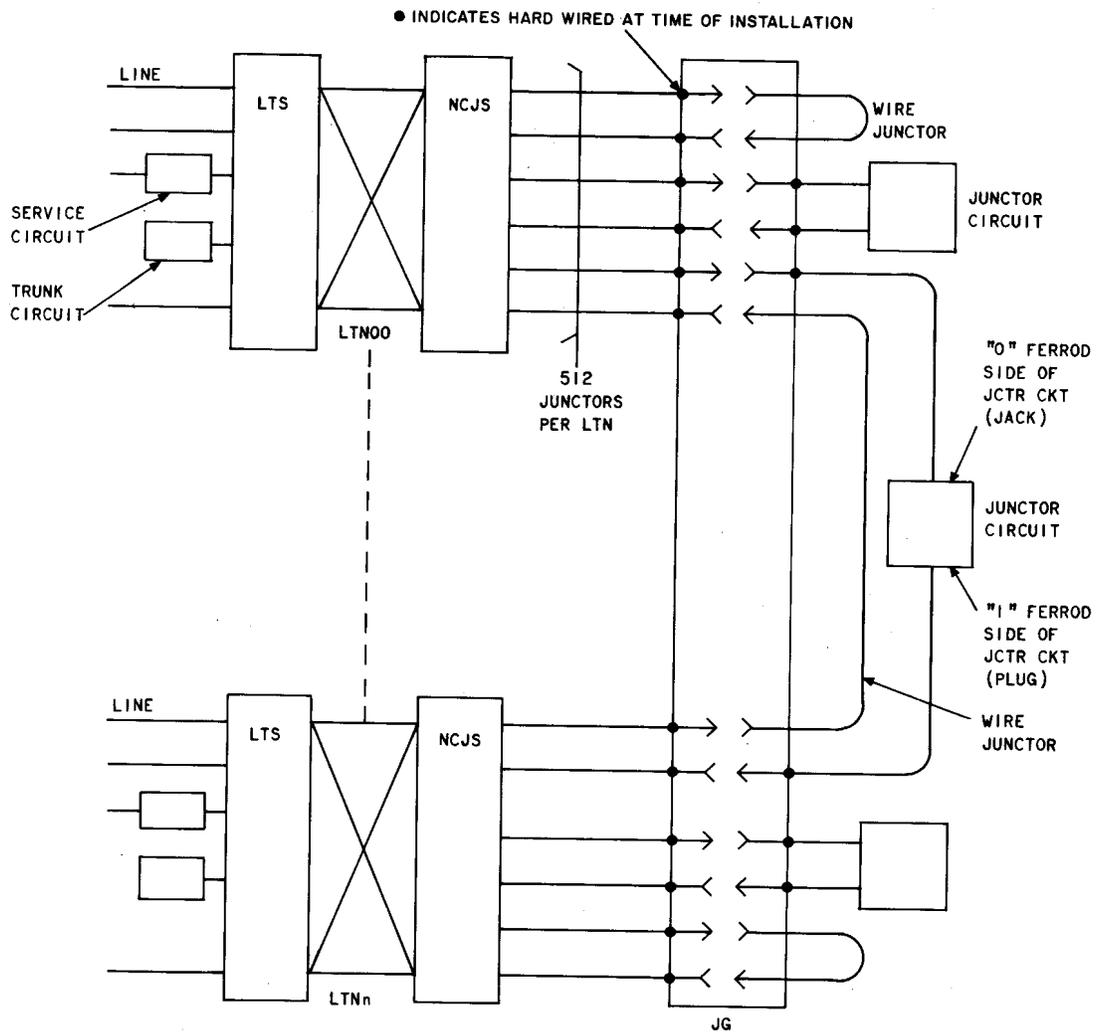


Fig. 3—No. 2 ESS Network Block Diagram

NETWORK JUNCTION SUBGROUP ASSIGNMENT

1.08 A standard pattern has been adopted for the appearances of the network junction

subgroups on the JGF, as shown in Table A and Fig. 4. The plug and jack arrangement is then configured using the junction assignment computer program (JAP-2), according to the specific requirements

TABLE A

JUNCTOR SUBGROUP TERMINATIONS ON CDF

JGF SHELF	PLUG TO JACK SLIP	PLUG ENDED			JACK ENDED		
		SUBGROUP	SWITCH	LEVELS	SUBGROUP	SWITCH	LEVELS
0	1	0-3	0	0-3	12-15	1	4-7
1	1	16-19	2	0-3	28-31	3	4-7
2	5	8-11	1	0-3	4-7	0	4-7
3	5	24-27	3	0-3	20-23	2	4-7
4	1	32-35	4	0-3	44-47	5	4-7
5	1	48-51	6	0-3	60-63	7	4-7
6	5	40-43	5	0-3	36-39	4	4-7
7	5	56-59	7	0-3	52-55	6	4-7

of the particular ESS office.

1.09 Interconnections between networks should be done, if possible, within the same shelf. Plugs corresponding to a given switch and level should be connected to jacks of another fixed switch and level. Subgroups corresponding to levels 0 to 3 on each switch are terminated on plugs on the JGF. The subgroups corresponding to levels 4 to 7 on each switch terminate on jacks. In the event that it is necessary to interconnect plugs and jacks on different shelves, the interconnections will be confined within a specific sector. A sector consists of a pair of shelves; shelves 0 and 1; 2 and 3; 4 and 5; 6 and 7. These pairs of shelves are referred to as sectors, 0 through 3, respectively (Fig. 2).

1.10 For minimal office blocking, slip patterns (see Fig. 5) are provided in the cabling between the LTNs and the terminal strips on the JGF. Two different slips will be wired into the network wiring on the JGF. A slip of one will

be wired on sectors 0 and 2 (shelves 0, 1, 4, and 5). A slip of five will be wired on sectors 1 and 3 (shelves 2, 3, 6, and 7).

1.11 Half the wiring from the networks to the JGF terminate on terminal strips dedicated to plugs and the other half terminate on terminal strips dedicated to jacks (see Fig. 4). The slip is incorporated only in the wiring between the networks and the terminal strips dedicated to plugs (see Fig. 5). All patch cords (originating either on a network vertical or circuit junctor vertical) are wired with zero slip.

CIRCUIT JUNCTOR SUBGROUP ASSIGNMENT

1.12 The circuit junctor subgroups will be assigned, to a circuit junctor file on the JGF. This assignment sequence will begin on circuit junctor file 1, filling file 1 before assigning to file 2, etc. Then, one file at most will be partially equipped with circuit junctor subgroups. All other circuit

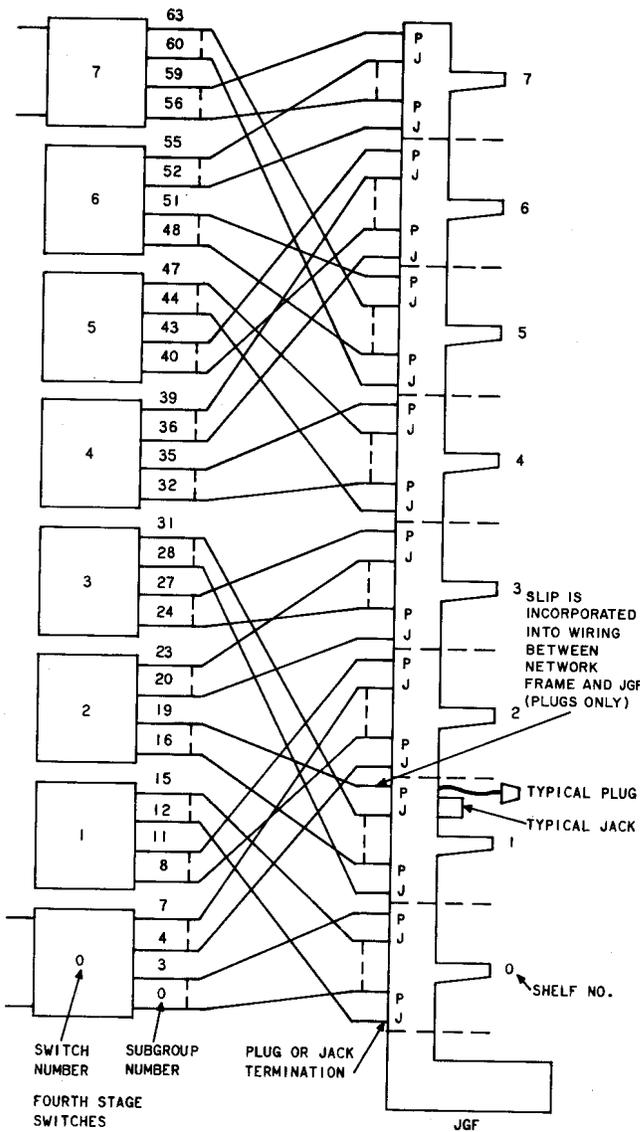


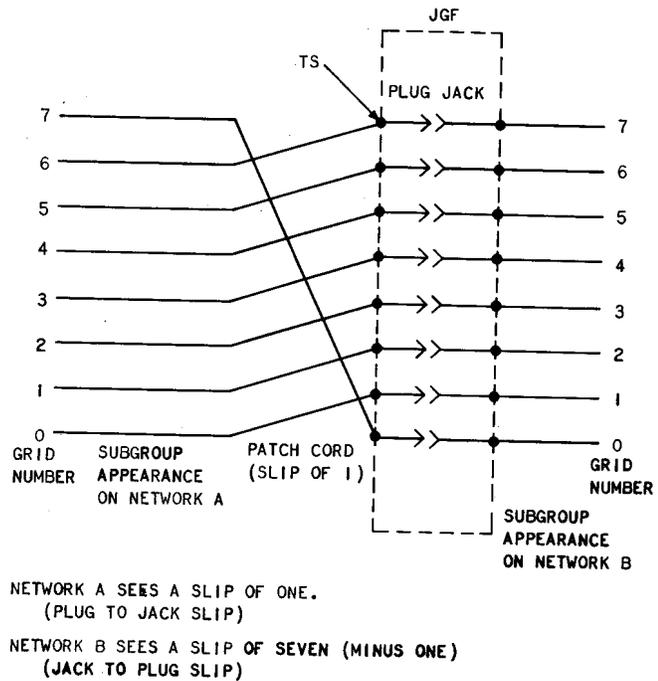
Fig. 4—Terminations of Subgroups on the JGF

junctor subgroup files will be either completely full or completely empty. This assignment method provides an even distribution of junctor circuit subgroups over the sectors on the JGF.

2. JUNCTOR ASSIGNMENT PROCESSING

ASSIGNMENT PROGRAM INPUT

2.01 Junctor assignments are processed for the operating companies by Western Electric



NETWORK A SEES A SLIP OF ONE.
(PLUG TO JACK SLIP)
NETWORK B SEES A SLIP OF SEVEN (MINUS ONE)
(JACK TO PLUG SLIP)

Fig. 5—Reversal Property of Slip

Company. The processing center utilizes JAP-2 for junctor assignment. This program will take into consideration the physical restrictions, as well as the many rules governing the development of a junctor pattern.

2.02 The input to the JAP-2 will be completed by the Western Electric Company line engineer using the data supplied to him by the operating company (Fig. 6). Figure 7 through 11 are provided as examples only. The latest versions of the forms are given in Translation Guide TG-2H. A description of each of the forms filled out by the line engineer and the operating company is as follows.

- (a) **Form ESS-2200:** This form (Fig. 7) is used to list the location of circuit junctors on the universal trunk and junctor frame (UTJF).
- (b) **Form ESS-2203:** This form (Fig. 8) is used to specify the type of appearance terminated on a particular JGF vertical.
- (c) **Form ESS-2206:** This form (Fig. 9) is used to list the junctor group sizes and is divided into two sections. The first half is used

to enter circuit-juncture group sizes and the second half is used to enter wire-juncture groups sizes. Only equipped juncture groups are to be entered.

- (d) **Form ESS-2503A:** This form (Fig. 10) is used to list the various enables and assignments for the universal trunk and juncture frame.
- (e) **Form ESS-2507:** This form (Fig. 11) is used to list any supporting information that does not result in data inserted into translation store.

ASSIGNMENT PROGRAM OUTPUT

2.03 The output of JAP-2 is in the form of a computer printout. A description and usage of the output is as follows.

- (a) **Section A:** The Input Data Printout lists the input that was determined to be error

free by the crossfield value range-checks routine (Fig. 12).

- (b) **Section B:** This section contains the input to the ODA juncture grouping translator program which gives a card image listing with column numbers of the translator input. This section is printed as a backup source listing (Fig. 13).

- (c) **Section C:** This section contains the juncture group size definitions. The listings of the wire-juncture and circuit-juncture group sizes requested and assigned are given (Fig. 14).

- (d) **Section D:** This section contains the JGF plug-jack interconnection pattern for Telephone Company (TELCo) reference. This section is to be used only by the TELCo, not the installer

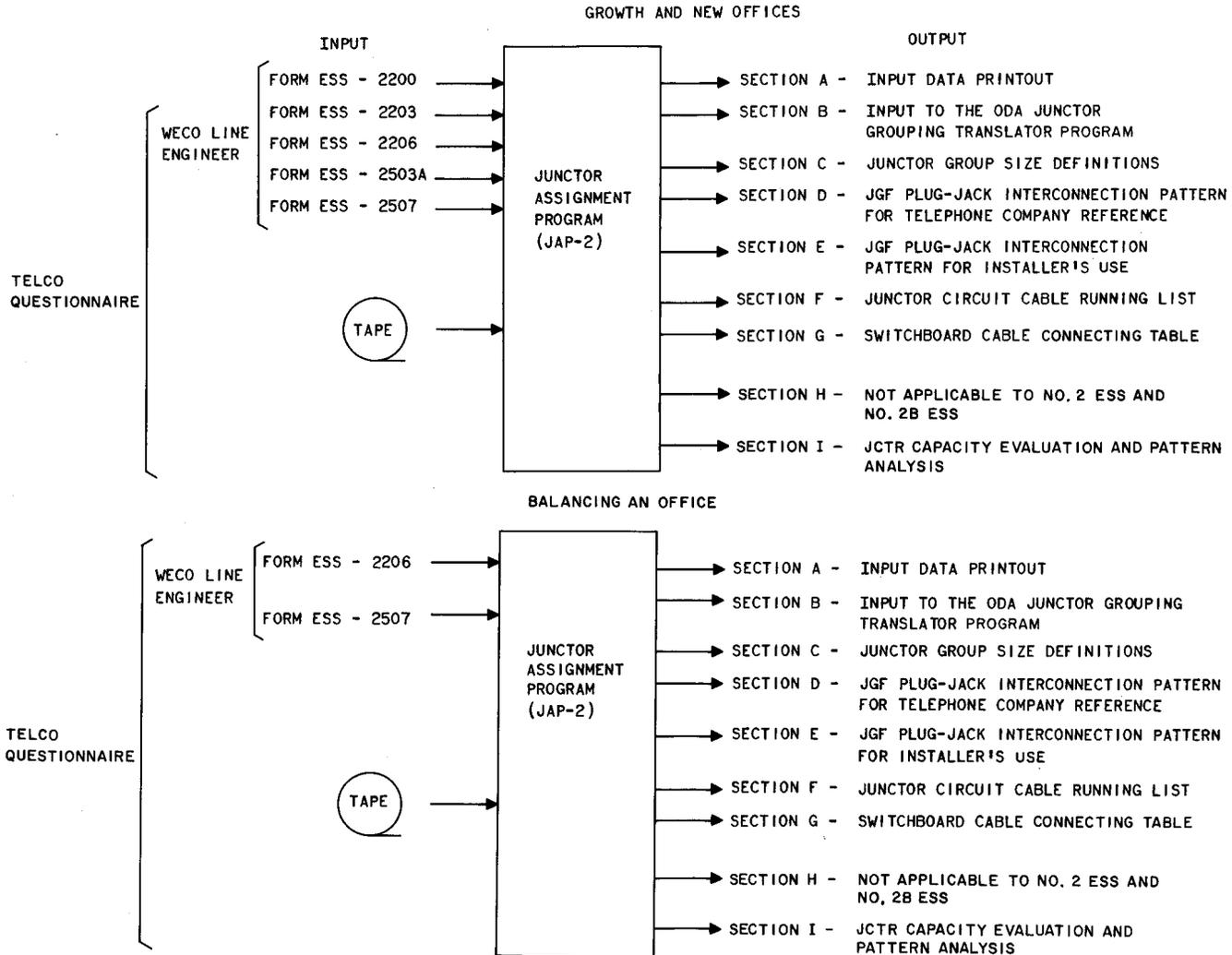


Fig. 6—Processing of Juncture Assignments

- After the sectors on the first day of junctor reassignment have been reconfigured (see 4.02, step 20).
- After removal of all reassignment flags (see 4.02, steps 22, 23, and 24).

2.07 The junctor reassignment should be performed during the low traffic period of the day. This precaution is necessary since the juncctors are reconfigured on a sector basis. This means that at least 25 percent of the juncctors are not available to call processing while the juncctors are being reconfigured.

2.08 Since four sectors are involved, the reassignment can be completed in anywhere from one to four days. Some of the factors that go into deciding how many days should be spent doing the junctor growth are as follows:

- Is the time interval during the low traffic period long enough to reconfigure more than one sector each day?
- Is there sufficient CCS capacity available at the juncctors to carry the office through each day's busy hour if the sectors are reconfigured on a one sector per day basis? If the answer is no, than two or more sectors must be reconfigured on a particular day.
- How many (if any) lines, trunks and service circuits must be reassigned to the new networks to maintain a reasonable matching loss?

2.09 The answers to the preceding questions and others pertaining to junctor growth are contained in Dial Facilities Management Practices Division H, Section 10t2, No. 2 ESS Transition Management Switching Network. The DFMP includes a set of work sheets which can be applied to any office to determine an appropriate junctor growth sequence.

3. METHOD OF DIAGNOSING CIRCUIT JUNCTORS USING NEW TRANSLATIONS

3.01 A circuit junctor is identified to the diagnostic programs by the junctor network number (JNN) and the network associated with the other end of the junctor (see office records). The JNN consists of the network, switch, level, and grid of the junctor. A scan point number (SPN) is also associated with each circuit junctor jack and junctor plug. Circuits are diagnosed specifying the two networks associated with the circuit junctor and the switch, level, and grid of the **jack**. A subgroup may be diagnosed by omitting the grid.

Existing Circuits

3.02 Same JNN, Same SPN—An existing circuit (existing SPN) which has **identical** JNNs (one for jack, one for plug) before its reassignment, as well as after, may be diagnosed in the normal manner.

3.03 New JNN, Same SPN—An existing circuit (existing SPN) which will have one or both new JNNs **after reassignment must be diagnosed using the new** assignment. The diagnostic printout will contain the **old** assignment **before** it is reassigned and will contain its **new** assignment **after** it is reassigned (see example).

Example: If the old and new assignment of a circuit junctor subgroup were as follows:

	NETWORK (MM)	NETWORK (NN)	SWITCH (S)	LEVEL (L)	SPN OF SUBGROUP
OLD	00	00	5	4	2010
NEW	02	02	4	4	2010

TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDER ESS PAGE
 JCTR ASSIGN PGM ISSUE 02 ** INPUT DATA PRINTOUT ** *** SECTION A ***
 FORM ESS 2200 UTJF LOCATIONS OF CIRCUIT JUNCTORS

EQPT	LOC OF JUNCTOR	BLOCKS IN BAY 0	ADDN	SEQUENCE
UTJF	FIRST MEMBER	LAST MEMBER		NUMBER
NO	PLT	PLT		
00H	19	27	*	001
01H	19	27	*	002
02H	19	27	*	003
02M	19	27	*	004

* INDICATES ADDED ON THIS ORDER.
 NOTE- CABLING OF ADDED CIRCUIT JUNCTORS SHALL BE MADE BEGINNING WITH THE LAST (MAY BE PARTIALLY WIRED) CIRCUIT JUNCTOR VERTICAL ON THE JUNCTOR GROUPING FRAME AND PROCEED IN THE SEQUENCE LISTED ABOVE AND IN ACCORDANCE WITH CIRCUIT JUNCTOR DISTRIBUTION RULE.

FORM ESS 2203 JUNCTOR GROUPING FRAME VERTICAL DEFINITIONS

JGF VERT NO	00	01	02	03	04	05	06	07	08	09	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
NETWORK NO	00	01	02	03																										
JUNCTOR																														
ADDN	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	

X INDICATES CIRCUIT JUNCTOR VERTICAL
 * INDICATES NEW VERTICAL ASSIGNMENT

FORM ESS 2503A PERIPHERAL UNIT ASSIGNMENT TABLE

UTJF NO	TSC NO	CPD NO	NEST	ADDN
00H	01	0	1	*
01H	02	0	2	*
02H	03	0	3	*
02M	03	0	4	*

* INDICATES NEW ASSIGNMENT PER THIS ORDER

WESTERN ELECTRIC CO., INC. PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 12—Input Data Printout

ESS TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDERS ESS PAGE
 JCTR ASSIGN PGM ISSUE 02 ** INPUT TO THE ODA JUNCTOR GROUPING TRANSLATOR ** *** SECTION B ***

CARD COLUMNS 1 2 3 4 5 6 7 8
 1234567890123456789012345678901234567890123456789012345678901234567890

100808J62E0	99	003500231					001
100808J62E0	99	0C0700115					002
100808J62E0	99	002500125					003
100808J62E0	99	007700611					004
100808J62E0	99	004500535					005
100808J62E0	99	006400705					006
100808J62E0	99	006700725					007
100808J62E0	99	0014000010312002H	1	0	0	3	008
100808J62E0	99	0024003050342002M	2524	015			009
100808J62E0	99	0054004010110000H	3332	016			010
100808J62E0	99	011700031					011
100808J62E0	99	010100177					012
100808J62E0	99	013600201					013
100808J62E0	99	011200043					014
100808J62E0	99	012600325					015
100808J62E0	99	013300273					016
100808J62E0	99	015600421					017
100808J62E0	99	014300577					018
100808J62E0	99	016200747					019
100808J62E0	99	014600505					020
100808J62E0	99	016700735					021
100808J62E0	99	0120003470341802M	8	9	0	7	022
100808J62E0	99	0104001050213001H	1716	010			023
100808J62E0	99	0174006010112000H	4140	020			024
100808J62E0	99	0150004430345002M	4849	124			025
100808J62E0	99	013501231					026
100808J62E0	99	010701115					027
100808J62E0	99	015401401					028
100808J62E0	99	015501421					029
100808J62E0	99	017701511					030
100808J62E0	99	014501535					031
100808J62E0	99	016601725					032
100808J62E0	99	0114010010212801H	1	0	0	2	033
100808J62E0	99	0124013050310802H	2524	014			034
100808J62E0	99	0164C17050112800H	5756	028			035
100808J62E0	99	021600021					036
100808J62E0	99	020100167					037
100808J62E0	99	022300377					038
100808J62E0	99	020700135					039
100808J62E0	99	022500335					040
100808J62E0	99	025500431					041
100808J62E0	99	027700631					042
100808J62E0	99	025300473					043
100808J62E0	99	027200653					044
100808J62E0	99	0235002110310002H	1110	0	6		045

CARD COLUMNS 1 2 3 4 5 6 7 8
 1234567890123456789012345678901234567890123456789012345678901234567890

WESTERN ELECTRIC CO., INC.

PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 13—Input to the ODA Junctor Grouping Translator

ESS	TOWN	STATE	TELCO	TELCO ORDER NO	BUILDING	TELCO BASE	DATE	RUN W.E.ORDER	ESS	PAGE																					
JCTR ASSIGN PGM ISSUE 02 ** JUNCTOR GROUP SIZE DEFINITIONS **									***	SECTION C	***																				
CIRCUIT GROUP SIZES REQUESTED					CIRCUIT GROUP SIZES ASSIGNED																										
LTN	CO	01	02	03	04	05	06	07	08	09	10	11	12	13	14	LTN	00	01	02	03	04	05	06	07	08	09	10	11	12	13	14
00	3	4	4	4	00	3	4	4	4	
01	..	3	4	4	01	..	3	4	4	
02	3	4	02	3	4	
03	3	03	3	
04	04	
05	05	
06	06	
07	07	
08	08	
09	09	
10	10	
11	11	
12	12	
13	13	
14	14	

WIRE GROUP SIZES REQUESTED															WIRE GROUP SIZES ASSIGNED																
LTN	00	01	02	03	04	05	06	07	08	09	10	11	12	13	14	LTN	00	01	02	03	04	05	06	07	08	09	10	11	12	13	14
00	6	9	9	9	00	7	11	9	12
01	..	6	9	9	01	..	7	11	10	
02	6	9	02	8	10	
03	6	03	7	
04	04	
05	05	
06	06	
07	07	
08	08	
09	09	
10	10	
11	11	
12	12	
13	13	
14	14	

WESTERN ELECTRIC CO., INC. PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 14—Junctur Group Size Definitions

ESS TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDER ESS PAGE
 JCTR ASSIGN PGM ISSUE 02 ** JGF PLUG-JACK INTERCONNECTION PATTERN FOR TELEPHONE COMPANY REFERENCE ** *** SECTION D ***

PLUGS ON JUNCTOR GROUPING FRAME 0 VERTICALS C THRU 5 ARE CONNECTED TO JACKS AS FOLLOWS:

S E C T O R	H L F	PLUG LEV	VERT 00		VERT 01		VERT 02		VERT 03		VERT 04		VERT 05	
			JACK LEV	JACK VERT										
7	31	31	01	31	02	31	03	29	01	28	03	NE...	NE	
7	30	31	00	30	01	30	00	29	00	29	00	NE...	NE	
7	29	29	93	39	93	39	92	39	94	28	02	NE...	NE	
7	28	28	00	28	04	29	04	31	04	28	01	NE...	NE	
3	6	27	25	00	25	01	27	00	27	01	25	02	NE...	NE
6	26	26	03	26	02	24	03	26	00	24	01	NE...	NE	
6	25	27	04	25	04	27	02	27	03	25	03	NE...	NE	
6	24	26	01	24	05	26	04	24	04	24	02	24	CO	
5	23	23	02	23	03	21	02	21	03	20	02	NE...	NE	
5	22	22	03	20	00	22	01	22	00	21	01	NE...	NE	
5	21	23	00	23	01	21	04	22	04	21	00	NE...	NE	
5	20	20	04	23	04	16	05	22	02	20	01	NE...	NE	
2	4	19	17	02	19	00	19	01	19	02	16	02	NE...	NE
4	18	18	01	17	01	18	02	18	00	16	03	NE...	NE	
4	17	17	04	19	03	18	03	17	00	17	03	NE...	NE	
4	16	16	04	16	01	19	04	18	04	16	00	20	C3	
3	15	13	02	15	00	15	01	15	02	12	00	NE...	NE	
3	14	14	01	14	03	10	02	14	00	12	01	NE...	NE	
3	13	12	04	14	02	15	03	12	01	12	03	NE...	NE	
3	12	15	04	14	04	12	02	13	04	13	03	NE...	NE	
1	2	11	11	02	11	03	9	02	9	03	8	03	NE...	NE
2	10	13	00	8	00	10	01	10	00	8	01	NE...	NE	
2	09	11	00	11	01	8	04	8	05	8	02	NE...	NE	
2	08	10	04	9	04	11	04	10	03	9	00	9	01	
1	07	5	00	5	01	7	00	7	01	4	00	NE...	NE	
1	06	6	03	6	02	4	03	6	00	5	02	NE...	NE	
1	05	6	04	4	04	7	02	7	03	4	02	NE...	NE	
1	04	6	01	7	04	1	04	5	04	5	03	NE...	NE	
0	0	03	3	01	3	02	3	03	1	01	0	00	NE...	NE
0	0	02	2	02	1	02	2	01	1	03	0	01	NE...	NE
0	0	01	2	03	3	00	2	00	0	05	4	01	NE...	NE
0	0	00	3	04	2	04	0	04	0	03	0	02	1	00

**** THE NUMBER OF CONNECTIONS THAT EXTEND MORE THAN TWENTY VERTICALS AND REQUIRE EXTENSION CORDS IS 0 . ****
 NOTE: 'NE...NE' MEANS THAT THIS PLUG LOCATION IS UNASSIGNED.

WESTERN ELECTRIC CO., INC. PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 15—JGF Plug-Jack Interconnection Pattern for Telephone Company Reference

ESS TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDER ESS PAGE
 JCTR ASSIGN PGM ISSUE 02 ** JUNCTOR CIRCUIT CABLE RUNNING LIST ** *** SECTION F ***

T2H102-11 FIG. 1 JUNCTOR CIRCUIT

LINE	CKT REF	AMT IN FEET	NO CAS	CKT CODE OR CA NOS	NBG	FROM	CA DES	DROP	EQPT LOC	TO JUNCTOR GROUP FRAME (JGF) VERTICAL FILE	SHELF	CA DES	DROP	EQPT LOC (CONN J & TS A)	ROUTE
						UTJF- 0 H BAY 0									
1	809A	0-3		0-3		PLT 19			TS A&B	04	00			00	
		0-3		0-3		PLT 20			TS A&B	04	02			08	
		0-3		0-3		PLT 21			TS A&B	04	04			16	
		0-3		0-3		PLT 22			TS A&B	04	06			24	
		0-3		0-3		PLT 23			TS A&B	04	01			04	
		0-3		0-3		PLT 24			TS A&B	04	03			12	
		0-3		0-3		PLT 25			TS A&B	04	05			20	
		0-3		0-3		PLT 26			TS A&B	04	07			28	
1	811A	0-3		0-3		PLT 27			TS A&B	04	00			01	
						UTJF- 0 H BAY 2									
1	809A	0-3		0-3		PLT 14			TS A&B	04	00			00	
		0-3		0-3		PLT 13			TS A&B	04	02			08	
		0-3		0-3		PLT 12			TS A&B	04	04			16	
		0-3		0-3		PLT 11			TS A&B	04	06			24	
		0-3		0-3		PLT 10			TS A&B	04	01			04	
		0-3		0-3		PLT 9			TS A&B	04	03			12	
		0-3		0-3		PLT 8			TS A&B	04	05			20	
		0-3		0-3		PLT 7			TS A&B	04	07			28	
1	811A	0-3		0-3		PLT 6			TS A&B	04	00			01	
						UTJF- 1 H BAY 0									
1	809A	0-3		0-3		PLT 19			TS A&B	04	02			09	
		0-3		0-3		PLT 20			TS A&B	04	04			17	
		0-3		0-3		PLT 21			TS A&B	04	06			25	
		0-3		0-3		PLT 22			TS A&B	04	01			05	
		0-3		0-3		PLT 23			TS A&B	04	03			13	
		0-3		0-3		PLT 24			TS A&B	04	05			21	
		0-3		0-3		PLT 25			TS A&B	04	07			29	
		0-3		0-3		PLT 26			TS A&B	04	00			02	
1	811A	0-3		0-3		PLT 27			TS A&B	04	02			10	

WESTERN ELECTRIC CO., INC. PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 17—Junctur Circuit Cable Running List

ESS TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDER ESS PAGE
 JCTR ASSIGN PGM ISSUE 02 ** SWITCHBOARD CABLE CONNECTING TABLE ** *** SECTION G ***

BINDER LEAD TERMINAL FUNCTIONAL OPTION BINDER LEAD TERMINAL FUNCTIONAL OPTION
 SECTION COLOR DESIGNATION & NOTE SECTION COLOR DESIGNATION & NOTE

809A CA FROM UTJ(COH), BAY 0
 J2H017A-1(J2H017DA-1 UNIT)
 UNIT T.S. A AND T.S. B

809A CA FROM UTJ(CCH), BAY 2
 J2H017A-1(J2H017DA-1 UNIT)
 UNIT T.S. A AND T.S. B

TO JGFR-00

TO JGFR-00

CCED 2H022010 SHEET 5
 J2H022A-1(J2H022AA-1 UNIT)

CCED 2H022-10 SHEET 5
 J2H022A-1(J2H022AA-1 UNIT)

VERT FILE(04),SHELF-0,UTJ PLATE NO. 19

VERT FILE(04),SHELF-0,UTJ PLATE NO. 14

T.S. A 00
 BL BL1W 1 OT1
 BL BL2W 2 OR1
 BL 01W 3 1T1
 BL 02W 4 1R1
 BL 01W 5 2T1
 BL G2W 6 2R1
 BL BR1W 7 3T1
 BL BR2W 8 3R1

T.S. A 00
 BL BL1W 9 OT1
 BL BL2W 10 OR1
 BL 01W 11 1T1
 BL 02W 12 1R1
 BL G1W 13 2T1
 BL G2W 14 2R1
 BL BR1W 15 3T1
 BL BR2W 16 3R1

CONN J 00
 BL S1W 1 CT
 BL S2W 2 OR
 BL BL1R 4 1T
 BL BL2R 5 1R
 BL 01R 7 2T
 BL 02R 8 2R
 BL G1R 10 3T
 BL G2R 11 3R

CONN J 00
 BL S1W 13 OT
 BL S2W 14 OR
 BL BL1R 16 1T
 BL BL2R 17 1R
 BL 01R 19 2T
 BL 02R 20 2R
 BL G1R 18 3T
 BL G2R 21 3R

VERT FILE(04),SHELF-2,UTJ PLATE NO. 20

VERT FILE(04),SHELF02,UTJ PLATE NO. 13

T.S. A 08
 BL BR1R 1 OT1
 BL BR2R 2 OR1
 BL S1R 3 1T1
 BL S2R 4 1R1
 BL BL1BK 5 2T1
 BL BL2BK 6 2R1
 BL 01BK 7 3T1
 BL 02BK 8 3R1

T.S. A 08
 BL BR1R 9 OT1
 BL BR2R 10 OR1
 BL S1R 11 1T1
 BL S2R 12 1R1
 BL BL1BK 13 2T1
 BL BL2BK 14 2R1
 BL 01BK 15 3T1
 BL 02BK 16 3R1

CONN J 08
 BL G1BK 1 OT
 BL G2BK 2 OR

CONN J 08
 BL G1BK 13 OT
 BL G2BK 14 OR

WESTERN ELECTRIC CO., INC. PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 18—Switchboard Cable Connecting Table

ESS TOWN STATE TELCO TELCO ORDER NO BUILDING TELCO BASE DATE RUN W.E.ORDER ESS PAGE
 JCTR ASGNS GEN SYS ISSUE 04 ** JCTR. CAPACITY EVALUATION AND PATTERN ANALYSIS ** *** SECTION 1 ***

JUNCTOR GROUP SIZES WHEN SECTOR 1 REMOVED FROM SERVICE

CIRCUIT							WIRE						
LTN	00	01	02	03	04	05	LTN	00	01	02	03	04	05
00	01	02	02	02	02	01	00	05	06	09	09	02	01
01		02	03	02	02	00	01		04	06	09	04	02
02			02	02	01	01	02			04	06	05	01
03					01	02	03				05	02	01
04						01	04					12	02
05							05						02

JUNCTOR GROUP SIZES WHEN SECTOR 1 RESTORED TO SERVICE

CIRCUIT							WIRE						
LTN	00	01	02	03	04	05	LTN	00	01	02	03	04	05
00	02	03	03	03	02	01	00	06	07	11	11	04	03
01		02	03	03	03	01	01		06	09	11	05	03
02			02	03	02	02	02			06	08	06	01
03				02	02	01	03				06	04	02
04					02	00	04					13	06
05						02	05						04

WESTERN ELECTRIC CO., INC.

PRINTED IN U.S.A.

*** SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER PAGE ***

Fig. 19—Juncture Capacity Evaluation and Pattern Analysis

To diagnose the subgroup, type in

```
M JC:DGN:mmnn sl!
mmnn = 0202
sl = 44
```

Before the subgroup is reassigned, the printout will be for the old subgroup (mmnn sl = 0000 54).

After the subgroup is reassigned, the printout will be for the new subgroup (mmnn sl = 0202 44).

New Circuits—New JNN, New SPN

3.04 These circuits were added as a result of growth and cannot be diagnosed until the sector on which they appear is removed from service in accordance with the procedures in Part 4. The JASINT program is then used to test these circuits. After a successful JASINT test, the circuits may be diagnosed using their new assignments.

4. JUNCTOR REASSIGNMENT PROCEDURES

4.01 A simplified flow chart of the junctor reassignment procedure is shown in Fig. 20.

4.02 ♦For No. 2B ESS, a minimum of six (6) tape cartridges (two in service plus four spares) must be available to properly back up the reassignment procedure. Do not proceed without four spare cartridges.♦ The specific procedures for junctor reassignment are as follows:

- (1) Perform a recent change update in accordance with Section 232-004-301 for No. 2 ESS and Section 232-304-301 for No. 2B ESS.
- (2) Request the peripheral unit (PU) status and network fabric out-of-service list, trunk and service circuit status list by the following input messages:

```
M PU:SI!
M TK:SI!
M SV:SI!
```

Note: ♦M SV:SI! message is not needed for LO-1, Issue 4.6 and later, EF-1, Issue 3.4 and later, and 2B—EF-1.♦

Clear any troubles indicated by these lists before proceeding. These include such items as bad access entries, bad address entries, scanner faults, controller troubles, and links out-of-service.

(3) Ensure that the four line ferros associated with the no test vertical TENs on added networks have been unstrapped (see SD-2H076 and SD-2H163).

(4) Load the new translations obtained from the ODA run in the program stores (PSs) using Section 232-124-301 ♦for No. 2B ESS and Section 232-324-301 for No. 2B ESS.♦

(5) Inhibit all scheduled automatic progression and machine demand requests for circuit junctor tests by typing the following input message:

```
M JC:AT:0!
```

The OK response indicates that the request has been completed.

(6) Junctor diagnostics should be run (in addition to system evaluation prescribed by Section 232-125-501 ♦for No. 2 ESS and Section 232-325-501 for No. 2B ESS.♦) and successfully passed using the new translations before proceeding (see Part 3 for method of diagnosing circuit junctors). In addition, a reasonable "soak" period for the new translations should be successfully completed before proceeding.

(7) ♦**Applies only to No. 2 ESS:** ♦ Obtain 8 A-type and 4 B-type spare memory cards for use in updating the RC area of CS to reflect the status of the Junctor Subgroup Flag Table after each sector is restored (see Step 19). The cards from the spare memory module for generic program patches may be used for this purpose.

Note: If service orders exist in the RC area at the time of update, more cards than previously specified will be required.

- (8) Clear any repeat tests which may be in progress by typing the following TTY input message:

◆For No. 2 ESS offices type in:◆

M SY:CLR!

◆For No. 2B ESS offices type in:

CLR:RPT:STEP!◆

- (9) Type the following TTY input messages to not allow the automatic long term periodic exercises.

◆For No. 2 ESS offices type in:◆

M CU:NLW:13! (Inhibit CU exercises)

M CU:NLW:14! (Inhibit PU exercises)

◆For No. 2B ESS offices type in:◆

INH:MSF 12! (Inhibit CU exercises)

INH:MSF 13! (Inhibit PU exercises)

- (10) Initialize the program to ◆begin the◆ junctor ◆reassignment procedure◆ by typing the following TTY input message:

M JP:BGN!

Note: This message initializes the control area for the junctor reassignment (JASINT) program. Part of this initialization is that the progression is automatically put in the forward mode (see Part 7).

- (11) To simplify recovery in case of machine problems, it is strongly recommended that:

- An RC update be performed before each sector is removed from service. ◆For No. 2B ESS, use the tape copy procedure in Section 232-309-305 to generate one backup cartridge. Label the cartridge appropriately and save for possible use in the Backing-Up Procedure. For No. 2 ESS, all◆ memory cards must be saved which are removed

from the machine during the junctor reassignment period.

- Service order activity should be strictly limited to a minimum during the entire junctor reassignment period to minimize the number of ◆customers that would be affected◆ in the case of machine problems which would require backing up (see Part 6).

- (12) Remove sector x from service by typing the following input message:

M JP:RMV:SCT x!

x = Sector number to be removed from service (0-3).

Note: Only one sector should be removed, tested and restored at a time before proceeding to the next sector.

The RMV message makes an RC entry in CS to update the Junctor Subgroup Flag Table and is effective immediately upon completion of the remove function. Strictly speaking the M JP RMV message does not remove any junctors from service. Instead, the RC entry for the Junctor Subgroup Flag Table serves to deny call processing access to the junctors on sector x. As soon as all the *in service* junctors in sector x become service idle the following output message will be printed:

MR JP SCT x IDL

Note: If, after five minutes, sector x has not become service idle, the following input message may be typed requesting a list of service busy junctors.

M JP:LST:SCT x!

A printout will follow indicating the service busy junctors.

MR JP LST a bb cde ff ghi

a = 0 for wire junctor or 1 for circuit junctor

bb = Network (LTN) A number (0-14)

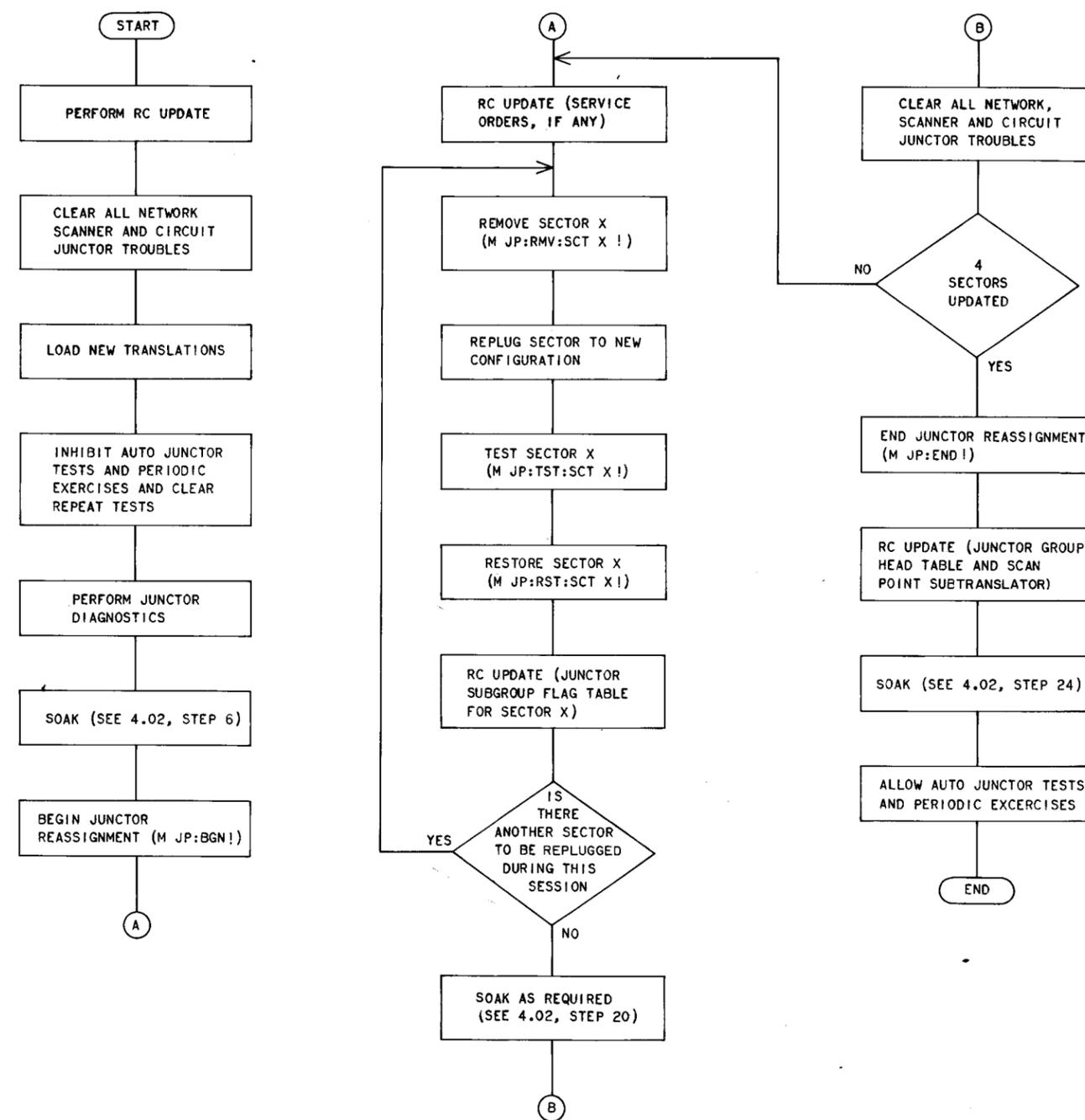


Fig. 20—Flow Chart of Junctor Reassignment Procedure

cde = Switch, level, grid of network A
in octal

ff = Network (LTN) B number (0-14)

ghi = Switch, level, grid of network B
in octal.

(13) After receiving the verification that all junctors are idle, remove all plugs in sector x and straighten out the cables.

(14) Replug sector x following Section E (see example, Fig. 16) of the junctor assignment output forms.

Note: When a sector plug-up is finished, no plugs will be left unplugged in that sector in all JGF verticals. This condition can be used as a partial check of the accuracy of the job.

(15) Request a test of all junctors (wire and circuit) located on JGF sector x by typing the following input message:

M JP:TST:SCT x!

Two tests are performed as follows:

- A dc continuity test on all wire and circuit junctors.

- A circuit junctor diagnostic test on all circuit junctors.

The failing junctors will be removed from service.

(16) The system will begin testing the junctors in sector x. One or more of the following output messages will be printed indicating the status of the tests:

MR JP TST ATP
MR JP TST tavy a bb cde ff ghi
MR JP TST REJ
MR JP TST END
MR JC ATP
MR JC END

Note: Refer to the output message manual (OM-2H200) for the explanation of the message and variable fields for each message.

(17) The output message

MR JP TST ATP

only indicates that all junctors in sector x have been tested, with a TTY message:

MR JP TST tavy a bb cde ff ghi

being generated for each junctor that failed the dc tests. See Table B for guidelines on trouble locating.

TABLE B

GUIDELINES FOR TROUBLE LOCATING

There are several conditions which may develop as a result of the junctor tests. The following listing of conditions and the actions to be taken may be helpful as a guideline for locating troubles.	
CONDITION	ACTION
(a) A multitude of junctor failures occur that cannot be corrected using the new junctor reassignment interconnection sheet, Section E (see example, Fig. 16).	This may indicate faulty translation data or incorrect Section E. An earnest attempt should be made to ensure that the translation data is really at fault. Once this is established the maintenance personnel should use the procedure in Part 6 to revert back to the original configuration. If the trouble is hardware oriented, repair if possible and proceed to 5.01(b).
(b) One or more subgroup failures.	Make sure that the connections involved are seated properly, and that the plug-jack arrangement on the JGF sector is correct. If several subgroups fail, tear down the new connections, replug the old connections (use procedures in part 6 to revert back), and try to locate the trouble. If the subgroups fail in pairs, there is probable a plug-jack connection error. Resubmit the test using TTY input message MP JP:TST:SCT x!
(c) A few junctor failures in one or two subgroups.	Individual junctor continuity can be checked with a volt-ohm-milliammeter. For circuit junctor failure, the cause can be determined by looking up the trouble number in the TLM. If the output indicates that the network controller FST points are not normal, perform a network diagnostic test by typing the following TTY input message: M NW:DGN:fg hi jklm!
(d) Output message printed indicating that the JGF sector has been tested, but the continuity tests for one or more of the junctors have not been performed due to the fact that no idle network paths, test verticals, or line terminals could be found to test the junctors. The output message will be: MR JP TST END	Resubmit the test on the specific sector using TTY input message MP JP:TST:SCT x!
(e) A circuit junctor passes the dc continuity test but fails the junctor diagnostic test.	The associated diagnostic-type printout should be used to locate the fault. After failing junctors have been repaired, submit the test request again. <i>Note:</i> A diagnostic test can be requested on any failing circuit junctor via the normal test by typing the following TTY input message: M JC:DGN:mmnn slg!

Caution: In no event should a sector be restored if an entire junctor subgroup is marked out of service. A subgroup marked out of service greatly reduces the call carrying capabilities for the two networks involved.

- (18) If all junctors passed the tests, restore sector x by typing the following TTY input message:

M JP:RST:SCT x!

This message makes an RC entry in CS to update the Junctor Subgroup Flag Table and is effective immediately upon completion of the restore function. As a result call processing is now allowed to select junctors on sector x (see Part 7).

Note: Junctors that failed tests in step 16 will be left out of service.

- (19) Update the RC area of CS to reflect the current status of the Junctor Subgroup Flag Table (see JTRSUB in PA-2H200) for the sector just restored. Follow the procedures in Section 232-004-301 to update the RC area of CS for No. 2 ESS or Section 232-304-301 to update the RC area of main store for No. 2B ESS. For No. 2B ESS, omit this step after restoring sector 3 only.

Note: For No. 2 ESS always use spare memory cards for the RC update (see Step 7). All cards which are removed should be saved and marked with the number of the sector which has just been restored to service. These cards (saved) will aid in the Backing-Up Procedure, should it become necessary (see Part 6). As a reminder, the newly written cards contain the modifications described above to JTRSUB, as well as any service orders currently in the machine.

Note: For No. 2B ESS, use the tape copy procedure in Section 232-309-305 to generate one backup cartridge. This cartridge should be saved and labeled with the number of the sector which has just been restored to service. This cartridge (saved) will be required in the Backing-Up Procedure, should it become necessary (see Part 6). As a reminder, the TRNSLN file of this tape cartridge contains

the modifications described above to JTRSUB as well as any service orders currently in the machine.

- (20) If more than one sector is to be reconfigured on the first day of junctor reassignment, repeat Steps 12 through 19 for each sector involved. A good practice is to reconfigure as few sectors as practical on the first day (see 2.07 through 2.09).

Note 1: A "soak" period should follow the first day's junctor reassignment. During this soak period perform any required network rebalancing (see DFMP Division H, Section 10t2).

Note 2: If any problems are encountered during the soak period proceed to Part 6.

- (21) After the soak period, reconfigure the remaining sectors over as many days as needed (see DFMP Division H, Section 10t2). Start by clearing the status lists per Step (2) and repeat Steps 11 through 19 for each sector to be reconfigured.

Note: If any problems are encountered proceed to Part 6.

- (22) After all four sectors have been updated type the following TTY input message:

M JP:END!

This message will automatically zero the "Reassignment in Progress" flags in the Junctor Group Head Table and change the "Subtranslator Code (STC)" for junctor circuits in the Scan Point Translator.

Caution: This data is placed in the RC area of CS and is not effective until a recent change update is performed in Step 23.

The system will respond with one of two printouts as follows:

MR JP RC RDY: This printout indicates that the RC area has been written as described by the M JP:END! input message and is ready to be updated.

- MR JP END REJ: This printout indicates that there was not enough space in the RC area. A recent change update must be performed and then the M JP END! request must be resubmitted.

(23) Update the RC area of CS using the procedure given in Section 232-004-301 for No. 2 ESS or update RC area of main store using procedure in Section 232-304-301 for No. 2B ESS. The cards removed from the No. 2 ESS machine in this step should be marked and saved.

(24) After the update is performed (Step 23) a "soak" period is suggested to verify that changes in the "Reassignment in Progress" flags and STC codes (see Step 22) results in a translation configuration that is consistent with the new junctor reassignment. If consistency of the translation configuration and the junctor reassignment does not exist, it will be necessary to reinsert the memory cards (saved in Step 23) into the No. 2 ESS machine (see Section 232-004-301, for translation data recovery) or perform a backdate bootstrap procedure in the No. 2B ESS office (see Section 232-304-301). If the translation data recovery does not clear the machine problems and these problems appear to be *related* to the junctor reassignment, follow the procedure for "backing up" (see Part 6).

(25) Allow the automatic junctor testing and automatic long term periodic exercises by typing TTY input messages:

For No. 2 ESS offices type in:

M JC:AT:1!
M CU:RNL:13!
M CU:RNL:14!

For No. 2B ESS offices type in:

M JC:AT:1!
ALW:MSF 12!
ALW:MSF 13!

This completes the junctor reassignment procedure.

5. TROUBLE RECOVERY

5.01 The personnel responsible for the junctor reassignment should be aware of the use of certain priority messages which will be useful if

troubles are encountered during the reassignment procedure.

- (a) If a RECENT CHANGE (STABLE or TRANSIENT) initialization must be performed for any reason during this procedure, type TTY input message

MP JP:RMV:SCT x!

x = sector being updated (0-3).

Note: This message should be inputted immediately after the initialization procedure. If the machine will not accept the message on the first try, repeat the message as soon as the machine starts to return to normal.

An initialization of RECENT CHANGE type will clear the Junctor Subgroup Flag Table and this input message is necessary to establish the reassignment at a known starting point by writing the proper information into the flag table.

- (b) If troubles were encountered during the first test of a sector and the test was not completed, clear the troubles and retest the sector by typing TTY input message:

MP JP:TST:SCT x!

x = sector to be tested (0-3)

If the second test is unsuccessful and the decision is made to back up, proceed to Part 6.

6. REVERTING (BACKING-UP) PROCEDURE

6.01 If trouble is encountered and it becomes necessary to revert to the original junctor configuration on one or more sectors for any reason, use the following procedure.

Note: Steps 1 through 4 should be attempted first. If any or all of Steps 1 through 4 are unsuccessful, it may become necessary to proceed with Steps 5 through 8.

- (1) Type the following TTY input message:

M JP:SEQ:BWD!

This message will allow the junctor reassignment to be performed in the reverse order (starting

with the sector presently being updated and proceeding back to sector 0).

- (2) Remove all trunks and service circuits on the new networks from service that were reassigned as a result of rebalancing the networks.
- (3) Reassign any lines on the new networks to their original networks.
- (4) Reconfigure junctors on a sector basis to the original configuration using the procedures in 4.02, Steps 11 through 19. Repeat Steps 11 through 19 until all sectors have been replugged to their original configuration or until a working configuration has been obtained. Nothing can be done at this point until the fault in the translation data can be determined. Since this may take some time be sure to allow long term periodic exercises by typing:

◆For No. 2 ESS offices type in:◆

M CU:RNL:13!
M CU:RNL:14!

◆For No. 2B ESS offices type in:

ALW:MSF 12!
ALW:MSF 13!◆

The new translation data can be left in the system including the junctor reassignment flags. When the problem has been resolved, the maintenance personnel can later go back to the forward progression mode by typing the following TTY input messages:

◆For No. 2 ESS offices type in:◆

M CU:NLW:13!
M CU:NLW:14!
M JP:SEQ:FWD!

◆For No. 2B ESS offices type in:

INH:MSF 12!
INH:MSF 13!
M JP:SEQ:FWD!◆

Note: All junctors must be service idle before the M JP:SEQ message will be accepted. Junctors are service idle as a result of input messages

M JP:RMV:SCT x!
and
M JP:TST:SCT x!



If any or all of Steps 1 through 4 cannot be completed due to machine problems, it may be necessary, as a last resort, to replace the memory cards ◆for No. 2 ESS (or load the backup tape cartridge for No. 2B ESS)◆ saved in Step 19 of 4.02. The cards (or tape) must be reinserted in reverse order, one sector at a time. If the decision is made to replace the memory cards (or tape) proceed with Steps 5 through 8.

- (5) Attempt to deny access to the most current sector being updated by typing TTY input message:

MP JP:RMV:SCT x!

x = sector number (0-3)

Note: Proceed immediately to Step 6 even if this message is not accepted.

- (6) Replug the sector specified in Step 5 to the original configuration.
- (7) ◆For No. 2 ESS offices:◆

Replace the original translation memory cards for the sector specified in Step 5 (see Section 232-004-301 for translation data recovery procedures).

◆For No. 2B ESS offices:

Replace the original translation memory data for the sector specified in Step 5. This is accomplished by performing a RECENT CHANGE initialization, replacing the current tape cartridges with the appropriate backup tape (either TDC), and performing the Recovery From Backdate Procedure-Method A (tape to store) in Section 232-304-301.◆

The specified RECENT CHANGE initialization will ensure that the junctors denied access in Step 5 are now allowed access as they were prior to reassignment for this sector,

and also because new translations can never be put into a system with data in the recent change area.

(8) Repeat Steps 5 through 7 for the remaining sectors until all junctors have been replugged to their original configuration or until a working configuration has been obtained. Nothing can be done at this point until the fault in the translation data can be determined. Since this may take some time be sure to allow long term periodic exercises by typing:

For No. 2 ESS offices type in:

M CU:RNL:13!
M CU:RNL:14!

For No. 2B ESS offices type in:

ALW:MSF 12!
ALW:MSF 13!

The new translation data can be left in the system including the junctor reassignment flags. When the problem has been resolved the maintenance personnel can later go back to the forward progression mode by typing the following TTY input messages:

For No. 2 ESS offices type in:

M CU:NLW:13!
M CU:NWL:14!
M JP:SEQ:FWD!

For No. 2B ESS offices type in:

INH:MSF 12!
INH:MSF 13!
M JP:SEQ:FWD!

Note: All junctors must be service idle before the M JP:SEQ message will be accepted. Junctors are service idle as a result of input messages

M JP:RMV:SCT x!
M JP:TST:SCT x!

7. EFFECTS OF SEQUENCE MESSAGES ON REMOVE AND RESTORE MESSAGES

7.01 The following information is provided to explain the effects of the sequence messages on the remove and restore messages for each sector.

(a) The purpose of TTY input message

M JP:SEQ:FWD!

is to change the progression of the junctor reassignment to proceed in the forward direction from the current sector to sector 3. This message remains in effect until the backward message [see(b)] is typed in or until the junctor reassignment procedure is ended. The FWD message affects the remove, test, and restore messages for each sector as follows:

(1) TTY input message

M JP:RMV:SCT x!

will deny call processing access to all junctors on sector x.

(2) TTY input message

M JP:TST:SCT x!

will test the **new** junctor configuration.

(3) TTY input message

M JP:RST:SCT x!

allows call processing access to junctors on sector x using the assignments for the **new** junctor configurations.

(b) The purpose of TTY input message

M JP:SEQ:BWD!

is to allow the progression of the junctor reassignment to proceed in the reverse direction, from the current sector to sector 0. The BWD message remains in effect until the forward message [see (a)] is typed in. This message affects the remove, test, and restore for each sector as follows:

(1) TTY input message

M JP:RMV:SCT x!

will deny call processing access to all junctors in sector x.

(2) TTY input message

M JP:TST:SCT x!

will test the configuration for sector x *prior* to reassignment.

(3) TTY input message

M JP:RST:SCT x!

will allow call processing access to the junctors using the assignments for sector x *prior to reassignment*.

8. WRONG SECTOR SPECIFIED ON A PRIORITY INPUT MESSAGE (MP JP __)

8.01 In the event the wrong sector number was specified on one of the following priority (P) input messages, it is imperative that the error be corrected immediately.

MP JP:RMV:SCT y!
MP JP:TST:SCT y!
MP JP:RST:SCT y!

8.02 If sector x (instead of sector y) was intended, the error can be corrected by typing the following TTY input messages.

◆For No. 2 ESS offices type in:◆

M CU:NLW:9!
MP JP:RMV:SCT x!

x = the correct sector number.

◆For No. 2B ESS offices type in:

INH:MSF 5!
MP JP:RMV:SCT x!

x = the correct sector number◆

Note: The M CU:NLW:9! for No. 2 ESS ◆or INH:MSF 5! for No. 2B ESS◆ message is only

required if the MP JP:TST:SCT x! message was specified. The M CU:NLW:9! for No. 2 ESS ◆or INH:MSF 5! for No. 2B ESS◆ message will abort the junctor reassignment test that is in progress.

After the abort message is printed, type in:

◆For No. 2 ESS offices type in:◆

M CU:RNL:9!

◆For No. 2B ESS offices type in:

ALW:MSF 5!◆

This will allow the multiscan function for junctor reassignment. At this point the reassignment may proceed normally.

9. HOW TO ABORT THE JUNCTOR REASSIGNMENT TEST

9.01 If for some reason, it is desired to abort the test specified by M(P) JP TST SCT X, type in the following TTY input message:

◆For No. 2 ESS offices type in:◆

M CU:NLW:9!

◆For No. 2B ESS offices type in:

INH:MSF 5!◆

After the abort message is printed type input message:

◆For No. 2 ESS offices type in:◆

M CU:RNL:9!

◆For No. 2B ESS offices type in:

ALW:MSF 5!◆

The junctor reassignment test is a multiscan function TTY request of on-line peripheral unit diagnostics and will be aborted by the preceding messages.