Lucent Technologies
Bell Labs Innovations

4ESS™ Switch With 1B Processor

XTSI Software Update

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Document Title: **4ESS**™ Switch With 1B Processor XTSI Software Update

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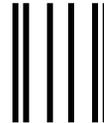
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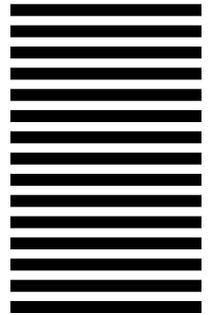
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Acceptance

Acceptance tests are not required for verification of the update procedures contained in this volume. The readiness of the XTSI software to become part of the operating system is established by the successful completion of the particular procedure in its entirety.

Perform Software Update on Operational XTSI Release 1 or 2 Cabinets (4E23 or Earlier Generic)

DO THE ITEMS BELOW IN THE ORDER LISTED	FOR DETAILS, GO TO	
Notes: 1. **4ESS**™ Switch must be running on 4E23 or earlier generic. 2. If coordinated updates are required between 1B Processor and XTSI, this procedure must be performed first. 3. This procedure is to be performed for one equipped XTSI at a time; and then repeated for each XTSI in office. 4. Appropriate Input/Output Manuals must be used if clarification of input message or output message is necessary. 5. This procedure must be performed during light traffic periods. 6. **4ESS**™ Switch and 3B APS operation must be closely monitored while performing this procedure. 7. Corrective action must be taken immediately on any activity that may cause any unit to go out of service (OOS). 8. Stability of office must be maintained throughout this procedure.		
1	If AIMS terminal is going to be used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
2	Notify next higher support group that XTSI software update is being performed. Request information to complete Steps 3 through 6.	TELCO	—
3	Ensure BWM containing new XTSI software is applied and permed.	TELCO	—
4	Ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
5	Ensure **4ESS** switch is in stable condition.	TELCO	DLP-501
6	Ensure any 1B Processor and/or system problems have been cleared before performing this procedure.	TELCO	—
7	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
8	At 3B MCRT, verify XTSI files stored in 3B computer (**EXC:ENVIR:UPROC,FN"/tools/prxtsihdrs"**).	TELCO	DLP-503

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
9	At 3B MCRT, enter message **INH:DMQ;SRC REX!** to inhibit 3B REX. Response: **INH DMQ COMPLETED**	TELCO	—
10	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
11	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
12	Verify XTSI unit type (UT) translator (even XTSI member number) (**VER:UTYPE:XTSI** a**,ME!**)	TELCO	DLP-504
13	Verify software versions and D3U and SPU (if equipped) equipage (**VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1** a**!**).	TELCO	DLP-505
14	At 1B Processor MTC terminal, copy current XTSI files in case back out is required.	TELCO	DLP-506
15	At 1B Processor MTC terminal, verify XTSI is fully operational and can operate with the other controller active (**SW:XTSI** a**!**).	TELCO	DLP-516
16	Recent change software version to 1 for controllers, D3Us, and SPUs, if equipped.	TELCO	DLP-507
17	Wait for 15-minute report and verify no problems are present from software that was copied. If problems are encountered, software version must be changed back to 0. **Do not continue if software version is changed back to 0**. Contact next higher support group for direction.	TELCO	DLP-508
18	Copy new XTSI system files from 3B computer to both XTSI CIA packs (version/location 0 within CIA).	TELCO	DLP-509
19	Verify XTSI system files were copied properly.	TELCO	DLP-510

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
20	If XTSI being updated is equipped with SPUs, perform Steps 21 and 22; otherwise, go to Step 23.	TELCO	—

	Notes:
	  1. SPUs should be updated in following SPU order: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, and 11.
	  2. Protection-switch SPUs are used temporarily to handle their respective traffic while client SPUs are updated.		

21	Update one operational SPU in XTSI being updated (**UPD:XTSI** a**,SPU** b**,SVN 0!**).	TELCO	DLP-513
22	Repeat Step 21 for each operational SPU (1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11).	TELCO	—

	Notes:
	  1. D3Us should be updated in following D3U order: 0, 1, 2, 3, 4, 5, 6, and 7.
	  2. Protection-switch D3Us are used temporarily to handle their respective traffic while client D3Us are updated.		

23	Update one operational D3U in XTSI being updated (**UPD:XTSI** a**,D3U** b**,SVN 0!**).	TELCO	DLP-511
24	Repeat Step 23 for each operational D3U (1, 2, 3, 4, 5, 6, 7).	TELCO	—
25	Update controllers 0 and 1 in XTSI being updated (**UPD:XTSI** a**,XTC,SVN 0!**).	TELCO	DLP-512
26	At 1B Processor MTC terminal, enter message **OP:OOSUNITS:XTSI!** and ensure XTSI being updated is not listed.	TELCO	—
27	Request next higher support group to determine if soak interval is required. Proceed as directed by next higher support group.	TELCO	—
28	Recent change and verify software version to 0 for controllers, D3Us, and SPUs, if equipped.	TELCO	DLP-508
29	Safe point to temporarily stop this procedure. If stopping, perform Steps 30 through 33; otherwise, go to Step 40.	TELCO	—
30	At 3B MCRT, enter message **ALW:DMQ;SRC REX!** to allow 3B REX. Response: **ALW DMQ ENABLED REX**	TELCO	—

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
31	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—
32	If AIMS terminal is being used to enter input messages, remove floppy disk from AIMS terminal and store per local practice.	TELCO	—
33	Stop procedure for now. Continue at Step 34 when resuming.	TELCO	—
34	If AIMS terminal is being used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
35	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
36	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
37	At 3B MCRT, enter message **INH:DMQ;SRC REX!** to inhibit 3B REX. Response: **INH DMQ COMPLETED**	TELCO	—
38	Request next higher support group to ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
39	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
40	Repeat Steps 12 through 29 for each operational XTSI.	TELCO	—
41	At 3B MCRT, determine if diagnostic software was changed during this update and if the diagnostics were changed. Determine which unit type was affected (controller, D3U, and/or SPU) (**DUMP:FILE:ALL,FN"/etc/bwm/**AAAxx-xxxx**/SCANS"!**).	TELCO	DLP-514
42	If controller, D3U, and/or SPU diagnostic software was changed, perform Step 43; otherwise, go to Step 44.	TELCO	—
43	At 1B Processor terminal, diagnose unit type(s) that was affected by diagnostic software changes (controller, D3U, and/or SPU) using restore message.	TELCO	DLP-515

	DO THE ITEMS BELOW IN THE ORDER LISTED	FOR DETAILS, GO TO	
44	At 3B MCRT, enter message **ALW:DMQ;SRC REX!** to allow 3B REX. Response: **ALW DMQ ENABLED REX**	TELCO	—
45	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—

Perform Software Update on Operational XTSI Release 1 or 2 Cabinets (4E23 to 4E24 Retrofit Only)

DO THE ITEMS BELOW IN THE ORDER LISTED	FOR DETAILS, GO TO	
Notes: 1. This procedure is only to be performed when retrofitting the 1B Processor from 4E23 to 4E24 generic. 2. **4ESS**™ Switch must be running on 4E23 generic. 3. If coordinated updates are required between 1B Processor and XTSI, this procedure must be performed first. 4. This procedure is to be performed for one equipped XTSI at a time; and then repeated for each XTSI in office. 5. Appropriate Input/Output Manuals must be used if clarification of input message or output message is necessary. 6. This procedure must be performed during light traffic periods. 7. **4ESS**™ Switch and 3B APS operation must be closely monitored while performing this procedure. 8. Corrective action must be taken immediately on any activity that may cause any unit to go out of service (OOS). 9. Stability of office must be maintained throughout this procedure.		
1 If AIMS terminal is going to be used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
2 Notify next higher support group that XTSI software update is being performed. Request information to complete Steps 3 through 6.	TELCO	—
3 Ensure BWM containing new XTSI software is applied and permed.	TELCO	—
4 Ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
5 Ensure **4ESS** switch is in stable condition.	TELCO	DLP-501
6 Ensure any 1B Processor and/or system problems have been cleared before performing this procedure.	TELCO	—
7 Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
8 At 3B MCRT, verify XTSI files stored in 3B computer (**EXC:ENVIR:UPROC,FN"/tools/prxtsihdrs"**).	TELCO	DLP-517

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
9	At 3B MCRT, enter message **INH:DMQ;SRC REX!** to inhibit 3B REX. Response: **INH DMQ COMPLETED**	TELCO	—
10	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED** **AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
11	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
12	Verify XTSI unit type (UT) translator (even XTSI member number) (**VER:UTYPE:XTSI** a,**ME!**)	TELCO	DLP-504
13	Verify software versions and D3U and SPU (if equipped) equipage (**VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1** a!).	TELCO	DLP-505
14	At 1B Processor MTC terminal, copy current XTSI files in case back out is required.	TELCO	DLP-506
15	At 1B Processor MTC terminal, verify XTSI is fully operational and can operate with the other controller active (**SW:XTSI** a**!**).	TELCO	DLP-516
16	Recent change software version to 1 for controllers, D3Us, and SPUs, if equipped.	TELCO	DLP-507
17	Wait for 15-minute report and verify no problems are present from software that was copied. If problems are encountered, software version must be changed back to 0. **Do not continue if software version is changed back to 0**. Contact next higher support group for direction.	TELCO	DLP-508
18	Copy new XTSI system files from 3B computer to both XTSI CIA packs (version/location 0 within CIA).	TELCO	DLP-509
19	Verify XTSI system files were copied properly.	TELCO	DLP-510

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
20	If XTSI being updated is equipped with SPUs, perform Steps 21 and 22; otherwise, go to Step 23.	TELCO	—
	Notes: 1. SPUs must be updated in following SPU order: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, and 11. 2. Protection-switch SPUs are used temporarily to handle their respective traffic while client SPUs are updated.		
21	Update one operational SPU in XTSI being updated (**UPD:XTSI** a,**SPU** b,**SVN 0!**).	TELCO	DLP-513
22	Repeat Step 21 for each operational SPU (1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11).	TELCO	—
	Notes: 1. D3Us must be updated in following D3U order: 0, 1, 2, 3, 4, 5, 6, and 7. 2. Protection-switch D3Us are used temporarily to handle their respective traffic while client D3Us are updated.		
23	Update one operational D3U in XTSI being updated (**UPD:XTSI** a,**D3U** b,**SVN 0!**).	TELCO	DLP-511
24	Repeat Step 23 for each operational D3U (1, 2, 3, 4, 5, 6, 7).	TELCO	—
	Note: Since the XTSI controllers can be audited for hash-sum and would report errors because none of the new expanded locations have any files stored at this time, it is necessary to temporarily base-level messages.		
25	At 1B Processor MTC terminal, enter message **INH:XTSI** a,**BLM!** where a = Even XTSI member number Response: **INH XTSI** a **BLM COMPLETED**	TELCO	—
26	Update controllers 0 and 1 in XTSI being updated (**UPD:XTSI** a,**XTC,SVN 0!**).	TELCO	DLP-512

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
27	At 1B Processor MTC terminal, enter message **OP:OOSUNITS:XTSI!** and ensure XTSI being updated is not listed.	TELCO	—
28	At 3B MCRT, enter message **ALW:DMQ;SRC REX!** to allow 3B REX. Response: **ALW DMQ ENABLED REX**	TELCO	—
29	Request next higher support group to determine if soak interval is required. If a soak is required, perform Steps 30 through 40; otherwise, go to Step 41.	TELCO	—
	Note: The DUMP message forces the controllers to look at the FDT in version 1 since the pointers are set to that version.		
30	Set the XTSI controller's FDT to the old 28-file version (**DUMP:TSIFILE;XTSI** a,**FILE 0,HADR 0,L 20!**).	TELCO	DLP-518
31	At 1B Processor MTC terminal, enter message **ALW:XTSI** a,**BLM!** where a = Even XTSI member number Response: **ALW XTSI** a **BLM COMPLETED**	TELCO	—
32	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—
33	If AIMS terminal is being used to enter input messages, remove floppy disk from AIMS terminal and store per local practice.	TELCO	—
34	Soak XTSI being updated for a minimum of 16 hours. Continue at Step 35 when resuming.	TELCO	—
35	If AIMS terminal is being used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
36	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
37	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
38	At 1B Processor MTC terminal, enter message **INH:XTSI** a,**BLM!** where a = Even XTSI member number Response: **INH XTSI** a **BLM COMPLETED**	TELCO	—
39	Request next higher support group to ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
40	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
	Note: Since the FDT should not need to go back to the smaller 28-version index, the new 48-location FDT index will be copied to the version 1 location.		
41	Recent change and verify software version to 0 for controllers 0 and 1.	TELCO	DLP-519
42	Copy the new 48-file FDT from CIA file 0 to CIA file 1.	TELCO	DLP-521
43	Recent change and verify controllers 0 and 1 software version to 1.	TELCO	DLP-520
44	Copy the new In-Band version 0 file locations with the FDT.	TELCO	DLP-522
45	Recent change software version to 0 for controllers, D3Us, and SPUs, if equipped.	TELCO	DLP-508
46	Copy the remainder of the new file locations with the FDT.	TELCO	DLP-523

DO THE ITEMS BELOW IN THE ORDER LISTED	FOR DETAILS, GO TO	
47	At 1B Processor MTC terminal, enter message **ALW:XTSI** a**,BLM!** where a = Even XTSI member number Response: **ALW XTSI** a **BLM COMPLETED**	TELCO	—
48	Safe point to temporarily stop this procedure. If stopping, perform Steps 49 through 51; otherwise, go to Step 57.	TELCO	—
49	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—
50	If AIMS terminal is being used to enter input messages, remove floppy disk from AIMS terminal and store per local practice.	TELCO	—
51	Stop procedure for now. Continue at Step 52 when resuming.	TELCO	—
52	If AIMS terminal is being used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
53	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
54	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
55	Request next higher support group to ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
56	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
57	Repeat Steps 12 through 48 for each operational XTSI.	TELCO	—

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
58	At 3B MCRT, determine if diagnostic software was changed during this update and if the diagnostics were changed. Determine which unit type was affected (controller, D3U, and/or SPU) (**DUMP:FILE:ALL,FN"/etc/bwm/**AAAxx-xxxx**/SCANS"!**).	TELCO	DLP-514
59	If controller, D3U, and/or SPU diagnostic software was changed, perform Step 61; otherwise, go to Step 62.	TELCO	—
60	At 1B Processor terminal, diagnose unit type(s) that was affected by diagnostic software changes (controller, D3U, and/or SPU) using restore message.	TELCO	DLP-515
61	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—

Perform Software Update on Operational XTSI Release 1, 2, or 3 Cabinets (4E24 or Later Generic)

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
	Notes: 1. **4ESS**™ Switch must be running on 4E24 or later generic. 2. If coordinated updates are required between 1B Processor and XTSI, this procedure must be performed first. 3. This procedure is to be performed for one equipped XTSI at a time; and then repeated for each XTSI in office. 4. Appropriate Input/Output Manuals must be used if clarification of input message or output message is necessary. 5. This procedure must be performed during light traffic periods. 6. **4ESS**™ Switch and 3B APS operation must be closely monitored while performing this procedure. 7. Corrective action must be taken immediately on any activity that may cause any unit to go out of service (OOS). 8. Stability of office must be maintained throughout this procedure.		
1	If AIMS terminal is going to be used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
2	At 1B Processor MTC terminal, enter message **VER:OFFICE!** and ensure office is running on 4E24 or later generic. If office generic is not 4E24 or later generic, this procedure must not be performed.	TELCO	—
3	Notify next higher support group that XTSI software update is being performed. Request information to complete Steps 4 through 7.	TELCO	—
4	Ensure BWM containing new XTSI software is applied and permed.	TELCO	—
5	Ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
6	Ensure **4ESS** switch is in stable condition.	TELCO	DLP-501
7	Ensure any 1B Processor and/or system problems have been cleared before performing this procedure.	TELCO	—
8	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
9	At 3B MCRT, verify XTSI files stored in 3B computer (**EXC:ENVIR:UPROC,FN"/tools/prxtsihdrs"**).	TELCO	DLP-517
10	At 3B MCRT, enter message **INH:DMQ;SRC REX!** to inhibit 3B REX. Response: **INH DMQ COMPLETED**	TELCO	—
11	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
12	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
13	Verify XTSI unit type (UT) translator (even XTSI member number) (**VER:UTYPE:XTSI** a,**ME!**)	TELCO	DLP-504
14	Verify software versions and D3U, SPU (if equipped) and SPU (if equipped) equipage (**VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1** a**!**).	TELCO	DLP-524
15	At 1B Processor MTC terminal, copy current XTSI files in case back out is required.	TELCO	DLP-525
16	At 1B Processor MTC terminal, verify XTSI is fully operational and can operate with the other controller active (**SW:XTSI** a**!**).	TELCO	DLP-516
17	Recent change and verify software version to 1 for controllers, IBU, D3Us, and SPUs.	TELCO	DLP-526
18	Wait for 15-minute report and verify no problems are present from software that was copied. If problems are encountered, software version must be changed back to 0. **Do not continue if software version is changed back to 0**. Contact next higher support group for direction.	TELCO	DLP-527
19	Copy new XTSI system files from 3B computer to both XTSI CIA packs (version/location 0 within CIA).	TELCO	DLP-528
20	Verify XTSI system files were copied properly.	TELCO	DLP-529

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
21	If IBU is defined in XTSI being updated, update IBU (**UPD:XTSI** a,**IBU** b,**SVN 0!**).	TELCO	DLP-530
22	If XTSI being updated is equipped with SPUs, perform Steps 23 and 24; otherwise, go to Step 25.	TELCO	—

	Notes:
	  1. SPUs should be updated in following SPU order: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, and 11.
	  2. Protection-switch SPUs are used temporarily to handle their respective traffic while client SPUs are updated.		

23	Update one operational SPU in XTSI being updated (**UPD:XTSI** a,**SPU** b,**SVN 0!**).	TELCO	DLP-513
24	Repeat Step 23 for each operational SPU (1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11).	TELCO	—

	Notes:
	  1. D3Us should be updated in following D3U order: 0, 1, 2, 3, 4, 5, 6, and 7.
	  2. Protection-switch D3Us are used temporarily to handle their respective traffic while client D3Us are updated.		

25	Update one operational D3U in XTSI being updated (**UPD:XTSI** a,**D3U** b,**SVN 0!**).	TELCO	DLP-511
26	Repeat Step 25 for each operational D3U (1, 2, 3, 4, 5, 6, 7).	TELCO	—
27	Update controllers 0 and 1 in XTSI being updated (**UPD:XTSI** a,**XTC,SVN 0!**).	TELCO	DLP-512
28	At 1B Processor MTC terminal, enter message **OP:OOSUNITS:XTSI!** and ensure XTSI being updated is not listed.	TELCO	—
29	Request next higher support group to determine if soak interval is required. Proceed as directed by next higher support group.	TELCO	—
30	Recent change and verify software version to 0 for controllers, IBU, if equipped, D3Us, and SPUs, if equipped.	TELCO	DLP-527
31	Safe point to temporarily stop this procedure. If stopping, perform Steps 32 through 35; otherwise, go to Step 42.	TELCO	—
32	At 3B MCRT, enter message **ALW:DMQ;SRC REX!** to allow 3B REX. Response: **ALW DMQ ENABLED REX**	TELCO	—

DO THE ITEMS BELOW IN THE ORDER LISTED		FOR DETAILS, GO TO	
33	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—
34	If AIMS terminal is being used to enter input messages, remove floppy disk from AIMS terminal and store per local practice.	TELCO	—
35	Stop procedure for now. Continue at Step 36 when resuming.	TELCO	—
36	If AIMS terminal is being used to enter input messages, load **UPDXTSI.1** command file into terminal.	TELCO	DLP-500
37	At 1B Processor MTC terminal, enter message **INH:MACLI,CLASS MTCE;REX!** to inhibit REX. Response: **REPT:MACLI,CLASS MTCE INHIBITED AUTOMATIC JOB SCHEDULING DISALLOWED**	TELCO	—
38	At 1B Processor MTC terminal, enter message **STOP:TEST;PUSYS!** to stop peripheral system tests. Response: **OK**	TELCO	—
39	At 3B MCRT, enter message **INH:DMQ;SRC REX!** to inhibit 3B REX. Response: **INH DMQ COMPLETED**	TELCO	—
40	Request next higher support group to ensure 1B Processor has not experienced any terminal suspends, bootstraps, diagnostic failures, or overloads within last 24 hours.	TELCO	—
41	Ensure 3B, 1B Processor, and peripheral units are operating in normal duplex mode.	TELCO	DLP-502
42	Repeat Steps 13 through 31 for each operational XTSI.	TELCO	—
43	At 3B MCRT, determine if diagnostic software was changed during this update and if the diagnostics were changed. Determine which unit type was affected (controller, D3U, SPU, and/or IBU) (**DUMP:FILE:ALL,FN"/etc/bwm/**AAAxx-xxxx**/SCANS"!**).	TELCO	DLP-514
44	If controller, D3U, SPU, and/or IBU diagnostic software was changed, perform Step 45; otherwise, go to Step 46.	TELCO	—
45	At 1B Processor terminal, diagnose unit type(s) that was affected by diagnostic software changes (controller, D3U, SPU, and/or IBU) using restore message.	TELCO	DLP-515

	DO THE ITEMS BELOW IN THE ORDER LISTED	FOR DETAILS, GO TO	
46	At 3B MCRT, enter message **ALW:DMQ;SRC REX!** to allow 3B REX. Response: **ALW DMQ ENABLED REX**	TELCO	—
47	At 1B Processor MTC terminal, enter message **ALW:MACLI,CLASS MTCE!** to allow REX. Response: **REPT: MACP AUTOMATIC JOB SCHEDULING RESUMED**	TELCO	—
48	**STOP! YOU HAVE COMPLETED THIS PROCEDURE.**	TELCO	—

Set Up Aims Terminal and Load Command Files

> ***Note:*** The floppy to be loaded contains the input messages to perform XTSI growth/degrowth. If there is a discrepancy between the messages on the floppy and this TOP, the TOP should be followed.

> ***Note:*** It is recommended to use an AIMS terminal assigned to SREC1 channel.

1. Ensure AIMS software is operating (colored function bar displayed at bottom of screen).

2. At AIMS terminal, depress **Shift** and **F4** keys simultaneously to clear screen memory.

3. Depress **F4** key.

   Response: **TERMINAL SETUP** menu displayed.

4. Using arrow keys, move cursor to **Session modes/Session options**.

5. Depress **Return** key.

   Response: **SESSION MODES/SESSION OPTIONS** menu displayed.

6. In **SESSION MODES/SESSION OPTIONS** menu, is **No wraparound/No line wrap** highlighted green?

   If **Yes**, go to Step 9.
   If **No**, go to Step 7.

7. Using arrow keys, move cursor to **No wraparound/No line wrap.**

8. Depress **Return** key to highlight **No wraparound/No line wrap.**

9. In **SESSION MODES/OPTIONS** menu, is **manual receive** highlighted green?

   If **Yes**, go to Step 12.
   If **No**, go to Step 10.

10. Using arrow keys, move cursor to **manual receive.**

11. Depress **Return** key to highlight **manual receive.**

12. Depress **F4** key.

    Response: **TERMINAL SETUP/TERMINAL OPTIONS** menu displayed.

13. Using arrow keys, move cursor to **User Files**.

14. Depress **Return** key **USER FILE SETUP** menu displayed.

15. Using arrow keys, move cursor to **Change user directory**.

16. Depress **Return** key.

    Response: **Change User Directory** window displayed.

17. Place floppy disk labeled **AIMS XTSI Rel-**a **Growth Aid** into drive (a = Release number of floppy).

18. In **Change User Directory** window type and enter **A:E**xx**R**y or B:xxRy (drive that will accept AIMS floppy disks).

    where   xx = office generic (21, 22, 23, 24)
                 y = generic release (1, 2, 3, 4).

19. Depress **F4** key twice.

    Response: Blank screen displayed.

20. Depress **F8** key to inhibit terminal screen from receiving output.

21. Ensure printer associated with AIMS terminal is receiving output.

    Note: No output will be received on terminal display. All output will be observed on the printer.

22. Depress **F3** key twice.

    Response:

    REGION COMMANDS menu displayed.

23. Using arrow keys, move cursor to **Read from file.**

24. Depress **Return** key.

    Response: List of command files displayed.

25. Using arrow keys, move cursor to file to be loaded.

26. Depress **Return** key.

    Response: **Form** entered messages displayed.

    Caution: You must ensure that cursor is on proper command before entering.

27. See TABLE A for running command list from AIMS terminal.

TABLE A	
A.	All portions of command list are in green except variables which are shown in white.
B.	Use Tab key to jump to variable.
C.	Only update variable(s) in command to be entered
D.	After filling in variable(s), depress **Shift** and **Return** keys simultaneously to get cursor to beginning of line.
E.	Use arrow keys to go to line that is desired.
F.	After entering command, cursor will stop at end of command just inputted.
G.	Output must be observed on printer associated with AIMS terminal.
H.	Observe output on printer to determine if command was accepted or blocked (needing to be sent again).
I.	When all commands are completed, depress **Shift** and **Return** keys simultaneously to clear out screen memory.
J.	**OP:RCFORM** a**!** message must not be entered when using AIMS terminal.

28. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Determine if Office is Stable

Note: Figure 1 is based on 7-day rolling average for all per-day measures; all other measures are as indicated. All resolved troubles are discounted from measures.

1. Using Figure 1 for guidelines and office printouts, determine if office meets stable condition.

2. Does office meet stable condition?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution. After resolving, procedure may continue.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

- INTERRUPTS

  1B PROCESSOR · $\leq$ 1 PER MEMBER NUMBER PER DAY
  $\leq$ 3 PER DAY TOTAL

  TMS, NC, PUB, IO, CCIS · · · · · · · · · · · · · · · · · · $\leq$ 2 PER MEMBER NUMBER PER DAY
  $\leq$ 4 PER DAY TOTAL

  PERIPHERY, PER OPERATIONAL SP CORE (SP, DT, TSI, XTSI) OR DIF · · · · $\leq$ 3 PER DAY
  (DIF, TSI COMPLEX) (THE ALLOWABLE DAILY INTERRUPT COUNT
  SHALL NOT EXCEED 25, REGARDLESS OF OFFICE SIZE)

  PBFRs · $\leq$ 1 PER DAY

- INTERJECTS · $\leq$ 2 PER MEMBER NUMBER PER DAY
  $\leq$ 5 PER DAY TOTAL

- BASE LEVEL
  ANALYZED, CORRECTED AND/OR UNDERSTOOD

- PHASES/DUPLEX FAILURES

  DIRECTED PHASE 1 · $\leq$ 1 IN 2 WEEKS

  SYSTEM PHASE 1 · $\leq$ 1 IN 1 WEEK

  PHASE 2 OR 3 · $\leq$ 1 IN 4 WEEKS

  DT, VIF, OR EST · $\leq$ 1 IN 4 WEEKS

  TGR/TER LINK PAIR · $\leq$ 1 IN 2 WEEKS

- OUT-OF-SERVICE UNITS

  NUMBER OUT-OF-SERVICE · $\leq$ 5 AT ANYTIME

  CRITICAL UNITS:
  CC   DIF   PS   TMSP
  CS   LN   PUB   TSI/XTSI
  CU   NCLK   SP

- 3B COMPUTER

  INTERRUPTS · $\leq$ 3 PER DAY

  PHASE 1 · $\leq$ 1 IN 4 WEEKS

  PHASE 2 OR 3 · $\leq$ 1 IN 4 WEEKS

  CNI RING INIT · $\leq$ 1 IN 2 WEEKS

  CNI RING TRANSPORT ERRORS · · · · · · · · · · · · · · · · · · $\leq$ 2 PER DAY

  DLNE ERRORS · $\leq$ 1 PER DAY

Figure 1. Office Stability Guidelines

Ensure all Units are In-Service

1. At 1B Processor MTC terminal, enter message **OP:OOSUNITS!**

2. Does printout list any units?

    If **Yes**, go to Step 3.
    If **No**, go to Step 11.

3. Contact next higher support group to determine if any units in printout must be restored before continuing with procedure.

4. Do any units need to be restored?

    If **Yes**, go to Step 5.
    If **No**, go to Step 11.

    > *Note:* Variables c and d are only to be used if submember is listed.

5. At 1B Processor MTC terminal, enter message for one listed unit **RST:**a b[**,**c d]!

    where   a = Listed unit type
                  b = Listed member number
                  c = Submember type (if listed)
                  d = Submember number (if listed)

    Response: **RST**: a b [c d] **COMPLETED**

6. Was printout received per the response message in Step 5.

    If **Yes**, go to Step 8.
    If **No**, go to Step 7.

7. Clear failure per TABLE A; ATP required. Repeat from Step 5.

TABLE A			
UNIT TYPE	**TROUBLE-CLEARING VOLUME**	**UNIT TYPE**	**TROUBLE-CLEARING VOLUME**
1B Processor	234-351-004	MFS	234-151-041
3B20D Model 1	254-301-812	MISC A, B, C	234-151-043
	254-301-813	NCLK	234-151-013
3B20D Model 2/3	254-302-812	PCDF J5A007B	234-351-025
3B21D	254-303-107	PCDF J5A007C	234-351-026
ADS (TUC and DUS)	234-351-010	PUBB	234-151-015
API	234-351-016	SCS	234-151-077
AUB	234-351-010	SP1	234-151-031
CNI	234-151-120	SP2	234-151-032
DIF	234-151-055	TGR	234-151-033
DT	234-151-045	TMS	234-151-011
EST	234-151-050	TSI	234-151-012
IO J5A006A	234-351-020	VIF	234-151-025
IO J5A006C	234-351-021	XTSI	234-351-011
IO J5A006D	234-351-022		

8. Are more units listed (Step 4)?

    If **Yes**, go to Step 9.
    If **No**, go to Step 10.

9. Repeat Step 5 for next listed unit.

10. Repeat from Step 1.

11. At 3B MCRT, enter message **OP:OOS!**

12. Does printout list any units?

    If **Yes**, go to Step 13.
    If **No**, go to Step 21.

13. Contact next higher support group to determine if any units in printout must be restored before continuing with procedure.

14. Do any units need to be restored?

    If **Yes**, go to Step 15.
    If **No**, go to Step 21.

15. At 3B MCRT, enter message for one listed unit **RST:**a b**!**

    where   a = Listed unit type
                  b = Listed member number.

    Response: **RST: a b COMPLETED**

16. Was printout received per the response message in Step 15?

    If **Yes**, go to Step 18.
    If **No**, go to Step 17.

17. Clear failure per TABLE; ATP required. Repeat from Step 15

18. Are more units listed (Step 14)?

    If **Yes**, go to Step 19.
    If **No**, go to Step 20.

19. Repeat Step 15 for next listed unit.

20. Repeat from Step 11.

21. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify XTSI Files Stored in 3B Computer

> ***Note:*** This command will take from 5 to 15 minutes to complete, depending on system call load.

1. At 3B MCRT, enter message **EXC:ENVIR:UPROC,FN "/tools/prxtsihdrs"**

   > ***Note:*** Some response output may appear in different order due to spooler processing.

   Response:

File(s)	Hash	Sums	Size		Header Date	Issue
EXC ENVIR UPROC /tools/prxtsihdrs STARTED						
xtsi/fdt/xfdtver0	0000	**xxxx**	0000	nnnn	**C**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/fdt/xfdtver1	0000	**xxxx**	0000	nnnn	**C**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xoprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xoprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xdgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xdgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/d3u/doprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/doprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/ddgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/ddgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/spu/soprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/soprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/sdgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/sdgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
EXC ENVIR UPROC /tools/prxtsihdrs COMPLETED SEGMENT 2						

   xxxx = Checksum/Hashsum value for file in hex

   In Header Date Column:
     C means FDT file
     D means DGN file
     O means OPR file

   In Issue Column:
     C means XTC subunit
     U means D3U subunit
     S means SPU subunit
     d.efg is file issue number

2. Using printout and the response message in Step 1, request next higher support
   group to verify that latest XTSI operating and diagnostic files are
   3B computer memory.

3. Save printout for later use to verify that XTSI subunits are
   running on latest software.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify Operational Status of XTSI Being Updated

1. At 1B Processor MTC terminal, enter message
   VER:UTYPE:XTSI a**,ME!**

   where   a = Even XTSI member number

   Response: **VER:UTMN;OPT(ME),CUR: FLN** a,   **UTYN XTSI,**

             MEMN b,   **ME OPER,**

             where   a = Floor location number
                       b = XTSI member number

2. Are message format and member identification correct per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. Using printout and the response message in Step 1, is **ME** set to **OPER**?

   If **Yes**, go to Step 6.
   If **No**, go to Step 5.

5. Software update cannot be performed on this XTSI. Stop update.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify Software Version Pointers and Equipage
For Even-Numbered XTSI

1. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!**
   for even-numbered XTSI being updated.

   where a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):**   **XTSI MEMN** a,

XTCN	0	1									
XTCHV	-,	-,									
XTCSV	0,	0,									
D3UN	0	1	2	3	4	5		6	7		
D3UEQ	b,	b,	b,	b,	b,	b,		b,	b,		
D3UHV	-,	-,	-,	-,	-,	-,		-,	-,		
D3USV	0,	0,	0,	0,	0,	0,		0,	0,		
SPUN	0	1	2	3	4	5	6	7	8	9	10	11
SPUEQ	b,	b,	b,	b,	b,	b,	b,	b,	b,	b,	b,	b,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,

   where   a = XTSI member number
           b = **U** for UNEQ, **G** for GROW, **S** for SGRO, or **O** for OPER.

2. Are message format and member identification correct per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. Using printout and the response message in Step 1, verify all fields for **XTCSV**, **D3USV** and **SPUSV** are set to **0**.

5. Are all **XTCSV**, **D3USV** and **SPUSV** fields set to **0**?

   If **Yes**, go to Step 7.
   If **No**, go to Step 6.

6. Software update cannot be performed on this XTSI. Stop update.

7. Using printout and the response message in Step 1, determine if all fields for **D3UEQ** are set to **O**.

8. Are all **D3UEQ** fields set to **O**?

    If **Yes**, go to Step 12.
    If **No**, go to Step 9.

    > **Note:** D3Us not in OPER state will not be updated.

    > **Caution: If D3U 6 or 7 is not set to O and is in-service, then no clients within the associated group may be updated.**

9. Verify with next higher support group that the D3Us not set to **O** are in proper state.

10. Are D3Us in proper state?

    If **Yes**, go to Step 12.
    If **No**, go to Step 11.

11. No update is allowed, unless D3Us are in proper state.

12. Record D3Us in OPER state for later use. Update will be performed later on these D3Us.

13. Are SPUs equipped in XTSI being updated?

    If **Yes**, go to Step 14.
    If **No**, go to Step 20.

14. Using printout and the response message in Step 1, determine if all fields for **SPUEQ** are set to **O**.

15. Are all **SPUEQ** fields set to **O**?

    If **Yes**, go to Step 19.
    If **No**, go to Step 16.

Note: SPUs not in OPER state will not be updated.

Caution: If SPU 10 or 11 is not set to O and is in-service, then no clients within the associated group may be updated.

16. Verify with next higher support group that the SPUs not set to **O** are in proper state.

17. Are SPUs in proper state?

    If **Yes**, go to Step 19.
    If **No**, go to Step 18.

18. No update is allowed, unless SPUs are in proper state.

19. Record SPUs in OPER state for later use. Update will be performed later on these SPUs.

20. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Back up Current XTSI Files

Note: Input messages must be entered one at a time in sequence from 1 through 7.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

1. At 1B Processor MTC terminal, enter one of the following messages:

   1. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 1!**
   2. **COPY:TSIFILE;XTSI** a**,SFN 4,DFN 5!**
   3. **COPY:TSIFILE;XTSI** a**,SFN 8,DFN 9!**
   4. **COPY:TSIFILE;XTSI** a**,SFN 12,DFN 13!**
   5. **COPY:TSIFILE;XTSI** a**,SFN 16,DFN 17!**
   6. **COPY:TSIFILE;XTSI** a**,SFN 20,DFN 21!**
   7. **COPY:TSIFILE;XTSI** a**,SFN 24,DFN 25!**

   where a = Even XTSI member number

2. Was **COMPLETE** message received?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

   If **Yes**, go to Step 6.
   If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 1 for Controllers, D3Us, and SPUs, if Equipped

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

    Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

    RC:UTYPE;CHG;OPT(SUXTSI),a**:   XTSI MEMN** b,

    ORNU c,

XTCN	0	1
XTCHV	-,	-,
XTCSV	d,	d,

    D3UN   0 1 2 3 4 5   6 7
    D3UEQ   -, -, -, -, -, -,   -, -,
    D3UHV   -, -, -, -, -, -,   -, -,
    D3USV   d, d, d, d, d, d,   d, d,

    SPUN   0 1 2 3 4 5 6 7 8 9   10 11
    SPUEQ   -, -, -, -, -, -, -, -, -, -,   -, -,
    SPUHV   -, -, -, -, -, -, -, -, -, -,   -, -,
    SPUSV   d, d, d, d, d, d, d, d, d, d,   d, d,

    REMARKS   ----------------!

    where   a = **FTA**
            b = XTSI member number
            c = RC order number
            d = **1** for fields in **XTCSV**, **D3USV**, and if equipped, **SPUSV**

3. Was **RC ORNU** c **ACTIVATED** message received?

    If **Yes**, go to Step 5.
    If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
    VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!**
    for even-numbered XTSI being updated.

    where   a = XTSI member number

    Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

XTCN	0	1									
XTCHV	-,	-,									
XTCSV	1,	1,									

D3UN	0	1	2	3	4	5	6	7
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,
D3USV	1,	1,	1,	1,	1,	1,	1,	1,

SPUN	0	1	2	3	4	5	6	7	8	9	10	11
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	1,	1,	1,	1,	1,	1,	1,	1,	1,	1,	1,	1,

    where   a = XTSI member number

6. Are message format and member identification correct per the response message in Step
    5?

    If **Yes**, go to Step 8.
    If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV**, **D3USV**, and
    SPUSV (if equipped) are set to **1**.

9. Are all **XTCSV**, **D3USV**, and **SPUSV** (if equipped) fields set to **1**?

    If **Yes**, go to Step 11.
    If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 0 for Controllers, D3Us, and SPUs, if Equipped

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

    Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

    RC:UTYPE;CHG;OPT(SUXTSI),a**:   XTSI MEMN** b,

    ORNU c,

XTCN	0	1									
XTCHV	-,	-,									
XTCSV	d,	d,									

D3UN	0	1	2	3	4	5	6	7			
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,			
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,			
D3USV	d,	d,	d,	d,	d,	d,	d,	d,			

SPUN	0	1	2	3	4	5	6	7	8	9	10 11
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-, -,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-, -,
SPUSV	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d, d,

    REMARKS   ----------------!

    where   a = **FTA**
              b = XTSI member number
              c = RC order number
              d = **0** for fields in **XTCSV**, **D3USV**, and if equipped, **SPUSV**

3. Was **RC ORNU** c **ACTIVATED** message received?

    If **Yes**, go to Step 5.
    If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!**
   for even-numbered XTSI being updated.

   where   a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

XTCN	0 1
XTCHV	-, -,
XTCSV	0, 0,

D3UN	0 1 2 3 4 5 6 7
D3UEQ	-, -, -, -, -, -, -, -,
D3UHV	-, -, -, -, -, -, -, -,
D3USV	0, 0, 0, 0, 0, 0, 0, 0,

SPUN	0 1 2 3 4 5 6 7 8 9 10 11
SPUEQ	-, -, -, -, -, -, -, -, -, -, -, -,
SPUHV	-, -, -, -, -, -, -, -, -, -, -, -,
SPUSV	0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0,

   where   a = XTSI member number

6. Are message format and member identification correct per the response message in Step 5?

   If **Yes**, go to Step 8.
   If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV**, **D3USV**, and **SPUSV** (if equipped) are set to **0**.

9. Are all **XTCSV**, **D3USV**, and **SPUSV** (if equipped) fields set to **0**?

   If **Yes**, go to Step 11.
   If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Copy New Files from 3B Computer to XTSI

Note: Input messages must be entered one at a time in sequence from 1 through 7. Each copy command may take approximately 3 minutes.

1. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:XTSI** a**,XTC,FDT,SVN 0,DVN 0!**
    2. **COPY:XTSI** a**,XTC,OPR,SVN 0,DVN 0!**
    3. **COPY:XTSI** a**,XTC,DGN,SVN 0,DVN 0!**
    4. **COPY:XTSI** a**,D3U,OPR,SVN 0,DVN 0!**
    5. **COPY:XTSI** a**,D3U,DGN,SVN 0,DVN 0!**
    6. **COPY:XTSI** a**,SPU,OPR,SVN 0,SVN 0!**
    7. **COPY:XTSI** a**,SPU,DGN,SVN 0,SVN 0!**

    where a = Even XTSI member number

2. Was **TASK COMPLETED** message received?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Refer trouble to installer for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

    If **Yes**, go to Step 6.
    If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify New System Files Were Copied to XTSI

Note: Input messages must be entered one at a time in sequence from 1 through 7.

1. At 1B Processor MTC terminal, enter one of the following messages:

   1. **DUMP:TSIFILE;XTSI** a**,FILE 0,HADR 0,L 20!** /xtc/fdtver0
   2. **DUMP:TSIFILE;XTSI** a**,FILE 4,HADR 0,L 20!** /xtc/oprver0
   3. **DUMP:TSIFILE;XTSI** a**,FILE 8,HADR 0,L 20!** /xtc/dgnver0
   4. **DUMP:TSIFILE;XTSI** a**,FILE 12,HADR 0,L 20!** /d3u/oprver0
   5. **DUMP:TSIFILE;XTSI** a**,FILE 16,HADR 0,L 20!** /d3u/dgnver0
   6. **DUMP:TSIFILE;XTSI** a**,FILE 20,HADR 0,L 20!** /spu/oprver0
   7. **DUMP:TSIFILE;XTSI** a**,FILE 24,HADR 0,L 20!** /spu/dgnver0

   where a = Even XTSI member number

   Response:

   DUMP:TSIFILE XTSI a **FILE** b **HADR 0 L 20:**
           COMPLETE

000000	**xxxx**	**0000**	**----**	**0000**	**----**	**yyyy**	**----** **----**
000008	**----**	**----**	**----**	**----**	**--zz**	**----**	**----** **----**
000010	**3**e**3**f	**3**d**--**	**0000**	**3**g**00**			

a	=	XTSI member number
b	=	File number (0, 4, 8, 12, 16, 20, or 24)
xxxx	=	Checksum/Hashsum value for this file in hex
yyyy	=	Type of file:
		4341 (for fdtver0) or
		4441 (for dgnver0) or
		4F41 (for oprver0)
zz	=	Type of subunit:
		43 (for XTC) or
		53 (for SPU) or
		55 (for D3U)
e f d g	=	File issue number d.efg

2. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. Using printout and above response (Step 1), compare checksum value in word 0 with associated checksum value from 3B printout saved earlier [DP-503]. Record discrepancy for later use.

5. Using printout and above response (Step 1), compare software release issue in words 10, 11 and 13 with associated software release issue from 3B printout saved earlier [DLP-503]. Record discrepancy for later use.

6. Using printout and above response (Step 1), verify type of file data in word 5 for associated message inputted. Record discrepancy for later use.

7. Using printout and above response (Step 1), verify subunit type in word C (hex) for associated message inputted. Record discrepancy for later use.

8. Have all messages in Step 1 been entered?

   If **Yes**, go to Step 10.
   If **No**, go to Step 9.

9. Repeat from Step 1, for next input message.

10. Were data words correct?

    If **Yes**, go to Step 12.
    If **No**, go to Step 11.

11. Contact next higher support group for resolution; after resolving, repeat from Step 1.

12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Update Operational D3U

1. At 1B Processor MTC terminal, enter message
    UPD:XTSI a**,D3U** b**,SVN 0!**

    where   a = XTSI member number
             b = D3U number

    Response: **UPD:XTSI** a **,D3U** b **,SVN 0**
                 D3U b **UPDATE COMPLETED**

2. Was printout received per the response message in Step 1?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Update Controllers 0 and 1

1. At 1B Processor MTC terminal, enter message
   UPD:XTSI a**,XTC,SVN 0!**

   where   a = XTSI member number

   Response: **UPD:XTSI** a ,**XTC** ,**SVN 0**
                      XTC UPDATE COMPLETED

2. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Update Operational SPU

1. At 1B Processor MTC terminal, enter message
   UPD:XTSI a**,SPU** b**,SVN 0!**

   where   a = XTSI member number
             b = SPU number

   Response: **UPD:XTSI** a **,SPU** b **,SVN 0**
               SPU b **UPDATE COMPLETED**

2. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Determine if XTSI Controller, D3U, and/or SPU
Diagnostics Were Changed by this Update

1. Obtain from the next higher support group the BWM number associated with this update.

2. At 3B MCRT, enter message
    DUMP:FILE:FN"/etc/bwm/AAAxx-xxxx**/SCANS"!**

    where AAAxx-xxxx = BWM number for this update

3. Using printout of the BWM scans file, determine if diagnostics for XTSI controller, D3U, and/or SPU were changed.

4. Were diagnostics changed?

    If **Yes**, go to Step 5.
    If **No**, go to Step 6.

5. Record each unit type (controller, D3U, and/or SPU) which had diagnostic changes for later use.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Diagnose XTSI Controllers, D3Us, SPUs, and/or IBUs, as Required

1. What diagnostic software was changed?

    If **Controller**, go to Step 2.
    If **D3U**, go to Step 9.
    If **SPU**, go to Step 16.
    If **IBU**, go to Step 23.

2. At 1B Processor MTC terminal, enter message for one controller
    RST:XTSI a**,CONTR** b**!**

    where   a = Even XTSI member number
              b = XTSI controller number (0 or 1)

    Response: **RST: XTSI** a**, CONTR** b **COMPL**

3. Was printout received per the response message in Step 2?

    If **Yes**, go to Step 5.
    If **No**, go to Step 4.

4. Determine cause and resolve; repeat from Step 2.

5. Have both XTSI controllers been diagnosed?

    If **Yes**, go to Step 7.
    If **No**, go to Step 6.

6. Repeat from Step 2 for second controller.

7. Have all unit types, with changed diagnostic software, been diagnosed?

    If **Yes**, go to Step 28.
    If **No**, go to Step 8.

8. Repeat from Step 1 for next unit type.

9. At 1B Processor MTC terminal, enter message for one equipped D3U
   RST:XTSI a,**D3U** b**!**

   where   a = Even XTSI member number
                  b = Equipped D3U number

   Response: **RST: XTSI** a**, D3U** b **COMPL**

10. Was printout received per the response message in Step 9?

    If **Yes**, go to Step 12.
    If **No**, go to Step 11.

11. Determine cause and resolve; repeat from Step 9.

12. Have all equipped D3Us been diagnosed?

    If **Yes**, go to Step 14.
    If **No**, go to Step 13.

13. Repeat from Step 9 for next D3U.

14. Have all unit types, with changed diagnostic software, been diagnosed?

    If **Yes**, go to Step 28.
    If **No**, go to Step 15.

15. Repeat from Step 1 for next unit type.

16. At 1B Processor MTC terminal, enter message for one equipped D3U
    RST:XTSI a,**SPU** b**!**

    where   a = Even XTSI member number
                   b = Equipped SPU number

    Response: **RST: XTSI** a**, SPU** b **COMPL**

17. Was printout received per the response message in Step 16?

    If **Yes**, go to Step 19.
    If **No**, go to Step 18.

18. Determine cause and resolve; repeat from Step 16.

19. Have all equipped SPUs been diagnosed?

    If **Yes**, go to Step 21.
    If **No**, go to Step 20.

20. Repeat from Step 16 for next SPU.

21. Have all unit types, with changed diagnostic software, been diagnosed?

    If **Yes**, go to Step 28.
    If **No**, go to Step 22.

22. Repeat from Step 1 for next unit type.

23. At 1B Processor MTC terminal, enter message
    RST:XTSI a**,IBU** b**!**

    where   a = Even XTSI member number
             b = Equipped IBU number (0 or 1)

    Response: **RST: XTSI** a**, IBU** b **COMPL**

24. Was printout received per the response message in Step 23?

    If **Yes**, go to Step 26.
    If **No**, go to Step 25.

25. Determine cause and resolve; repeat from Step 23.

26. Have all equipped IBUs been diagnosed?

    If **Yes**, go to Step 28.
    If **No**, go to Step 27.

27. Repeat from Step 23 for next IBU.

28. Have all unit types, with changed diagnostic software, been diagnosed?

    If **Yes**, go to Step 30.
    If **No**, go to Step 29.

29. Repeat from Step 1 for next unit type.

30. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify XTSI Controllers 0 and 1 Are Operational

At 1B Processor MTC terminal, enter message **SW:XTSI** a**!**

where a = Even XTSI member number

Response:　**SW:XTSI** a **COMPL**

1. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

2. Contact next higher support group for resolution; after resolving, repeat from Step 1.

3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify XTSI Files Stored in 3B Computer

Note: This command will take from 5 to 15 minutes to complete, depending on system call load.

1. At 3B MCRT, enter message **EXC:ENVIR:UPROC,FN "/tools/prxtsihdrs"**

   Note: Some response output may appear in different order due to spooler processing.

   Response:

EXC ENVIR UPROC /tools/prxtsihdrs STARTED

File(s)	Hash	Sums	Size		Header Date	Issue
xtsi/fdt/xfdtver0	0000	**xxxx**	0000	nnnn	**C**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/fdt/xfdtver1	0000	**xxxx**	0000	nnnn	**C**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xoprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xoprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xdgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xdgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/d3u/doprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/doprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/ddgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/d3u/ddgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**U**<nn>**d.efg**
xtsi/spu/soprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/soprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/sdgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/spu/sdgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**S**<nn>**d.efg**
xtsi/xtc/xcspver0	0000	**xxxx**	0000	nnnn	**S**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/xtc/xcspver1	0000	**xxxx**	0000	nnnn	**S**Ann/nn/nn	nn:nn:nn**C**<nn>**d.efg**
xtsi/ibu/ioprver0	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/ioprver1	0000	**xxxx**	0000	nnnn	**O**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/idgnver0	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/idgnver1	0000	**xxxx**	0000	nnnn	**D**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/imfver0	0000	**xxxx**	0000	nnnn	**M**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/imfver1	0000	**xxxx**	0000	nnnn	**M**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/idtmfver0	0000	**xxxx**	0000	nnnn	**F**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/idtmfver1	0000	**xxxx**	0000	nnnn	**F**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/ivpaver0	0000	**xxxx**	0000	nnnn	**V**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/ivpaver1	0000	**xxxx**	0000	nnnn	**V**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/itstver0	0000	**xxxx**	0000	nnnn	**T**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**
xtsi/ibu/itstver1	0000	**xxxx**	0000	nnnn	**T**Ann/nn/nn	nn:nn:nn**I**<nn>**d.efg**

EXC ENVIR UPROC /tools/prxtsihdrs COMPLETED SEGMENT 2

See next page for Legend.

Legend:

xxxx = Checksum/Hashsum value for file in hex

In Header Date Column:
  C means FDT file
  D means DGN file
  O means OPR file
  M means MF file
  F means DTMF file
  V means VPA file
  T means TST file

In Issue Column:
  C means XTC subunit
  U means D3U subunit
  S means SPU subunit
  I means IBU subunit
  d.efg is file issue number

2. Using printout and the response message in Step 1, request next higher support
   group to verify that latest XTSI operating and diagnostic files are
   3B computer memory.

3. Save printout for later use to verify that XTSI subunits are
   running on latest software.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Set XTSI Controller's FDT to Old 28-File Version

1. At 1B Processor MTC terminal, enter messages
   DUMP:TSIFILE;XTSI a,**FILE 0,HADR 0,L 20!**

   where a = Even XTSI member number

   Response:

   DUMP:TSIFILE XTSI a **FILE 0 HADR 0 L 20:**
   COMPLETE
   ```
   000000   xxxx   0000    ----    0000    ----    4341    ----    ----
   000008   ----   ----    ----    ----    --43    ----    ----    ----
   000010   3e3f   3d--    0000    3g00
   ```

   > ***Note:*** The data in the response message does not need to be verified. The DUMP message forces the controllers to look at the FDT in version 1 since the pointers are set to that version.

2. Was printout received similar to the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 0 for Controllers 0 and 1

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

    Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

    RC:UTYPE;CHG;OPT(SUXTSI),a**:   XTSI MEMN** b,

    ORNU c,

XTCN	0	1									
XTCHV	-,	-,									
XTCSV	d,	d,									

D3UN	0	1	2	3	4	5	6	7			
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,			
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,			
D3USV	-,	-,	-,	-,	-,	-,	-,	-,			

SPUN	0	1	2	3	4	5	6	7	8	9	10	11
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,

    REMARKS ----------------!

    where   a = **FTA**
            b = XTSI member number
            c = RC order number
            d = **0** for fields in **XTCSV**

3. Was **RC ORNU** c **ACTIVATED** message received?

    If **Yes**, go to Step 5.
    If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!**
   for even-numbered XTSI being updated.

   where   a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

XTCN	0 1
XTCHV	-, -,
XTCSV	0, 0,

D3UN	0 1 2 3 4 5 6 7
D3UEQ	-, -, -, -, -, -, -, -,
D3UHV	-, -, -, -, -, -, -, -,
D3USV	-, -, -, -, -, -, -, -,

SPUN	0 1 2 3 4 5 6 7 8 9 10 11
SPUEQ	-, -, -, -, -, -, -, -, -, -, -, -,
SPUHV	-, -, -, -, -, -, -, -, -, -, -, -,
SPUSV	-, -, -, -, -, -, -, -, -, -, -, -,

   where   a = XTSI member number

6. Are message format and member identification correct per the response message in Step 5?

   If **Yes**, go to Step 8.
   If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV** fields are set to **0**.

9. Are **XTCSV** fields set to **0**?

   If **Yes**, go to Step 11.
   If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 1 for Controllers 0 and 1

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

   Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

   RC:UTYPE;CHG;OPT(SUXTSI),a**:**   **XTSI MEMN** b**,**

   ORNU c**,**

XTCN	0	1									
XTCHV	-,	-,									
XTCSV	d,	d,									

D3UN	0	1	2	3	4	5	6	7
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,
D3USV	-,	-,	-,	-,	-,	-,	-,	-,

SPUN	0	1	2	3	4	5	6	7	8	9	10	11
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,

   REMARKS   ----------------!

   where   a = **FTA**
             b = XTSI member number
             c = RC order number
             d = **1** for fields in **XTCSV**

3. Was **RC ORNU** c **ACTIVATED** message received?

   If **Yes**, go to Step 5.
   If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
    VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!**
    for even-numbered XTSI being updated.

    where   a = XTSI member number

    Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

        XTCN   0 1
        XTCHV   -, -,
        XTCSV   1, 1,

        D3UN   0 1 2 3 4 5   6 7
        D3UEQ   -, -, -, -, -, -,   -, -,
        D3UHV   -, -, -, -, -, -,   -, -,
        D3USV   -, -, -, -, -, -,   -, -,

        SPUN   0 1 2 3 4 5 6 7 8 9   10 11
        SPUEQ   -, -, -, -, -, -, -, -, -, -,   -, -,
        SPUHV   -, -, -, -, -, -, -, -, -, -,   -, -,
        SPUSV   -, -, -, -, -, -, -, -, -, -,   -, -,

        where   a = XTSI member number

6. Are message format and member identification correct per the response message in Step
    5?

    If **Yes**, go to Step 8.
    If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV** fields are set to **1**.

9. Are **XTCSV** fields set to **1**?

    If **Yes**, go to Step 11.
    If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Copy the New 48-File FDT From CIA File 0 to CIA File 1

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

1. At 1B Processor MTC terminal, enter message
    COPY:TSIFILE;XTSI a**,SFN 0,DFN 1!**

    where a = Even XTSI member number

2. Was **COMPLETE** message received?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Copy the New In-Band Version 0 File
Locations With the FDT

Note: Input messages must be entered one at a time in sequence from 1 through 5.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

1. At 1B Processor MTC terminal, enter one of the following messages:

   1. **COPY:TSIFILE;XTSI** a,**SFN 0,DFN 28!**
   2. **COPY:TSIFILE;XTSI** a,**SFN 0,DFN 32!**
   3. **COPY:TSIFILE;XTSI** a,**SFN 0,DFN 36!**
   4. **COPY:TSIFILE;XTSI** a,**SFN 0,DFN 40!**
   5. **COPY:TSIFILE;XTSI** a,**SFN 0,DFN 44!**

   where a = Even XTSI member number

2. Was **COMPLETE** message received?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

   If **Yes**, go to Step 6.
   If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Copy the Remainder of the New File
Locations With the FDT

Note: Input messages must be entered one at a time in sequence from 1 through 15.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

1. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 29!**
    2. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 30!**
    3. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 31!**
    4. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 33!**
    5. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 34!**
    6. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 35!**
    7. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 37!**
    8. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 38!**
    9. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 39!**
    10. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 41!**
    11. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 42!**
    12. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 43!**
    13. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 45!**
    14. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 46!**
    15. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 47!**

    where a = Even XTSI member number

2. Was **COMPLETE** message received?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

    If **Yes**, go to Step 6.
    If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify software versions and D3U, SPU (if equipped), and SPU (if equipped) equipage

1. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!** for even-numbered degrowth XTSI.

   where   a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):**   **XTSI MEMN** a,

XTCN	**0**	**1**
XTCHV	**-,**	**-,**
XTCSV	**0,**	**0,**

	CSP,	PUBI,	GRAM,	CIA,	ALF,	EXEC,
XTC0 PACKS	b,	c,	**0,**	**0,**	**0,**	c,
XTC1 PACKS	b,	c,	**0,**	**0,**	**0,**	c,

   XTSI FRAME INBAND ENABLED -,

IBUEQ	d,
IBUHV	**-,**
IBUSV	**0,**

D3UN	**0**	**1**	**2**	**3**	**4**	**5**		**6**	**7**			
D3UEQ	d,	d,	d,	d,	d,	d,		d,	d,			
D3UHV	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**		**-,**	**-,**			
D3USV	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**		**0,**	**0,**			

SPUN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**
SPUEQ	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,
SPUHV	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
SPUSV	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**	**0,**

   where   a = XTSI member number
             b = **0** if CSP pack is not installed or **1** if CSP pack is installed
                (In-Band Capable)
             c = must all be **1** if CSP is 1; otherwise, they can be all **0** or all **1**,
                but may not be mixed **0** and **1**
             d = **O** (for OPER) or **U** (for UNEQ)

2. Are message format and member identification correct per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. Using printout and the response message in Step 1, verify all fields for **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** are set to **0**.

5. Are all **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** fields set to **0**?

   If **Yes**, go to Step 7.
   If **No**, go to Step 6.

6. Software update can not be performed on this XTSI. Stop update.

7. Using printout and the response message in Step 1, determine if **IBUEQ** field is set to **O**.

8. Is **IBUEQ** field set to **O**?

   If **Yes**, go to Step 12.
   If **No**, go to Step 9.

   > ***Note:*** IBU not in OPER state will not be updated.

9. Verify with next higher support group that the IBU not set to **O** is in proper state.

10. Is IBU in proper state?

   If **Yes**, go to Step 12.
   If **No**, go to Step 11.

11. No update is allowed, unless IBU is in proper state.

12. Record IBU is in OPER state for later use. Update will be performed later.

13. Using printout and the response message in Step 1, determine if all fields for **D3UEQ** are set to **O**.

14. Are all **D3UEQ** fields set to **O**?

    If **Yes**, go to Step 18.
    If **No**, go to Step 15.

    > **Note:** D3Us not in OPER state will not be updated.

    > **Caution: If D3U 6 or 7 is not set to O and is in-service, then no clients within the associated group may be updated.**

15. Verify with next higher support group that the D3Us not set to **O** are in proper state.

16. Are D3Us in proper state?

    If **Yes**, go to Step 18.
    If **No**, go to Step 17.

17. No update is allowed, unless D3Us are in proper state.

18. Record D3Us in OPER state for later use. Update will be performed later on these D3Us.

19. Are SPUs equipped in XTSI being updated?

    If **Yes**, go to Step 20.
    If **No**, go to Step 26.

20. Using printout and the response message in Step 1, determine if all fields for **SPUEQ** are set to **O**.

21. Are all **SPUEQ** fields set to **O**?

    If **Yes**, go to Step 25.
    If **No**, go to Step 22.

    Note: SPUs not in OPER state will not be updated.

    Caution: If SPU 10 or 11 is not set to O and is in-service, then no clients within the associated group may be updated.

22. Verify with next higher support group that the SPUs not set to **O** are in proper state.

23. Are SPUs in proper state?

    If **Yes**, go to Step 25.
    If **No**, go to Step 24.

24. No update is allowed, unless SPUs are in proper state.

25. Record SPUs in OPER state for later use. Update will be performed later on these SPUs.

    Note: If either CSP is installed (EQL 14-066 and/or 31-066), then **CSP** field(s) must be set to **1**.

26. Using printout and the response message in Step 1, determine if **CSP** field for **XTC0 PACKS** and **XTC1 PACKS** are set properly for the XTSI being updated.

27. Are **CSP** fields set properly?

    If **Yes**, go to Step 29.
    If **No**, go to Step 28.

28. Contact next higher support group to determine error and decide on corrective action. After resolving, repeat from Step 1.

    Note: If CSP is installed, then **KBN23** must be installed (EQL 52-137 and/or 61-137) and **PUBI** field(s) must be set to **1**.

29. Using printout and the response message in Step 1, determine if **PUBI** fields for **XTC0 PACKS** and **XTC1 PACKS** are set properly for the XTSI being updated.

30. Are **PUBI** fields set properly?

    If **Yes**, go to Step 32.
    If **No**, go to Step 31.

31. Contact next higher support group to determine error and decide on corrective action. After resolving, repeat from Step 1.

> ***Note:*** If CSP is installed, then **UN549** must be installed (EQL 52-114 and/or 61-114) and **EXEC** field(s) must be set to **1**.

> ***Caution: The PUBI/EXEC pack combinations must be either* KBN16/UN548** or **KBN23/UN549**; no other combinations will work.

32. Using printout and the response message in Step 1, determine if **EXEC** fields for **XTC0 PACKS** and **XTC1 PACKS** are set properly for the XTSI being updated.

33. Are **EXEC** fields set properly?

    If **Yes**, go to Step 35.
    If **No**, go to Step 34.

34. Contact next higher support group to determine error and decide on corrective action. After resolving, repeat from Step 1.

35. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Back up Current XTSI Files

Note: Input messages must be entered one at a time in sequence from 1 through 5.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

1. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:TSIFILE;XTSI** a**,SFN 0,DFN 1!**
    2. **COPY:TSIFILE;XTSI** a**,SFN 4,DFN 5!**
    3. **COPY:TSIFILE;XTSI** a**,SFN 8,DFN 9!**
    4. **COPY:TSIFILE;XTSI** a**,SFN 12,DFN 13!**
    5. **COPY:TSIFILE;XTSI** a**,SFN 16,DFN 17!**

        where a = Even XTSI member number

2. Was **COMPLETE** message received?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

    If **Yes**, go to Step 6.
    If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. Does XTSI, being updated, contain SPUs?

    If **Yes**, go to Step 7.
    If **No**, go to Step 12.

Note: Input messages must be entered one at a time in sequence 1 then 2.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

7. At 1B Processor MTC terminal, enter one of the following messages:

1. **COPY:TSIFILE;XTSI** a**,SFN 20,DFN 21!**
2. **COPY:TSIFILE;XTSI** a**,SFN 24,DFN 25!**

where a = Even XTSI member number

8. Was **COMPLETE** message received?

If **Yes**, go to Step 10.
If **No**, go to Step 9.

9. Contact next higher support group for resolution; after resolving, repeat from Step 7 by entering same message in error.

10. Have all input messages in Step 7 been entered?

If **Yes**, go to Step 12.
If **No**, go to Step 11.

11. Repeat from Step 7 for next input message.

Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.

12. Does XTSI, being updated, contain CSP pack(s)?

If **Yes**, go to Step 13.
If **No**, go to Step 16.

13. At 1B Processor MTC terminal, enter message
COPY:TSIFILE;XTSI a**,SFN 20,DFN 21!**

where a = Even XTSI member number

14. Was **COMPLETE** message received?

If **Yes**, go to Step 16.
If **No**, go to Step 15.

15. Contact next higher support group for resolution; after resolving, repeat from Step 13.

16. Is IBU defined for XTSI being updated?

    If **Yes**, go to Step 17.
    If **No**, go to Step 22.

    > **Note:** Input messages must be entered one at a time in sequence from 1 through 4.

    > **Caution: Ensure correct SFN and DFN numbers are entered to prevent wrong file being copied.**

17. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:TSIFILE;XTSI** a**,SFN 32,DFN 33!**
    2. **COPY:TSIFILE;XTSI** a**,SFN 36,DFN 37!**
    3. **COPY:TSIFILE;XTSI** a**,SFN 40,DFN 41!**
    4. **COPY:TSIFILE;XTSI** a**,SFN 42,DFN 43!**

    where a = Even XTSI member number

18. Was **COMPLETE** message received?

    If **Yes**, go to Step 20.
    If **No**, go to Step 19.

19. Contact next higher support group for resolution; after resolving, repeat from Step 17 by entering same message in error.

20. Have all input messages in Step 17 been entered?

    If **Yes**, go to Step 22.
    If **No**, go to Step 21.

21. Repeat from Step 17 for next input message.

22. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 1 for Controllers, IBU, D3Us, and SPUs

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

    Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

    RC:UTYPE;CHG;OPT(SUXTSI),a**:   XTSI MEMN** b**,**

    ORNU c**,**

XTCN	**0**	**1**
XTCHV	-,	-,
XTCSV	d,	d,

	CSP,	**PUBI,**	**GRAM,**	**CIA,**	**ALF,**	**EXEC,**
XTC0 PACKS	-,	-,	-,	-,	-,	-,
XTC1 PACKS	-,	-,	-,	-,	-,	-,

    XTSI FRAME INBAND ENABLED -,

IBUEQ	-,
IBUHV	-,
IBUSV	d

D3UN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,
D3USV	d,	d,	d,	d,	d,	d,	d,	d,

SPUN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,

    REMARKS   ----------------!

    where   a = **FTA**
             b = XTSI member number
             c = RC order number
             d = **1**

3. Was **RC ORNU** c **ACTIVATED** message received?

   If **Yes**, go to Step 5.
   If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!** for even-numbered growth XTSI.

   where   a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

XTCN	**0**	**1**
XTCHV	**-,**	**-,**
XTCSV	**1,**	**1,**

	CSP,	**PUBI,**	**GRAM,**	**CIA,**	**ALF,**	**EXEC,**
XTC0 PACKS	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
XTC1 PACKS	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**

   XTSI FRAME INBAND ENABLED -,

IBUEQ	**-,**
IBUHV	**-,**
IBUSV	**1,**

D3UN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**
D3UEQ	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
D3UHV	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
D3USV	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**

SPUN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**
SPUEQ	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
SPUHV	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**	**-,**
SPUSV	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**	**1,**

   where   a = XTSI member number

6. Are message format and member identification correct per the above response (Step 5)?

   If **Yes**, go to Step 8.
   If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** fields are set to **1**.

9. Are **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** fields set to **1**?

    If **Yes**, go to Step 11.
    If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Recent Change and Verify Software Version to 0 for Controllers, IBU, D3Us, and SPUs

Caution: Calling up RC form will cause all CRT data to be cleared.

1. At 1B Processor MTC terminal, enter message **OP:RCFORM 705!**

   Response: CRT displays RC Form 705

2. Fill in blanks on RC Form 705 per the following and enter message:

   RC:UTYPE;CHG;OPT(SUXTSI),a**:   XTSI MEMN** b,

   ORNU c,

XTCN	**0**	**1**
XTCHV	-,	-,
XTCSV	d,	d,

	CSP,	**PUBI,**	**GRAM,**	**CIA,**	**ALF,**	**EXEC,**
XTC0 PACKS	-,	-,	-,	-,	-,	-,
XTC1 PACKS	-,	-,	-,	-,	-,	-,

   XTSI FRAME INBAND ENABLED -,

IBUEQ	-,
IBUHV	-,
IBUSV	d

D3UN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,
D3USV	d,	d,	d,	d,	d,	d,	d,	d,

SPUN	**0**	**1**	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,	d,

   REMARKS   ----------------!

   where   a = **FTA**
           b = XTSI member number
           c = RC order number
           d = **0**

3. Was **RC ORNU** c **ACTIVATED** message received?

   If **Yes**, go to Step 5.
   If **No**, go to Step 4.

4. Determine cause for rejection and repeat from Step 1, with the correct input message.

5. At 1B Processor MTC terminal, enter message
   VER:VFUNC SUXTSI:FD1 XTSIMEMN,DT1 a**!** for even-numbered growth XTSI.

   where   a = XTSI member number

   Response: **VER:MISC;OPT(SUXTSI):   XTSI MEMN** a**,**

XTCN	**0 1**	
XTCHV	**-, -,**	
XTCSV	**0, 0,**	

	CSP,	PUBI,	GRAM,	CIA,	ALF,	EXEC,
XTC0 PACKS	-,	-,	-,	-,	-,	-,
XTC1 PACKS	-,	-,	-,	-,	-,	-,

   XTSI FRAME INBAND ENABLED -,

IBUEQ	**-,**
IBUHV	**-,**
IBUSV	**0,**

D3UN	0	1	2	3	4	5	6	7
D3UEQ	-,	-,	-,	-,	-,	-,	-,	-,
D3UHV	-,	-,	-,	-,	-,	-,	-,	-,
D3USV	0,	0,	0,	0,	0,	0,	0,	0,

SPUN	0	1	2	3	4	5	6	7	8	9	10	11
SPUEQ	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUHV	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,	-,
SPUSV	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,	0,

   where   a = XTSI member number

6. Are message format and member identification correct per the above response (Step 5)?

   If **Yes**, go to Step 8.
   If **No**, go to Step 7.

7. Determine the cause and resolve; repeat from Step 5.

8. Using the printout and the response message in Step 5, verify **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** fields are set to **0**.

9. Are **XTCSV**, **IBUSV**, **D3USV**, and **SPUSV** fields set to **0**?

   If **Yes**, go to Step 11.
   If **No**, go to Step 10.

10. Determine cause for rejection and repeat from Step 1, with correct input message.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Copy New Files from 3B Computer to XTSI Being Updated

> ***Note:*** Input messages must be entered one at a time in sequence from 1 through 5. Each copy command may take approximately 3 minutes.

1. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:XTSI** a**,XTC,FDT,SVN 0,DVN 0!**
    2. **COPY:XTSI** a**,XTC,OPR,SVN 0,DVN 0!**
    3. **COPY:XTSI** a**,XTC,DGN,SVN 0,DVN 0!**
    4. **COPY:XTSI** a**,D3U,OPR,SVN 0,DVN 0!**
    5. **COPY:XTSI** a**,D3U,DGN,SVN 0,DVN 0!**

    where a = Even XTSI member number

2. Was **TASK COMPLETED** message received?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1 by entering same message in error.

4. Have all input messages in Step 1 been entered?

    If **Yes**, go to Step 6.
    If **No**, go to Step 5.

5. Repeat from Step 1 for next input message.

6. Does XTSI, being updated, contain SPU packs?

    If **Yes**, go to Step 7.
    If **No**, go to Step 12.

Note: Input messages must be entered one at a time in sequence 1 then 2. Each copy command may take approximately 3 minutes.

7. At 1B Processor MTC terminal, enter one of the following messages:

   1. **COPY:XTSI** a**,SPU,OPR,SVN 0,DVN 0!**
   2. **COPY:XTSI** a**,SPU,DGN,SVN 0,DVN 0!**

   where a = Even XTSI member number

8. Was **TASK COMPLETED** message received?

   If **Yes**, go to Step 10.
   If **No**, go to Step 9.

9. Contact next higher support group for resolution; after resolving, repeat from Step 7 by entering same message in error.

10. Have both input messages in Step 7 been entered?

    If **Yes**, go to Step 12.
    If **No**, go to Step 11.

11. Repeat from Step 7 for second input message.

12. Does XTSI, being updated, contain CSP pack(s)?

    If **Yes**, go to Step 13.
    If **No**, go to Step 16.

13. At 1B Processor MTC terminal, enter message
    COPY:XTSI a**,XTC,CSP,SVN 0,DVN 0!**

    where a = Even XTSI member number

14. Was **TASK COMPLETED** message received?

    If **Yes**, go to Step 16.
    If **No**, go to Step 15.

15. Contact next higher support group for resolution; after resolving, repeat from Step 13.

16. Is IBU defined for XTSI being updated?

    If **Yes**, go to Step 17.
    If **No**, go to Step 22.

    > ***Note:*** Input messages must be entered one at a time in sequence from 1 through 4. Each copy command may take approximately 3 minutes.

17. At 1B Processor MTC terminal, enter one of the following messages:

    1. **COPY:XTSI** a**,IBU,OPR,SVN 0,DVN 0!**
    2. **COPY:XTSI** a**,IBU,DGN,SVN 0,DVN 0!**
    3. **COPY:XTSI** a**,IBU,XCVR,SVN 0,DVN 0!**
    4. **COPY:XTSI** a**,IBU,TST,SVN 0,DVN 0!**

       where a = Even XTSI member number

18. Was **TASK COMPLETED** message received?

    If **Yes**, go to Step 20.
    If **No**, go to Step 19.

19. Contact next higher support group for resolution; after resolving, repeat from Step 17 by entering same message in error.

20. Have all input messages in Step 17 been entered?

    If **Yes**, go to Step 22.
    If **No**, go to Step 21.

21. Repeat from Step 17 for second input message.

22. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Verify System Files Were Copied Properly

Note: Input messages must be entered one at a time in sequence from 1 through 5.

1. At 1B Processor MTC terminal, enter one of the following messages:

    1. **DUMP:TSIFILE;XTSI** a**,FILE 0,HADR 0,L 20!** /xtc/fdtver0
    2. **DUMP:TSIFILE;XTSI** a**,FILE 4,HADR 0,L 20!** /xtc/oprver0
    3. **DUMP:TSIFILE;XTSI** a**,FILE 8,HADR 0,L 20!** /xtc/dgnver0
    4. **DUMP:TSIFILE;XTSI** a**,FILE 12,HADR 0,L 20!** /d3u/oprver0
    5. **DUMP:TSIFILE;XTSI** a**,FILE 16,HADR 0,L 20!** /d3u/dgnver0

    where a = Even XTSI member number

Response:

DUMP:TSIFILE XTSI a **FILE** b **HADR 0 L 20:**
                    COMPLETE
000000   xxxx   0000   ----   0000   ----   yyyy   ----   ----
000008   ----   ----   ----   ----   --zz   ----   ----   ----
000010   3e**3**f   **3**d**--   0000   3**g**00**

a	=	XTSI member number
b	=	File number (0, 4, 8, 12, or 16)
xxxx	=	Checksum/Hashsum value for this file in hex
yyyy	=	Type of file:

                    4341 (for fdtver0) or
                    4441 (for dgnver0) or
                    4641 (for dtmfver0)
                    4D41 (for mfver0)
                    4F41 (for oprver0)
                    5341 (for cspver0)
                    5441 (for tstver0)
                    5641 (for vpaver0)

zz	=	Type of subunit:

                    43 (for XTC) or
                    49 (for IBU) or
                    53 (for SPU) or
                    55 (for D3U)

e f d g	=	File issue number d.efg

2. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Determine cause and resolve; repeat from Step 1.

4. Using printout and response message in Step 1, compare checksum value in word 0 with associated checksum value from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

5. Using printout and response message in Step 1, compare software release issue in words 10, 11 and 13 with associated software release issue from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

6. Using printout and response message in Step 1, verify type of file data in word 5 for associated message inputted. Record discrepancy for later use.

7. Using printout and response message in Step 1, verify subunit type in word C (hex) for associated message inputted. Record discrepancy for later use.

8. Have all messages in Step 1 been entered?

   If **Yes**, go to Step 10.
   If **No**, go to Step 9.

9. Repeat from Step 1, for next input message.

10. Were data words correct?

    If **Yes**, go to Step 12.
    If **No**, go to Step 11.

11. Contact next higher support group for resolution; after resolving, repeat from Step 1.

12. Does XTSI, being updated, contain SPU packs?

    If **Yes**, go to Step 13.
    If **No**, go to Step 24.

Note: Input messages must be entered one at a time in sequence 1 then 2.

13. At 1B Processor MTC terminal, enter one of the following messages:

    1. **DUMP:TSIFILE;XTSI** a,**FILE 20,HADR 0,L 20!** /spu/oprver0
    2. **DUMP:TSIFILE;XTSI** a,**FILE 24,HADR 0,L 20!** /spu/dgnver0

    where a = Even XTSI member number

Response:

DUMP:TSIFILE XTSI a **FILE** b **HADR 0 L 20:**
          COMPLETE
000000 **xxxx** **0000**   **----**   **0000**   **----** **yyyy** **----** **----**
000008 **----**   **----**   **----**   **----**   **--53**   **----** **----** **----**
000010 **3**e**3**f   **3**d**--**   **0000**   **3**g**00**

a	=	XTSI member number
b	=	File number (20 or 24)
xxxx	=	Checksum/Hashsum value for this file in hex
yyyy	=	Type of file:
		4441 (for dgnver0) or
		4F41 (for oprver0)
e f d g	=	File issue number d.efg

14. Was printout received per the response message in Step 13?

    If **Yes**, go to Step 16.
    If **No**, go to Step 15.

15. Determine cause and resolve; repeat from Step 13.

16. Using printout and response message in Step 13, compare checksum value in word 0 with associated checksum value from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

17. Using printout and response message in Step 13, compare software release issue in words 10, 11 and 13 with associated software release issue from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

18. Using printout and response message in Step 13, verify type of file data in word 5 for associated message inputted. Record discrepancy for later use.

19. Using printout and response message in Step 13, verify **53** is received in word C (hex) for associated message inputted. Record discrepancy for later use.

20. Have all messages in Step 13 been entered?

    If **Yes**, go to Step 22.
    If **No**, go to Step 21.

21. Repeat from Step 13, for next input message.

22. Were data words correct?

    If **Yes**, go to Step 24.
    If **No**, go to Step 23.

23. Contact next higher support group for resolution; after resolving, repeat from Step 13.

24. Does XTSI, being updated, contain CSP pack(s)?

    If **Yes**, go to Step 25.
    If **No**, go to Step 34.

25. At 1B Processor MTC terminal, enter message
    DUMP:TSIFILE;XTSI a**,FILE 28,HADR 0,L 20!** /xtc/cspver0

    where a = Even XTSI member number

    Response:

    DUMP:TSIFILE XTSI a **FILE 28 HADR 0 L 20:**
                  COMPLETE

000000	**xxxx**	**0000**	----	**0000**	----	**5341**	----	----
000008	----	----	----	----	**--43**	----	----	----
000010	**3**e**3**f	**3**d**--**	**0000**	**3**g**00**				

          a   =   XTSI member number
       xxxx =   Checksum/Hashsum value for this file in hex
      e f d g =   File issue number d.efg

26. Was printout received per the response message in Step 25?

    If **Yes**, go to Step 28.
    If **No**, go to Step 27.

27. Determine cause and resolve; repeat from Step 25.

28. Using printout and response message in Step 25, compare checksum value in word 0 with associated checksum value from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

29. Using printout and response message in Step 25, compare software release issue in words 10, 11 and 13 with associated software release issue from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

30. Using printout and response message in Step 25, verify **5341** is received in word 5 for associated message inputted. Record discrepancy for later use.

31. Using printout and response message in Step 25, verify **43** is received in word C (hex) for associated message inputted. Record discrepancy for later use.

32. Were data words correct?

    If **Yes**, go to Step 34.
    If **No**, go to Step 33.

33. Contact next higher support group for resolution; after resolving, repeat from Step 25.

34. Is IBU defined for XTSI being updated?

    If **Yes**, go to Step 35.
    If **No**, go to Step 50.

35. At 1B Processor MTC terminal, enter message
    VER:VFUNC XTSIIBU:FD1 XTSIMEMN,DT1 a**!** for even-numbered XTSI.

    where   a = XTSI member number

    Response: **VER:MISC;OPT(XTSIIBU):   XTSI MEMN** a**,**

                  TANS RESERVED -,

                  MF OR DTMF b**,**

                  XCVR CONFIG c**,**

                  where   a = Even XTSI member number
                             b = **MF** or **DTMF** for type of digits served
                             c = **CCT** (for 16 continuity check transceiver) or
                                  MISC (for 12 CCTs plus 4 miscellaneous service circuits)

36. Was printout received per the response message in Step 25?

    If **Yes**, go to Step 4.
    If **No**, go to Step 3.

37. Determine the cause and resolve; repeat from Step 1.

38. Using printout and the response message in Step 35, determine type of digits served and
    types of service circuits supported. Record for later use when dumping files 40 and 44.

Note: Input messages must be entered one at a time in sequence from 1 through 4.

39. At 1B Processor MTC terminal, enter one of the following messages:

     1. **DUMP:TSIFILE;XTSI** a,**FILE 32,HADR 0,L 20!** /ibu/oprver0
     2. **DUMP:TSIFILE;XTSI** a,**FILE 36,HADR 0,L 20!** /ibu/dgnver0
     3. **DUMP:TSIFILE;XTSI** a,**FILE 40,HADR 0,L 20!** /ibu/mfver0 or /ibu/dtmfver0
     4. **DUMP:TSIFILE;XTSI** a,**FILE 44,HADR 0,L 20!** /ibu/vpaver0 or /ibu/tstver0

     where a = Even XTSI member number

Response:

DUMP:TSIFILE XTSI a **FILE** b **HADR 0 L 20:**
                    COMPLETE
000000   xxxx   0000   ----   0000   ----   yyyy   ----   ----
000008   ----   ----   ----   ----   --49   ----   ----   ----
000010   3e3f   3d--   0000   3g00

a	=	XTSI member number
b	=	File number (32, 36, 40, or 44)
xxxx	=	Checksum/Hashsum value for this file in hex
yyyy	=	Type of file:
		4441 (for dgnver0) or
		4641 (for dtmfver0)
		4D41 (for mfver0)
		4F41 (for oprver0)
		5441 (for tstver0)
		5641 (for vpaver0)
e f d g	=	File issue number d.efg

40. Was printout received per the response message in Step 39?

     If **Yes**, go to Step 42.
     If **No**, go to Step 41.

41. Determine cause and resolve; repeat from Step 39.

42. Using printout and response message in Step 39, compare checksum value in word 0 with associated checksum value from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

43. Using printout and response message in Step 39, compare software release issue in words 10, 11 and 13 with associated software release issue from 3B printout saved earlier [DLP-517]. Record discrepancy for later use.

44. Using printout and response message in Step 39, verify type of file data in word 5 for associated message inputted. Record discrepancy for later use.

45. Using printout and response message in Step 39, verify **49** is received in word C (hex) for associated message inputted. Record discrepancy for later use.

46. Have all messages in Step 39 been entered?

    If **Yes**, go to Step 48.
    If **No**, go to Step 47.

47. Repeat from Step 39, for next input message.

48. Were data words correct?

    If **Yes**, go to Step 50.
    If **No**, go to Step 49.

49. Contact next higher support group for resolution; after resolving, repeat from Step 39.

50. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Update Operational IBU

1. At 1B Processor MTC terminal, enter message
   UPD:XTSI a**,IBU** b**,SVN 0!**

   where   a = Even XTSI member number
            b = Equipped IBU number (0 or 1)

   Response: **UPD:XTSI** a **,IBU** b **,SVN 0**
                IBU b **UPDATE COMPLETED**

2. Was printout received per the response message in Step 1?

   If **Yes**, go to Step 4.
   If **No**, go to Step 3.

3. Contact next higher support group for resolution; after resolving, repeat from Step 1.

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

Checklist

ITEM	ISSUE	ITEM	ISSUE	ITEM	ISSUE	ITEM	ISSUE
IXL-001 NTP-002 NTP-003 NTP-004 NTP-005		DLP-530 CKL-891 TNG-893					
DLP-500 DLP-501 DLP-502 DLP-503 DLP-504							
DLP-505 DLP-506 DLP-507 DLP-508 DLP-509							
DLP-510 DLP-511 DLP-512 DLP-513 DLP-514							
DLP-515 DLP-516 DLP-517 DLP-518 DLP-519							
DLP-520 DLP-521 DLP-522 DLP-523 DLP-524							
DLP-525 DLP-526 DLP-527 DLP-528 DLP-529							

• Revised or added item

□ Canceled item

HOW TO USE THIS DOCUMENT

This document gives you all the step-by-step instructions you need to do your job (task). These instructions are given in the order that they *must* be done. Failure to follow the instructions in the order given may cause service interruptions.

This document is divided into parts called procedures. Each procedure is given a 3-digit number. These numbers range from 001 through 893. Procedures are arranged in this document in numerical order beginning with 001.

Figure 1 is a typical IXL-001 procedure and is titled *Task Index List*. It is an alphabetical listing of the jobs that you may have to do. To use an IXL-001 procedure, just find the job you need to do in the FIND YOUR JOB IN THE LIST BELOW column. Next, follow the dotted line to the procedure number and begin the task. For example, suppose you are given the job of doing a system test. On the IXL-001 procedure, as shown in Figure 1, notice that it is listed in the THEN GO TO column as NTP-016. It could have been any other 3-digit number.

Figure 2 is an example of an NTP (Non-Trouble Procedure). Each NTP provides specific instructions for doing a job. It consists of numbered items (or steps) listed in the order that you must do them to complete your job. To use this procedure, you must start with item 1 in the DO THE ITEMS BELOW IN ORDER LISTED column and continue until all items have been done. When you get to an item that you do not know how to do, look for the procedure number for that item under the FOR DETAILS, GO TO column. This is the number of the procedure that will give you detailed, step-by-step instructions to do that item. Note that item 2 in Figure 2 uses lettered (A, B) entries. This means that there are alternate ways of doing item 2 depending on equipment options or equipment conditions. You do only the one that fits your equipment options or equipment conditions.

For example, suppose you are doing a System Test. The IXL-001 as shown in Figure 1, has directed you to NTP-016 as shown in Figure 2, and you are on item 8 ''Mount Tape'' in the DO ITEMS BELOW IN ORDER LISTED column. Mount the tape if you know how. If you do not know how to mount the tape, go to the procedure number listed in the FOR DETAILS, GO TO column for the detailed, step-by-step instructions. In this case, it happens to be DLP-500. In either case, you must continue with the next item listed in NTP-016 until you complete the job.

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Issue 2

IXL-001
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TASK INDEX LIST

FIND YOUR JOB IN THE LIST BELOW	**THEN GO TO**
Alert; External - Horn, Ringer, Etc. - Remove...........................	NTP-028
Amplifiers; Channel - Recorded Announcement Frame - Test.....	NTP-009
BRDG LED - Does Not Light - Correct	TAP-117
Bridging Controller; Trunk - J1C015MB - Replace	DLP-572
Channel Amplifiers - Recorded Announcement Frame - Test......	NTP-009
Extended Station Capability - Nonkey Set Only - Reported Failure	TAP-123
External Alert - Horn, Ringer, Etc. - Remove...........................	NTP-028
Interchange Two Working Station Numbers.............................	NTP-081
LED: BRDG - Does Not Light - Correct	TAP-117
Loudspeaker Paging - Add	NTP-059
New International Trunk, R1 Signaling - Incoming - Establish	NTP-010
New Tandem Trunk - T-Carrier and Digroup Terminal - Establish	NTP-008
Station Capability; Extended - Nonkey Set Only - Reported Failure	TAP-123
System Test - Perform...	NTP-016
Trunk Bridging Controller - J1C015MB - Replace	DLP-572

Figure 1.   Typical List of Jobs You May Have to Do

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Issue 2

NTP-016
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PERFORM SYSTEM TEST

DO THE ITEMS BELOW IN ORDER LISTED	FOR DETAILS, GO TO
1 Test Local Maintenance Terminal	DLP-531
2 Place SEC/SEB in Off-Line Mode	
A. If in On-Line Mode, Change System From On-Line to Off-Line	DLP-509
B. If Powered Down, Condition System for Off-Line Operation as Follows	
1. Power up Minicomputer	DLP-503
2. Power up Line Printer	DLP-503
3. Power up Maintenance Terminal	DLP-510
. . .	
. . .	
. . .	
. . .	
. . .	
. . .	
7 Run Computer Display Terminal Test For All Positions	DLP-513
8 Mount Tape	DLP-500
9 Test Computer Display	DLP-522

Figure 2.   Typical List of Specific Instructions for Doing a Job

Figure 3 is a typical page of a DLP-500 (Detailed Level Procedure - 500) that gives numbered, step-by-step instructions. To use this procedure, you must start with Step 1 and proceed as directed by the instructions until you complete this procedure. Note that Step 1 of this procedure is preceded by a statement called a SUMMARY.   A summary is used as a memory jogger, and briefly tells you how to do the procedure and what measurements or results you can observe. If you can do the procedure after reading the SUMMARY, go ahead and do it without reading any further.

Now, look at Step 6 of DLP-500 as shown in Figure 3. Note that following the action statement there is the sentence, For help see DLP-563. When you see a statement like this, it means that additional step-by-step instructions for doing just that step are given in the referenced procedure. In this case, DLP-563 gives you the details on how to ensure that the write-enable ring is not installed on the file reel. If you, in this case, cannot do Step 6, then go to DLP-563. In either case, you must continue with Step 7 until you have completed the procedure. In some cases, you may be directed to a procedure where the procedure number is preceded by the letters TAP (Trouble Analysis Procedure); for example, TAP-109. This means that you have trouble in the equipment, and in this case TAP-109 will give you step-by-step instructions to fix the trouble. After you have fixed the trouble, you must return to Step 1 of the procedure that sent you to TAP-109. However, if you came directly from IXL-001 to TAP-109, then your job is completed when you have fixed the trouble.

Admonishments: Three admonishments are used in this document as follows:

> ***DANGER: This means there is a possibility of personal injury.***
>
> ***Caution: This means there is a possibility of service interruption.***
>
> ***WARNING: This means there is a possibility of equipment damage.***

Important Items: Table A lists the more important items used in this document.

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Issue 2   Page 1 of 2

MOUNT TAPE

SUMMARY: Install tape with or without write enable ring, as required.
Thread tape and position tape at BOT (Beginning Of Tape) marker.

1. Get file reel and empty take-up reel.
2. Set **START/STOP** switch to **STOP**.
3. Set **ON LINE/OFF LINE** switch to **OFF LINE.**
4. Set **LOAD/BR REL** switch to center position.
5. Is data to be written on tape?
   If **yes**, then install write enable ring on file reel and go
   to Step **7**.
   If **no**, then do Step **6**.
6. Ensure that write enable ring is not installed on file reel. For help
   see DLP-563.
7. Open tape transport door.

Figure 3.   Typical List of Detailed Instructions for Doing a Job

TABLE A Important Procedural Items and Definitions

Item	Definition
Acceptance (NTP-002)	Provides information and identifies jobs to be done to accept equipment after it is installed.
Maintenance Philosophy	The maintenance philosophy, when provided, gives an overview of the considerations designed into the trouble-clearing procedures.
DLP (Detailed Level Procedure)	Detailed, step-by-step instructions.
TAP (Trouble Analysis Procedure)	Step-by-step, trouble-clearing instructions to locate and/or fix troubles.
NTP (Non-Trouble-Clearing Procedure)	A list of items to perform normal work other than trouble-clearing.