FIRST CLASS MAIL

Installation Engineering Handbook 269
(File Ahead of Section 300)
Western Electric
Northern Illinois Works

UIS SECTION 300
10-10-80
Replaces: UIS 300
dated 5-16-80

URGENT INFORMATION OR INSTRUCTION SUPPLEMENT NO. 3 ESS PROCESSOR AND PERIPHERAL TEST PROGRAM (X-RAY) TESTS

1. GENERAL

1.1 This instruction is issued to provide information regarding testing of Tape Data Controller (TDC) units using the procedures provided in PA-592601. The KS-21447, list two minirecorders are currently being manufactured without the write-enable switch. The function of this switch is to report the position of the arrowhead (slot) on the data cartridge. This function is not used by the application software and therefore has been eliminated. This change necessitates elimination of that portion of the manual TDC tests which verifies the operation of the write-enable switch.

1.2 This instruction also provides patches for the 3A CC Common System X-ray Programs which are contained on tape cartridge TP-592600, Issue 2.

1.21 The patches shown in Table A provide I/O channel assignments for Teletype Controllers (TTYC) 5 and 7 and also provide a means of defining serial I/O main channel 1 when TTYC 7 is equipped. This allows OCU test 74 to test both main channels.

NOTE: These patches need not be applied if TTYCs 5 and/or 7 are not equipped in the office.

1.22 The patches shown in Table B provide recovery test code for OCU tests 29, 30 and 31. This code is required due to a new device on the JL16 memory boards which requires additional store cycles in order to insure proper recovery from error conditions applied in these tests. This condition manifests itself as a failure in OCU test 57 in store 0, only when the OCU tests are run in sequence.

2. INSTRUCTIONS

2.1 If the KS-21447,L2 Minirecorder is not equipped with a write-enable switch, delete Steps 12, 13 and 14 on pages 33 and 34 of PA-592601, Issue 2.

2.2 After the tape has been bootstrapped and the system parameters entered, use the procedures contained in PA-592601 (pages 14 and 15) to verify the patch information and/or apply patches to the TP-592600, Issue 2 tape cartridges.

2.21 If required, apply the patches shown in Table A only if the office is equipped with TTYC 5 and/or 7.

NOTE: If these patches are inputted on site, type $ and reinput the system parameters.

2.22 If the patches shown in Table B are not on the tape, apply the patches using the procedure in PA-592601 as noted above.

TABLE A

CSECT	OFFSET	FROM	TO	INSTRUCTION
XRYCTL	2B	0000	1EC0	DATA X(1EC0)
XRYCTL	2D	0000	2D50	DATA X(2D50)
XRYCTL	1081	0050	00F0	DATA X(00F0)
XRYCTL	109F	A0CF	ACB1	DATA X(ACB1)
XRYCTL	10A0	D2A0	B1AC	DATA X(B1AC)
XRYCTL	10B4	0715	3E00	BL X(2200)
XRYCTL	10B5	000C	2200	(PATCH OFFSET 0)
XRYCTL	1127	B110	BE00	BL X(220A)
XRYCTL	1128	2507	220A	(PATCH OFFSET A)
XRYCTL	1129	0712	8712	(ADD BA BIT TO NI)
PATCH	0	0000	8715	OI R1, D(11)
PATCH	1	0000	000B	X(B)
PATCH	2	0000	5000	BCL X(35B7)
PATCH	3	0000	35B7	(XRYCTL OFFSET 10B7)
PATCH	4	0000	0715	OI R1, D(12)
PATCH	5	0000	000C	X(C)
PATCH	6	0000	5100	BNCL X(35A6)
PATCH	7	0000	35A6	(XRYCTL OFFSET 10A6)
PATCH	8	0000	3E00	BL X(35B7)
PATCH	9	0000	35B7	(XRYCTL OFFSET 10B7)
PATCH	A	0000	B110	LL R1, MONFLGS
PATCH	B	0000	2507	(XRYCTL OFFSET 7)
PATCH	C	0000	2810	TBN R1, S(SYS3)
PATCH	D	0000	5100	BNCL X(3629)
PATCH	E	0000	3629	(XRYCTL OFFSET 1129)
PATCH	F	0000	3120	LL R2, TTYC_ACT
PATCH	10	0000	2500	(XRYCTL OFFSET 0)
PATCH	11	0000	2827	TBN R2, 7
PATCH	12	0000	0701	LI R0, X(0)
PATCH	13	0000	0000	DATA X(0)
PATCH	14	0000	5100	BNCL X(3630)
PATCH	15	0000	3630	(XRYCTL OFFSET 1130)
PATCH	16	0000	0701	LI R0, X(1)
PATCH	17	0000	0001	DATA X(1)
PATCH	18	0000	3E00	BL X(3630)
PATCH	19	0000	3630	(XRYCTL OFFSET 1130)

TABLE B

CSECT	OFFSET	FROM	TO	INSTRUCTION
OCU29	188	BE10	BE00	BL X(221A)
OCU29	189	60C5	221A	(PATCH OFFSET 1A)
OCU29	2E9	BE10	BE00	BL X(2221)
OCU29	2EA	60C5	2221	(PATCH OFFSET 21)
OCU29	49B	BE10	BE00	BL X(2228)
OCU29	49C	60C5	2228	(PATCH OFFSET 28)
PATCH	1A	0000	BE10	BSA X(2300)
PATCH	1B	0000	2300	(PATCH OFFSET 100)
PATCH	1C	0000	BE10	BSA X(60C5)
PATCH	1D	0000	60C5	OCU27 OFFSET 3B8
PATCH	1E	0000	0C31	DATA X(C31)
PATCH	1F	0000	BE00	BL X(9356)
PATCH	20	0000	9356	(OCU29 OFFSET 18B)
PATCH	21	0000	BE10	BSA X(2300)
PATCH	22	0000	2300	(PATCH OFFSET 100)
PATCH	23	0000	BE10	BSA X(60C5)
PATCH	24	0000	60C5	(OCU27 OFFSET 3B8)
PATCH	25	0000	0C31	DATA X(C31)
PATCH	26	0000	BE00	BL X(94B7)
PATCH	27	0000	94B7	(OCU29 OFFSET 2EC)
PATCH	28	0000	BE10	BSA X(2300)
PATCH	29	0000	2300	(PATCH OFFSET 100)
PATCH	2A	0000	BE10	BSA X(60C5)
PATCH	2B	0000	60C5	(OCU27 OFFSET 3B8)
PATCH	2C	0000	0C31	DATA X(C31)
PATCH	2D	0000	BE00	BL X(9669)
PATCH	2E	0000	9669	(OCU29 OFFSET 49E)
PATCH	100	0000	F310	HA
PATCH	101	0000	3170	LL R7, MONFLGS
PATCH	102	0000	2507	(XRYCTL OFFSET 7)
PATCH	103	0000	287D	TBN R7, S(128K)
PATCH	104	0000	5826	BC X(2329)
PATCH	105	0000	3E10	BSA X(60C5)
PATCH	106	0000	60C5	(OCU27 OFFSET 3B8)
PATCH	107	0000	0C31	DATA X(C31)
PATCH	108	0000	3200	LAL R0+4, RA0
PATCH	109	0000	230C	(PATCH OFFSET 10C)
PATCH	10A	0000	BE00	BL INTRPN03
PATCH	10B	0000	5848	(CDGNM OFFSET 44)
PATCH	10C	0000	0016	STOP_OFF
PATCH	10D	0000	4EC6	SEND MMSOFF, BR

TABLE B (CONT'D)

CSECT	OFFSET	FROM	TO	INSTRUCTION
PATCH	10E	0000	0040	
PATCH	10F	0000	80B0	
PATCH	110	0000	2005	MICRO BRXMMS%
PATCH	111	0000	CACA	
PATCH	112	0000	4021	NO3_CODE +2
PATCH	113	0000	0000	
PATCH	114	0000	2315	
PATCH	115	0000	BE10	BSA X(B73E)
PATCH	116	0000	B73E	(OCU41 OFFSET 472)
PATCH	117	0000	86D0	ZR R13
PATCH	118	0000	31C0	LL R12, BASADDR
PATCH	119	0000	580C	(CDGNM OFFSET 8)
PATCH	11A	0000	118F	RLN R8, 1
PATCH	11B	0000	888E	SN R8, 2
PATCH	11C	0000	1CC8	IRM R12, R8, 3
PATCH	11D	0000	0003	
PATCH	11E	0000	0637	LN R3, 7
PATCH	11F	0000	8627	LN R2, 7
PATCH	120	0000	9600	MIMODE L SDR1, R0
PATCH	121	0000	E453	
PATCH	122	0000	0122	MSTFX R2(RA0)
PATCH	123	0000	BC40	DATA X(BC40)
PATCH	124	0000	3C20	BX R2, X(2320)
PATCH	125	0000	2320	(PATCH OFFSET 120)
PATCH	126	0000	3C30	BX R3, X(231F)
PATCH	127	0000	231F	(PATCH OFFSET 11F)
PATCH	128	0000	1888	TZ R8
PATCH	129	0000	5BF2	BNC X(231B)
PATCH	12A	0000	DD10	BTSAG

Manager, Development Engineering -
Network Systems

10-3-80