DIMENSION® 2000 AND CUSTOM PBX
MICRODIAGNOSTIC 0

CONTENTS

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1. GENERAL INFORMATION

1.1 Description

1.11 This section describes the procedures to be followed to perform Microdiagnostic 0 . The Microdiagnostics are a series of tests of the processor that are initiated from the Alarm Panel. The machine instructions for the performance of these tests are provided in the processor on Read Only Memories (ROM). The circuits tested by this section are shown in SD-1E480-01.

1.2 Sequence of Operations

1.21 This test should be run after the Application of Power Section and before any Call Processing Sections. All Microdiagnostics should be run in order (Tests 0 through 9, covered in Sections 310 through 319) since each Microdiagnostic uses some of the circuits tested by the previous test while testing additional circuits.

2. RECORDS AND REQUIREMENTS

2.1 Records

2.11 Form SD-97-1313 is required for recording the results of this test.

2.2 Requirements

2.21 The tests in this section are based on the performance requirements for SD-1E480-01.

3. TEST EQUIPMENT

3.1 There are no test sets, cords, or accessories needed for this section.

4. PROCEDURE

4.1 Test Description

4.11 This test tests the 3A registers, R1 and R2, R Mux, and the Miscellaneous I inputs. This test and all other Microdiagnostics (MD's) except MD9, run continuously repeated cycles until some other operation is initiated.

4.12 Any alarm indicators in the right section of the Processor Alarm Panel (under title, "MAAP PROCEDURE") except the first two, TAPE and I/0, should be ignored during all the MD's.

4.13 A diagram of the Processor Alarm Panel is shown in FIG. 1.

4.2 Test Procedure

4.21 If either the "OVER TEMP or FUSE" indicators are lighted in the "CONTROL" section of the Alarm Panel, clear these conditions before proceeding.

4.22 Remove any tape cartridge that might be in the tape transport.

4.23 Place GO/HALT switch in GO position.

4.24 Set the Alarm Panel TEST SELECT Thumbwheel Switch to Position O.

4.25 Momentarily operate the Alarm Panel RESET switch.

4.26 Momentarily depress the Alarm Panel ENABLE switch.

4.27 If this system has dual processors, perform this test on both.

4.3 Test Results Indications

4.31 The following indicators should light steadily: MJ, PROC, and PASS.

4.32 If this test passes, proceed to SEC. 311. If this test fails, proceed to PAR. 4.33.

4.33 Test failure will cause the following indicators to light steadily: MJ and PROC. The FAIL indicator will light steadily or blink.

4.34 To replace a circuit pack in the processor carrier, operate the Alarm Panel GO/HALT switch to the HALT position, replace the circuit pack, operate the GO/HALT switch to the GO position, and operate the RESET switch.

4.35 If the Alarm Panel indicators are not as described in either PAR. 4.31 or 4.33, replace the LC147 circuit pack and repeat the test. If the indicator display is unchanged, return the replaced LC147 to its original slot and investigate the Alarm Panel wiring and LED's.

4. PROCEDURE (Cont'd)

4.3 Test Results Indications (Cont'd)

4.36 If the test fails, replace the LC455 (or LC143) circuit pack and repeat the test.

4.37 If the test still fails and the LC143 pack is used in this system, replace the LC142 pack and retest.

4.38 End of Section.

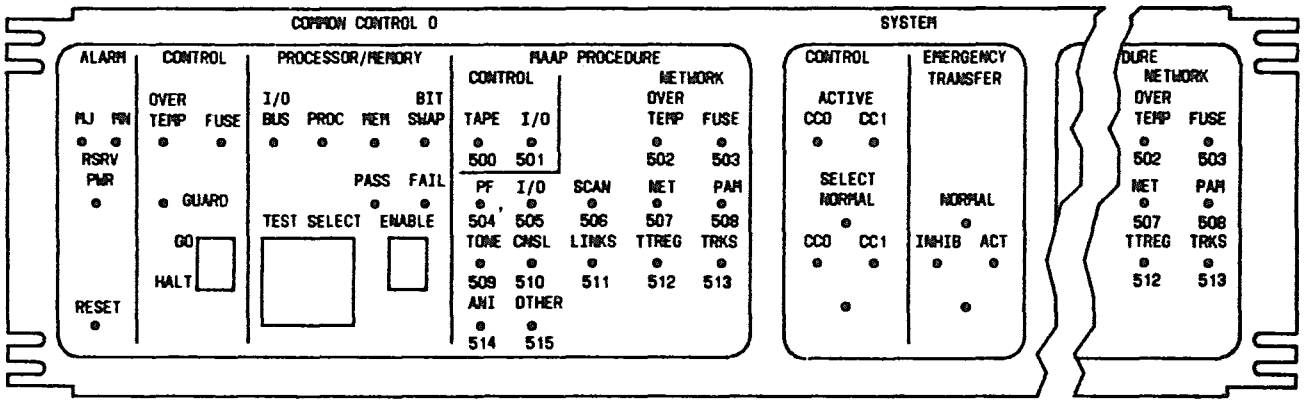

FIG. 1 - DIMENSION 2000 and Custom PBXs
Duplicated and Nonduplicated Common Control Alarm Panels

Reason for Issue:
Update

Manager, Denver PBX PECC