# DIMENSION® 2000 AND CUSTOM PBX
# MICRODIAGNOSTIC 4

## CONTENTS



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## 1. GENERAL INFORMATION

### 1.1 Description

1.11 This section describes the procedures to be followed to perform Microdiagnostic 4. The Microdiagnostics are a series of tests of the processor that are initiated from the Alarm Panel. The machine instructions for the performance of these tests are provided in the processor on Read Only Memories (ROM). The circuits tested by this section are shown in SD-1E480-01.

### 1.2 Sequence of Operations

1.21 This test should be run after the Application of Power Section and before any Call Processing Sections. All Microdiagnostics should be run in order (Tests 0 through 9, covered in Sections 310 through 319) since each Microdiagnostic uses some of the circuits tested by the previous test while testing additional circuits.

## 2. RECORDS AND REQUIREMENTS

### 2.1 Records

2.11 Form SD-97-1313 is required for recording the results of this test.

### 2.2 Requirements

2.21 The tests in this section are based on the performance requirements for SD-1E480-01.

## 3. TEST EQUIPMENT

3.1 There are no test sets, cords, or accessories needed for this section.

## 4. PROCEDURE

### 4.1 Test Description

4.11 This test tests the Parity Generator-Checker Circuit. This test and all other Microdiagnostics, except MD9, run continuously repeated cycles until some other operation is initiated.

4.12 A diagram of the Alarm Panel is shown in Section 310.

4.13 If it is necessary to replace a circuit pack while performing this test section, follow the procedure given in Paragraph 4.34 of Section 310.

4.14 If a pack is replaced, repeat all previously run Microdiagnostic Tests.

### 4.2 Test Procedure

4.21 Set the Alarm Panel TEST SELECT Thumbwheel Switch to Position 4.

4.22 Momentarily operate the ENABLE switch.

4.23 If this system has dual processors perform this test section on both processors.

### 4.3 Test Results Indications

4.31 The following indicators should light steadily: MJ, PROC, BIT SWAP, and PASS.

4.32 If this test passes, set the GO/HALT switch to HALT and then GO (to turn off the BIT SWAP indicator), then proceed to Section 315. If this test fails, proceed to Paragraph 4.33.

4.33 Test failure will cause the following indicators to light steadily: MJ and PROC. The FAIL indicator will light steadily or blink.

4.34 If this test fails, replace one at a time, each of the circuit packs on the following list, in the order listed, returning each pack that does not change the symptoms:

LC143 (or LC455)
LC142
LC35 (or LC135)
LC36 (or LC136)
LC37 (or LC137 or LC138 or LC454)
1st LC28 (or LC128 or LC346) pack (i.e. the pack with memory block 0)

Retest after each circuit pack replacement.

4.36 End of section.

Reason for Issue:
Update

Manager, Denver PBX PECC