

DIMENSION® 2000 AND CUSTOM PBX
(CSS 201L)
X-RAY TEST 05
(Scanner/Distributor, Digital & Analogue Network)

1. GENERAL INFORMATION
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1. GENERAL INFORMATION

1.1 Description

1.11 This section describes the procedures to be followed to perform X-Ray Test 05.

1.12 X-Ray Test 05 checks the tone boards - LC04, LC05B. This test takes approximately 68 seconds.

1.2 Test Description

1.21 X-Ray Test 05 executes subroutine PAMXRY, which connects each of eight tones in turn to the tone monitor circuit and verifies the tone monitor output is correct. The sequence for checking each tone is as follows:

- a) Clear the tone port to be tested, which also clears the monitor.
- b) Assign the next time slot to the tone port, which assigns the same time slot to the monitor.
- c) Wait for monitor to stabilize and check it.

1.22 A maximum of two tone board pairs per module is expected and any number of modules. A time shared technique allows all tone boards to be tested in the same test interval.

2. RECORDS

2.1 Form SD-97-1313 is required for recording the results of this test.

3. TEST EQUIPMENT

3.1 Test Sets - See Section 400 of Handbook 281.

PRIVATE

THE INFORMATION CONTAINED HEREIN SHOULD NOT BE DISCLOSED TO UNAUTHORIZED PERSONS. IT IS MEANT SOLELY FOR USE BY AUTHORIZED BELL SYSTEM EMPLOYEES.

4. TEST PROCEDURES

4.1 Preliminary

4.11 Before executing this test, X-Ray Test 04 should have passed.

4.2 Test Execution

4.21 To execute X-Ray Test 05, momentarily operate the EXECUTE, 0, 5 keys.

4.3 Test Pass Indication

4.31 The SEE NOTE, MAAP LED lighting (or the Alarm Panel - PASS lighting) indicates that X-Ray Test 05 passed.

4.4 This completes the x-ray testing per X-Ray Test 05.

5. MINIMUM CIRCUIT PACK REQUIREMENTS (8K Memory, Low Speed, AD)

X-RAY Test 05

Basic Control Carrier
J58882AA

<u>CARRIER SLOT</u>	<u>CIRCUIT PACK CODE</u>	<u>FUNCTION</u>
00	LC35	RAM Bit Swap
01	LC28B or C	RAM (0-1FFF*)
02	LC28B or C	RAM (2000-3FFF*)
03	LC28B or C	RAM (4000-5FFF*)
04	LC28B or C	RAM (6000-7FFF*)
05	LC28B or C	RAM (8000-9FFF*)
06	LC28B or C	RAM (A000-BFFF*)
07	LC28B or C	RAM (C000-DFFF*)
08	LC28B or C	RAM (E000-FFFF*)
09	LC28B or C	RAM (10000-11FFF*)
10	LC28B or C	RAM (12000-13FFF*)
11	LC28B or C	RAM (14000-15FFF*)
12	LC28B or C	RAM (16000-17FFF*)
13	LC28B or C	RAM (18000-19FFF*)
14	None Required	
15	None Required	
16	None Required	
17	LC36	RAM Data Buffer
18	LC37	RAM Control
19	LC147	Alarm Control
20	LC143	Processor
21	LC143	Processor
22	LC142	ROM
23	None Required	
24	LC133	I/O Memory Decode
25	LC132	I/O Buffer
26	LC29B	Tape Controller II
27	LC30B	Tape Controller I
28	None Required	
29	None Required	
30	LC34B or LC172	Data Link**
31	None Required	
32	None Required	
33	None Required	
34	None Required	
35	None Required	
36	None Required	
37	None Required	
38	None Required	
39	LC130	4 MHZ Channel Control
40	LC131	4 MHZ Subchannel
41	None Required	

*Memory locations

**The tape will load and the processor x-ray will run without this circuit pack; however, MAAP I/O is not possible.

5. MINIMUM CIRCUIT PACK REQUIREMENTS (8K Memory, High Speed)

X-RAY Test 05

Basic Control Carrier
J58882AA

<u>CARRIER SLOT</u>	<u>CIRCUIT PACK CODE</u>	<u>FUNCTION</u>
00	LC135	RAM Bit Swap
01	LC28C	RAM (0-1FFF*)
02	LC28C	RAM (2000-3FFF*)
03	LC28C	RAM (4000-5FFF*)
04	LC28C	RAM (6000-7FFF*)
05	LC28C	RAM (8000-9FFF*)
06	LC28C	RAM (A000-BFFF*)
07	LC28C	RAM (C000-DFFF*)
08	LC28C	RAM (E000-FFFF*)
09	LC28C	RAM (10000-11FFF*)
10	LC28C	RAM (12000-13FFF*)
11	LC28C	RAM (14000-15FFF*)
12	LC28C	RAM (16000-17FFF*)
13	LC28C	RAM (18000-19FFF*)
14	None Required	
15	None Required	
16	None Required	
17	LC136	RAM Data Buffer
18	LC137	RAM Control
19	LC147	Alarm Control
20	LC143	Processor
21	LC143	Processor
22	LC142	ROM
23	None Required	
24	LC133	I/O Memory Decode
25	LC134	I/O Buffer
26	LC29B	Tape Controller II
27	LC30B	Tape Controller I
28	None Required	
29	None Required	
30	LC34B or LC172	Data Link**
31	None Required	
32	None Required	
33	None Required	
34	None Required	
35	None Required	
36	None Required	
37	None Required	
38	None Required	
39	LC130	4 MHZ Channel Control
40	LC131	4 MHZ Subchannel
41	None Required	

*Memory locations

**The tape will load and the processor x-ray will run without this circuit pack; however, MAAP I/O is not possible.

5. MINIMUM CIRCUIT PACK REQUIREMENTS (16K Memory, High Speed)

X-RAY Test 05

Basic Control Carrier
J58882AA

<u>CARRIER SLOT</u>	<u>CIRCUIT PACK CODE</u>	<u>FUNCTION</u>
00	LC135	RAM Bit Swap
01	LC128B	RAM (0-3FFF*)
02	LC128B	RAM (4000-7FFF*)
03	LC128B	RAM (8000-BFFF*)
04	LC128B	RAM (A000-DFFF*)
05	LC128B	RAM (E000-11FFF*)
06	LC128B	RAM (12000-15FFF*)
07	LC128B	RAM (16000-19FFF*)
08	None Required	
09	None Required	
10	None Required	
11	None Required	
12	None Required	
13	None Required	
14	None Required	
15	None Required	
16	None Required	
17	LC136	RAM Data Buffer
18	LC138	RAM Control
19	LC147	Alarm Control
20	LC143	Processor
21	LC143	Processor
22	LC142	ROM
23	None Required	
24	LC133	I/O Memory Decode
25	LC134	I/O Buffer
26	LC29B	Tape Controller II
27	LC30B	Tape Controller I
28	None Required	
29	None Required	
30	LC34B or LC172	Data Link**
31	None Required	
32	None Required	
33	None Required	
34	None Required	
35	None Required	
36	None Required	
37	None Required	
38	None Required	
39	LC130	4 MHZ Channel Control
40	LC131	4 MHZ Subchannel
41	None Required	

*Memory locations

**The tape will load and the processor x-ray will run without this circuit pack; however, MAAP I/O is not possible.

5. MINIMUM CIRCUIT PACK REQUIREMENTS

X-RAY Test 05

Module Control Carrier
J58882BA

<u>CARRIER SLOT</u>	<u>CIRCUIT PACK CODE</u>	<u>FUNCTION</u>
00	None Required	
01	LC103	Central L/T PAM Amplifier
02	None Required	
03	None Required	
04	None Required	
05	None Required	
06	None Required	
07	None Required	
08	None Required	
09	None Required	
10	None Required	
11	None Required	
12	None Required	
13	None Required	
14	None Required	
15	None Required	
16	None Required	
17	None Required	
18	None Required	
19	None Required	
20	None Required	
21	None Required	
22	None Required	
23	None Required	
24	None Required	
25	None Required	
26	None Required	
27	LC121	Network Clock
28	None Required	
29	None Required	
30	None Required	
31	None Required	

5. MINIMUM CIRCUIT PACK REQUIREMENTS

X-RAY Test 05

Line Group Control Carrier
J58882BB

<u>CARRIER SLOT</u>	<u>CIRCUIT PACK CODE</u>	<u>FUNCTION</u>
01	None Required	
02	LC102	Group PAM Amplifier
03	None Required	
04	LC101	Carrier PAM Amplifier
05	LC04	Tone Plant-A
06	LC05B	Tone Plant-B
07	LC02	Line Circuit
08	None Required	
09	None Required	
10	None Required	
11	None Required	
12	None Required	
13	None Required	
14	None Required	
15	None Required	
16	None Required	
17	None Required	
18	LC122	Network Control
19	None Required	
20	LC46	Scanner/Distributor
21	LC123	Network I/O
22	LC124	Network Data

Reason for Issue:

New Section

Manager, Denver PBX PECC