DIMENSION ®2000 AND CUSTOM PBX

NETWORK INDICATOR AND TEST
(PROC 581)

CONTENTS

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1. GENERAL

1.1 This section provides information to interrogate PROC 581 in the event that the craftsperson is directed to this procedure due to a NETWORK - NETWORK FAILURE. Whenever this type of failure occurs, the ALARM PANEL - NETWORK - MINOR and NET lamps will be lit.

2. RECORDS

2.1 Form SD-97-1313 is required for recording the results of this test.

3.   MAAP DISPLAY FIELDS (PROC 581 FORMAT)

3.1   The following describes each of the display fields for the NETWORK INDICATOR AND TEST (PROC 581) format.

FIELD   DESCRIPTION

1   TEST NO - displays active test number (maximum of 4) per the following encodes.

ENCODE	DESCRIPTION
1	Not used in this procedure, go to procedure 507 to obtain failure history.
2	Tests all circuits in system if TEST TYPE = 0 (FIELD 2). Tests all circuits in a module if TEST TYPE = 1 (FIELD 2). Display test summary.
3	Same as TEST 2, except that the identity of the failing circuit is displayed.
4	Tests a particular circuit continuously.

2   TEST TYPE - (TESTS 2 and 3) indicates whether all shift register circuits in the system (encode = 0) or all circuits in a module will be tested (encode = 1).

3   EQUIPMENT LOCATION - MODULE - (TESTS 2, 3, and 4) indicates the module under test.

4 - 7   EQUIPMENT LOCATION - CAB/CAR/SLOT/CKT - (TEST 3) indicates location (cabinet/carrier/slot/ circuit) of failing circuit. (TEST 4) indicates location of circuit under test.

8   TYPE - (TESTS 3 and 4) indicates type of circuit being displayed in FIELDS 3 thru 7, per the following encodes:

ENCODES	DESCRIPTION
1	Line circuit
2	Trunk circuit
3	Link circuit

9   MTCE BUSY STAT - (TESTS 3 and 4) indicates the Main-
        tenance Busy Status of circuit per the following:

        ENCODE   DESCRIPTION

        0   Not busied out
        1   Busied out
        2   Unequipped

10   SUMMARY - TOTAL CIRCUITS TESTED - (TEST 2 and 3)
        indicates total number of circuits tested.

11   SUMMARY - TOTAL CIRCUITS FAILED - (TESTS 2 and 3)
        indicates total number of circuit failures found.

12   FAILURE CODE - (TESTS 3 and 4) indicates failure code for
        circuit displayed in FIELDS 3 thru 7, per the following
        encodes:

        ENCODE   DESCRIPTION

        0   Pass (TEST 4 only)
        1   Busy shift register verify
        2   Shift register stuck at 1 (in all time slots)
        3   Shift register stuck at 0 (no time slots found)
        4   Wrong time slot in shift register
        5   Time slot on wrong buss
        7   Circuit not tested (couldn't find an idle time slot)
        8   Network order incomplete
        9   No time slot

4.   MAAP CONTROL KEY SEQUENCES

4.1   PROC NO. 5, 8, 1, ENTER – Causes program for procedure 581 to be loaded into memory from tape for execution.

4.2   BUSY OUT – (TESTS 3 and 4) busies out circuit displayed in FIELDS 3 thru 7.

4.3   NEXT CIRCUIT – (TESTS 3 and 4) causes circuit displayed in FIELDS 3 thru 7 to display the next failed circuit (TEST 3) or to display the next testable circut (TEST 4).

4.4   NEXT DATA – (TESTS 2 and 3) alters TEST TYPE in FIELD 2.

4.5   NEXT UNIT – (TESTS 2, 3 and 4) advances test to the next module.

4.6   NEXT TEST – (TESTS 1, 2, 3, and 4) advances the program to the next test.

4.7   RELEASE BUSY OUT – (TESTS 3 and 4) releases the circuit displayed in FIELDS 3 thru 7 from a Maintenance Busy condition.

4.8   RESET – (TESTS 1, 2, 3, and 4) resets procedure back to the beginning of TEST 1.

4.9   STOP – (TESTS 2, 3, and 4) stops test from further execution.

5.   PROCEDURES

5.   General

5.1.1   The following describes the repair procedures that should be followed in the event that a NETWORK - NETWORK ALARM occurs.

5.1.2   If not previously performed, execute TESTS 2 and 3 of PROC 507 and record the results.

5.1.3   Execute TEST 3 (with TEST TYPE = 0) and record the results. If there are no failures, execute TEST 4 to test the failures recorded in Step 5.1.2 above. If failures are detected, use the following repair procedures:

a)   Failure Code = 1, 2, 3 or 4.

    aa)   Single Port Circuit or Individual Port Circuits.

        aaa)   Replace circuit packs associated with Ports displayed in FIELDS 3 thru 7.

    ab)   All ports within same slot.

        aba)   Replace circuit board associated with Ports displayed.

        abb)   Replace LC49 within same carrier.

    ac)   Common Port Circuits in all slots in a carrier.

        aca)   Replace LC49.

        acb)   Disconnect Port Circuits one at a time until fault is cleared, replace circuit pack that caused the fault to clear.

    ad)   All Port Circuits in a group of 4 slots.

        ada)   Replace LC39 within same carrier.

        adb)   Disconnect Port Circuits one at a time until fault is cleared. Replace board that cleared fault.

ae) All Port Circuits in a carrier.

 aea) Replace LC49 within same carrier.

 aeb) Replace LC101 within same carrier.

 aec) Replace Lc122 within same cabinet.

 aed) Replace LC123 within same cabinet.

 aec) Replace LC46 within same cabinet.

 aef) Replace LC124 within same cabinet.

af) All Port Circuits within a cabinet.

 afa) Replace LC122 within same cabinet.

 afb) Replace LC123 within same cabinet.

 afc) Replace LC46 within same cabinet.

 afd) Replace LC124 within same cabinet.

 afe) Unplug on-line LC121 to cause clock switch; replace unplugged LC121 if fault is cleared.

ag) All Port Circuits within a module.

 agg) Unplug on-line LC121 to cause clock switch; replace unplugged LC121 if failure cleared.

b) Failure Code = 5

ba) Replace LC101 within same carrier.

c) Failure Code = 7

ca) Try executing the test again.

d) Failure Code = 8

da) Replace LC122.

e) Failure Code = 9

ea) No time slot available on buss. Try executing again.

6.   TROUBLESHOOTING AIDS

6.1   General test information.

6.1.1   TEST 1 - TEST 1 is the default test when the procedure is
first read in from the tape or when the MAAP-RESET key
is operated. TEST 1 is normally reserved for failure history
displays and in the case of NETWORK FAILURES Procedure
507 must be used instead. Therefore, any operation of the
MAAP keys other than Next TEST will cause an error.

6.1.2   TEST 2 - The purpose of TEST 2 is to test all shift registers
(either in a module or a system) and to display a summary
of the failures when the test is completed. If no failures
are found and if the ALARM PANEL - NETWORK - NET
ALARM was lit, it will be extinguished.

When the test is first called, the MAAP will display a 2 in
FIELD 1 and 0 in FIELD 2, which indicates that a test of
all the shift registers in the system will be made. If it is
desired to test all the shift registers in a module, the NEXT
DATA key must be operated and a module number must be
entered. If it is desired to go to another module, then the
NEXT UNIT key may be operated until the desired one appears.
Once the test is started (by operating the EXECUTE key)
it may be stopped by operating the STOP key.

6.1.3   TEST 3 - The purpose of TEST 3 is to test all shift registers,
either in the system or a particular module. When a circuit
failure is detected, the test will stop and display the failure
location. Operation of the MAAP-NEXT CIRCUIT key will
cause the test to continue until another circuit failure is
detected.

If it is desired to test all the shift registers in a particular
module, the MAAP-NEXT DATA key is operated until a 1
is displayed in FIELD 2 (TEST TYPE). Operating the MAAP-
NEXT UNIT key will cause module number to advance in
FIELD 3. The test , if executing, may be stopped by operating
the MAAP-STOP key.

If during the execution of this test, no failures were detected,
the ALARM PANEL MINOR and NETWORK-NET lamps will
be turned off if they were turned on. A circuit displayed
in FIELDS 3 thru 7 may be busied out or unbusied by operating
either the BUSY OUT and RLS BUSY OUT keys, respectively.

6.1.4   TEST 4 - The purpose of test 4 is to test a particular circuit continuously and to display the failure code in FIELD 12. If a default circuit is not (last figure displayed) displayed, the craftsperson must enter a circuit. If the displayed circuit is not the circuit desired, it may be changed by the CHANGE FIELD sequence, or by operating the NEXT CIRCUIT key (advances to the next circuit) or by operating the NEXT UNIT (advances to the next module).

The test is started by operating the MAAP-EXECUTE key. The WAIT lamp is not turned on since the test is continuous. The failure code is flashed in FIELD 12 to indicate that the test is running. The test may be stopped by either operating the STOP key (which then allows a NEXT CIRCUIT or a NEXT UNIT key operation) or by operating the NEXT TEST, RESET, or PROC NO keys.

Circuits displayed in FIELDS 3 thru 7 may be busied out or unbusied by operating the BUSY OUT or RLS BUSY OUT .

6.2   General troubleshooting information

6.2.1   PROC 581 checks the time slot shift registers by first checking to see if there are any time slots available within a particular module. If there are none, a failure code of 7 is indicated.

If the circuit under test is indicated in call records as being busy, then the time slot for that circuit is verified. If the circuit is on the wrong time slot buss, a failure code of 5 is indicated. If the circuit under test indicates an idle shift register or that port shift register is not on the right time slot, a failure code of 1 is indicated.

If the port circuit in call records indicates that it should be idle, the shift register is cleared, and then checked to see that it is cleared. If it cannot be cleared, a fault code 2 is indicated.

The port shift register is then tested with a particular buss. If no time slot can be found associated with a particular buss, a fault code 9 is indicated. If a time slot cannot be written into the port shift register, then a fault code of 3 is indicated. If a time slot cannot be written onto the correct buss, then a fault code of 5 is indicated. If the time slot written into the port shift register is not the correct one, a fault code of 4 is indicated.

Anytime the processor fails to receive an I/O completion signal, a fault code of 8 is indicated.

After the above has been performed for buss 0 the port shift register

LC123

LC124

LC46

LC49

PORT CIRCUIT

IOXPO- 39	(TP1)	(TP9)	23	RDATA	23
IOXNO- 38	(TP2)	(TP13)	25	INCLK	25
		(TP10)	21	EVENX	21
IOXP1- 89	(TP3)	(TP11)	20	EVEN	20
IOXN1- 88	(TP4)	(TP12)	65	RESET	65

18	RTRN	18	(TP3)
16	BUFF	16	(TP4)
14	ENDEC*	14	(TP8)
63	NBUFF*	16	(TP5)
69	FULL	69	(TP2)

4	NA00*	4
54	NA01*	54
5	NA02*	5
55	NA03*	55
6	NA04*	6
56	NA05*	56
7	NA06*	7
57	NA07*	57
8	NA08*	8
58	NA09*	58
9	NA10*	9
59	NA11*	59
61	NA15*	60
53	WRIO*	53

PORT SELECT

BOARD SELECT

CARRIER SELECT

13	SAD00*	93
15	SAD01*	43
14	SAD02*	46
12	SAD03*	96
16	SAD04*	45
17	SAD05*	95
	SCEN(0-6)*	36
10	SAD10	44
75	NPTEN*	91
19	BSYT*	

PORT SELECT

BOARD SELECT

| BPS(0-3)* | 33 |
| BBS(0-15)* | 24 |

LCO2, LCO8, ETC.

LC122

61	NA15*	61	19
60	NA13*	60
3	RDIO*	3
53	WRIO*	53
41	ND00*	41
91	ND01*	91
42	ND02*	42
92	ND03*	92
43	ND04*	43
93	ND05*	93	47
44	ND06*	44	96
94	ND07*	94	46
45	ND08*	45	95
95	ND09*	95	94
52	IODN*	52

IORPO- 36	94	ND07*
IORNO- 35	95	ND09*
	46	ND10*
IORP1- 86	96	ND11*
IORN1- 85	47	ND12*

MODULE CONTROL CABINET
CABLING FOR SCEN-*, SRD-, SBI-, SRCK-, SCBI-, SRDBUS-, WT-, TSCK- LEADS

NOTES: -( ) DENOTES SCEN-* LEAD
-DESIGNATIONS MC40, LG14 ETC. DENOTE
CARRIER BACKPLANE CONNECTORS
-LEVEL (-) DENOTES POSITION OF CARRIER
IN CABINET, LEVEL 0 = LOWEST LEVEL.

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12.

LINE CABINET

TABLING FOR SCEN-*, SRD-,
SBI-, SRCK-, SCBI-, SRDBUS-
WT- & TSCK- LEADS

```
LINE          LG22  <<    >>  LK06   LINE
GROUP                               PORT
CONTROL                             CARRIER
CARRIER

              LG25  <<              >>  LK06   LINE
                                               PORT
                                               CARRIER

              LG21  <<                          >>  LK06   LINE
                                                           PORT
                                                           CARRIER

              LG20  <<                                       >>  LK06   LINE
                                                                        PORT
                                                                        CARRIER
```
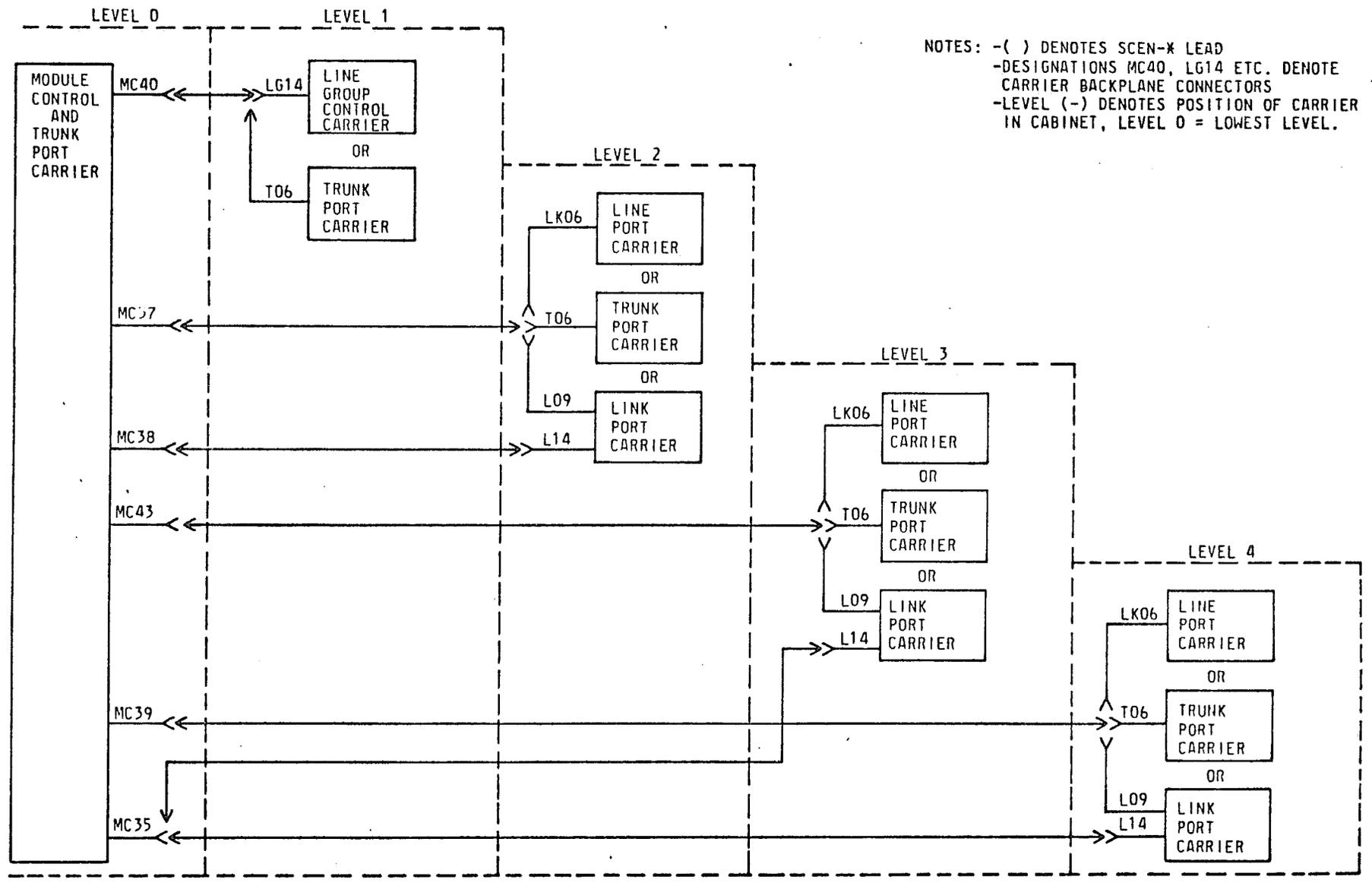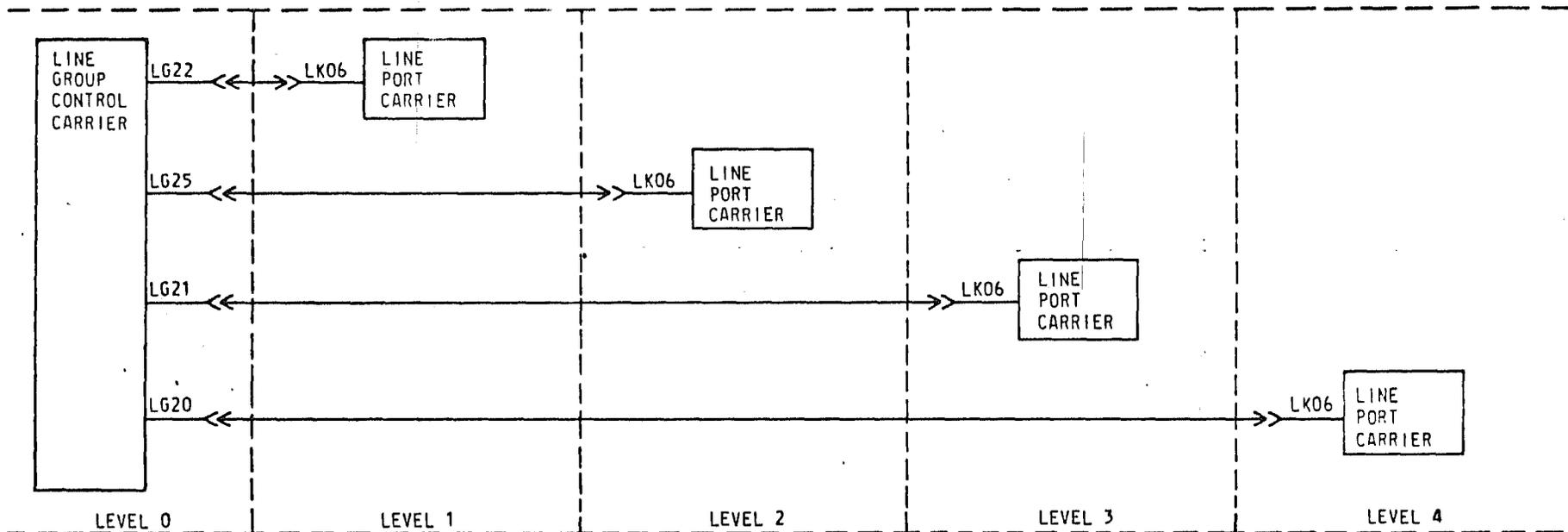
LEVEL 0   LEVEL 1   LEVEL 2   LEVEL 3   LEVEL 4

FLAT CABLE WIRING	
TERM	DESIG.
-1	DGRD
2	SCEN(-)*
3	BB(-)2*
4	BB(-)0*
5	
6	
7	SRD(-)
8	SBI(-)*
9	SRCK(-)
10	DGRD
11	
12	BB(-)3*
13	BB(-)1*
14	SCBI(-)*
15	
16	SRDBUS(-)
17	WT(-)*
18	
19	TSCK(-)
20	

```
20  o┌──┐o  10
    │••│
    │••│
    │••│
    │••│
    │••│
11  o└──┘o   1
```

BACK VIEW OF FLAT CABLE
CARRIER CONNECTOR

NOTES: -( ) DENOTES SCEN(-)* LEAD
-DESIGNATIONS LG22, LK06, ETC., DENOTE CARRIER
 BACKPLANE CONNECTORS
-LEVEL (-) DENOTES POSITION OF CARRIER IN
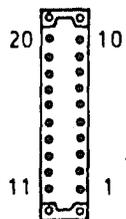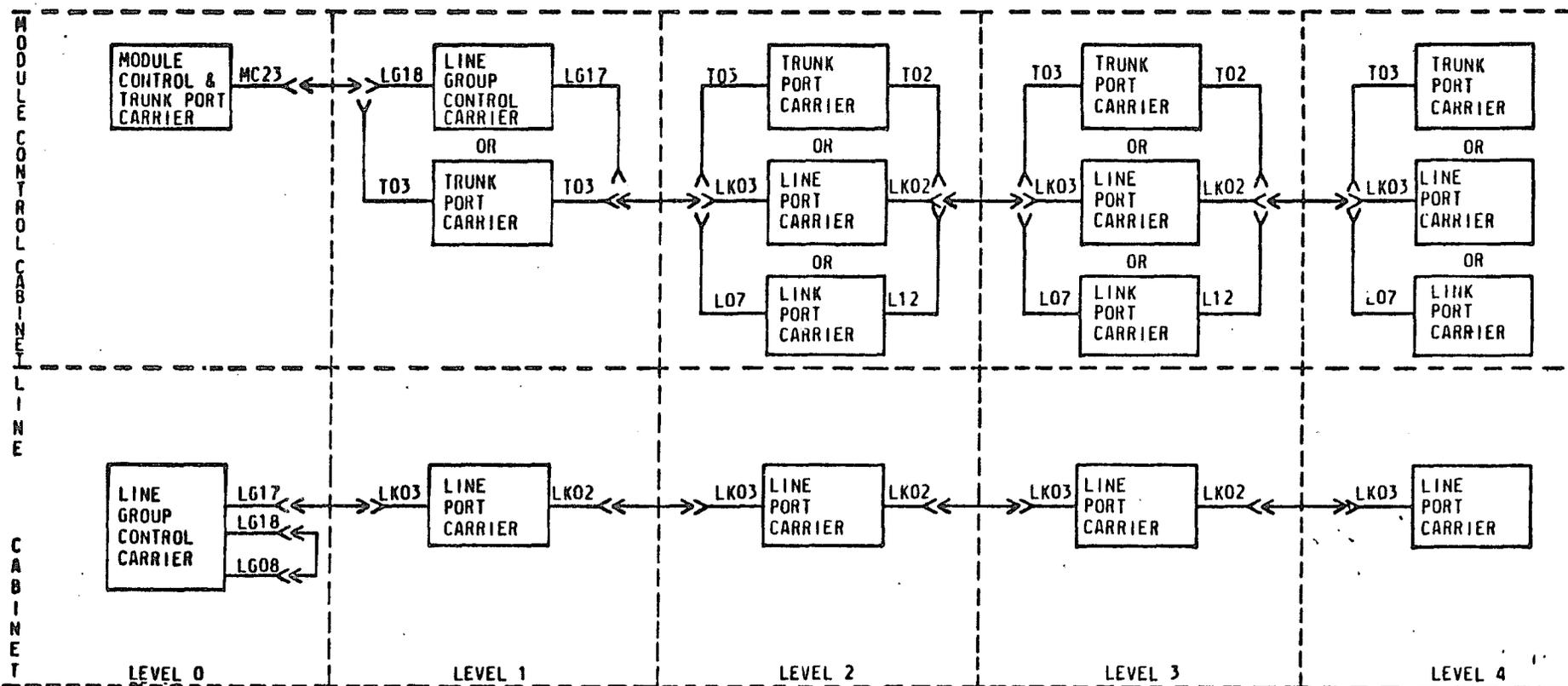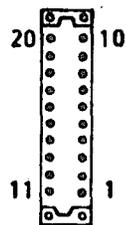 CABINET, LEVEL 0 = LOWEST LEVEL

MODULE CONTROL CABINET

LEVEL 0

MODULE CONTROL & TRUNK PORT CARRIER — MC23

LEVEL 1

LG18 — LINE GROUP CONTROL CARRIER — LG17

OR

TO3 — TRUNK PORT CARRIER — TO3

LEVEL 2

TO3 — TRUNK PORT CARRIER — TO2

OR

LKO3 — LINE PORT CARRIER — LKO2

OR

LO7 — LINK PORT CARRIER — L12

LEVEL 3

TO3 — TRUNK PORT CARRIER — TO2

OR

LKO3 — LINE PORT CARRIER — LKO2

OR

LO7 — LINK PORT CARRIER — L12

LEVEL 4

TO3 — TRUNK PORT CARRIER

OR

LKO3 — LINE PORT CARRIER

OR

LO7 — LINK PORT CARRIER

LINE CABINET

LEVEL 0

LINE GROUP CONTROL CARRIER — LG17, LG18, LG08

LEVEL 1

LKO3 — LINE PORT CARRIER — LKO2

LEVEL 2

LKO3 — LINE PORT CARRIER — LKO2

LEVEL 3

LKO3 — LINE PORT CARRIER — LKO2

LEVEL 4

LKO3 — LINE PORT CARRIER

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14.

FLAT CABLE WIRING	
TERM	DESIG.
1	GRD D
2	-
3	-
4	SPTENX
5	SDA1X
6	SADO1X
7	SAD10
8	SADO4X
9	SADO2X
10	DGRD
11	-
12	BUSELX
13	-
14	NPTENX
15	SDA2X
16	SADO0X
17	SCLKX
18	SADO5X
19	SADO3X
20	-

20 10

11 1

BACK VIEW OF CARRIER CONNECTOR

NOTES: LEVEL - REFERS TO CARRIER LEVEL IN CABINET.
LEVEL O = LOWEST LEVEL
DESIGNATIONS MC23, LG18, ETC. DENOTE CARRIER
BACKPLANE CONNECTORS

```
2MHZ*
SRCK*
TSCKA*
TSCKB*
BIT 15
CCLK
A11B(Q)     LC124
A2A(Q)
NET*
NO1O*
ADCLK
N1O*
RTRN
ENDEC*      LC123
START*
RESET
```
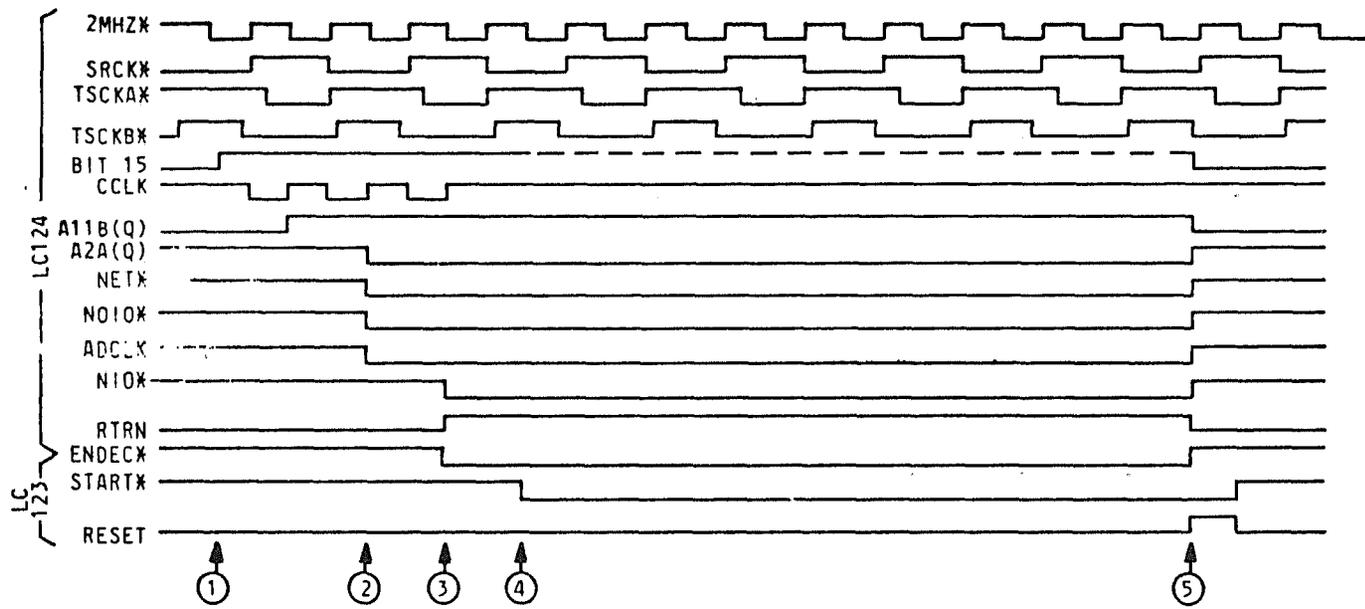
① BIT 15 INDICATES RECEIPT OF MESSAGE.

② NETWORK NET*/NO1O ACTIVATED

③ N1O*, RTRN, & ENDEC* ACTIVE ENABLES A RETURN MESSAGE

④ START* ACTIVE STARTS THE RETURN MESSAGE

⑤ RESET ACTIVE RESETS LC123 & LC124

NOTE: TRANSMIT SUB CYCLE SAME AS TRANSMIT
       ·SUB-CYCLE FOR DISTRIBUTE (DATA*/W1O*)

NETWORK (1ST MESSAGE)
NET*/NO1O* CYCLE
SHEET 1 OF 1
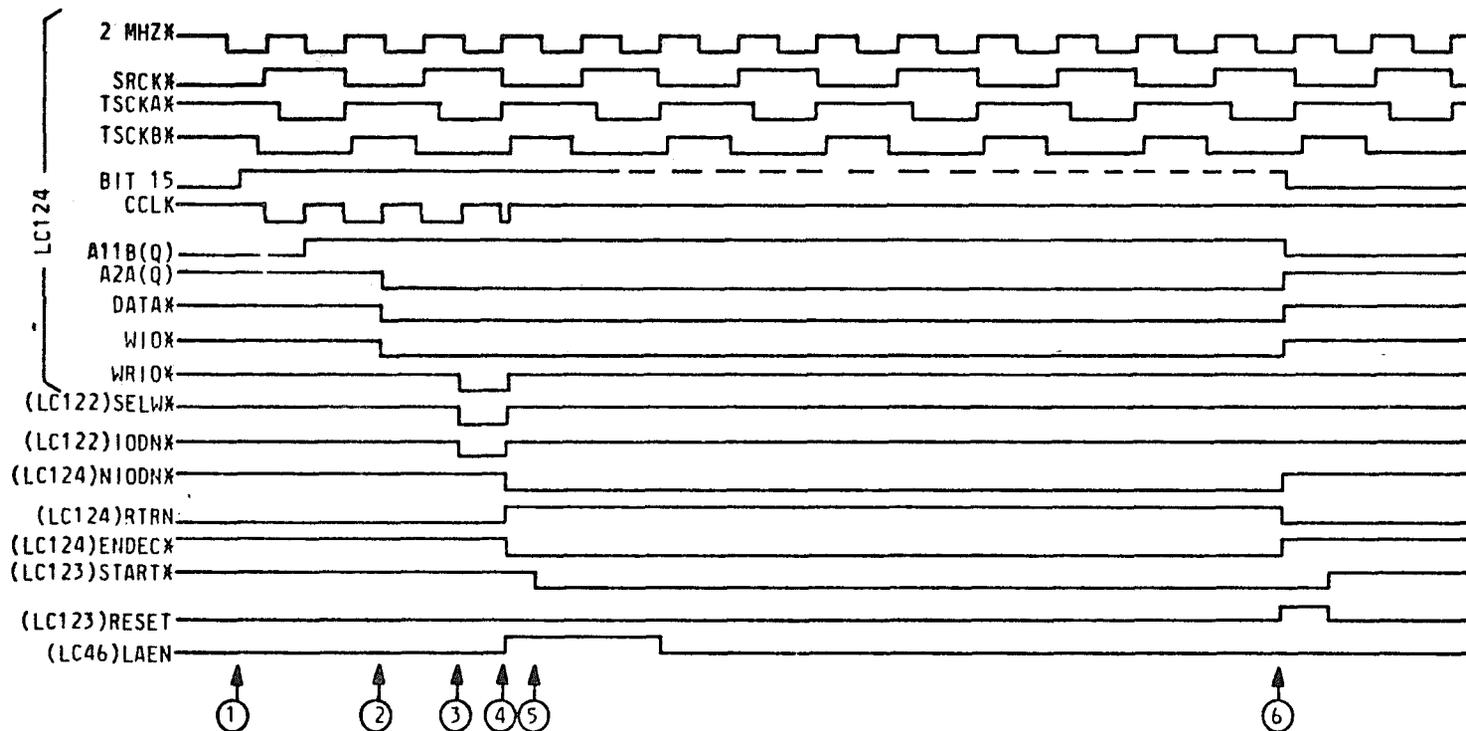
① BIT 15 ACTIVE INDICATES A MESSAGE HAS BEEN RECEIVED.

② DATAX/WIOX ACTIVE INDICATES DATA/WRITE I/O OPERATION.

③ WRIOX ACTIVATES SELWX & IODNX

④ NIODNX DEACTIVATES WRIOX, SELWX, IODNX & ACTIVATES RTRN
   AND ENDECX TSCKBX ACTIVATES LAEN.

⑤ STARTX ALLOWS THE RETURN MESSAGE TO BE SENT BACK.

⑥ RESET ACTIVE RESETS LC123 & LC124.

NOTE: TRANSMIT SUB CYCLE IS SAME AS THE DISTRIBUTE
       (DATAX/WIOX) TRANSMIT SUB-CYCLE.

NETWORK (2ND MESSAGE)
DATAX/WIOX CYCLE
SHEET 1 OF 1
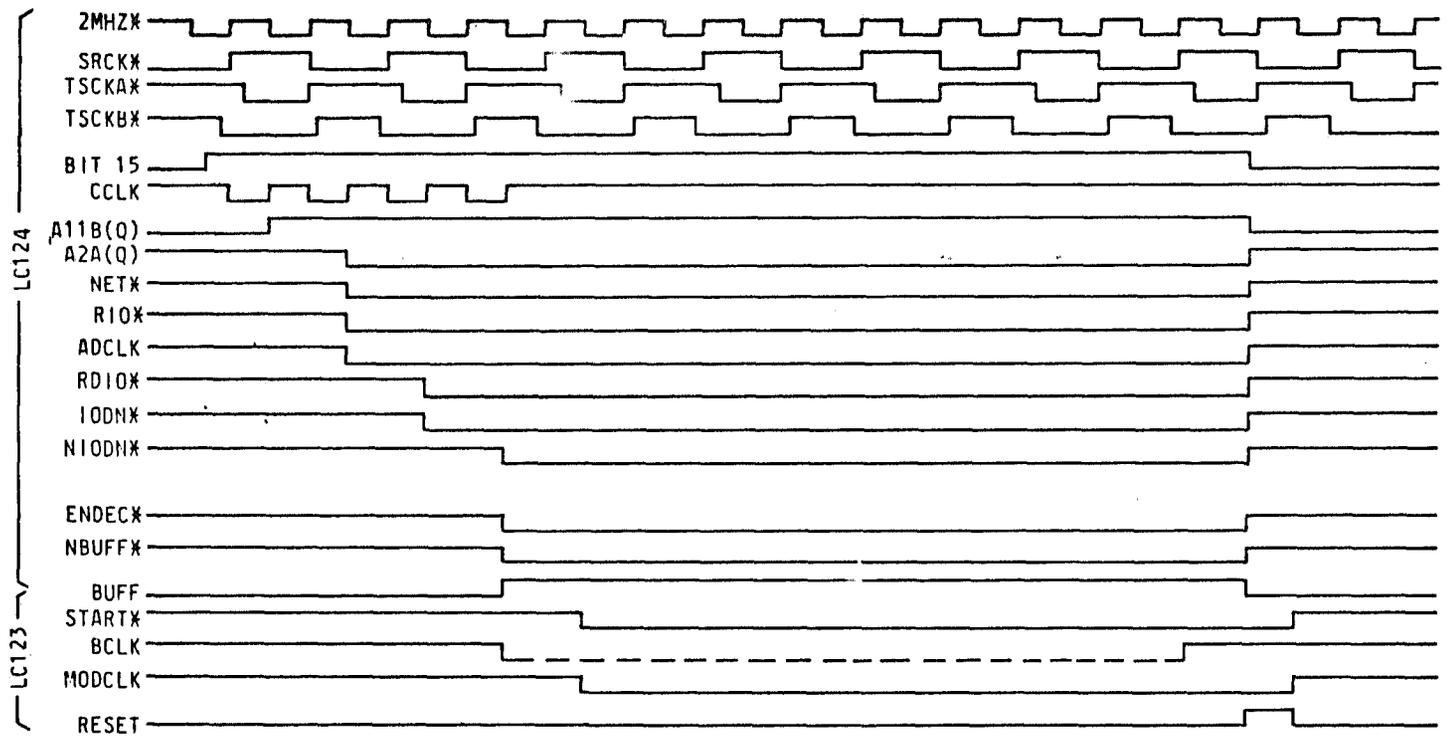
① BIT 15 ACTIVE INDICATES A MESSAGE HAS BEEN RECEIVED.

② MESSAGE IS DECODED AS A NETWORK/READ IO OPERATION.

③ RDIO✗ IS DELAYED, TO ALLOW FOR SETTLING OF ADDRESS. LEADS IODN✗ INDICATES THAT LC122 RECEIVED MESSAGE.

④ NIODN✗ ACTIVATED ON THE NEXT NEGATIVE 2MHZ✗ CLOCK.

⑤ START✗ ACTIVE SIGNALS THE START OF RETURN MESSAGE.

⑥ RESET ACTIVE RESETS LC123 & LC124.

NETWORK (3RD MESSAGE)

NET✗/RIO✗ CYCLE

SHEET 1 OF 2

LC123

12 MHZ
2 MHZ*
SRCK
START*
A10B(Q)
Q*
PS
A7(QA)
A7(QB)
A7(QC)
A7(QD)
CLEAR*
RESET
MODCLK
BCLK
A13B(Q̄)
A3(Q) PAREN
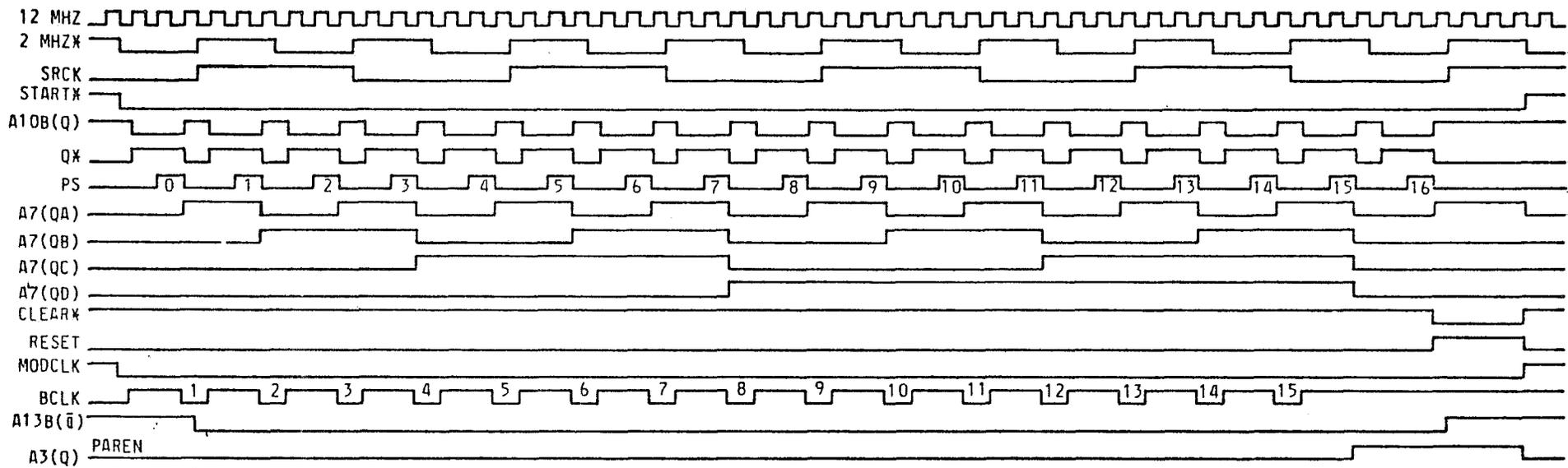
① START* ACTIVE BEGINS THE RETURN OF RETURNED MESSAGE.

② A13B(Q̄) ACTIVE DISABLES MODCLK SIGNAL.

③ PAREN ACTIVE, ENABLES PARITY BIT.

④ RESET ACTIVE, RESETS LC123 & LC124.

NETWORK (3RD MESSAGE)
NETX/RIOX TRANSMIT CYCLE
SHEET 2 OF 2

18.

MODULE CONTROL CARRIERS
CBI_ _ _*, GF_ _ _*, LCP_ _*, FLAT CABLE CONNECTIONS

LEAD DESIG.	FROM				VIA CABLE			TO				COMM-ENTS
	CAB.	LC-	SLOT	TERM.	CONN.	CONN. TERM.	CONN.	CAB.	LC-	SLOT	TERM.	
CBI0A0*	0/1	101	--	96	---	--	---	0/1	102	03	83	Bus 0
CBI0B0*	0/1	101	01	96	T09	18	MC15	0/1	102	03	82	Bus 0
CBI0C0*	0/1	101	01	96	T09	18	MC08	0/1	102	03	81	Bus 0
CBI0D0*	0/1	101	01	96	T09	18	MC02	0/1	102	03	89	Bus 0
CBI0E0*	0/1	101	01	96	T09	18	MC01	0/1	102	03	88	Bus 0
CBI1A0*	0/1	101	--	76	---	--	---	0/1	102	04	83	Bus 1
CBI1B0*	0/1	101	01	76	T09	13	MC15	0/1	102	04	82	Bus 1
CBI1C0*	0/1	101	01	76	T09	13	MC08	0/1	102	04	81	Bus 1
CBI1D0*	0/1	101	01	76	T09	13	MC02	0/1	102	04	89	Bus 1
CBI1E0*	0/1	101	01	76	T09	13	MC01	0/1	102	04	88	Bus 1

NOTE: When a line group control carrier is in a Module Control Cabinet, see Line Group Control Carrier in cabinets "0" and "1".

LEAD DESIG.	FROM				VIA CABLE			TO				COMM-ENTS
GBI04*	0	121	27	57	---	--	---	0	102	03	80	Bus 0
GBI05*	0	121	27	56	MC27	2	MC21	1	102	03	80	Bus 0
GBI14*	1	121	27	57	MC28	6	MC22	0	102	04	80	Bus 1
GBI15*	1	121	27	56	---	--	---	1	102	04	80	Bus 1
LCP04*	0	121	27	5	---	--	---	0	102	03	56	Bus 0
LCP05*	0	121	27	4	MC27	4	MC21	1	102	03	56	Bus 0
LCP14*	1	121	27	5	MC28	8	MC22	0	102	04	56	Bus 1
LCP15*	1	121	27	4	---	--	---	1	102	04	56	Bus 1

LINK CARRIER CABLING
CBI_ _ _*, GBI_ _*, FLAT CABLE CONNECTIONS

LEAD DESIG.	FROM				VIA CABLE			TO				COMM-ENTS
	CAB.	LC-	SLOT	TERM.	CONN.	CONN. TERM.	CONN.	CAB.	LC-	SLOT	TERM.	
CBI0A2*	0/1	101	03	96	---	--	---	0/1	102	01	83	Bus 0
CBI0B2*	0/1	101	16	96	---	--	---	0/1	102	01	82	Bus 0
CBI0C2*	0/1	101	03	96	L10	2	L08	0/1	102	01	81	Bus 0
CBI0D2*	0/1	101	16	96	L10	6	L08	0/1	102	01	89	Bus 0
CBI1A2*	0/1	101	03	76	---	--	---	0/1	102	02	83	Bus 1
CBI1B2*	0/1	101	16	76	---	--	---	0/1	102	02	82	Bus 1
CBI1C2*	0/1	101	03	76	L10	4	L08	0/1	102	02	81	Bus 1
CBI1D2*	0/1	101	16	76	L10	8	L08	0/1	102	02	89	Bus 1
GBI06*	0	121	27	55	MC10	9	L01	0	102	01	80	Bus 0
GBI07*	0	121	27	54	MC16	9	L01	1	102	01	80	Bus 0
GBI16*	1	121	27	55	MC10	9	L03	0	102	02	80	Bus 1
GBI17*	1	121	27	54	MC16	9	L03	1	102	02	80	Bus 1

LINE GROUP CONTROL CARRIER IN CABINETS "0" OR "1"
CBI_ _ _*, GBI_ _*, LCP_ _*, FLAT CABLE CONNECTIONS

LEAD DESIG.	FROM				VIA CABLE			TO				COMMENTS
	CAB.	LC-	SLOT	TERM.	CONN.	CONN. TERM.	CONN.	CAB.	LC-	SLOT	TERM.	
CBI0A1*	0/1	101	--	96	---	--	---	0/1	102	02	83	Bus 0
CBI0B1*	--	--	--	--	---	--	---	--	--	--	--	--
CBI0C1*	0/1	101	01	96	2LK09	96	MC08	0/1	102	02	81	Bus 0
CBI0D1*	0/1	101	01	96	3LK09	96	MC02	0/1	102	02	89	Bus 0
CBI0E1*	0/1	101	01	96	4LK09	96	MC01	0/1	102	02	88	Bus 0
CBI1A1*	0/1	101	--	76	---	--	---	0/1	102	02	83	Bus 1
CBI1B1*	--	--	--	--	---	--	---	--	--	--	--	--
CBI1C1*	0/1	101	01	76	2LK09	76	MC08	0/1	102	03	81	Bus 1
CBI1D1*	0/1	101	01	76	3LK09	76	MC02	0/1	102	03	89	Bus 1
CBI1E1*	0/1	101	01	76	4LK09	76	MC01	0/1	102	03	88	Bus 1

NOTE: CABLING LG13 to MC17 routes CBI(0-1) (C-E) 1* leads from Module Control Carrier to line group control carrier.

LEAD DESIG.	FROM				VIA CABLE			TO				COMMENTS
GBI03*	0	121	27	58	MC31	3	LG23	0	102	02	80	Bus 0
GBI02*	0	121	27	59	MC33	3	LG23	1	102	02	80	Bus 0
GBI13*	1	121	27	58	MC31	3	LG26	0	102	03	80	Bus 1
GBI12*	1	121	27	59	MC33	3	LG26	1	102	03	80	Bus 1
LCP03*	0	121	27	7	MC31	2	LG23	0	102	02	56	Bus 0
LCP02*	0	121	27	6	MC33	2	LG23	1	102	02	56	Bus 0
LCP13*	1	121	27	7	MC31	2	LG26	0	102	03	56	Bus 1
LCP12*	1	121	27	6	MC33	2	LG26	1	102	03	56	Bus 1

LINE GROUP CONTROL CARRIER IN CABINETS 2 THROUGH 4
CBI _ _ _*, GBI _ _*, LCP _ _*, FLAT CABLE CONNECTIONS

LEAD DESIG.	FROM				VIA CABLE			TO				COMM-ENTS
	CAB.	LC-	SLOT	TERM.	CONN.	CONN. TERM.	CONN.	CAB.	LC-	SLOT	TERM.	
CBI0A1*	2-4	101	--	96	---	--	---	2-4	102	02	83	Bus 0
CBI0B1*	2-4	101	01	96	1LK09	19	LG12	2-4	102	02	82	Bus 0
CBI0C1*	2-4	101	01	96	2LK09	19	LG03	2-4	102	02	81	Bus 0
CBI0D1*	2-4	101	01	96	3LK09	19	LG04	2-4	102	02	89	Bus 0
CBI0E1*	2-4	101	01	96	4LK09	19	LG01	2-4	102	02	88	Bus 0
CBI1A1*	2-4	101	--	76	---	--	---	2-4	102	03	83	Bus 1
CBI1B1*	2-4	101	01	76	1LK09	14	LG12	2-4	102	03	82	Bus 1
CBI1C1*	2-4	101	01	76	2LK09	14	LG03	2-4	102	03	81	Bus 1
CBI1D1*	2-4	101	01	76	3LK09	14	LG04	2-4	102	03	89	Bus 1
CBI1E1*	2-4	101	01	76	4LK09	14	LG01	2-4	102	03	88	Bus 1
GBI00*	0	121	27	61	MC32	3	LG23	2	102	02	80	Bus 0
GBI01*	0	121	27	60	MC30	3	LG23	3	102	02	80	Bus 0
GBI02*	0	121	27	59	MC33	3	LG23	4	102	02	80	Bus 0
GBI10*	1	121	27	61	MC32	3	LG26	2	102	03	80	Bus 1
GBI11*	1	121	27	60	MC30	3	LG26	3	102	03	80	Bus 1
GBI12*	1	121	27	59	MC33	3	LG26	4	102	03	80	Bus 1
LCP00*	0	121	27	9	MC32	2	LG23	2	102	02	36	Bus 0
LCP01*	0	121	27	8	MC30	2	LG23	3	102	02	36	Bus 0
LCP02*	0	121	27	7	MC33	2	LG23	4	102	02	36	Bus 0
LCP10*	1	121	27	9	MC32	2	LG26	2	102	03	36	Bus 1
LCP11*	1	121	27	8	MC30	2	LG26	3	102	03	36	Bus 1
LCP12*	1	121	27	7	MC33	2	LG26	4	102	03	36	Bus 1

Reason for Issue:
  New Section

                              MANAGER, DENVER PBX PECC