

DIMENSION[®] 2000 AND CUSTOM PBX
ADDING SECOND COMMON CONTROL CABINET
(J58882A-1)
AS A COMMON CONTROL 0 (COMCCO)

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1. GENERAL

- 1.1 This section provides a procedure for adding a second Common Control Cabinet, J58882A-1 as a Common Control 0 (COMCCO) to a non-duplicated common control system for a DIMENSION 2000 and Custom PBX with a minimal service interruption.
- 1.2 This section is not applicable for adding the second Common Control Carrier, J58882AA or J58882 AC unit, to single Common Control Cabinet.

2. RECORD

- 2.1 Form SD97-1313 should be used to record the results of these tests.

PRIVATE

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THE INFORMATION CONTAINED HEREIN SHOULD NOT BE DISCLOSED TO UNAUTHORIZED PERSONS. IT IS MEANT SOLELY FOR USE BY AUTHORIZED BELL SYSTEM EMPLOYEES.

3. TEST EQUIPMENT

- 3.1 1-ITE - 5632 Digital Voltmeter
 - or
 - ITE - 5250
 - or
 - ITE - 5356
- 1-ITE - 5654 Logic Probe
- 1-MAAP (J58882DC)
- 1-ITE - 5783 X-ray Test Program

4. TOOLS

- 4.1 1-1/8" Allen Hex Wrench
- All other tools as specified in reference HB sections.

5. DRAWINGS AND DOCUMENT

- 5.1 SD-1E480 - 01
- SD-1E481 - 01
- ED-1E368 - 01
- ED-1E362 - 70
- ED-1E366 - 70 or ED-1E 398 - 70
- ED-1E367 - 11
- ED-1E368 - 01
- ED-1E374 - 01
- ED-1E375 - 70
- ED-1E376 - 70

5.2 Summary of Materials:

4MHZ Coaxial Cable Running List:

ED-1E367 - 11, G6XX, type of coaxial cables.

Flat Cable Running List:

2-ED-1E367 - 11, G206, Flat Cable
 1-ED-1E367 - 11, G201, Flat Cable
 2-ED-1E367 - 11, G2XX, Flat Cable (ANI Option)

MAAP Daisy Chain Cable Running List

AUX Cabinet Alarm Mult Running List

Cable Running List for: Console(s) (optional)
 DCTS(s) (optional)
 SMDR(s) (optional)*
 Display Unit(s) (optional)

*Beware the possible incompatibility of WYE Cable (G921 , G922, G951 and G952) for two different vintage processor carriers.

ED-1E369 - 70 GRP 3 MAAP connector bracket for duplicated control system (GRP 1 and GRP 3 MAAP CONNECTORS are compatible. If the Common Control Cabinet 1 (COMCCO) is equipped with GRP 4 MAAP CONNECTOR, it should be replaced with GRP 3 MAAP CONNECTOR).

6. PREREQUISITE

6.1 Prerequisites for this installation and test are:

<u>DCS</u>	<u>OR</u>	<u>CPCN</u>	<u>BRIEF DESCRIPTION OF CHANGE</u>
1E480-19		557DR	LC142 V7, LC143 V8 (micro code issue 5)
1E480-19		559DR	Designation Strip
1E480-21		565DR	LC28C (optional as required)
-		566DR	Alarm Panel (issue 22 SD-1E480-01 Alarm Panel)
-		570DR	LC142 V7, LC143 V9 (micro code issue 5)
1E480-23		571DR	LC146 V5 and adding one wire in Common Control Carrier. (This change can be applied during the addition).

6.2 The DIMENSION system should be functioning properly before the addition of the second processor.

6.3 Insure that all the materials and cables that are required for the addition are available such as (in general terms):

6.3.1 Coaxial cables from the new processor to each module control cabinet.

6.3.2 WYE cables for console(s), DCTS(s), SMDR and/or Display Unit(s).

6.3.3 Three flat data cables between two Common Control Carriers and two flat data cables for ANI.

6.3.4 Two LC146 V5 (or later). One LC146 is required in each common control.

6.3.5 New Duplicated Alarm Panel (J58882DB Unit - Issue 22 or higher of SD-1E480 - 01).

6.3.6 Ground lead between new Alarm Panel and Ground Bar.

- 6.3.7 At least two additional copies of generic tapes (identical translation and patches to the tapes in Common Control 1) for the new processor. (A total of 4 program tapes should be at job site).
- 6.3.8 MAAP Daisy Chain cables and AUX Cabinet Alarm multiple cable(s).

7. PROCEDURE AND TEST

- 7.1 Perform a visual inspection on the new Common Control Cabinet (COMCCO). Insure that the number of memory circuit packs, dual data channel circuit packs, RMATS data channel (LC171), ANI, and etc. are the same as circuit pack option as of Common Control 1 Cabinet. Verify that LC172 of Common Control 0 is set properly (see Information Note 304 of SD-1E480 - 01).

Note: Do not remove or change any circuit pack from Common Control 1 at this time!

- 7.2 Perform a visual inspection on the new Alarm Panel.
- 7.3 Install floor rail and ductwork per HB 281, Section 100, for the new processor.
- 7.4 Install ground cable per HB 281, Section 101, for the new processor.
- 7.5 Install AC power for the new processor as required.
- 7.6 Lay down and dress the following types of cable in the cable trough:
- 7.6.1 Coaxial cables from the new processor to each Module Control Cabinet and each Line Group Control Cabinet. Connect BHS(x) and/or GHS(x) connector(s) to the Common Control 0; but do not connect the other ends of these coaxial cables to the Module Control Cabinet(s) and to the Line Group Control Cabinet(s) at this time. (See Table A for reference).
- 7.6.2 All WYE cables (do not connect) for console(s), DCTS, SMDR and/or Display Unit(s).
- 7.6.3 Data cables (flat) between two processors (do not connect these cables at this time):

CABLES (ED-1E367-11)	FROM			TO		
	Comm. Cont.	Carrier	Conn	Comm. Cont.	Carrier	Conn
G206	1	Basic	BC06	0	Basic	BC06
G206	1	Basic	BC07	0	Basic	BC07
G201	1	Basic	BC11	0	Basic	BC09

7.6.4 For those systems equip with ANI, lay down the 2 Group 200 data (flat) cables.

7.7 Preparation of the new Alarm Panel (J58882DB unit):

7.7.1 Verify that the Duplicated Common Control Alarm Panel is permanently mounted in the new Common Control Cabinet 0.

7.7.2 Verify that the ground lead (COMCODE NO. 842156580) is connected between TB1 of the new Alarm Panel and Terminal 7 of Ground Block (which is located adjacent to ACT 3 Terminal Strip) on the J58882PA-1 Unit of Common Control 0.

Use a 1/8" Allen Hex Wrench to terminate one end of the ground lead.

7.8 Temporarily connect the following three new cables between the new Alarm Panel (DB-1 Unit) and Common Control 0 (the new processor). The purpose of these temporarily connections is to test Common Control 1 portion of the new Alarm Panel:

CABLES (ED-1E367-11)	FROM			TO	
	Comm. Cont.	Pos	Conn	Alarm Pnl	Conn
G503	0	0	BX03	NEW	AP3
G506	0	0	BX04	NEW	AP4
G506	0	0	MAAP3	NEW	AP6

7.9 Testing the New Processor and the New Alarm Panel:

7.9.1 On the new Alarm Panel, turn the SYSTEM CONTROL SELECT key to CCl position and operate the HALT switch (CCl side).

7.9.2 On the new processor, turn off both AC INPUT circuit breakers on the power supplies; and turn on all +5V circuit breakers on the left side of both Basic and Growth Common Control Carriers.

7.9.3 Connect and apply power to the new processor.

- 7.9.4 Turn on both AC INPUT circuit breakers on the new processor.
- 7.9.5 Check power supply voltages per Section 300 of HB 281.
- 7.9.6 Test CONTROL OVER TEMP LED on CC1 portion of the new Alarm Panel as follows:
 - 7.9.6.1 Momentarily, place a WIAP Test Cord between Terminal 1 and 4 of TB1 (or short across the two terminals of Heat Sensor S1) on the Thermal Assembly, ED-1E364-70, of the new processor.
 - 7.9.6.2 Verify that CONTROL OVER TEMP and MN LEDS on the Common Control 1 of the new Alarm Panel are turned on.
 - 7.9.6.3 Operate RESET key to retard the OVER TEMP and MN LEDS.
- 7.9.7 Connect the MAAP to THIS CONTROL (or Common Control) MAAP connector of the new processor.
- 7.9.8 Place a X-ray tape into mini-record of the new processor.
- 7.9.9 Perform Microdiagnostics 0 to 9 per Sections 310 thru 319 of HB 281.
- 7.9.10 Run X-ray TESTS 0, 1, 12 and 27.
- 7.9.11 On the new processor, turn off the AC INPUT circuit breaker of Basic Common Control rectifier.
- 7.9.12 Disconnect the GRP503 and 506 from BX03, BX04 and MAAP3 connectors of the new processor and reconnect the GRP 502 and 505 from AP1, AP2 and AP5 to BX03, BX04 and MAAP3 connectors of the new processor, respectively. (Testing Common Control 0 portion of the new Alarm Panel).
- 7.9.13 On the new Alarm Panel, turn the SYSTEM CONTROL SELECT key to CCO position and operate the HALT switch (CCO side).
- 7.9.14 On the new processor, turn on the AC INPUT circuit breaker of Basic Common Control rectifier.
- 7.9.15 Test CONTROL OVER TEMP LED on CCO portion of the new Alarm Panel by repeating Steps 7.9.6.1 thru 7.9.6.3.
- 7.9.151 Set LC172 in the new Processor to option A per Information Note 304 of SD-1E480-01.
- 7.9.16 Add (if not) LC146 (V5 or higher) to Common Control 0.
- 7.9.17 Perform Microdiagnostic 0 to 9 again.

- 7.9.18 Run X-ray TESTS 0, 1, 12 and 27.
- 7.9.19 Load generic program into the new processor (the program tape should be identical to Common Control 1).
- 7.9.20 On the new Alarm Panel, Common Control 0 would show I/O, SCAN, and NET alarms.
- 7.9.21 Permanently connect both ends of the three flat data cables to both processors.

CABLES (ED-1E367-11)	FROM			TO		
	Comm. Cont.	Carrier	Conn	Comm. Cont.	Carrier	Conn
G206	1	Basic	BC06	0	Basic	BC06
G206	1	Basic	BC07	0	Basic	BC07
G201	1	Basic	BC11	0	Basic	BC09

- 7.9.22 Verify that Common Control 1 is functioning properly and no alarms are shown.
- 7.9.23 Verify that Common Control 0 still has heart beats and no additional alarms other than those specified in Step 7.9.20.
- 7.10 Program Common Control 0 Tape to Duplicate Common Control System
- 7.10.1 On the new processor, call in PROC 275 (Depress PROC NO., 2, 7, 5, and ENTER keys. Wait for the WAIT lamp to extinguish).
- 7.10.2 Depress DISPLAY and EXECUTE keys.
- 7.10.3 Verify that FIELD 10 is shown as a "0".
- 7.10.4 Change FIELD 10 to a "1" by depressing CHANGE FIELD, 1, 0, ENTER, 1, and ENTER keys.
- 7.10.5 Verify that FIELD 10 is indicated with a digit "1" and no changes on all other fields.
- 7.10.6 Depress ADD and EXECUTE keys to add the new information to RAM memory (NOTE: If the tape start reload which indicates defective LC146, LC132, and/or Group 206 flat cables).
- 7.10.7 OTHER (DUPLICATE PROCESSOR FAILURE) alarm will appear on CCO of the new Alarm Panel. Call in PROC 515 and verify that is the case.

- 7.10.8 Depress RUN TAPE and EXECUTE keys to put new translation onto tape. (NOTE: There is a possibility that the new processor might suicide - major alarm and no "heart" beats; then it is necessary to momentarily operate the RESET switch, or HALT/GO and RESET switches to regain "heart" beats).
- 7.10.9 Wait for the WAIT lamp to extinguish and did not generate a TAPE alarm on the new Alarm Panel (this indicates the new translation has been written onto the tape successfully).
- 7.10.10 Remove the tape cartridge from the CCO mini-recorder and put temporary label on the tape cartridge for identification. Insert second duplicate copy of tape cartridge into the CCO mini-recorder. Then repeat Steps 7.10.8 and 7.10.9 to add the new translation onto the second tape (put temporary label on this tape cartridge also. CAUTION: Only make two copies of the program tapes with duplicated processor definition at this time).
- 7.11 Apply CPCN 571DR to Common Control 0 As Required (if not required, go to Step 7.12)
- 7.11.1 Remove AC power from Common Control Cabinet 0 (Processor 0).
- 7.11.2 Apply wiring to Basic Common Control Carrier 0 per CPCN 571DR.
- 7.11.3 Remove the LC146 V4 circuit pack from Basic Common Control Carrier 0 and replace it with a LC146 V5 circuit pack.
- 7.11.4 Re-apply AC power to Common Control Cabinet 0.
- 7.11.5 Re-run X-ray TESTS 0 and 1.
- 7.11.6 Reload generic program (tape with new translation) with power down procedure.
- 7.11.7 Wait for heart beats, I/O, SCAN, NET and DUPLICATE PROCESSOR FAILURE alarms.
- 7.11.8 End of procedure for the application of CPCN 571DR.
- 7.12 On the new Alarm Panel, turn SYSTEM CONTROL SELECT switch to CCl position which will turn on ACT CCl LED. Verify that both HALT/GO switches are in GO position.
- 7.13 Relocate MC42 and LG27 coaxial cables to MC41 and LG24 connectors. See Table A for cabling information. (NOTE: Relocating these cables will affect transient calls for that cabinet).

Permanently connect the coaxial cables associated with the new processor to Module Control Cabinets and Line Group Cabinet(s), (see TABLE A for reference).

For system equips with ANI, relocate both MC25 cables of MODCC0 and MODCC1 to MC24 connectors, and then add the two new GRP 200 cables to MC25 connectors.

See table below:

<u>CCO</u>		<u>MODCC0</u>	<u>MODCC1</u>
BC10	TO	MC25	-
BC8	TO	-	MC25

- 7.14 Permanently connect the following types of WYE cables among two processors and their associated equipments:
- A. All console(s)
 - B. SMDR
 - C. DCTS
 - D. Display Unit(s)
- 7.15 Test the above equipment(s) for proper operation. Clear any alarm that appears on the Unduplicated Alarm Panel (J58882DA Unit).
- 7.16 At this time, the system should be functioning proper with Processor 1 and with the Unduplicated Alarm Panel.
- 7.17 On the duplicated Alarm Panel, insure that the SYSTEM CONTROL SELECT switch is in CC1 position, HALT/GO switch of CC1 is in GO position, and heart beats at CC0.
- 7.18 Reset I/O, SCAN and NET alarms on Common Control 0 with PROC 520 as follows:
- 7.18.1 Connect MAAP to THIS CONTROL (off-line common control - CC0) MAAP connector.
 - 7.18.2 Call in PROC 520 by depressing PROC NO., 5, 2, 0, and ENTER keys. Wait for the WAIT LED to extinguish.
 - 7.18.3 Depress CHANGE FIELD, 1, ENTER, 9 and ENTER keys.

- 7.18.4 To zero all maintenance data, depress CLEAR DATA and EXECUTE keys. Insure that all failure history is cleared.
- 7.18.5 Only alarm that will recur again at CCO is the DUPLICATE PROC FAILURE alarm.
- 7.19 Switch System To Duplicated Alarm Panel
- 7.19.1 Halt Common Control 0 by operating the HALT key on the Duplicated Alarm Panel.
- 7.19.2 This step will stop call processing telephone calls for maximum of 90 seconds but should not disturb the established calls. (NOTE: Read Step 7.19.3 first before initiating this step).

NOTE: The AC power cannot be removed from Common Control 1 more than 90 seconds within once a day (24 hours) without causing a tape reload when re-applying AC power to the Common Control later. The Nickel-Cadium batteries when they are fully charged should provide sufficient DC power to hold the memory voltages within operating limits for at least 90 seconds.

Turn off the AC power on Common Control 1 (Caution - Do Not shut off the AC INPUT circuit breaker at the rectifier). Use the associated circuit breaker on the AC Module Box.

- 7.19.3 Disconnect Plug BX03, BX04 and MAAP 3 from CCl and connect AP3, AP4, and AP6 cables (GRP 503 and 506) from the Duplicated Alarm Panel to BX03, BX04, and MAAP 3 connectors of CCl.
- Disconnect Plug AP7 from the Unduplicated Alarm Panel and reconnect to AP7 of the Duplicated Alarm Panel. (NOTE: This will cause the 609A emergency transfer relays to drop).
- 7.19.4 Re-apply AC power to Common Control 1 by re-operating the associated circuit breaker at the AC Module Box.
- 7.19.5 On Common Control 1, heart beats will resume from 6 to 40 seconds. The system should restore to full service and verify that the system is functioning properly. Place SYSTEM CONTROL SELECT switch to NORMAL position. (NOTE: If Common Control 1 fails to work properly, try to switch Common Control 0 on-line by means of Steps 7.20.1 thru 7.20.6; if that fails, then reconnect the original four cables (GRP 502, 505 and AP7) back to the Unduplicated Alarm Panel).
- 7.20 Switch Common Control 0 On-Line

- 7.20.1 On Common Control 0 portion of the new Alarm Panel, operate the HALT/GO switch to GO position. Look for proper heart beats.
- 7.20.2 When heart beats are shown on CCO, switch the SYSTEM CONTROL SELECT key to NORMAL position and operate the HALT/GO switch of Common Control 1 to HALT position (this should force to switch Common Control 0 on-line).
- 7.20.3 Verify for continuous heart beats and proper system operation (such as station, trunk, link, console and DCTS calls, SMDR, Display Unit(s) and etc.).
- 7.20.4 Verify that only DUPLICATED PROCESSOR Alarm is shown at CCO portion of the Alarm Panel. Lock CCO on-line by turning SYSTEM CONTROL SELECT switch to CCO position.
- 7.20.5 Test both MAAp connectors on Common Control 0 by calling Procedures 515 and 520.
- 7.20.6 If the system is working 100%, then go to the next step. If neither common control works, then transfer back to the Unduplicated Alarm Panel by selecting SYSTEM CONTROL SELECT switch to CC1, and then, repeating Steps 7.19.2 thru 7.19.4. But I hope you do not have to do that!
- 7.21 Convert Common Control 1 to a Part of the Duplicated System
- 7.21.1 If it is required to replace GRP 4, ED1E369-70, MAAp CONNECTOR BRACKET with GRP 3, go to next step; otherwise, go to Step 7.21.5.
- 7.21.2 Remove power from Common Control 1 by turning off all AC INPUT circuit breaker(s) at the rectifier(s) (for the battery reserve system, also turn off the associated -48v circuit breaker(s).
- 7.21.3 Replace the GRP4, ED1E369-70, Bracket with GRP 3.
- 7.21.4 Re-apply power to Common Control 1.
- 7.21.5 If it is required to apply CPCN 571DR, (check for wire from Slot 19 term 53 to Slot 28 term 60 on Basic Common Control Carrier - SD1E 480-01 Issue 23 option YD) go to next step; otherwise, go to Step 7.21.10.

- 7.21.6 Remove power from Common Control 1 by turning off all AC INPUT circuit breaker(s) at the rectifier(s) (for the battery reserve system, also turn off the associated -48V circuit breaker(s)).
- 7.21.7 Select HALT/GO switch to HALT for CCl.
- 7.21.8 Apply wiring to the Basic Common Control Carrier of processor 1 per CPCN 571DR.
- 7.21.9 Re-apply power to CCl.
- 7.21.10 HALT Common Control 1, if not.
- 7.21.11 Add LC146 V5 to Basic Common Control Carrier of processor 1. Replace LC34B with LC171 and LC172 as required (insure that circuit pack option on LC172 for Common Control 1 is set properly per information Note 304 of SD-1E 480-01).
- 7.21.12 Select HALT/GO switch to GO; run microdiagnostics 0 thru 9 and run X-ray tests 0 and 1.
- 7.21.13 Load the updated generic program tape (tape with new translation) with power down procedure.
- 7.21.14 On the Common Control 1, wait for heart beats and verify that no alarms are indicated on the new Alarm Panel.
- 7.22 Test Both MAAP Connectors on Common Control 1 with Common Control 0 On-line.
- 7.22.1 Connect MAAP to THIS CONTROL MAAP connector on Common Control 1.
- 7.22.2 Call in PROC 526 and perform MAAP test.
- 7.22.3 Disconnect MAAP from THIS CONTROL and reconnect it to ON-LINE CONTROL MAAP connector on Common Control 1.
- 7.22.4 Call in PROC 521 and perform TEST 2 (testing LC146 circuit packs between two common controls).
- 7.22.5 Then call in PROC 520 and use INIT NO. 9 to clear DUPLICATED PROCESSOR alarm.
- 7.23 Test Both MAAP Connectors on Common Control 0 with Common Control 0 On-Line.
- 7.23.1 Connect MAAP to ON-LINE CONTROL MAAP Connector on COMMON CONTROL 0.

- 7.23.2 Call in PROC 526 and perform MAAP test.
- 7.23.3 Connect MAAP to THIS CONTROL MAAP connector on Common Control 0.
- 7.23.4 Call in PROC 526 and perform MAAP test again.
- 7.24 Test All Four MAAP Connectors with Common Control 1 On-line
- 7.24.1 Switch Common Control 1 on-line as follows:
 - 7.24.1.1 For those systems equipped with Issue L8 or later generic tape, place SYSTEM CONTROL SELECT switch to NORMAL position first and then use PROC 521 test to do a soft switch.
 - 7.24.1.2 For those systems equipped with L7.9 or earlier generic tape, place SYSTEM CONTROL SELECT switch to NORMAL position (if not) and then operate the HALT switch of Common Control 0 (NOTE: After the common control is switched, the system will stop call processing for approximately 6 to 40 seconds and console(s) will be placed in night service. It is necessary to inform console operator(s) to place console(s) out-of-night service as required. However, all the established calls should not be disturbed).
 - 7.24.1.3 Retest all four MAAP connectors again by call in PROC 520.
- 7.25 Test Control Fuse Leds
- 7.25.1 Turn off the MAAP +5CBM circuit breaker of Common Control 0.
- 7.25.2 Verify that the CONTROL FUSE and MN LEDS of Common Control 0 are on.
- 7.25.3 Re-operate the MAAP +5CBM circuit breaker of Common Control 0 to extinguish the CONTROL FUSE and MN LEDS.
- 7.25.4 Repeat Steps 7.25.1 thru 7.25.3 for Common Control 1 Fuse Alarm.
- 7.26 Test Emergency Transfer Switch
- 7.26.1 Turn EMERGENCY TRANSFER switch to INHIB position and verify that INHIB LED is on.

- 7.26.2 Activate emergency transfer.
- 7.26.3 Verify that ACT LED is on and all emergency relays are released.
(NOTE: All emergency CO trunks will be connected to the emergency stations and some CO calls will be dropped).
- 7.26.4 Set EMERGENCY TRANSFER switch to NORMAL position which should extinguish ACT LED and re-operate all emergency transfer relays.
- 7.27 Test RSVR PWR Alarm LEDS (for System Equipped with Reserve Power).
- 7.27.1 Generate a battery plant alarm (such as high voltage, low voltage, or fuse alarm) from the reserve battery plant.
- 7.27.2 Verify that both RSVR PWR alarm LEDS are turned on.
- 7.27.3 Remove the battery plant alarm. Reset RSVR PWR alarm by operating the reset switches.
- 7.28 Update Program Tapes
- 7.28.1 Update the other two program tapes by performing a run tape operation at the on-line Common Control.
- 7.29 Power Down Test
- 7.29.1 Perform a power down test per Section 8780 of HB 281 as required.
- 7.30 Wrap Up
- 7.30.1 Verify that no alarms are indicated on either common control. Place several calls (station, trunk, and console) through the system and insure the system (both processors) is functioning properly.
- 7.30.2 Wrap up.
- 7.30.3 End of test procedure.

TABLE A - 4 MHZ DATA CHANNEL

LC-131 SLOT	DESTINATION		PROCESSOR Ø		CABLES		PROCESSOR 1		CABLES	
	MOD	CAB	FROM		TO		FROM		TO	
			CARR	DESIG	CARR	DESIG	CARR	DESIG	CARR	DESIG
40	00	0	AA	BHS1	BA	MC42	AA	BHS1	BA	MC41
40	00	1	AA	BHS1	BA	MC42	AA	BHS1	BA	MC41
40	00	2	AA	BHS1	BB	LG27	AA	BHS1	BB	LG24
40	00	3	AA	BHS1	BB	LG27	AA	BHS1	BB	LG24
40	00	4	AA	BHS2	BB	LG27	AA	BHS2	BB	LG24
40	01	0	AA	BHS2	BA	MC42	AA	BHS2	BA	MC41
40	01	1	AA	BHS2	BA	MC42	AA	BHS2	BA	MC41
40	01	2	AA	BHS2	BB	LG27	AA	BHS2	BB	LG24
40	01	3	AA	BHS3	BB	LG27	AA	BHS3	BB	LG24
40	01	4	AA	BHS3	BB	LG27	AA	BHS3	BB	LG24
40	02	0	AA	BHS3	BA	MC42	AA	BHS3	BA	MC41
40	02	1	AA	BHS3	BA	MC42	AA	BHS3	BA	MC41
40	02	2	AA	BHS4	BB	LG27	AA	BHS4	BB	LG24
40	02	3	AA	BHS4	BB	LG27	AA	BHS4	BB	LG24
40	02	4	AA	BHS4	BB	LG27	AA	BHS4	BB	LG24
40	03	0	AA	BHS4	BA	MC42	AA	BHS4	BA	MC41
35	03	1	AB	GHS1	BA	MC42	AB	GHS1	BA	MC41
35	03	2	AB	GHS1	BB	LG27	AB	GHS1	BB	LG24
35	03	3	AB	GHS1	BB	LG27	AB	GHS1	BB	LG24
35	03	4	AB	GHS1	BB	LG27	AB	GHS1	BB	LG24
35	04	0	AB	GHS2	BA	MC42	AB	GHS2	BA	MC41
35	04	1	AB	GHS2	BA	MC42	AB	GHS2	BA	MC41
35	04	2	AB	GHS2	BB	LG27	AB	GHS2	BB	LG24
35	04	3	AB	GHS2	BB	LG27	AB	GHS2	BB	LG24

HB 281 :

15.

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TABLE A - 4 MHZ DATA CHANNEL

LC-131 SLOT	DESTINATION		PROCESSOR 0				PROCESSOR 1			
	MOD	CAB	FROM		TO		FROM		TO	
			CARR	DESIG	CARR	DESIG	CARR	DESIG	CARR	DESIG
35	04	4	AB	GHS2	BB	LG27	AB	GHS2	BB	LG24
35	05	0	AB	GHS3	BA	MC42	AB	GHS3	BA	MC41
35	05	1	AB	GHS3	BA	MC42	AB	GHS3	BA	MC41
35	05	2	AB	GHS3	BB	LG27	AB	GHS3	BB	LG24
35	05	3	AB	GHS3	BB	LG27	AB	GHS3	BB	LG24
35	05	4	AB	GHS4	BB	LG27	AB	GHS4	BB	LG24
35	06	0	AB	GHS4	BA	MC42	AB	GHS4	BA	MC41
35	06	1	AB	GHS4	BA	MC42	AB	GHS4	BA	MC41
36	06	2	AB	GHS4	BB	LG27	AB	GHS4	BB	LG24
36	06	3	AB	GHS5	BB	LG27	AB	GHS5	BB	LG24
36	06	4	AB	GHS5	BB	LG27	AB	GHS5	BB	LG24
36	07	0	AB	GHS5	BA	MC42	AB	GHS5	BA	MC41
36	07	1	AB	GHS5	BA	MC42	AB	GHS5	BA	MC41
36	07	2	AB	GHS6	BB	LG27	AB	GHS6	BB	LG24
36	07	3	AB	GHS6	BB	LG27	AB	GHS6	BB	LG24
36	07	4	AB	GHS6	BB	LG27	AB	GHS6	BB	LG24
36	08	0	AB	GHS6	BA	MC42	AB	GHS6	BA	MC41
36	08	1	AB	GHS7	BA	MC42	AB	GHS7	BA	MC41
36	08	2	AB	GHS7	BB	LG27	AB	GHS7	BB	LG24
36	08	3	AB	GHS7	BB	LG27	AB	GHS7	BB	LG24
36	08	4	AB	GHS7	BB	LG27	AB	GHS7	BB	LG24
36	09	0	AB	GHS8	BA	MC42	AB	GHS8	BA	MC41
36	09	1	AB	GHS8	BA	MC42	AB	GHS8	BA	MC41
36	09	2	AB	GHS8	BB	LG27	AB	GHS8	BB	LG24

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TABLE A - 4 MHZ DATA CHANNEL

LC-131 SLOT	DESTINATION		PROCESSOR Ø		CABLES		PROCESSOR 1		CABLES	
	MOD	CAB	FROM		TO		FROM		TO	
			CARR	DESIG	CARR	DESIG	CARR	DESIG	CARR	DESIG
38	09	3	AB	GHS8	BB	LG27	AB	GHS8	BB	LG24
38	09	4	AB	GHS9	BB	LG27	AB	GHS9	BB	LG24
38	10	0	AB	GHS9	BA	MC42	AB	GHS9	BA	MC41
38	10	1	AB	GHS9	BA	MC42	AB	GHS9	BA	MC41
38	10	2	AB	GHS9	BB	LG27	AB	GHS9	BB	LG24
38	10	3	AB	GHS10	BB	LG27	AB	GHS10	BB	LG24
38	10	4	AB	GHS10	BB	LG27	AB	GHS10	BB	LG24
38	11	0	AB	GHS10	BA	MC42	AB	GHS10	BA	MC41
38	11	1	AB	GHS10	BA	MC42	AB	GHS10	BA	MC41
38	11	2	AB	GHS11	BB	LG27	AB	GHS11	BB	LG24
38	11	3	AB	GHS11	BB	LG27	AB	GHS11	BB	LG24
38	11	4	AB	GHS11	BB	LG27	AB	GHS11	BB	LG24
38	12	0	AB	GHS11	BA	MC42	AB	GHS11	BA	MC41
38	12	1	AB	GHS12	BA	MC42	AB	GHS12	BA	MC41
38	12	2	AB	GHS12	BB	LG27	AB	GHS12	BB	LG24
38	12	3	AB	GHS12	BB	LG27	AB	GHS12	BB	LG24
39	12	4	AB	GHS12	BB	LG27	AB	GHS12	BB	LG24
39	13	0	AB	GHS13	BA	MC42	AB	GHS13	BA	MC41
39	13	1	AB	GHS13	BA	MC42	AB	GHS13	BA	MC41
39	13	2	AB	GHS13	BB	LG27	AB	GHS13	BB	LG24
39	13	3	AB	GHS13	BB	LG27	AB	GHS13	BB	LG24
39	13	4	AB	GHS14	BB	LG27	AB	GHS14	BB	LG24
39	14	0	AB	GHS14	BA	MC42	AB	GHS14	BA	MC41
39	14	1	AB	GHS14	BA	MC42	AB	GHS14	BA	MC41

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TABLE A - 4 MHZ DATA CHANNEL

LC-131 SLOT	DESTINATION		PROCESSOR 0		CABLES		PROCESSOR 1		CABLES	
	MOD	CAB	FROM		TO		FROM		TO	
			CARR	DESIG	CARR	DESIG	CARR	DESIG	CARR	DESIG
39	14	2	AB	GHS14	BB	LG27	AB	GHS14	BB	LG24
39	14	3	AB	GHS15	BB	LG27	AB	GHS15	BB	LG24
39	14	4	AB	GHS15	BB	LG27	AB	GHS15	BB	LG24
39	15	0	AB	GHS15	BA	MC42	AB	GHS15	BA	MC41
39	15	1	AB	GHS15	BA	MC42	AB	GHS15	BA	MC41
39	15	2	AB	GHS16	BB	LG27	AB	GHS16	BB	LG24
39	15	3	AB	GHS16	BB	LG27	AB	GHS16	BB	LG24
39	15	4	AB	GHS16	BB	LG27	AB	GHS16	BB	LG24

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Reason for issue:

MANAGER, DENVER PBX PECC

New Section