

AHG1 TIMING ALARM DATA SHEET SYNCHRONIZATION DISTRIBUTION EXPANDER

INTRODUCTION

The AHG1 TA (Timing Alarm) plug-in unit is used in the SDE (Synchronization Distribution Expander) to monitor and process the plug-in unit (circuit pack) alarm information. The TA output alarm is routed to the FA (Fuse and Alarm) panel where the alarm is reported.

FEATURES

The features of the AHG1 TA include:

- One-Bit Erasable Alarm Memory
- Alarm Status LED (light-emitting diode) indicators for each plug-in unit
- Alarm Inhibit Options for TD (Timing Distributor) plug-ins 2 through 4.

DESCRIPTION

The AHG1 TA is equipped with three alarm options that prevent alarm information that is received from unused TD slots from being reported. The options are explained in the Options and Installation portion of this document. In addition, the TA is also equipped with a resettable one-bit memory register that provides a visual indication of all failures of plug-in units in the SDE shelf.

The plug-in unit alarms are categorized into major and minor alarms. The failure of a TD registers as a major alarm. The failure of a single TI (timing interface) registers as a minor alarm. The failure of two TIs will register as a major alarm. The alarm information is displayed visually on the faceplate of the TA. The TA also supplies the alarm information to the Fuse and Alarm Panel. The Fuse and Alarm Panel uses relay closures to

report office alarms (audible and visual) and provides lamp indicators for major, minor, and fuse alarms. There are colored alarm LEDs for plug-in units (amber for the TIs and red for the TDs). When a plug-in unit fails, the appropriate LED is lighted.

Warning: This plug-in unit contains devices that are subject to damage or decreased reliability from static discharges. When handling this unit, proper anti-static measures should be taken, such as wearing grounding bracelets and handling the unit by the faceplate only.

OPTIONS AND INSTALLATION

The option plugs located on the faceplate of the TA are used to disable alarm information from up to three unused TDs. The plug positions are grouped 2, 3, and 4 on the faceplate (Figure 1). These labels correspond to the TDs in positions 2 through 4. The TD position 1 is not included since at least one TD is required for the operation of the SDE and position 1 is the default position. These options are selected by inserting the appropriate plug into the vertical series of sockets located on the faceplate. These sockets are labeled on the left-hand side "IN, IN, IN" and on the right-hand side "2, 3, 4" (Figure 1). Below each IN position of the socket is an unlabeled OUT position. To prevent the alarm information received from a particular TD from being transmitted, the option plug is placed in the OUT socket corresponding to that TD. The IN position should be used as a storage position for the unused option plugs. The alarm information is sent from the TA plug-in unit to the Fuse and Alarm Panel.

PLUG-IN UNIT COMPATIBILITY

Timing Alarm plug-in unit, AHG1, is compatible with the following SDE plug-in units:

- AHG2 — Timing Interface (DS-1/CC)
- AHG2B — Timing Interface (DS-1/CC)
- AHG3 — Timing Distributor (CC)
- AHG4 — Timing Distributor (analog)
- AHG5 — Timing Distributor (DS-1)
- AHG15 — Timing Interface (DS-1C/DS-1/CC).

SPECIFICATIONS

The environmental specifications for the AHG1 TA include temperature and humidity. The acceptable limits are as follows:

Temperature:

+40 to +100 degrees -long term
+4.4 to +37.8 degrees C

+35 to +120 degrees F - short term
+1.6 to +49 degrees C

Humidity:

Min. Relative Humidity Range:

20 to 80 percent - short term
20 to 55 percent - operating

ORDERING INFORMATION

The AHG1 TA may be ordered as a single plug-in unit using the code given below.

PLUG-IN UNIT CLEI CODE

AHG1 D4 PQ 100AXX

REFERENCES

The following publications provide more information on the SDE plug-in-units:

- SD-7C389-01
- SD-7C389-02
- AT&T Practice 314-913-220 — Description and Operation.

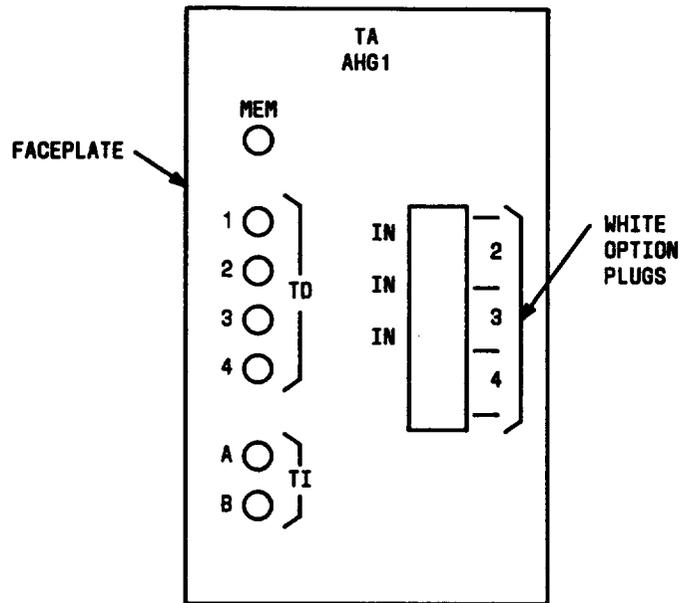


Figure 1. Location of Options on the Faceplate of AHG1 Timing Interface Circuit Pack