

MC97734A1 XTC CONTROLLER UNIT — 5SXTA00BXX

DATA SHEET

SLC® SERIES 5 CARRIER SYSTEM

The MC97734A1 controller unit (XCU) is used in the extended test controller (XTC) J1D182XA-1L1. The XCU controls the various XTC plug-in units. The XTC performs channel unit tests and provides access between the SLC Series 5 carrier system and SARTS (switched access remote test system) MLT-1 (Mechanized Loop Testing System-1), and MLT-2. The XCU also handles performance monitoring and fault diagnosis for the XTC.

This practice has been reissued to change the code, to change test point access, and to add the CLEI code.

The XCU contains a microcomputer with random access memory, a system program memory, associated timing and reset circuits, an alphanumeric error code display, a fail indicator, and an alarm test pin jack. The XCU provides a serial interface to the other plug-ins in the XTC to control the plug-ins and indicator lights.

Figure 1 is a functional block diagram of the unit. Figure 2 shows the faceplate. Indicators on the faceplate provide the following information.

FAIL (RED LED): When lighted, this LED indicates that the failure has been sectionalized to this XCU.

L1: Presently not used.

L2: Used during functional tests of XTC tester units.

ALM TST (PIN JACK): Presently not used.

TST1: Presently not used.

TST2: Used in conjunction with indicator L2 for tester unit testing.

ERROR CODE (SEVEN SEGMENT DISPLAY): This alphanumeric display indicates the nature of the failure within the XTC or between the XTC and a carrier system. Refer to AT&T Practice 363-205-300, XTC Description, Installation, and Maintenance for XTC operating procedures. The meaning of a displayed code is given below:

DIGIT DISPLAYED	MEANING
1	Permanent Seize
2	Permanent Seize Busy
3	Permanent Major Alarm (TMA)
4	Permanent Tone Detect
6	Permanent SLV A
7	Permanent SLV B
8	Permanent SLV C
9	Permanent SLV D

ISSUING ORGANIZATION

Published by
The AT&T Documentation Management Organization.

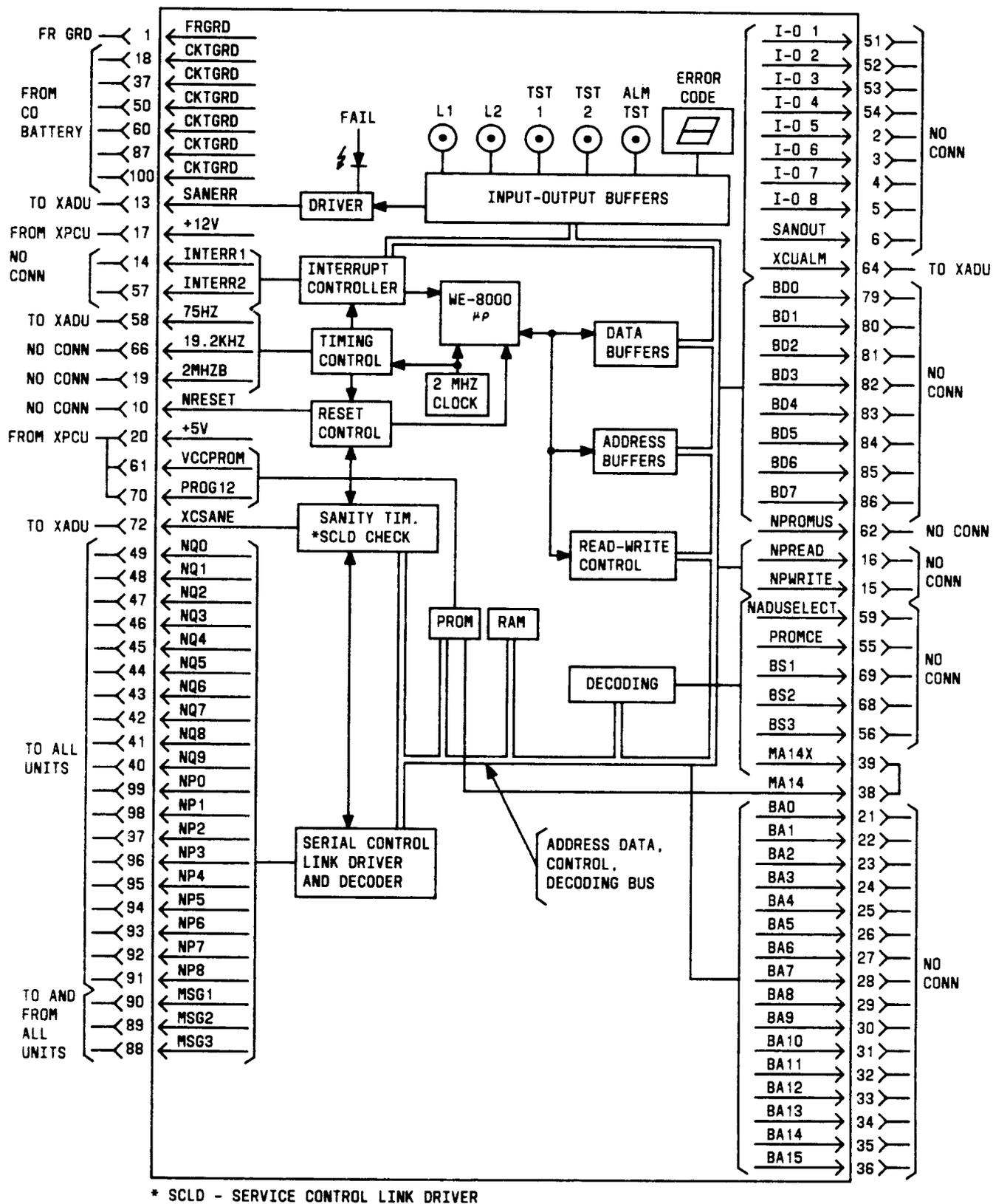


Fig. 1—MC97734A1 XTC Controller Unit Block Diagram

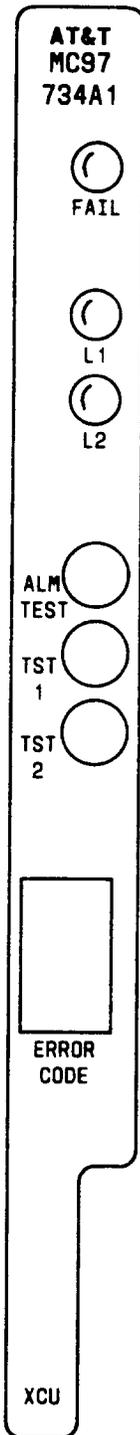


Fig. 2—MC97734A1 Faceplate Diagram