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AUB66 XTC FAN-OUT UNIT

DATA SHEET

SLC® SERIES 5 CARRIER SYSTEM

The AUB66 XTC fan-out unit (XFOU) is one of the peripheral boards for the extended test controller (XTC). The XFOU design is based on a microcomputer, which in turn is controlled by the XTC controller unit (XCU). The two major functions of the XFOU are described in the following paragraphs.

The first major function of the AUB66 XFOU connects the SLC Series 5 carrier systems, equipped for Feature Package C capability, to the XTC tester unit AUB67 (XTUC). A matrix of relays permits interconnections, as indicated by the crosspoint diagram in Fig. 1, between two groups of three each dual banks and five XTC testers. Each tester consists of a set of three units, one AUB65 (XTUD), one AUB67 (XTUC), and one AUB68 (XTUB). Five balanced pairs are needed between the carrier system and the AUB67 (XTUC). One pair is used for the 333-Hz/64-kHz composite clock, and the other four pairs are used for the transmit and receive test busses from the carrier system. Thus, each crosspoint is a 10-wire switch.

In its second major function, the AUB66 (XFOU) acts as a communication link between the XTC controller (XCU) and the SLC Series 5 carrier system through a universal asynchronous receiver-transmitter (UART) interface. Both the transmit path and the receive path are balanced pairs and are connected to the carrier system through the carrier system digital test unit (DTU).

Figure 1 is a functional block diagram of the AUB66 XTC fan-out unit. Figure 2 shows the faceplate.

The AUB66 has two LEDs on its faceplate. The red (FAIL) LED will light whenever a failure is detected within the AUB66 plug-in. The yellow (LINK) LED will be associated with maintenance of the XTC link to the SLC Series 5 carrier system. The AUB66 notifies the XTC alarm display unit, XADU, of an alarm by grounding the common bus, ALMBUS.

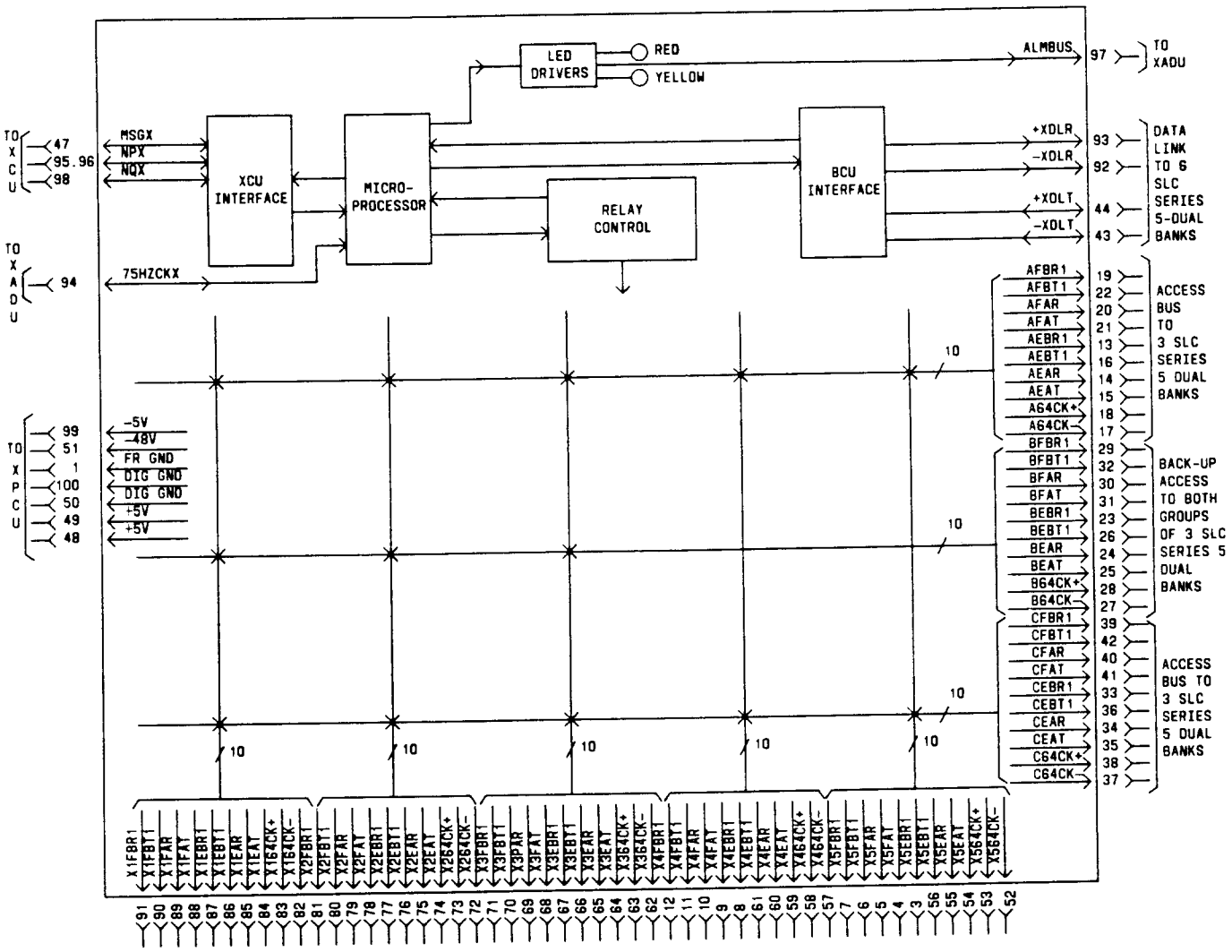

Fig. 1—AUB66 XFOU Block Diagram

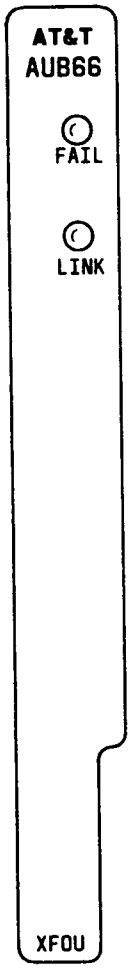

Fig. 2—AUB66 Faceplate Diagram