AT&T 363-005-254
Issue 1, December 1985

AUB69 XTC COMPOSITE CLOCK UNIT

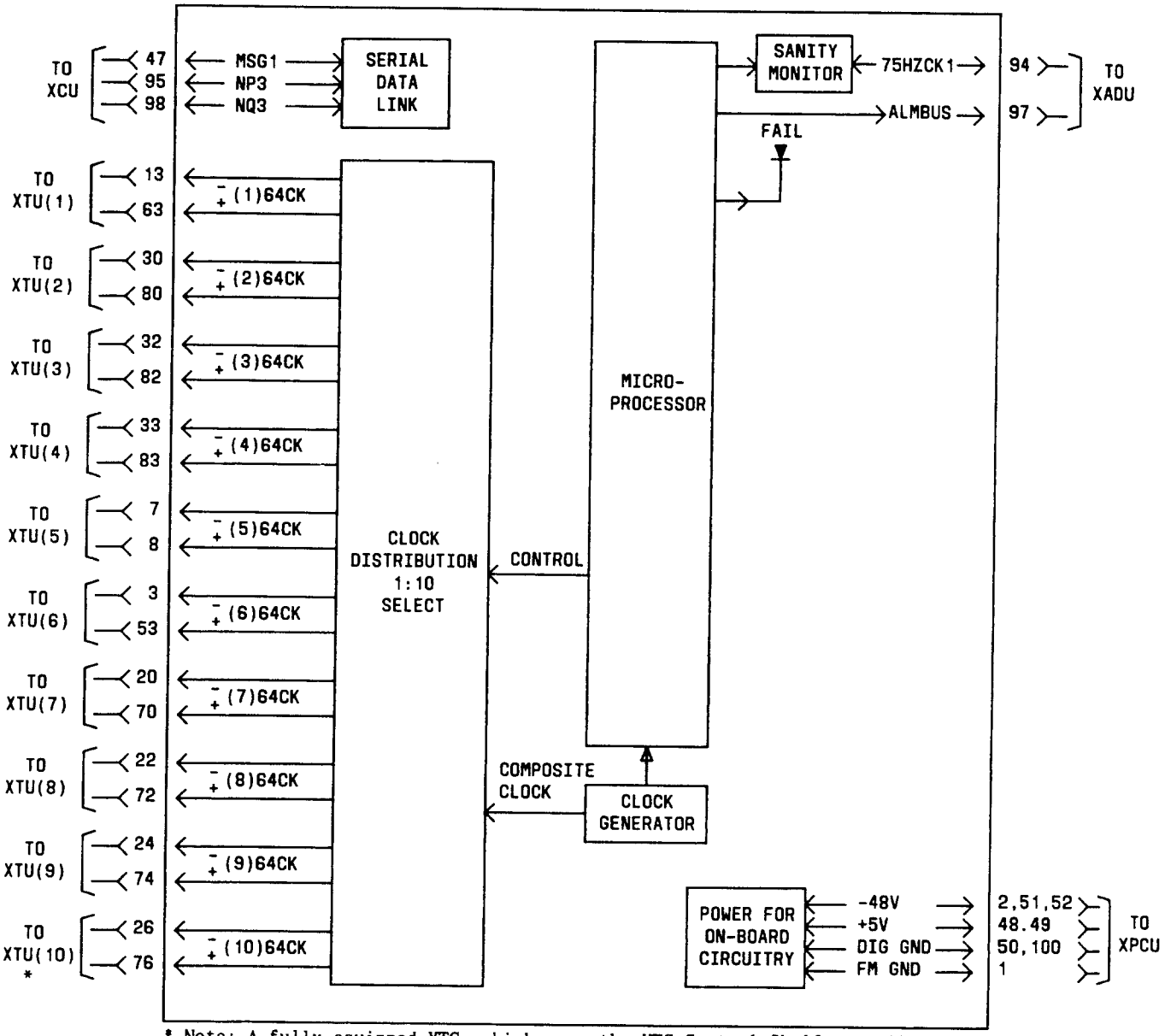
DATA SHEET

SLC® SERIES 5 CARRIER SYSTEM

The AUB69 XTC composite clock unit (XCCU) is used in the extended test controller (XTC). The AUB69 design is based on an on-board microcomputer which is controlled by the MC97734-01 XTC controller unit (XCU). A serial data link using the standard XTC backplane protocol is used to communicate with the XCU.

The AUB69 XCCU generates and distributes a composite clock for self testing the XTUC (AUB67), XTUB (AUB68), and XTUD (AUB65) test units. The composite clock is a 64-kHz bipolar clock (5/8, 3/8 duty cycle) with bipolar violations every 333 Hz. Normally, composite clock is not connected to any tester. For self test of the testers, the composite clock can be connected to one XTC tester unit (XTU) at a time.

Figure 1 is a block diagram of the AUB69 XCCU. Figure 2 shows the faceplate. The red (FAIL) LED becomes lighted when the XCCU has failed a self test or could not perform a task commanded by the XCU. The AUB69 also notifies the XTC alarm display unit (XADU) of an alarm by grounding the common bus, ALMBUS.



* Note: A fully equipped XTC, which uses the XTC Control Shelf as well as the expansion shelf, may contain up to ten tester units. One XTU tester unit is comprised of the XTUB (AUB68), XTUC (AUB67), and XTUD (AUB65) units.

Fig. 1—AUB69 XCCU Block Diagram

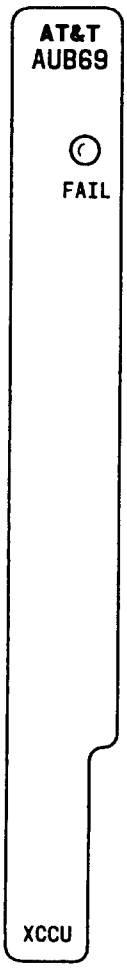


Fig. 2—AUB69 XCCU Faceplate Diagram