

J98726JA TUI AND J98726JB OIU-5
INDEPENDENT DIGROUP TIMING
CIRCUIT PACKS
DATA SHEET
D4 CHANNEL BANK

DESCRIPTION

Two circuit packs, the TUI and OIU-5, together provide independent digroup timing in a D4 channel bank. The TUI circuit pack is required in the TU slot of digroup A. The OIU-5 circuit pack is required in the OIU slot (top shelf). Independent digroup timing is applicable to all D4 modes of operation except Mode 1. Figure 1 shows the TUI and OIU-5 faceplates.

The timing options are set by option plugs located on the faceplate of the OIU-5 circuit pack. The timing option possibilities are as follows. The B digroup can be optioned independent of the A digroup for either external, loop, or local timing. The A digroup can be optioned independent of the B digroup for loop timing, or it can be optioned to lock to B digroup timing. Lock to B digroup timing times the A digroup to the B digroup, and thus times the entire D4 bank to whatever timing option is selected for the B digroup.

APPLICATIONS

Some independent digroup timing applications are:

1. When one D4 bank must be timed to two separate timing sources, i.e., two digital switches which are not synchronized to each other.
2. When one D4 bank digroup (B) must be timed to an external composite clock for digital data and the other digroup (A) must be timed to a loop timing source such as digital switch.
3. When one D4 bank digroup (B) is locally timed and the other digroup (A) is timed to a loop timing source.

FEATURES

Features of independent digroup timing are:

1. Two regenerated external composite clock outputs are provided referenced to the B digroup.
2. In the B digroup, loss of external composite clock, a red alarm, or a loop failure condition causes an automatic switch to free run timing when the B digroup is optioned for external timing.
3. When the A digroup is loop timed and has a red alarm or is looped, it automatically switches to timing from the B digroup.
4. The OIU-5 provides jack access to external composite clock timing to allow testing with the dataport transmitter and receiver test sets. The jack is on the faceplate and is labeled TST.
5. No backplane wiring changes are required.

OPTIONS

There are no options on the TUI circuit pack. All timing options are on the OIU-5 circuit pack. The options are:

B-EXT: Times the B digroup to an external composite clock source.

B-LT: Loop times the B digroup to the incoming pulse stream from the far-end equipment.

B-LOC T: Times the B digroup to a free running local clock source.

A-LT: Loop times the A digroup to the incoming pulse stream from the far-end equipment. This is independent of the timing option selected for the B digroup.

The timing option for the A digroup is then determined by the option selected for the B digroup (local, loop, or external).

ISSUING ORGANIZATION

A-LCK B: Times the A digroup from the B digroup.

Published by
The AT&T Documentation Management Organization

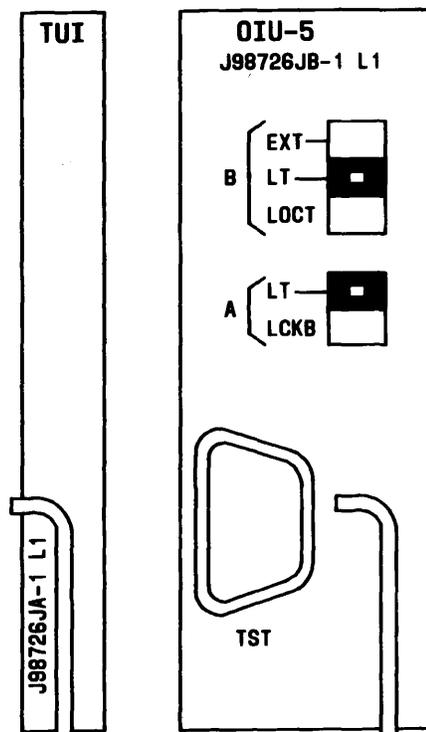


Fig. 1—TUI and OIU-5 Circuit Pack Faceplates