

81D1 TELETYPEWRITER SWITCHING SYSTEM
SWITCHING CENTER CIRCUITS
THEORY SCHEMATICS

1. GENERAL

1.01 This section lists, but does not include, the theory schematic drawings covering the 81D1 teletypewriter switching system switching center circuits.

1.02 Copies of the drawings listed herein, if required, may be obtained on order through the proper channels from the General Service Department of the Bell Telephone Laboratories, Incorporated.

1.03 Reference should be made to Section 580-101-400 for the theory schematic drawing covering the outlying station circuit using the SOTUS unit.

2. DRAWING LIST

2.01 The drawing numbers of all the switching center theory schematic drawings are listed in Table A.

TABLE A
THEORY SCHEMATIC DRAWINGS — 81D1 SWITCHING CENTER

<u>Title</u>	<u>Drawing</u>	<u>Issue</u>
Incoming Line or Incoming Trunk Circuit - 75-speed Operation	ES-883383	4
Incoming Line or Incoming Trunk Circuit - 75-speed Operation as Incoming Trunk or Single Station Incoming Line - With Tape Changing Typing Reperforator	ES-883384	5
Originating Station Circuit - Shown With 19TTY and Director Arranged to not Discard Multiple Address Codes	ES-883385	6
Director Circuit - Arranged to not Discard Multiple Address Codes - Shown With Incoming Line	ES-883386	5
Sequence Circuit	ES-883387	5
Outgoing Line or Single Outgoing Trunk Circuit - Reperforator Side	ES-883388, Sheet 1	9
Outgoing Line or Single Outgoing Trunk Circuit - Transmitter Side	ES-883388, Sheet 2	9
Transmitter Start Circuit	ES-883389	4
Willful and Miscellaneous Intercept Circuits	ES-883390	11
Local Outlet Circuit	ES-883391	7
Group Code Circuit - Associated With Single Stage Multiple Address Circuit or Second Stage of Two-stage Multiple Address Circuit	ES-883392	4

TABLE A (Cont)

THEORY SCHEMATIC DRAWINGS — 81D1 SWITCHING CENTER (Cont)

<u>Title</u>	<u>Drawing</u>	<u>Issue</u>
One-stage Multiple Address Circuit - Arranged to Receive Local Multiple Address Codes	ES-883393, Sheets 1, 2, and 3	8
Multiple Address Intercept Circuit	ES-883394	6
Director Fan Circuits	ES-883397	3
48-volt Power Circuit	ES-883400	4
130-volt Power Circuit	ES-883401	3
Outgoing Multichannel Trunk Circuit	ES-897013	5
Supplementary Multiple Address Circuit	ES-897014	5
Incoming Line or Incoming Trunk Circuit - 75-speed Operation as Incoming Trunk or Single Station Incoming Line With Tape Changing Reperforator-Transmitter	ES-897133	3
Director Circuit Arranged to Discard Multiple Address Codes - Shown With Incoming Line	ES-897523	5
One-stage Multiple Address Circuit - Arranged to not Receive Local Multiple Address Codes	ES-897524 Sheets 1, 2, and 3	6
Originating Station Circuit - Shown With 19TTY and Director Arranged to Discard Multiple Address Codes	ES-897525	4
Two-stage Multiple Address Circuit With First Stage Group Code Circuit	ES-941694 Sheets 1, 2, and 3	5
28F Reperforator-Transmitter Unit - 28B R-T Adapter Unit and 100E Cabinet	B-130544	1
28D Reperforator-Transmitter Set - 28A R-T Adapter Unit and 28B Apparatus Cabinet	B-130545	1
Originating Station Circuit Shown With 28TTY and Director Arranged to Discard Multiple Address Codes	B-130624	1
Incoming Line or Incoming Trunk Circuit With 28-Type Reperforator-Transmitter	B-130696	1
Incoming Line or Incoming Trunk Circuit - Operation as Incoming Trunk or Single Station Incoming Line with Tape Changing 28-Type Reperforator-Transmitter	B-130697	1