

CODE DIGIT 5 FEATURE		CODE	CODE DIGITS 6 & 7 FEATURE		CODE	CODE DIGITS 6 & 7 FEATURE				CODE	
TYPE	APPARATUS	A	OCU	LOGIC AND ALARM	01	T1DM	64 KB PORT CIRCUIT				13
	EQUIPMENT	B		2.4-KB CLOCK GENERATOR	02		PERFORMANCE MONITORING				B0
				4.8/9.6-KB CLOCK GENERATOR	03		CONTROL SWITCH (1)		HL10	B1	
				56 KB CLOCK GENERATOR	04				HL90	D6	
				DRIVER TERMINATOR	05		REGISTER		SHORT	B2	
				DSX-0 JACKS	17		ELASTIC STORE		LONG	B3	
				COMMON TIMING & OSCILLATOR	06		COUNTER			B4	
				LOOP-SIDE INTERFACE	0A		CONTROL			B5	
				PORT SWITCH	07		T1 INTERFACE				B6
				SUBRATE PORT CIRCUITS	08		SYNCHRONIZATION CIRCUIT				/B7/
				COMMON LOGIC	09		IMPROVED				BA
				TEST ACCESS	10		CLOCK CIRCUIT				B8
				COMMON SWITCH	11		SWITCH		TRANSMIT	B9	
				GROUP SCAN	12		RECEIVE			C1	
			SRDM	TIMING & SCANNING CONTROL	A1	NTS	TIMING SUPPLY		NODAL	T1	
				TEST TRANSMISSION LOGIC	A2		DISPLAY & CONTROL		SECONDARY	T2	
				FRAMING	A3		CODE DIGIT 8				
				PORT SELECT	A4	REFERENCE	5	6	7	CODE	
				STATE-CONTROL	A5	ED-82512-30	IB	10	1	B*%	
				TEST ANALYSIS	A6	SD-73083-01	IB	10	1,2	C*%	
				BAY, BUS AND ALARM	A7	HL- CP	IA	10	2 TO 9,	A*%	
				SUBRATE PERFORMANCE MONITOR RELAYS	A8			11	0 TO 3,		
								17			
								A	1 TO 8		
								B	0 TO 9,		
								IA			
								C	1		
								D	6		

NOTES: (1) SEE SD-73078-01 NOTE 114

DESCRIPTION: DIGITAL DATA SYSTEM - CIRCUIT PACKS



CODE DIGIT 5 FEATURE	CODE	CODE DIGITS 6 & 7 FEATURE	CODE	CODE DIGITS 6 & 7 FEATURE	CODE
APPARATUS	A	CLOCK LINE DRIVER	14	STAND ALONE CLOCK	18
		LINE TERMINATOR	/F1/	RECEIVE LOGIC	/H1/
	LTS AND BCPA	WITH REGEN	FA	IMPROVED	H5
		ALARM	F2	TRANSMIT LOGIC	H2
		RELAY	F3	ANALOG TRANSMITTER	H3
		LOGIC	F4	ANALOG RECEIVER	H4
	LTS AND STS	PHASE-LOCKED LOOP	F4	DIGITAL EXTENSION	H6
		PHASE-LOCKED LOOP MONITOR	/F5/		
		IMPROVED	FB	CONTROL & TEST CODE GEN	T1
	NTS	PHASE-LOCKED LOOP CONTROL	F6	LINE DRIVER	T2
		ALARM LOGIC	F7	MSU CONTROL & TIMING	T3
		TIMING SUPPLY	F8	MSU I/O AND MEMORY	T4
	NTS, STS AND LTS	LINE DRIVERS	F8		
		OUTPUT CIRCUITS	F9	ACCESS	J1
		INTERFACE UNITS	G7	BYPASS	J2
		PHASE METERING CIRCUIT	G1	INTERFACE	J3
		HIGH	G2		
		FREQUENCY COUNTDOWN	G3		
		LOW	G3		
	NTS	INTEGRAL & SUM REGISTERS	G4		
		ACCUMLATOR & SLIP DETECTOR	G5		
		PHASE COMPARATOR	G6		
				CODE DIGIT 8	
				REFERENCE	5 6 7 CODE
				HL- CP	A 11 14, 8
					IF 11 TO 9,
					IA, B
					IG 11 TO 7
					IH 11 TO 6
					IT 11 TO 4
					IJ 11 TO 3

NOTES:

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