PERFORMANCE REQUIREMENTS
2000-CYCLE SUPPLY UNIT J98613AJ
LOAD TRANSFER UNIT J98613W
GENERAL EQUIPMENT REQUIREMENTS
COMMON SYSTEMS

1. GENERAL

1.01 This section covers the performance requirements which the odd and even 2000-cycle supply circuits and the associated load transfer circuit shall meet before turnover to the telephone company.

1.02 Reference shall be made to Section 800-630-180, covering general requirements and definitions, for additional information necessary for the proper application of the requirements.

1.03 This section is reissued to change frequency requirements for revised 2000-cycle supply circuit, SD-98081-01, Issue 9A and subsequent reissues.

1.04 ***Test Equipment.*** Transmission measurements specified herein shall be made with No. 13A transmission measuring set or electron tube voltmeter.

(a) Frequency measurements shall be made with No. 72A frequency meter per J64072A or beat frequency checking circuit in the load transfer circuit.

1.05 Circuits under test shall be in operating condition for at least 2 hours immediately prior to the tests.

1.06 Ambient temperature shall not change more than 10°F during warmup and testing periods.

2. REQUIREMENTS

A. Transmission Tests

2.01 Output measured at the OSC TST jack of the odd or even supply circuit shall be:

TMS READING DBM	VTVM READING VOLT
+1.0 ±0.5	0.87 ±0.05

2.02 Frequency measured at the OSC TST jack of the odd or even supply circuit shall be 2000 ±30 cps.

2.03 With normal output indication at even OSC TST jack, the load transfer circuit shall connect the odd load circuit to the even supply circuit when output at odd OSC TST jack drops 2 db below normal value as the result of adjusting the odd OSC LEV potentiometer.

(a) Load transfer circuit shall reconnect odd load circuit to odd supply circuit when odd output is restored to normal operating value.

2.04 With normal output indication at odd OSC TST jack, the load transfer circuit shall connect the even load circuit to the odd supply circuit when output at even OSC TST jack drops 2 db below normal value as the result of adjusting the even OSC LEV potentiometer.

(a) Load transfer circuit shall reconnect even load circuit to even supply circuit when even output is restored to normal operating value.

2.05 Load transfer circuit shall reconnect each load circuit to its own supply circuit when outputs of both supply circuits are concurrently at trouble values 2 db below the normal values.

(a) All circuits shall restore to idle state after both outputs are restored to the normal operating values.