

**PERFORMANCE REQUIREMENTS
DISTRICT SELECTORS
SUBSCRIBER AND KEYPULSING "A" SENDERS
GENERAL EQUIPMENT REQUIREMENTS
PANEL SYSTEMS**

1. GENERAL

1.01 Description of Section: This section covers the performance requirements for panel district selectors, subscriber

senders and key pulsing "A" senders. This section supplements specification X-65500 Issue 5-D covering performance requirements for panel central offices and replaces the requirements specified therein for districts and senders.

2. ROUTINE AND SUPPLEMENTARY TEST REQUIREMENTS

2.01 Table of Requirements

Rou- tine No.	Name of Test	# Para- graph No.	ROUTINE				# See Note	SUPPLEMENTARY		
			Frequency in Work- ing Days		Performance			# Para- graph No.	Interval in Weeks before Turnover	
			Min.	Max.	Cum. % Fail- ure	Over Last No. of Cycles				Name of Test
	<u>District Selectors and Associated Equipment</u>	3.05					1		3.054	
2120	Link Dist.	3.051							(a)	-
2120A	Charge		1-3	2-1*	0.4	4*	3&4	Misc. Funct. of	(a)	-
2120B	Brush		1-5	1-1	0.2	2	5&6	Link Districts	(b)	4
2120C	Repeat Coil Out		1-5	1-1	0.4	2	1&2	Overtime Coin	(b)	4
2120D	Don't. Answer		1-10	1-1	0.2	2		Coll.	(c)	8
2120E	Overflow		1-5	1-1	0.2	2		2 Pty. MR Conn.	(c)	8
2120F	Early Release		1-10	1-1	0.2	2		Chain Cct.	(d)	-
2120G	Late Release		1-10	1-1	0.2	2		Tone Test	(d)	-
2121	Dist. Coin Con- trol Cct.	3.052	1-3	1-1	Clear	1		Timing Features	(e)	4
2122	2 Pty. MR Conn.	3.053	1-3	3-1	Clear	6		of Link Dist. (With Multiple Registration & Overtime Coin Non-Link Dist. (Mod. for Mult. Registration) L.S. Dist. (Mod. for Mult. Registration) Timing Cct. Pulse Check Cct.	(e)	4
									(f)	-
									(g)	-
									(h)	2
									(i)	2
	<u>Subs. & K.P. Senders</u>	3.07							3.075	
2140	Subs. Sender	3.071							(a)	-
2140A	Full Selector (FS)		1-1	5-1	0.5	10	3,7&9	Misc. Opr. Fea- tures of Subs. and K.P. Senders	(a)	-
2140B	Panel Call Indi- cator (PCI)		1-1	10-1	0.4	15	10	Reg. Test of Trouble Ind.	(b)	4
2140C	Inc.&Off.OFL (FS)		1-5	2-1	0.2	4		Decoder and De- coder-Connector Alarm Tests	(c)	4
2140D	Spl. Serv. Opr.		1-5	2-1	0.2	4			(c)	4
2140E	3 Digit Opr. (FS)		1-10	1-1	0.2	2			(c)	4
2140F	Register Control (FS)		1-5	2-1	0.2	4			(c)	4
2140G	Late Release (FS)		1-10	3-2	0.2	2		Misc. Opr. Fea- tures of De- coders and De- coder-Connec- tors	(d)	-
2141	Key Pulsing "A" Board Sender	3.072							(d)	-
2141A	Full Selector (FS)		1-1	5-1	0.5	10	3,9& 12		(d)	-
2141B	Call Indicator (CI)		1-1	10-1	0.4	15	10		(d)	-

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ROUTINE							SUPPLEMENTARY			
Rou- tine No.	Name of Test	# Para- graph No.	Frequency in Work- ing Days		Performance		# See Note	Name of Test	# Para- graph No.	Interval in Weeks before Turnover
			Min.	Max.	Max. Cum. % Fail- ure	Over Last No. of Cycles				
2141C	Operator's Error		1-5	1-1	0.2	2				
2141D	3 Digit Operator		1-10	1-1	0.2	2				
2141E	Inc. and Off. OFL (FS)		1-5	1-1	0.2	4				

*In the case of zone registration circuits, the maximum frequency shall be one test per day per each zone for which cross connections are provided and the last number of cycles shall be two times the number of zones for which cross connections are provided.

#Refers to paragraphs or notes in X-65500.

Bell Telephone Laboratories, Inc.