

2

ELECTRONIC SWITCHING SYSTEMS

NO. 3

SCANNER, PERIPHERAL PULSE
DISTRIBUTOR AND PERIPHERAL DECODER
ASSIGNMENT RULES
AND
COMPATIBILITY GUIDE

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 This schematic describes rules used to specify the assignment of scan points, peripheral pulse distributor (PPD) points, and peripheral decoder (PD) points in a No. 3 ESS. Also included is the compatibility guide for interfacing No. 3 ESS trunks and other circuits with circuits in other switching systems.

SECTION II - DETAILED DESCRIPTION

1.01 For detailed description, see SD-3H912-01.

SECTION III - REFERENCE DATA

1.01 None.

SECTION IV - REASONS FOR REISSUE

D. Description of Changes

- D.1 Added note 1.1 to define the No. 3 ESS power bus arrangement.
- D.2 Made minor changes to clarify Assignment Rules and correct drawing errors.
- D.3 Added Assignment Rule 1.A.3 to show how to assign circuit packs that have more than two circuits.

- D.4 Added Fig. 7 to show how to assign PD packs when two universal trunk units are mounted on the same miscellaneous frame.
- D.5 Expanded signalling and compatibility tables.

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