

CIRCUIT DESCRIPTION

CD-4C065-01  
ISSUE 1  
APPENDIX 3M  
DWG ISSUE 4M  
DISTN CODE BZ30

COMMON SYSTEMS  
3B20D MODELS 1, 2 and 3 PROCESSOR  
PORT SWITCH UNIT  
CIRCUIT

*CHANGES*

*D. Description of Change*

- D.1 CAD Note 2, CAD 004 and CAD 005 designated W option added.
- D.2 CAD's 002 and 003 designated X option are rated Manufactured Discontinued (MD) with the application of option W.

*E. Reason for Change*

- E.1 To provide a power distribution cable for the Port Switch Unit.

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CIRCUIT DESCRIPTION

CD-4C065-01  
ISSUE 1  
APPENDIX 2D  
DWG ISSUE 3D  
DISTN CODE 1N98

COMMON SYSTEMS

3B20D MODELS 1, 2 & 3 PROCESSOR

PORT SWITCH UNIT

CIRCUIT

CHANGES

D. Description of Changes

- D.1 Changed title of SD from "Common Systems/3B Processor/Port Switch Unit Circuit" to title shown at the top of this appendix - to provide processor type/model information.
- D.2 Added Information Note 304 containing circuit pack CLEI code information.
- D.3 Changed circuit rating from AT&TCo Provisional to AT&TCo Standard.
- D.4 Added PS 4, Symbols 1 through 5, Scan/Signal Interface (TF 2) (documentation only), to provide information for a user application.
- D.5 Added CADs 100 through 111 and 200 through 203 for 3B20D, Model 3 Processor.

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CIRCUIT DESCRIPTION

CD-4C065-01  
ISSUE 1  
APPENDIX 1B  
DWG ISSUE 2B  
DISTN CODE 7N98

COMMON SYSTEMS

3B PROCESSOR

PORT SWITCH UNIT

CIRCUIT

CHANGES

D. Description of Changes

D.1 Added options Y and Z to FS 1 and FS 2. Option Z is assigned to existing port switch (TF3) circuit packs. Option Y is assigned to the new port switch circuit pack (TF4) which is required in Generic 2.

D.2 Changed App. Figs. 1 and 2, Equipment Notes 201, 202, and 203, and Information Notes 302 and 303 to include options Y and Z.

D.3 Updated CLEI code information in Equipment Note 205.

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CIRCUIT DESCRIPTION

CD-4C065-01  
ISSUE 1  
DWG ISSUE 1  
DISTN CODE 7N98

COMMON SYSTEMS  
3B PROCESSOR  
PORT SWITCH UNIT  
CIRCUIT

SECTION 1 - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 This circuit provides a means of selecting one of two peripheral controllers to drive a single RS 232 peripheral device. The port switch can also be equipped with scan distribute interface circuits which provide buffering for non 3B S/D circuits.

2. GENERAL DESCRIPTION OF OPERATION

2.01 In 3B20D/DMERT processors the port switch unit is provided in peripheral control frame 1 only. The port switch unit circuit is fused and wired to power 1 or 2 communities of 3 port switches. The port switch circuits are designed to switch a single local maintenance CRT and/or printer to the on-line MTTY (TN83) peripheral controller. The controller is located in community 0 slot 0 of the IOP basic unit at either PCF 0 or 1; switching is normally controlled by software but may be manually switched on a per pack basis. This unit may also be equipped with from 1 to 5 S/D interface circuits which are documented only at the frame level. The S/D interface circuits require no power and do not interconnect with the port switches.

2.02 The port switch circuit is described in CPS-TF3 and the SC/SD interface circuit is described in CPS-TF2.

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SECTION II - DETAILED DESCRIPTION

2.01 The 48 volt power and CAD for connecting to other circuits constitute the port switch unit circuit. The port switch and the SC/SD interface are described at the circuit pack level as noted in SECTION I, paragraph 2.02 of this CD.

SECTION III - REFERENCE DATA

1. WORKING LIMITS

-48 volts - range 42.75 to 52.0 volts at less than 1 AMP.

2. CONNECTING CIRCUITS

A. Peripheral Control Frame SD4C059-02, SD4C106-01

B. Circuit Packs

1. SC/SD (A-E) - TF2

2. PSU-TF3

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