

CIRCUIT DESCRIPTION

CD-4C102-01  
ISSUE 1  
APPENDIX 1B  
DRAWING ISSUE 2B  
DISTN CODE 7N98

COMMON SYSTEMS  
3B20D MODEL 2 & 3 PROCESSOR  
POWER DISTRIBUTION UNIT  
CIRCUIT

CHANGES

D. Description of Changes

D.1 Added CADs 50 through 53 for 3B20D Model 3 Processor.

AT&T BELL LABORATORIES

DEPT 45151-RPV-MWR

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CIRCUIT DESCRIPTION

CD-4C102-01  
ISSUE 1  
DWG ISSUE 1  
DISTN CODE 7N98

COMMON SYSTEMS

3B20D MODEL 2 PROCESSOR  
POWER DISTRIBUTION UNIT  
CIRCUIT

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 The power distribution unit provides input power fusing and distribution. Feeders from the power distributing frame bring -48 volt power to six 30D fuse blocks. Each fuse block supplies up to four fused leads to various loads in the frame. Higher current leads use a fast-blow/slow-blow fuse pair; lower current leads use only a slow-blow (indicating-type) fuse.

SECTION II - DETAILED DESCRIPTION

Information for this section will be covered in a subsequent issue.

SECTION III - REFERENCE DATA

Information for this section will be covered in a subsequent issue.

BELL TELEPHONE LABORATORIES, INCORPORATED

DEPT 45142-LEL-MWR