COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

CHANGES

1. Description of Change

1.01 A connection has been added to the "FPWRFO" net from TN9
(04-072-034), to UN25B (04-110-056). This should be included
in the X option for DUPLEX applications only.

2. Reason for Change

2.01 Corrects diagnostic failure in DUPLEX applications.

AT&T BELL LABORATORIES

DEPT 45142 – TRC – MPV

Printed in U.S.A.

CIRCUIT DESCRIPTION

CD-4C115-01
ISSUE 1
APPENDIX 5B
DWG ISSUE 6B
DISTN CODE 7N98

COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

CHANGES

1. Description of Change

1.01 Add new BIOP backplane, ED4C457-30, in SD4C115-01. ED4C457-30 replaces ED4C382-30.

2. Reason for Change

2.01 Add cost reduced BIOP backplane.

AT&T BELL LABORATORIES

DEPT 45142 - TRC - MPV

CIRCUIT DESCRIPTION

CD-4C115-01
ISSUE
APPENDIX 4B
DWG ISSUE 5B
DSTN CODE 7N98

COMMON SYSTEMS.
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

CHANGES

D. DESCRIPTION OF CHANGES

D.1 Added option V to FS 4/1, App Fig 1, Notes 302, 303 and 304 to
     provide a TN6B circuit pack at EQL 04-162. Option V provides
an IO power control circuit with additional inrush protection.

AT&T BELL LABORATORIES

DEPT 45182-MPV-HAD

Printed in U.S.A.

COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

CHANGES

1. DESCRIPTION OF CHANGE

A.1 Add wiring option **Y** to existing drawing that provides alarm connections for AT&T 3B20S Computer applications consistent with current method of interconnection.

B.1 Add wiring option **X** which replaces option **Y** when unit is to be used in an AT&T 3B20D Computer. The duplex system requires separate alarm, scan/sd, and cooling unit start nets that are added in this option.

2. REASON FOR CHANGE

A.2 To allow the use of this unit for IOP growth applications of the 3B20D-Model 3 and remain consistent with the current methods of interconnection for alarms, scan/sd, and cooling unit start leads.

AT&T BELL LABORATORIES

DEPT 45182-JLW-PGW

COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

CHANGES

1. DESCRIPTION OF CHANGES

1.01 Released option information, and wiring information for the Enhanced DFC TN19/UN55 combination and added note to define the minimum release required for this feature.

1.02 Moved the IOP controller TN84B to the system level and introduced MC4C130A1 for the TIOP feature.

AT&T Bell Laboratories

Dept 45154-DPR-RGW

COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

D. Description of Changes

D.1 Isolate specified pins in nets (P5I, P5REF) on ED4C409.

D.2 Remove specified wire on net (P5VG) and add PW GRDs.

D.3 Update 300 series notes, remove wiring options X, Y, Z; define nets involved in system wiring options as specified in LDI, and rename net (STRP004A) to (STRP003A).

BELL TELEPHONE LABORATORIES, INC.

DEPT. 45142-DVP-MWR

Printed in U.S.A.

COMMON SYSTEMS
3B20S MODEL 2 PROCESSOR
IO PROCESSOR AND DFC CONTROLLER UNIT
CIRCUIT

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 The Input/Output Processor and Disk File Controller Unit
      consists of the Basic Input/Output Processor (BIOP) and the
Growth I/O - Disk File Controller (GIOD) which provide
communications access between the 3B20S and all peripheral
equipment. The BIOP consists of an IOP controller and various
Peripheral Controllers (PCs). The BIOP provides access, via the
PCs. from the processor to various peripherals. like magnetic tape
transports. the diagnostic processor. data terminals. and data
links. The GIOD provides access to the disk file community and
capability to add additional PCs.

2. GENERAL DESCRIPTION OF OPERATION

2.01 Information for this section will be supplied in a subsequent
      issue.

SECTION II - DETAILED DESCRIPTION

1.02 Information for this section will be supplied in a subsequent
      issue.

SECTION III - REFERENCE DATA

1.03 Information for this section will be supplied in a subsequent
      issue.

BELL TELEPHONE LABORATORIES, INCORPORATED

DEPT 45142-DVP-MWR