

CIRCUIT DESCRIPTION

CD-4C122-01
ISSUE 1
APPENDIX 7B
DWG ISSUE 8B
DISTN CODE 7N98

COMMON SYSTEMS
3B20D MODEL 2 & 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESSTM

CHANGES

D. Description of Changes

D.1 Added shield ground to DDSBS end of the DSCH cable. This causes the cable shield to be grounded at both ends. The change helps reduce the DSCH cable susceptibility.

AT&T INFORMATION SYSTEMS

DEPT-620-45182-JLW-MPV

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CIRCUIT DESCRIPTION

CD-4C122-01
ISSUE 1
APPENDIX 6AC
DRAWING ISSUE 7AC
DISTN CODE 7N98

COMMON SYSTEMS
3B20D MODEL 2 AND MODEL 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESSTM

CHANGES

1. DESCRIPTION OF CHANGES

For the Model 2 system, add Option M which provides the new micro-code MC4C127A1B (TN19) 16K writable microcontrol store for the disk file controllers.

For the Model 3 system, add Option H which provides the new micro-code MC4C127A1B (TN19) 16K writable microcontrol store for the disk file controllers.

2. REASON FOR CHANGE

Adding the MC4C127A1B (TN19) circuit pack to the disk file controller corrects a B5 timeout error.

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DEPT 45182-JLW-RGW

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CIRCUIT DESCRIPTION

CD-4C122-01
ISSUE 1
APPENDIX 5A
DWG ISSUE 6A
DISTN CODE 7N98

COMMON SYSTEMS

3B20D MODEL 2 & 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESSTM

CHANGES

D. Description of Changes

D.1 SD-4C122-01, App Fig. 26, was incorrectly shown on LDI (laboratories design information) issue 2F and should be changed:

from requiring J1C173B-1, List 7
to requiring J1C173B-1, List 8.

D.2 SD-4C122-01, App Fig. 27, was incorrectly shown on LDI issue 2F and should be changed:

from requiring J1C173B-1, List 8
to requiring J1C173B-1, List 9.

D.3 The above changes correct a feature released on a previous LDI.

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COMMON SYSTEMS
3B20D MODEL 2 AND MODEL 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESSTM

CHANGES

D. Description of Changes

3B20D MODEL 2 PROCESSOR

- D.1 Change App. Fig. 9 to provide only the Store Address Translator Circuit Pack (UN45C).
- D.2 Add App. Fig. 11 that provides backplane wiring which enables circuitry on the STORE ADDRESS TRANSLATOR (UN45C) and provides the IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE.

3B20D MODEL 3 PROCESSOR

- D.3 Change App. Fig. 5 to provide only the Store Address Translator Circuit Pack (UN45C).
- D.4 Add App. Fig. 7 that provides backplane wiring which enables circuitry on the STORE ADDRESS TRANSLATOR (UN45C) and provides the IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE.

E. Reason for Change

- E.1 To correct an error in a previously-released feature. The backplane wiring that enables the IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE is not downward compatible to previous DMERT Generic 1 and 2 software releases.

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DEPT 45182-JLW-RGW/MFS

CIRCUIT DESCRIPTION

CD-4C122-01
ISSUE 1
APPENDIX 3B
DWG ISSUE 4B
DISTN CODE 7N98

COMMON SYSTEMS
3B20D MODEL 2 AND MODEL 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESS™

CHANGES

D. Description of Changes

D.1 Add J1C215A-1, Peripheral Interface Cabinet to the 3B20D Model 3 Computer. The PIC cabinet provides growth IOP capabilities in addition to the two IOPs located in the Processor Control Cabinets. The cabinet can be equipped with up to two Basic IO Processors and two IOP Growth Units. A fully equipped cabinet can support up to 32 peripheral control circuit packs.

D.2 Provide clarification to note concerning peripheral control circuit pack assignment information within the Processor Control Cabinet.

E. Reason for Change

E.1 Provide growth IOP capabilities in addition to the Processor Control Cabinet, per application request.

E.2 Provide instructions concerning peripheral control circuit pack assignment within the Processor Control Cabinet, for both the Basic and Growth Units.

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DEPT 45182-JLW-RGW/MFS

COMMON SYSTEMS
3B20D MODEL 2 & 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESSTM

CHANGES

D. DESCRIPTION OF CHANGES

D.1 Change KS list numbers to provide new color units:

- Tape Unit from KS23113L10 to KS23113L14
- Disk Drive from KS22875L11 to KS22875L10
- Disk Power Supply from KS22987L8 to KS22987L3
- Video Terminal from KS22921L2 to KS22921L3

These changes modify KS list numbers in order to provide constant color combination for 5ESS equipment.

D.2 Tape unit KS22762 is not required as a feature for 5ESS. App. Figs. 220 and 221 and associated equipment, tape unit KS22762L1, are rated Manufacture Discontinued (MD) for this circuit and replaced with App. Figs. 222 and 223 tape unit, KS23113L14.

D.3 This change is issued to correct information about the Memory Expansion and Store Address Translator Feature given in Issue 1D. The list numbers have been changed to allow ordering of individual circuit packs that make up this feature. This will correct information released in a previous LDI and change App. Fig. numbers and equipment list numbers to make circuit packs orderable on an individual basis.

D.4 This change updates the Feature and Option table to include the FAST BACK-UP TAPE UNIT feature associated with the 340 MB fixed media disk drives.

COMMON SYSTEMS
3B20D™ MODEL 2 AND 3 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESS™

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 This system provides a processor system configuration for the 3B20D Model 2 and 3 Processor, processor system cabinet, for the 5ESS™ Switch.

2. GENERAL DESCRIPTION OF OPERATION

3.01 The processor system is made up of a processor control cabinet and a tape/disk cabinet connected together to form the 3B20D Model 3 Processor for the 5ESS Switch.

3.02 Information about the processor control cabinet circuit is contained in CD-4C119-01.

3.03 Information about the tape/disk cabinet circuit is contained in CD-4C126-01.

SECTION II - DETAILED DESCRIPTION OF OPERATION

None

SECTION III - REFERENCE DATA

None.

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DEPT 45153-CAB-MFS

COMMON SYSTEMS
3B20D™ MODEL 2 PROCESSOR
PROCESSOR SYSTEM CABINET
FOR 5ESS™

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 This system provides a processor system configuration for the 3B20D Model 2 Processor, processor system cabinet, for the 5ESS Switch.

2. GENERAL DESCRIPTION OF OPERATION

2.01 The processor system is made up of a processor control cabinet, disk power cabinet, moving head disk drive, and a tape unit cabinet connected together to form the 3B20D Model 2 Processor for the 5ESS Switch.

2.02 Information about the processor control cabinet circuit is contained in CD-4C119-01.

2.03 Information about the disk power cabinet circuit is contained in CD-4C121-01.

2.04 Information about the moving head disk drive circuit is contained in CD-4C056-02.

2.05 Information about the tape unit cabinet circuit is contained in CD-4C120-01.

SECTION II - DETAILED DESCRIPTION OF OPERATION

None

SECTION III - REFERENCE DATA

None

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DEPT 45140-ANK-MPV