

5ESS® SWITCHING EQUIPMENT  
APPLICATION SCHEMATIC FOR  
5ESS SYSTEM  
(6FT CABINETS)

SECTION I - GENERAL DESCRIPTION

1. PURPOSE OF CIRCUIT

1.01 The 5ESS System Application Schematic provides a functional block diagram of system circuits. It identifies functional circuit interconnection and controlling documentation. It also identifies appropriate circuit, equipment, and information notes. Ordering information necessary to equip the 3B20D Processor, 3B20D Model 2 Processor, 3B20D Model 3 Processor, and 3B21D Computer System for 5ESS to meet 5ESS requirements is also provided.

SECTION II - DETAILED DESCRIPTION

1.01 For a detailed description, refer to the circuit descriptions associated with the schematic drawings listed in the supporting information block of SD-5D014-02. The 5ESS Application Schematic is a generalized document with no equipment exclusively associated with it.

SECTION III - REFERENCE DATA

1.01 None.

SECTION IV - REASON FOR REISSUE

D. Description Of Change

D.1 Add the 3B21D Processor for US large office applications.

D.2 Add the 3B21D Processor for International large office applications.

D.3 Remove unnecessary restriction on the 3B21D Processor when used in CM2C applications. Also, add additional Flex IOP equipage rules.

D.4 Remove the TN1839 Synchronous Circuit Pack from note 211 for US applications since it is only supported in international applications.

D.5 Update note 211 to reflect US flexible assignments for 5E7 and later. Also change the SCAN reference to SCANS2 to agree with software.

D.6 Add the CMPU and CNI to AS18 for CM2C Applications. AS18 will now be used for both international and US applications of the CM2C.

D.7 Provide general updates required for the CM2C when used in US applications.

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DEPT NA5350300-FNG-MJK