

CIRCUIT DESCRIPTION

ALARM/TELEMETRY PROCESSOR CIRCUIT PACK PROVIDES THE TELEMETRY AND ALARM INTERFACE BETWEEN THE VPH AND THE GTP.

INPUT/OUTPUT INFORMATION (CONT)

INPUT/OUTPUT INFORMATION

PIN#	DESIG	DESCRIPTION (BOARD FUNCTION)	I/O	SIGNAL
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PRIMARY BUS

002	EAD0	EXTERNAL LATCHED DATA BUS	I/O	TTL
102	EAD1	EXTERNAL LATCHED DATA BUS	I/O	TTL
003	EAD2	EXTERNAL LATCHED DATA BUS	I/O	TTL
103	EAD3	EXTERNAL LATCHED DATA BUS	I/O	TTL
004	EAD4	EXTERNAL LATCHED DATA BUS	I/O	TTL
104	EAD5	EXTERNAL LATCHED DATA BUS	I/O	TTL
005	EAD6	EXTERNAL LATCHED DATA BUS	I/O	TTL
105	EAD7	EXTERNAL LATCHED DATA BUS	I/O	TTL
008	ELA0	EXTERNAL LATCHED ADDRESS BUS	I	TTL
108	ELA1	EXTERNAL LATCHED ADDRESS BUS	I	TTL
009	ELA2	EXTERNAL LATCHED ADDRESS BUS	I	TTL
109	ELA3	EXTERNAL LATCHED ADDRESS BUS	I	TTL
010	ELA4	EXTERNAL LATCHED ADDRESS BUS	I	TTL
110	ELA5	EXTERNAL LATCHED ADDRESS BUS	I	TTL
011	ELA6	EXTERNAL LATCHED ADDRESS BUS	I	TTL
111	ELA7	EXTERNAL LATCHED ADDRESS BUS	I	TTL
012	ELA8	EXTERNAL LATCHED ADDRESS BUS	I	TTL

006	-ERD	EXTERNAL READ CONTROL LINE ACTIVE LOW	I	TTL
106	-EHR	EXTERNAL WRITE CONTROL LINE ACTIVE LOW	I	TTL
007	ERES	EXTERNAL RESET SIGNAL ACTIVE HIGH	I	TTL
029	-PHEN	EXTERNAL BOARD SELECT SIGNAL ACTIVE LOW	I	TTL
120	ECLK	EXTERNAL CLOCK 4.9152MHZ	I	TTL

PIN#	DESIG	DESCRIPTION (BOARD FUNCTION)	I/O	SIGNAL
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AUXILIARY INPUT/OUTPUTS

028	P13	AVAILABLE STRAP INPUT TO THE 8031	I	TTL
128	CONTRF	CONTROL OUTPUT INDICATING CONTRF	I	TTL
129	-EXINT0	AVAILABLE EXTERNAL INTERRUPT TO 8031	I	TTL
130	-EXINT1	AVAILABLE EXTERNAL INTERRUPT TO 8031	I	TTL
132	CPUF	EXTERNAL FLAG INDICATING CONTR FAIL	I	TTL
035	ALM-RST	EXTERNAL ALARM RESET INPUT	I	
135	ALM-RSTR	RETURN PATH FOR ALM-RST CONTACT POINT	I	
036	MNV	MINOR VISUAL ALARM CONTACT POINT	I	
136	MNV	RETURN PATH FOR MNV CONTACT POINT	O	
037	MN	MINOR ALARM CONTACT POINT	I	
137	MNR	RETURN PATH FOR MN CONTACT POINT	O	
038	USRALM	DISCRETE TELEMETRY ALARM CONTACT POINT	I	
138	USRALMR	RETURN PATH FOR USRALM CONTACT POINT	O	
039	ACOV	ALARM CUTOFF VISUAL CONTACT POINT	I	
139	ACOV	RETURN PATH FOR ACOV CONTACT POINT	O	
151	AMR110B_RST	REMOTE RESET FOR AMR110B	O	TTL
053	+RXD	RS422 TELEMETRY INPUT INTERFACE TO GTP	I	
153	-RXD	COMPLEMENT OF +RXD	I	
052	+TXD	RS422 TELEMETRY OUTPUT INTERFACE TO GTP	O	
152	-TXD	COMPLEMENT OF +TXD	O	
J2-01	SIM-INPUT	INPUT FROM SIMULATOR- RS423 LEVEL	I	
J2-02	SIM-OUTPUT	OUTPUT TO SIMULATOR- RS423 LEVEL	O	
J2-03	SIM-CNTRL	CONTROL TO TRISTATE +/- TXD & +/-RXD	I	

PIN#	DESIG	DESCRIPTION (BOARD FUNCTION)	I/O	SIGNAL
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POWER BUS AND RESERVED PINS

000	GRD	GROUND BUS		G
100	GRD	GROUND BUS		G
001	GRD	GROUND BUS		G
101	GRD	GROUND BUS		G
055	GRD	GROUND BUS		G
155	GRD	GROUND BUS		G
056	GRD	GROUND BUS		G
156	GRD	GROUND BUS		G
045	+5VIN	VCC +5V SOURCE INPUT		P
145	+5VIN	VCC +5V SOURCE INPUT		P
046	+5VIN	VCC +5V SOURCE INPUT		P
146	+5VIN	VCC +5V SOURCE INPUT		P
047	+5VIN	VCC +5V SOURCE INPUT		P
147	+5VIN	VCC +5V SOURCE INPUT		P
021	VCC1	RESERVED +12V SOURCE INPUT		P
121	VCC1	RESERVED +12V SOURCE INPUT		P
040	VEE	RESERVED -5V SOURCE INPUT		P
140	VEE	RESERVED -5V SOURCE INPUT		P
041	VEE	RESERVED -5V SOURCE INPUT		P
141	VEE	RESERVED -5V SOURCE INPUT		P
042	VEE	RESERVED -5V SOURCE INPUT		P
142	VEE	RESERVED -5V SOURCE INPUT		P
043	VEE	RESERVED -5V SOURCE INPUT		P
143	VEE	RESERVED -5V SOURCE INPUT		P
019	VEE1	RESERVED -12V SOURCE INPUT		P
119	VEE1	RESERVED -12V SOURCE INPUT		P

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