SHEET INDEX

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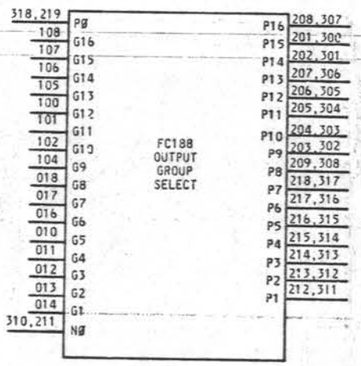
SYMBOL



SYMBOL

ØUTPUT GRØUP SELECT

ELEMENT IDENT.
A

TERM. MØD	FUNCT	TERM.	LØC	TERM. MØD	FUNCT	TERM.	LØC
G1	I	014	2F2	P13	Ø	207	2B9
G2	I	013	2F2	P13	Ø	306	2C9
G3	I	012	2E2	P14	Ø	202	2B9
G4	I	011	2E2	P14	Ø	301	2B9
G5	I	010	2E2	P15	Ø	201	2B9
G6	I	016	2C2	P15	Ø	300	2B9
G7	I	017	2D2	P16	Ø	208	2B9
G8	I	018	2D2	P16	Ø	307	2B9
G9	I	104	2D2				
G10	I	102	2C2				
G11	I	101	2C2				
G12	I	100	2C2				
G13	I	105	2B2				
G14	I	106	2B2				
G15	I	107	2B2				
G16	I	108	2A2				
NØ	I	211	2F2				
NØ	I	310	2F2				
PØ	I	219	2A2				
PØ	I	318	2A2				
P1	Ø	212	2F9				
P1	Ø	311	2F9				
P2	Ø	213	2F9				
P2	Ø	312	2F9				
P3	Ø	214	2E9				
P3	Ø	313	2F9				
P4	Ø	215	2E9				
P4	Ø	314	2E9				
P5	Ø	216	2E9				
P5	Ø	315	2F9				
P6	Ø	217	2D9				
P6	Ø	316	2E9				
P7	Ø	218	2D9				
P7	Ø	317	2D9				
P8	Ø	209	2D9				
P8	Ø	308	2D9				
P9	Ø	203	2D9				
P9	Ø	302	2D9				
P10	Ø	204	2C9				
P10	Ø	303	2C9				
P11	Ø	205	2C9				
P11	Ø	304	2C9				
P12	Ø	206	2C9				
P12	Ø	305	2C9				

RECORD OF CHANGES				
DWG ISS	PREV FURN	STD	MFR DISC	SEE NOTE

CIRCUIT DESCRIPTION

FUNCTIONS

THIS CIRCUIT PROVIDES FOR CONTROLLING ONE OUT OF SIXTEEN HIGH CURRENT PULSE PATHS FROM RELATIVELY LOW POWER SIGNALS ON INPUTS G1-16.

DETAILED DESCRIPTION

THE HIGH AMPLITUDE CURRENT PULSE FLOWS INTO THE PØ LEAD AND OUT ONE OF THE LEADS P1-P16. THE PARTICULAR LEAD IS SELECTED BY APPLYING 13 VOLTS ON THE CORRESPONDING INPUT LEAD G1-16 AND GROUNDING INPUT LEAD NØ.

NOTES:

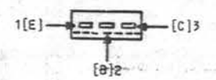
1. UNLESS OTHERWISE SPECIFIED:
   RESISTANCE VALUES ARE IN OHMS
   CAPACITANCE VALUES ARE IN MICROFARADS
   VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
2. POWER AND GROUND TERMINALS FOR INTEGRATED CIRCUITS:

IC CODE	GRD TERM.			

3. BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL

4. THE TERMINAL NUMBER ARRANGEMENT OF THE 85A TRANSISTOR IS:



5. CLOSEST HORIZONTAL MOUNTING CENTERS IS 0.750 INCH.
6. INITIAL USE OF THE FC188 CIRCUIT PACK IS IN SD-3H110-01.

DWG ISSUE	DATE ISSUE	DRAWN	APPD
1	12-12-75	LRE LRE	LDW LEG RFG
2D1	8-4-77	AA DAB LRE	LDW EN RFG

SYSTEM USED ON	DESIGN CONTROL
NO. 3 ESS	IH

CURRENT DRAIN: 0 mA

SHEET INDEX NOTES

1. FOR SINGLE REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.
2. FOR CONCURRENT REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE HIGHEST ISSUE NUMBER AFFECTING THAT SHEET.
3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

SUPPORTING INFORMATION

CATEGORY	NUMBER
CONNECTOR ON FRAME	947A, 947C OR 947E
CIRCUIT PACK INFORMATION DRAWING	
SERIES FOR LATEST CLASS "A" CHANGE	
ACCEPTABLE SERIES	4

1711

FC188 CIRCUIT PACK

OUTPUT GROUP SELECT CIRCUIT

AT&TCO STANDARD

DWG SIZE	ISSUE
6S	2D1

BELL LABORATORIES | CPS-FC188 | 2 SHEETS

5262-038778

COMPONENT LIST

CAPACITOR

DESIG	CODE
[16] C1-C16	KS-20736 L1, .1

DIODE

DESIG	CODE
[16] CR1-CR16	456B
[17] CR17-CR33	533B

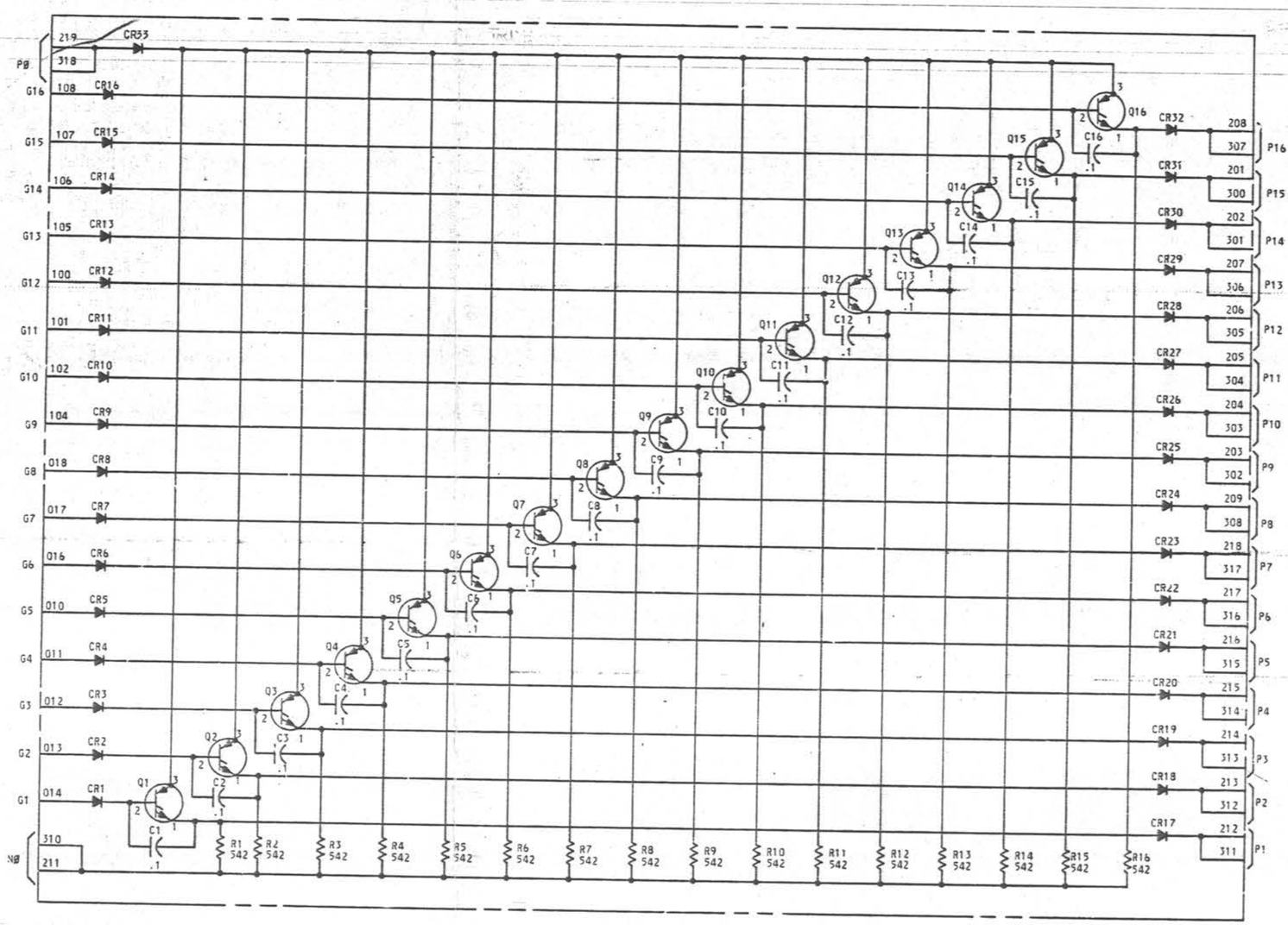
RESISTOR

DESIG	CODE
[16] R1-R16	KS-20810 L1A, 542

TRANSISTOR

DESIG	CODE
[16] Q1-Q16	85A

OUTPUT GROUP SELECT CIRCUIT



FC188 CIRCUIT PACK	CPS-FC188 SHEET 2
BELL TELEPHONE LABORATORIES INCORPORATED	6S

ISSUE 2D1