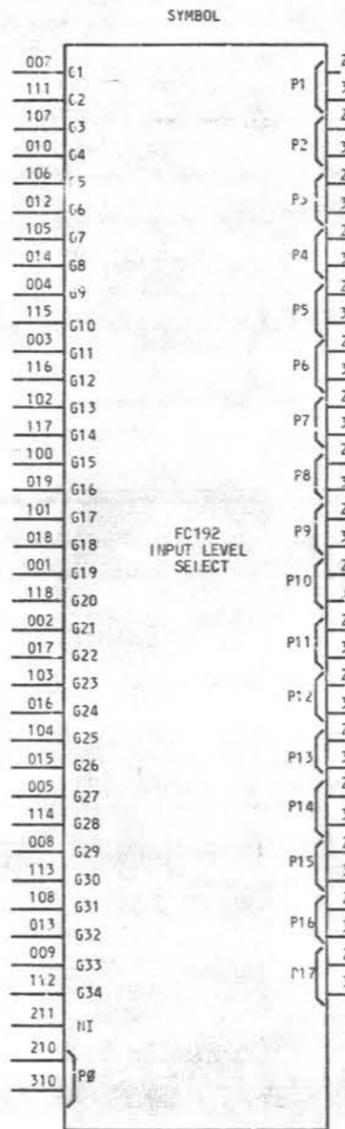


SHEET INDEX		
CONTENTS	SHEET NO.	SHEET ISSUE
SHEET INDEX SUPPORTING INFORMATION CURRENT DRAIN USED ON NOTES SYMBOL RECORD OF CHANGES CIRCUIT DESCRIPTION	1	2
CIRCUIT SCHEMATIC COMPONENT LIST	2	2

SYMBOL
INPUT LEVEL SELECT
GROUP IDENT.
A

TERM.	MFD	FUNCT	TERM.	LBC	TERM.	MFD	FUNCT	TERM.	LBC
G1		I	007	2A2	P1	0	206	2A9	
G2		I	111	2A2	P1	0	305	2A9	
G3		I	107	2A2	P2	0	205	2A9	
G4		I	010	2A2	P2	0	304	2A9	
G5		I	106	2A2	P3	0	204	2B9	
G6		I	012	2B2	P3	0	303	2B9	
G7		I	105	2B2	P4	0	203	2B9	
G8		I	014	2B2	P4	0	302	2B9	
G9		I	004	2B2	P5	0	202	2B9	
G10		I	115	2B2	P5	0	301	2B9	
G11		I	003	2B2	P6	0	201	2B9	
G12		I	116	2C2	P6	0	300	2C9	
G13		I	102	2C2	P7	0	207	2C9	
G14		I	117	2C2	P7	0	306	2C9	
G15		I	100	2C2	P8	0	209	2C9	
G16		I	019	2C2	P8	0	308	2C9	
G17		I	101	2C2	P9	0	200	2C9	
G18		I	018	2D2	P9	0	307	2D9	
G19		I	001	2D2	P10	0	212	2D9	
G20		I	118	2D2	P10	0	311	2D9	
G21		I	002	2D2	P11	0	213	2D9	
G22		I	017	2D2	P11	0	312	2D9	
G23		I	103	2D2	P12	0	214	2E9	
G24		I	016	2D2	P12	0	313	2E9	
G25		I	104	2E2	P13	0	215	2E9	
G26		I	015	2E2	P13	0	314	2E9	
G27		I	005	2E2	P14	0	219	2E9	
G28		I	114	2E2	P14	0	318	2E9	
G29		I	008	2E2	P15	0	218	2E9	
G30		I	113	2E2	P15	0	317	2F9	
G31		I	108	2F2	P16	0	217	2F9	
G32		I	013	2F2	P16	0	316	2F9	
G33		I	009	2F2	P17	0	216	2F9	
G34		I	112	2F2	P17	0	315	2F9	
HI		I	211	2F2					
P0		I	210	2G2					
P0		I	310	2G2					



RECORD OF CHANGES				
DWG ISS	PREV FURN	STD	MFR DISC	SEE NOTE

CIRCUIT DESCRIPTION

FUNCTION
THIS CIRCUIT PROVIDES FOR CONTROLLING ONE OUT OF SEVENTEEN HIGH CURRENT PULSE PATH FROM RELATIVELY LOW POWER SIGNALS. THIS FUNCTION IS PROVIDED FOR DUPLICATED CONTROLLERS.

DETAILED DESCRIPTION
THE HIGH AMPLITUDE CURRENT PULSE FLOWS INTO ONE OF THE P1-17 LEADS AND OUT THE P0 LEAD. A PARTICULAR PATH IS SELECTED BY APPLYING 13 VOLTS ON ONE OF THE G1-34 LEADS AND GROUNDING LEAD NI.

- NOTES:
- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS CAPACITANCE VALUES ARE IN MICROFARADS VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
 - POWER AND GROUND TERMINALS FOR INTEGRATED CIRCUITS:

IC CODE	GRD TERM			
 - BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
+3V	C00
GRD	200, 219
 - THE TERMINAL NUMBER ARRANGEMENT OF THE 89A TRANSISTOR IS:
 - CLOSEST HORIZONTAL MOUNTING CENTERS IS 0.750 INCHES.
 - INITIAL USE OF THE FC192 CIRCUIT PACK IS IN SD-3H11C-01.

SYSTEM USED ON	DESIGN CONTROL
NO. 3 ESS	IH

CURRENT DRAIN: C mA

SHEET INDEX NOTES	SUPPORTING INFORMATION		NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.	
1. FOR SINGLE REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1. 2. FOR CONCURRENT REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE HIGHEST ISSUE NUMBER AFFECTING THAT SHEET. 3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.	CATEGORY	NUMBER	FC192 CIRCUIT PACK INPUT LEVEL SELECT CIRCUIT	
	CONNECTOR ON FRAME	947A, 947C OR 947E		
	CIRCUIT PACK INFORMATION DRAWING			AT&T STANDARD
	SERIES FOR LATEST CLASS "A" CHANGE		DWG SIZE	ISSUE
	ACCEPTABLE SERIES	4	6S	2D1
			BELL LABORATORIES	CPS-FC192
				2 SHEETS

INPUT LEVEL SELECT CIRCUIT

COMPONENT LIST

CAPACITOR

DESIG CODE
[17]C1-C17 KS-20736.L1. .1

DIODE

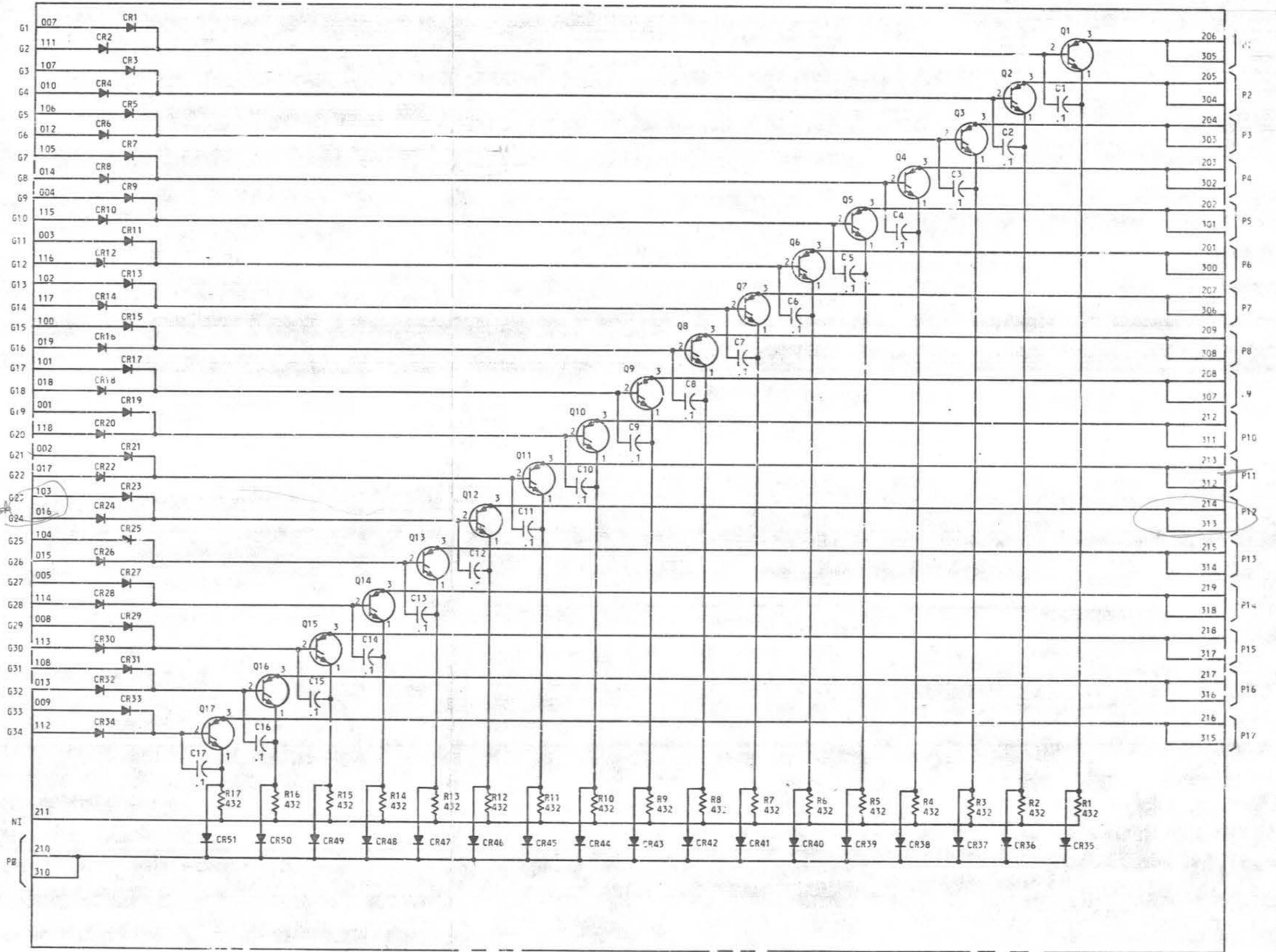
DESIG CODE
[34]CR1-CR34 456B
[17]CR35-CR51 533B

RESISTOR

DESIG CODE
[17]R1-R17 KS-20810.L1A. 432

TRANSISTOR

DESIG CODE
[17]Q1-Q17 85A



Good - [handwritten mark]

BAD - [handwritten mark]

ISSUE 2DI

FC192 CIRCUIT PACK

CPS-FC192 SHEET 2

BELL TELEPHONE LABORATORIES INCORPORATED

6S

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