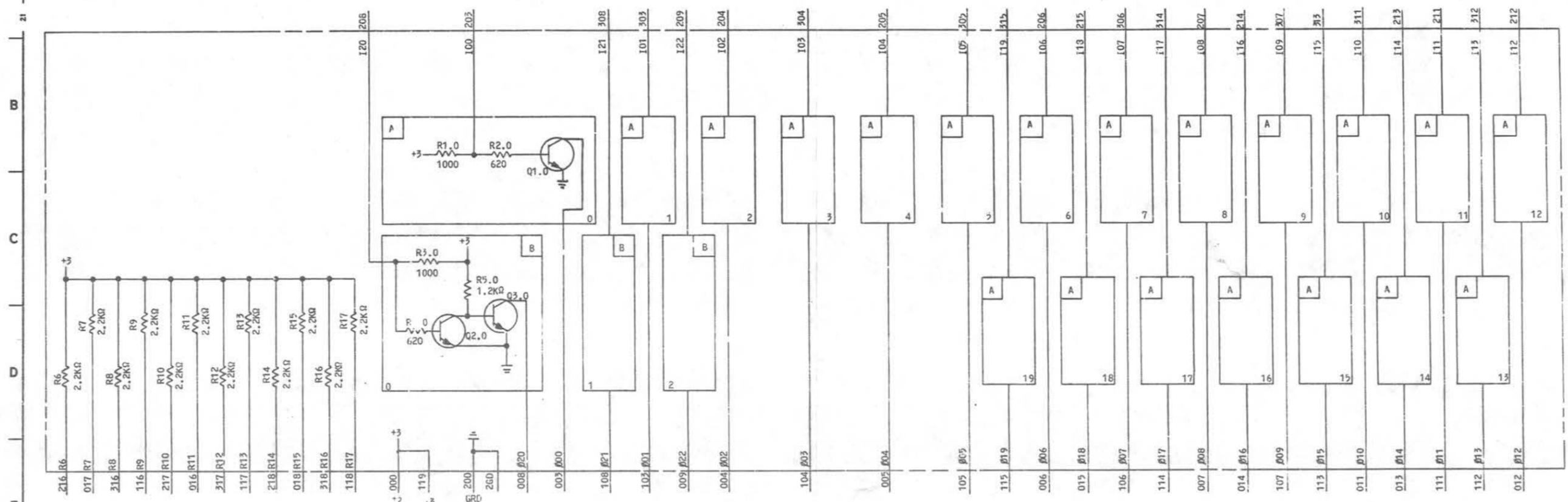


PART OF CPS FC209
LAMP & RELAY DRIVER



COMPONENT LIST

RESISTOR

DESIG	CODE
[20] R1.0-R1.19	KS-20200,L1, 1000
[20] R2.0-R2.19	KS-20200,L1, 620
[3] R3.0-R3.2	KS-20200,L1, 1000
[3] R4.0-R4.2	KS-20200,L1, 620
[3] R5.0-R5.2	KS-20200,L1, 1.2KΩ
[12] R6-R17	KS-20200,L1, 2.2KΩ

TRANSISTOR

DESIG	CODE
[20] Q1.0-Q1.19	66S
[3] Q2.0-Q2.2	66S
[3] Q3.0-Q3.2	66S

CPS-FC209

FC209 CIRCUIT PACK		2	CPS-FC209 SHEET 2
BELL TELEPHONE LABORATORIES INCORPORATED		6S	

ISSUE 2D1

PART OF CPS FC209

LAMP AND RELAY DRIVER

CIRCUIT DESCRIPTION

A. FUNCTION

CIRCUIT PACK FC209 PROVIDES RELAY (LAMP) DRIVER CIRCUITS AND PULLUP RESISTORS FOR USE WITH THE NO. 2B ESS AND NO. 3 ESS SYSTEM STATUS PANEL CONTROLLER (SSPC) CIRCUIT.

B. DETAILED DESCRIPTION

THIS CIRCUIT PACK PROVIDES 20 GENERAL PURPOSE LAMP/RELAY DRIVER INVERTER CIRCUITS AND 3 DOUBLE-INVERTER LAMP/RELAY DRIVER CIRCUITS. THE SIGNAL SOURCE THAT CONTROLS THESE CIRCUITS MUST BE CAPABLE OF SINKING 3.5 MA CURRENT AT V MAX IN LESS THAN 0.4 VDC (EG, A 1A LOGIC OPEN COLLECTOR GATE). EACH DRIVER OUTPUT WILL SINK 50 MA FROM A VOLTAGE SOURCE LESS THAN 26 VDC. SINCE THIS CP IS USED IN CONJUNCTION WITH LOW-LEVEL LOGIC CIRCUITS, THE CURRENT SWITCHING SHOULD BE MINIMIZED TO AVOID NOISE COUPLING PROBLEMS.

THIS CP ALSO PROVIDES 12 GENERAL PURPOSE PULLUP RESISTORS FOR USE WITH OPEN COLLECTOR 1A LOGIC GATES.

C. SYMBOL/LEAD MNEMONICS

MNEMONIC	DEFINITION
I01-I19	INPUTS TO SINGLE-INVERTER LAMP/RELAY DRIVER CIRCUITS (A)
I20-I22	INPUTS TO DOUBLE-INVERTER LAMP/RELAY DRIVER CIRCUITS (B)
O01-O19	OUTPUTS OF SINGLE-INVERTER LAMP/RELAY DRIVER CIRCUITS (A)
O20-O22	OUTPUTS OF DOUBLE-INVERTER LAMP/RELAY DRIVER CIRCUITS (B)
R6-R17	GENERAL PURPOSE PULLUP RESISTOR OUTPUTS CAPABLE OF SOURCING 1 MA AT 0.7 VOLTS

CPS-FC209

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FC209 CIRCUIT PACK

2

CPS-FC209
SHEET 3

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