CPS-JK16

SHEET INDEX

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SYMBOL
TAPE UNIT CONTROLLER
BOARD A
ELEMENT IDENT
A

TERM. MOD	FUNCT	TERM.	LOC	TERM. MOD	FUNCT	TERM.	LOC
BLØCK0	I	009	3A2	FSTFØRC0	ØI	210	2H5
BLØCK1	I	318	3A1	FSTREVC0	ØI	112	2H5
IBGSTP0	I	0C8	3A3	FWD0	ØI	207	2H3
INF010	I	101	2A1	INF000	ØI	201	3A8
INF040	I	002	2A1	INF020	ØI	202	3A8
INF050	I	102	2A1	INF030	ØI	302	3A8
INF070	I	308	2A7	INF080	ØI	217	3A4
INIT0	I	219	2A7	INF090	ØI	212	2A5
LBEN1	I	304	3A7	INF100	ØI	213	2A6
PSTAT1	I	001	3A0	INF110	ØI	214	2A6
RC0	I	103	2A0	INF120	ØI	115	3A5
RWDINGB0	I	019	3A3	INF130	ØI	015	3A6
STØPP0	I	113	2A0	INITB0	ØI	205	2H7
TIMB0	I	106	2A3	MAINT0	ØI	007	3H7
WETRK10	I	105	2A9	MØTCINH1	ØI	006	2H3
BKSPAC0	Ø	110	2H5	RDBLKC0	ØI	010	2H5
CLRCRCC0	Ø	311	2H6	READC0	ØI	011	2H5
ENRD0	Ø	004	2H0	TRCKA01	ØI	300	3H5
ENRD1	Ø	114	2H0	TRCKA11	ØI	303	3H5
INF140	Ø	203	3H4	TTFF1	ØI	013	2H3
INF150	Ø	204	3H1	TTFR1	ØI	108	2H3
MAINT1	Ø	305	3H7	TTSF1	ØI	014	2H1
RDCL0	Ø	215	2H0	TTSR1	ØI	118	2H3
REV0	Ø	208	2H2	TUCRC1	ØI	301	2H0
RTA00	Ø	117	3H5	WENAA0	ØI	109	2H1
RTA10	Ø	317	3H6	WENAB1	ØI	003	2H2
RWTSTPC0	Ø	313	2H6	WRITE0	ØI	104	2H8
SFTCC0	Ø	312	2H6	WTBG	ØI	005	2H8
STØPC0	Ø	012	2H6	+5	P	000,119	3G7
STREQC0	Ø	310	2H6	GRD	G	200,319	3G7
SWTSTPC0	Ø	314	2H6				
TTFF0	Ø	216	2H4				
TTFR0	Ø	016	2H5				
TTINIT0	Ø	209	2H8				
TTMSTP0	Ø	309	2H6				
TTREWC0	Ø	111	2H6				
TTSEL0	Ø	315	3H7				
TTSF0	Ø	116	2H4				
TTSR0	Ø	316	2H4				
TUCCØDE0	Ø	211	2H0				
WRCKTEN0	Ø	107	2H1				
WRENAB0	Ø	100	2H2				
WTA00	Ø	218	3H5				
WTA10	Ø	017	3H6				

RECORD OF CHANGES

DWG ISS	PREV FURN	STD	MSR DISC	SEE NOTE

SYSTEM USED ON	DESIGN CONTROL
COMMON SYSTEMS	IH

CURRENT DRAIN: 325mA

NOTES:

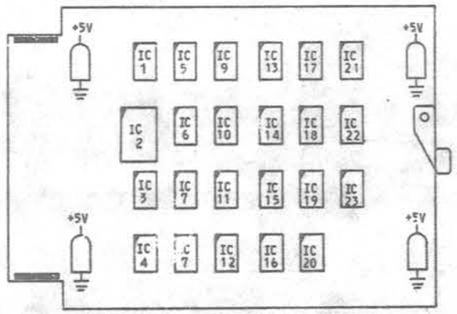
1. ⏚ GROUND RETURN
2. UNLESS OTHERWISE SPECIFIED,
   RESISTANCE VALUES ARE IN OHMS
   CAPACITANCE VALUES ARE IN MICROFARADS
   VALUES PRECEDED BY THE SYMBOL +(PLUS) OR -(MINUS) ARE IN VOLTS
3. BATTERY AND GROUND TERMINALS FOR INTEGRATED CIRCUITS

IC CODE	BAT. TERM.	GRD TERM.
41AD	16	8
41AE		7,8
41BP		8
41BR		7,8
41C4		8
41CC		8
41CF		7,8
41CK		8
41U		8
41W	16	8
129A	24	12

4. BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
+5	000,119
GRD	200,319

5. HORIZONTAL MOUNTING CENTERS AT 0.50 INCH.
6. INTEGRATED CIRCUIT LOCATION GUIDE:
   (COMPONENT SIDE SHOWN)



UNMARKED COMPONENTS ARE FILTER CAPACITORS

DWG ISSUE	ECE OR CD ISSUED	DATE ISSUE	DRAWN	APPD
1	—	10-11-74	LRE	KWF MTS RFG
2A	—	1-5-76	MPPL LRE	JEJ LEG RFG

SUPPORTING INFORMATION

CATEGORY	NO.
CIRCUIT PACK CODE	JK16
CONNECTOR ON FRAME	947C OR 947A
ACCEPTABLE SERIES	1-2,2

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE FIRST SHEET.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE FIRST SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

ISSUE 2A

1N98

JK16 CIRCUIT PACK

CARTRIDGE TAPE TRANSPORT CONTROLLER, BOARD A CIRCUIT

(2)

AT&TCO STANDARD

CPS-JK16
4 SHEETS

BELL TELEPHONE LABORATORIES INCORPORATED

6S

CPS-JK16

8062-000075

PART OF CPS JK16

CARTRIDGE TAPE TRANSPORT CONTROLLER, BOARD A

RC1 IC12 41BP E
RCG IC12 41BP D
TUCCØDE IC16 41CK A
SP1 IC7 41W B
SP2 IC12 41BP C
SP3 IC20 41AD A
NC
DECØDER IC2 129A A
INFØ701 IC1 41BP A
STPØR1 IC12 41BP B
RWND1 IC12 41BP A
R1 2.2K
SCRC1 IC7 41W D
RDCLO IC9 41BR B
R1 10KΩ
C1 20pF
START IC15 41CC A 150mS
WRITEPO IC11 41U C
FWD1 IC7 41W C
FFWD1 IC6 41BP B
REV1 IC6 41BP A
FREV1 IC6 41BP C
MØTREG IC10 41CF A
MSTPØRO IC21 41U D
TINITO IC22 41U D
INIT1 IC13 41BP D
ØWRIT1 IC1 41BP E
ØWIBG1 IC1 41BP F
TKAP1 IC8 41CA B
R2 2.2KΩ
MSTP1 IC21 41U A
MSTPO IC21 41U B
INITO IC13 41BP C
WRITAO IC3 41U D
WIBGAO IC3 41U A
MAINT1 IC3 41U C
MAINTO IC3 41U B
SLTO IC1 41BP D
MSTØPO IC18 41W B
RDENAB IC14 41AE B
WRENAB IC14 41AE A
WRCKTENO IC11 41U D
WRENABO IC12 41BP F
WRITPO IC12 41BP G
MØTCINH1 IC7 41W A
REVO IC9 41BR D
FWDO IC9 41BR C
TTSFO IC6 41BP G
TTSRO IC6 41BP F
TTFFO IC6 41BP E
TTFRO IC6 41BP D
WTBG
WRITEO

JK16 CIRCUIT PACK	2
BELL TELEPHONE LABORATORIES INCORPORATED	CPS-JK16 SHEET 2
65	SIZE 2A

CPS-JK16

PART OF CPS JK16

CARTRIDGE TAPE TRANSPORT CONTROLLER, BOARD A

PSTAT1 · BLØCK1 · BLØCKO · IBGSIPO · RWDIHGBO · INF080 · INF120 · INF130 · LBEN1 · INF000 · INF020 · INF030

WENAB1 · WTBG · WRITEO · WENAAO · TTSF1 · TTSF1 · TTFF1 · TTSR1 · TTFR1 · STØPPO · MØTCINH1

WWIBG1 IC17 41U D · WWIBGO IC17 41U A · WIBGO IC17 41U C · WRITEO IC17 41U B · RDBLKO IC21 41U C · READO IC22 41U C · FSEARO IC19 41W D · BCKSPO IC18 41W D · RSEARO IC19 41W B · CMDO10 IC22 41U B · RWD1 IC22 41U A

STATC1 IC18 41W A · STATB1 IC19 41W C · STATA1 IC19 41W A

INF0801 IC13 41BP E · MØTCINHO IC13 41BP F · INF1201 IC13 41BP A · INF1301 IC13 41BP B · TKCØMO IC11 41U A · TKINHO IC11 41U B · TKAO IC9 41BR A · DECINH1 IC13 41BP G · TKCLK1 IC1 41BP G

INF0000 IC4 41CA C · INF0200 IC4 41CA B · INF0300 IC4 41CA A · MILVL1 IC18 41W C · INF0001 IC20 41AD B · INF0201 IC20 41AD C · INF0301 IC20 41AD D

INF0900A IC23 41CA D · INF1300A IC8 41CA A · INF1500A IC4 41CA D · INF1000A IC23 41CA C · INF1100A IC23 41CA B · INF0800A IC23 41CA A · INF1200A IC8 41CA C · INF1400A IC8 41CA D

TKADA IC5 41AE A · TKADB IC5 41AE B · TKAAO IC1 41BP B · TKABO IC1 41BP C

C2 5 · C3 5 · C4 5 · C5 5 · C6 0.1 · C7 0.1 · (C8-C27) 0.1 · C28 0.1

INF090 2A6 · INF130 · INF150 204 · INF100 · INF110 2B6 · INF080 · INF120 B6 · INF140 203 · TRCKA01 300 · WTA00 218 · RTA00 117 · TRCKA11 303 · WTA10 017 · RTA10 317

ISSUE 2A

JK16 CIRCUIT PACK (2) · CPS-JK16 SHEET 3

BELL TELEPHONE LABORATORIES INCORPORATED · 6S

CPS-JK16

PART OF CPS JK16

CARTRIDGE TAPE TRANSPORT CONTROLLER, BOARD A

COMPONENT LIST

INTEGRATED CIRCUIT

LOC CODE ELEM ID	IC1 41BP DESIG	SH LOC	IC2 129A DESIG	SH LOC	IC3 41U DESIG	SH LOC	IC4 41CA DESIG	SH LOC	IC5 41AE DESIG	SH LOC	IC6 41BP DESIG	SH LOC	IC7 41W DESIG	SH LOC	IC8 41CA DESIG	SH LOC	IC9 41BR DESIG	SH LOC	IC10 41CF DESIG	SH LOC	IC11 41U DESIG	SH LOC
A	INFO701	2B7	DECØDER	2B5	WIBGA0	2E8	INF0300	3B8	TKADA	3F5	REV1	2D4	MØTCINH1	2G3	INF1300A	3F1	TKA0	3D6	MØTREG	2E4	TKCØMO	3D4
B	TKAA0	3G5			MAINT0	2F7	INF0200	3B7	TKADB	3F6	FFWD1	2D4	SP1	2A2	TKAP1	2D9	RDCLO	2E0			TKINHO	3D5
C	TKAB0	3G6			MAINT1	2F6	INF0000	3B7			FREV1	2D5	FWD1	2D4	INF1200A	3F4	FWDO	2F3			WRITEPO	2D2
D	SLTO	2G7			WRITA0	2E8	INF1500A	3F1			TTFRO	2F4	SCRC1	2D0	INF1400A	3F4	REVO	2F3			WRCKTENO	2G1
E	ØWRIT1	2D8									TTFFO	2G4										
F	ØWIBG1	2D8									TTSRO	2F4										
G	TKCLK1	3E5									TTSFO	2G4										

LOC CODE ELEM ID	IC12 41BP DESIG	SH LOC	IC13 41BP DESIG	SH LOC	IC14 41AE DESIG	SH LOC	IC15 41CC DESIG	SH LOC	IC16 41CK DESIG	SH LOC	IC17 41U DESIG	SH LOC	IC18 41W DESIG	SH LOC	IC19 41W DESIG	SH LOC	IC20 41AD DESIG	SH LOC	IC21 41U DESIG	SH LOC	IC22 41U DESIG	SH LOC	IC23 41CA DESIG	SH LOC
A	RWNC1	2C7	INF1201	3B5	WRENAB	2F1	START	2E1	TUCCØDE	2C1	WWIBGO	3B1	STATC1	3E0	STATA1	3E3	SP3	2A3	MSTP1	2E7	RWD1	3D3	INF0800A	3F3
B	STPØR1	2C7	INF1301	3B6	RDENAB	2F0					WRITEG	3D1	MSTØPO	2F8	RSEAR0	3D3	INF0001	3E8	MSTPO	2E7	CMD010	3B3	INF1100A	3F3
C	SP2	2A2	INITO	2E7							WIBGO	3D0	HILVL1	3E7	STATB1	3E2	INF0201	3E8	RDBLKO	3D1	READO	3D1	INF1000A	3F2
D	RCO	2B0	INIT1	2D7							WWIBG1	3B0	BCKSPO	3D2	FSEAR0	3D2	INF0301	3E9	MSTPØRO	2D7	TINITO	2D7	INF0900A	3F0
E	RC1	2A0	INF0801	3B4																				
F	WRENABO	2G2	MØTCINHO	3B5																				
G	WRITPO	2G2	DECINH1	3E4																				

CAPACITOR

DESIG	CODE
C1	KS-19774 L1,20ΩF
[4] C2-C5	601A,5
[25] C6-C28	KS-19774 L5,0.1

RESISTOR

DESIG	CODE
R1	KS-20616 L1A,10KΩ
R2	KS-20616 L1A,2.2KΩ
R3	KS-20616 L1A,2.2KΩ

CIRCUIT DESCRIPTION:

THIS CIRCUIT PACK IS ONE OF FOUR ASSOCIATED WITH THE CARTRIDGE TAPE TRANSPORT CONTROLLER PORTION OF THE TDC CIRCUIT. IT CONTAINS THE COMMAND DECODER, MOTION CONTROL, TRACK SELECTION, READ AND WRITE ENABLE REGISTERS, PRIMARY STATUS, AND MAINTENANCE CIRCUITRY.

THE 41CK, TUCCØDE, AND ASSOCIATED GATES, INF0001, INF0201, AND INF0301 ARE USED TO RECOGNIZE THE PROPER 3/6 DEVICE CODE AND MINIMIZE LOADING ON THE SPI PARALLEL BUS. USING THE INPUT RCO AS A STROBE, THIS CIRCUITRY INHIBITS ALL COMMANDS WHICH DO NOT CARRY THE PROPER DEVICE CODE. OUTPUTS FROM TUCCØDE STROBE THE 129A COMMAND DECODER. FROM INPUTS INF090, INF100, INF110 AND GATED INF080, THE COMMAND DECODER PULSES ONE OF ITS 16 OUTPUT COMMAND LEADS. THE 16 COMMANDS ARE: READ CONTINUOUS (READCO), READ-ONE-BLOCK (RDBLKCO), WRITE (FEEDS ØWRITI), WRITE IBG (FEEDS ØWIBG1), BACKSPACE (BKSPACO), FAST FORWARD (FSTFØRCO), FAST REVERSE (FSTREVCO), REWIND (TTREWCO), STOP (STØPCO), SET WRITE STOP (SWTSTPCO), RESET WRITE STOP (RWTSTPCO), SHIFT CRC (SCRCCO), CLEAR CRC (CLRCRCCO), STATUS REQUEST (STREQCO), SET MAINTENANCE (MTCSETO), AND RESET MAINTENANCE (MTCRSETO). COMMANDS LISTED ABOVE WHICH REQUIRE ANY OF THE FOLLOWING MOTIONS OF THE TRANSPORT, SLOW FORWARD, SLOW REVERSE, FAST FORWARD, OR FAST REVERSE, FEED FWD1, REV1, FFWD1, AND FREV1, RESPECTIVELY. THE OUTPUTS FROM THESE GATES FEED THE 41CF, MØTREG, WHICH IS THE TAPE MOTION CONTROL REGISTER. THE 41CC, START, WHICH IS TRIGGERED BY TUCCØDE1, STROBES MØTREG APPROXIMATELY 150 ns AFTER DECØDER IS STROBED. THIS DELAY PROVIDES SETUP TIME FOR COMMANDS FROM THE DECODER. THE FOUR MOTION COMMANDS FROM MØTREG ARE BUFFERED BY TTSFO, TTSRO, TTFFO, AND TTFRO, AND FED DIRECTLY TO THE TRANSPORT. A STOP COMMAND OR ALL ONE OF SEVERAL OTHER CONDITIONS ON JK18 WILL CAUSE STØPPO TO PULSE LOW. THE LEADING EDGE OF THIS PULSE CLEARS MØTREG. LEAD INF080 IS USED TO DESIGNATE MOTION COMMANDS. IT MUST BE IN A LOW STATE FOR THE 129A TO DECODE ANY OF THE EIGHT COMMANDS WHICH REQUIRE TRANSPORT MOTION. MØTCINH1 REMAINS IN A HIGH STATE IF ANY OF THE FOUR MOTION STATES ARE SET IN MØTREG. NOTE THAT IF INF080 IS IN A LOW STATE AND MØTCINH1 IS IN A HIGH STATE, THEN DECINH1 WILL BE IN A HIGH STATE WHICH INHIBITS THE 129A DECODER. THIS CIRCUITRY REQUIRES THAT THE TRANSPORT BE IN A STOP STATE BEFORE ANY MOTION COMMAND CAN BE DECODED. THE EIGHT COMMANDS WHICH DO NOT REQUIRE MOTION ARE DECODED IRRESPECTIVE OF THE STATE OF MØTCINH1.

READ AND WRITE OPERATIONS ARE CONTROLLED BY TWO D-TYPE FLIP-FLOPS, RDENAB AND WRENAB, RESPECTIVELY. NOTE THAT ALL COMMANDS REQUIRING SLOW FORWARD MOTION OF THE TRANSPORT (HIGH STATE OF FWD1) WILL SET RDENAB.

A SHIFT CRC COMMAND ALSO SETS RDENAB. A WRITE OR WRITE IBG COMMAND, WHICH SETS BOTH RDENAB AND WRENAB TO CREATE A READ AFTER WRITE CONDITION, IS GATED THROUGH WRITAO OR WIBGAO BY TKAP1. TKAP1 IS ACTIVE, OR IN A HIGH STATE, WHEN EITHER WETRK10 OR TKAO IS IN A LOW STATE. LEAD WETRK10 REMAINS HIGH TO WRITE PROTECT INFORMATION ON THE TAPE PROGRAM TRACK, TRACK 1. TKAO IS IN A HIGH STATE ONLY WHEN BOTH INF120 AND INF130 ARE IN HIGH STATES. A HIGH STATE ON BOTH INF120 AND INF130 INDICATE THE SELECTION OF TRACK 1.

GATED INF120 AND INF130 FEED TKADA AND TKADB, RESPECTIVELY. THESE TWO D-TYPE FLIP-FLOPS CONTROL TRACK SELECTION USING A 2-BIT BINARY CODE. NOTE THAT TKINHO ONLY ALLOWS A CHANGE IN TRACK SELECTION WHEN THE TRANSPORT IS IN A STOP STATE, THAT IS, MØTCINH1 IS IN A LOW STATE.

PRIMARY STATUS OF THE CONTROLLER IS GENERATED BY A DISCRETE GATE ENCODER. SEVEN OF THE MOTION COMMANDS, WRITE IBG, WRITE, READ-A-BLOCK, READ, FAST FORWARD, BACKSPACE, AND FAST REVERSE ARE SELECTED BY GATES WIBGO, WRITEG, RDBLKO, READO, FSEARO, BCKSPO, AND RSEARO, RESPECTIVELY. INPUTS BLØCK1 AND BLØCKO ARE THE OUTPUT OF A FLIP-FLOP LOCATED ON JK18 WHERE BLØCK1 IS IN A HIGH STATE WHEN ONLY ONE BLOCK IS TO BE READ, THAT IS A READ-A-BLOCK OR A BACKSPACE COMMAND HAS BEEN ISSUED. (BACKSPACE IS ESSENTIALLY A REVERSE READ OPERATION, EXCEPT THAT NO DATA IS TRANSFERRED.) OUTPUTS FROM THESE SEVEN GATES, WHICH INDICATE THE OPERATING MODE OF THE TRANSPORT, SELECTIVELY FEED STATC1, STATB1, AND STATA1. THE OUTPUTS OF THESE THREE GATES GENERATE A 3-BIT CODE WHICH IS GATED THROUGH INF0900A, INF1000A, AND INF1100A BY PSTAT1. OUTPUTS FROM THESE THREE GATES RECREATE THE ORIGINAL STATES, ON I/Ø LEADS INF090, INF100, AND INF110, WHICH WERE PRESENT WHEN THE COMMAND WAS ISSUED FROM THE PARALLEL BUS. PSTAT1 IS ACTIVE OR IN A HIGH STATE WHEN THE BUS IS PREPARED TO RECEIVE A REPLY FROM THE CONTROLLER.

CMD010 INDICATES WHETHER THE CONTROLLER IS IN A STOP STATE. ITS OUTPUT IS IN A HIGH STATE WHENEVER THE TRANSPORT IS EITHER STOPPED OR IN THE PROCESS OF STOPPING. WHEN A STOP COMMAND IS ISSUED, INPUT IBGSTPO, WHICH IS AN OUTPUT FROM JK18, GOES LOW IMMEDIATELY AND REMAINS LOW UNTIL STØPPO GOES LOW. STØPPO GOES LOW ONLY AFTER THE TRANSPORT HAS REACHED AN IBG ON THE TAPE AND WILL REMAIN LOW UNTIL THE TRANSPORT HAS REACHED A COMPLETE STOP, APPROXIMATELY 40 ms. AS STATED EARLIER, THE LEADING EDGE OF STØPPO CLEARS MØTREG WHICH CAUSES MØTCINH TO GO LOW. THE OUTPUT OF CMD010 WILL REMAIN IN A HIGH STATE UNTIL MØTCINH IS DRIVEN HIGH AGAIN.

SINCE A REWIND OPERATION OF THE TRANSPORT IS NOT NORMALLY STOPPED BY THE CONTROLLER, PROVISIONS WERE MADE IN THE STATUS CIRCUITRY WHERE RWDINGBO, CAN OVERRIDE THE HIGH (OR STOP) STATE OF CMD010. IF THE TRANSPORT IS IN A REWIND MODE, RWDINGBO WILL BE IN A LOW STATE. THIS CAUSES THE OUTPUT OF RWD1 TO GO HIGH, IRRESPECTIVE OF THE STATE OF CMD010. GATING OF RWD1 THROUGH INF0800A COMPLETES THE 4-BIT CODE WHICH INDICATES THE FUNCTIONAL STATUS OF THE TRANSPORT.

THE OUTPUT OF TKADA FEED INF1200A AND INF1400A, AND THE OUTPUT OF TKADB FEED INF1300A AND INF1500A. THESE FOUR GATES INDICATED THE STATUS OF THE TRACK SELECTED WHEN PSTAT1 IS ACTIVE. INF0000, INF0200, AND INF0300 ARE DRIVEN LOW BY LBEN1 WHENEVER A STATUS IS REQUESTED FROM THE TAPE CONTROLLER. THESE GATES DRIVE I/Ø LEADS INF000, INF020, AND INF030, WHICH ARE THE ACTIVE 3 BITS OF THE TAPE CONTROLLER'S 3/6 DEVICE CODE.

INPUT IKITO IS USED TO INITIALIZE THE CONTROLLER CIRCUITS. THE OUTPUT FROM INITO RESETS THE TTL AND GATE FLIP-FLOPS, MAINT1/MAINTO AND MSTP1/MSTPO, RESETS RDENAB AND WRENAB AND CLEARS MØTREG WHICH COMPLETES INITIALIZATION OF JK16. MAINT1/MAINTO CAN BE SET BY EITHER A MAINTENANCE COMMAND (PULSE ON MTCSETO) OR A MAINTENANCE STOP COMMAND (PULSE ON STØPCO WHILE INF070 IS IN A LOW STATE). IT IS RESET BY EITHER A RESET MAINTENANCE COMMAND (PULSE ON MTCRSETO) OR A PULSE ON INITO. A LOW LEVEL FROM MAINTO GATED THROUGH SLTO WILL INHIBIT ALL INPUTS TO THE TRANSPORT AND UNDER NORMAL OPERATING CONDITIONS, CAUSE THE TRANSPORT TO STOP. SETTING OF THIS MAINTENANCE FLIP-FLOP ALSO ALLOWS THE CONTROLLER TO BE EXERCISED WITHOUT USE OF THE TRANSPORT. THE SECOND MAINTENANCE FLIP-FLOP MSTP1/MSTPO CAN BE SET BY A MAINTENANCE STOP COMMAND AND RESET BY EITHER A RESET MAINTENANCE COMMAND OR A PULSE ON INITO. WHEN MSTP1 IS IN A HIGH STATE, TTMSTPO GOES LOW. THIS WILL DISABLE THE TRANSPORT AND STOP ANY MOTION, IRRESPECTIVE OF WHAT MODE OF OPERATION THE TRANSPORT WAS IN. (A HIGH LEVEL ON TTSELO WILL NOT STOP A REWIND SEQUENCE.) A LOW PULSE FROM TTNITO WILL INITIALIZE THE TRANSPORT TO THE LOAD POINT ON TAPE. THIS PULSE IS THE COMBINATION OF A LOW LEVEL ON INPUT INF070 AND A LOW PULSE ON MTCRSETO (RESET MAINTENANCE COMMAND).

ISSUE 2A

JK16 CIRCUIT PACK (2)

CPS-JK16 SHEET 4

BELL TELEPHONE LABORATORIES INCORPORATED 6S

CPS-JK16