

CABLE TESTING - GENERAL
GENERAL RULES FOR USING
IDENTIFICATION TONE

NOTES CONCERNING THIS ADDENDUM

This addendum supplements Section G50. 210. 1 and is being issued to provide additional information relative to the identification of circuits used for bell and lights and air raid siren warning systems.

The following paragraphs should be marked "See Addendum" and treated as indicated.

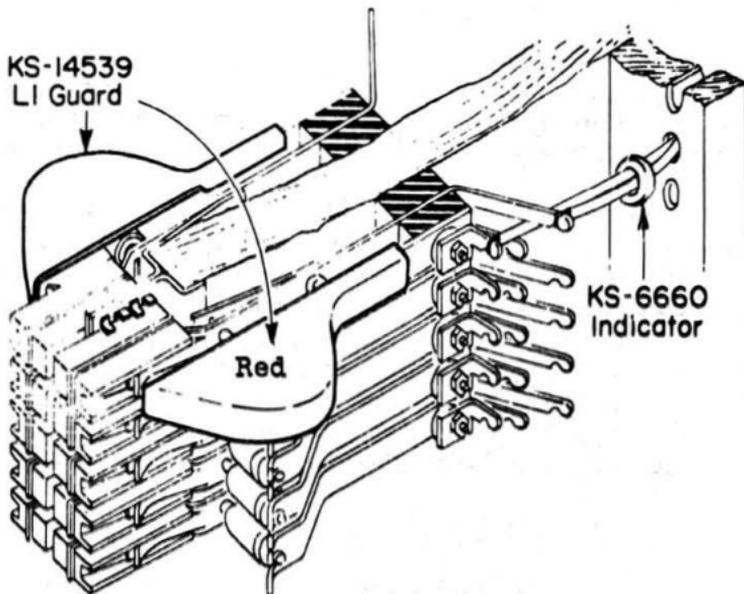
- Paragraph 3. 08 - Supplemented
- Paragraph 3. 09 - Added
- Paragraph 3. 10 - Added
- Paragraph 5. 01 - Supplemented

3. PRECAUTIONS

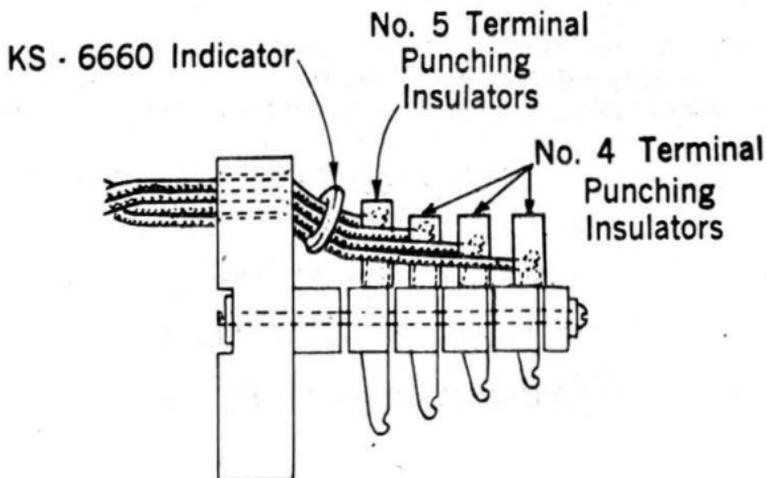
3. 08 SSM (Special Safeguarding Measures) Circuits: Cable conductors so designated for the federal government, armed forces, and certain special services including bell and lights and air raid siren warning systems will be given special safe guarding treatment as determined by the commercial department. These circuits are of extreme importance to the welfare of the nation; therefore, momentary shorts, crosses, opens, or grounds on the conductors should be guarded against in order to prevent serious service reactions.

3. 09 Pairs or quads used for such circuits are specially marked and guarded at central office main frames, as illustrated below, to provide maximum protection from accidental contact.

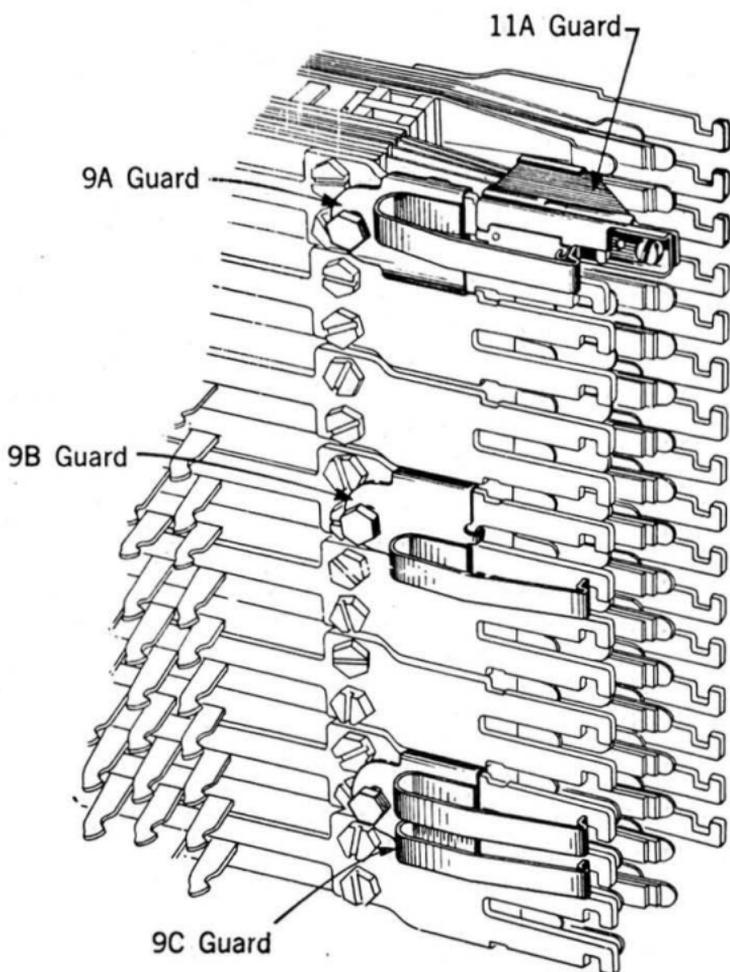
(a) Protector Type Terminations



(b) Terminal Strip Terminations



(c) Terminals at 444 Jacks.



3.10 At cable terminals, all terminating lugs on which "SSM" circuits appear shall be marked with an important service tag in addition to the binding post insulators that are required for special circuits, as specified in other sections of the practices.

5. AUDIBLE TONE FROM A TERMINATION

5.01 Test sets or receivers shall not be connected to terminations associated with "SSM" protected lines until a release is obtained from the controlling test center. Cable pairs in cables

or complements containing "SSM" protected lines as indicated by the test center transfer sheet or work prints shall be identified in strict accordance with instructions for identifying special circuits as covered in Part 5 and Part 6 of this section.