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COVER SHEET FOR TECHNICAL MEMORANDUM

TITLE— Design of a Modified Raised Cosine Filter
Using Hybrid Integrated Filter Sections

MM 67—5316—6

CASE CHARGED— 38763

DATE— September 14, 1967

FILING CASE#— 38763-17

AUTHOR— M. S. Lane

FILING SUBJECTS— Active Filters
Hybrid Integrated Circuit

ABSTRACT

Synthesis of a modified raised cosine filter using hybrid integrated filter sections consisting of tantalum thin film RC networks and monolithic silicon integrated operational amplifiers is examined. The filter function is derived from a discrete passive RLC filter and the filter is realized by a cascade of first and second order active networks. The filter was built and tested; the results show that the active filter meets the specifications for the passive filter. This memorandum describes the derivation and synthesis of this filter and also includes the results of the measurements made on this active filter. The work described in this memorandum was done by the author while on a Graduate Study Program rotational assignment in Department 5316.

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**BELL TELEPHONE LABORATORIES
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SUBJECT: Design of a Modified Raised Cosine Filter
Using Hybrid Integrated Filter Sections
Case 38763-17

DATE: September 14, 1967

FROM: M. S. Lane

MM67-5316-6

MEMORANDUM FOR FILE

Introduction

Filters with modified raised cosine response find wide use in data communication systems. One such application is in the receiver of the 304 Data Set. In order to reduce the relatively large proportion of the overall equipment volume of the 304 Data Set occupied by the use of passive RLC filters, a modified raised cosine filter was designed using hybrid integrated filter sections consisting of tantalum thin film RC networks and monolithic silicon integrated operational amplifiers.

The filter design is based on the passive RLC modified raised cosine filter shown in Figure 1. The design procedure used is to determine a transfer function from this network using the IBM 7094 and network synthesis techniques. Then the filter transfer function is broken up into a product of first and second order transfer functions. These are then realized using a basic second order filter section.

The basic second order filter section used is the "Active RC Filter Building Block using Frequency Emphasizing Networks" described by G. S. Moschytz.¹ This network offers the economical advantages of circuit standardization and the advantages of a low sensitivity network. The building blocks also possess inherent isolating characteristics so that there is negligible interaction between them.

This memorandum describes the derivation of the transfer function of the modified raised cosine filter and the application of the second order building blocks to realize this transfer function.

Modified Raised Cosine Function

The in-band amplitude-frequency characteristic of a f_0 kilo-bit modified full raised-cosine roll-off filter is:

$$W(f) = \frac{\left(\frac{\pi f}{f_0}\right)}{\sin\left(\frac{\pi f}{f_0}\right)} \cos\left[\frac{\pi f}{2f_0}\right], \quad 0 < f < f_0 \text{ KHz}$$

Using trigonometric identities, this function is reduced to:

$$W(f) = \left(\frac{\pi f}{2f_0}\right) \cdot \cot\left(\frac{\pi f}{2f_0}\right), \quad 0 < f < f_0 \text{ KHz}$$

The in-band and out-of-band specifications for a 36 KB modified full raised cosine roll-off filter for the 304 Data Set are given in Figure 2.

Transfer Function

With frequency scaling from 40.8 kHz to 36 kHz, the S-plane plots in Figures 3 and 4 will have a transfer function which satisfies the specifications for the modified full raised cosine roll-off filter. These S-plane plots are derived by determining the transfer function of the 40.8 KB RLC filter in Figure 1 and then using the IBM 7094 to modify this transfer function.

The RLC filter in Figure 1 consists of four "constant-resistance" sections in cascade. Because of the "constant-resistance" property, the loaded transfer function of this RLC filter is the product of the loaded transfer functions of the cascaded constant-resistance sections.

$$T(s) = T_1(s) \cdot T_2(s) \cdot T_3(s) \cdot T_4(s)$$

For ease in determining the transfer function of these sections, the mutual inductance in the second and fourth sections is converted into an equivalent inductance. The resulting second and fourth section of the filter are then converted to simple lattice networks using the conversion techniques given in Appendix A. The application of impedance normalization and frequency scaling then results in the network shown in Figure 5.

The loaded transfer function of the first section in Figure 5 is determined from the network schematic using the IBM 7094.² This transfer function is:

$$T_1(s) = \frac{.7197(10^{-2})s^4 + .1896s^2 + 1.0}{.03525s^5 + .2396s^4 + .8112s^3 + 1.7403s^2 + 2.333s + 2.0} , f_o = 40.8$$

The loaded transfer functions for a symmetrical lattice and for a bridge-tee are calculated in Appendix B and Appendix C, respectively. Applying the results of these calculations, the loaded transfer functions for the second, third, and fourth sections in Figure 5 are:

$$T_2(s) = \frac{-1(s-1.081)}{(s+1.081)} , f_o = 40.8$$

$$T_3(s) = \left[\frac{.2130s^3 + .8816s^2 + 1.3479s + 2.8501}{.3941s^3 + .9627s^2 + 2.3534s + 3.3003} \right] , f_o = 40.8$$

$$T_4(s) = \left[\frac{s^2 - 1.750s + 2.360}{s^2 + 1.750s + 2.360} \right] , f_o = 40.8$$

The poles and zeroes of the transfer function of the passive RLC filter are determined by factoring the polynomials of the preceding transfer functions on the GE computer.³ A study of these poles and zeroes shows that the second and fourth sections are nonminimum phase all-pass sections while the first and third sections are minimum phase sections which determine the

amplitude response of the filter. S-plane plots of the nonminimum phase sections and of the minimum phase sections are given in Figures 6 and 7.

The closeness of some poles & zeroes in Figure 7 indicated it might be possible to cancel them. Using the s-plane plot of the minimum phase sections as a basis, additional s-plane plots with an acceptable amplitude response function are calculated on the IBM 7094.⁴ The resulting simplified s-plane plot for the minimum phase portion of the response function is shown in Figure 4. Minimum phase functions which meet amplitude response requirements will also meet phase requirements; therefore, there is no need to alter the transfer function of the all-pass sections. The final s-plane plots shown in Figures 3 and 4 are used as the basis for the synthesis of the hybrid integrated filter.

Synthesis Techniques

The use of hybrid integrated circuits dictates constraints on the filter design in addition to the system specifications in Figure 2. Additional factors, which must be considered, are:

- A. Sensitivity
- B. Stability
- C. Signal Level
- D. Impedance Level
- E. Size
- F. Power Supply Voltage
- G. Power Dissipation
- H. Component Values

A synthesis system for development of multipole active RC networks using less sensitive two-pole networks is used because network sensitivity increases rapidly with the order of the network. Also, active linear networks are potentially unstable inasmuch as their transmission poles are not inherently constrained from crossing the $j\omega$ -axis into the right-half plane. Thus, higher order networks are more difficult to stabilize.⁵ It has become

common practice in active filter design to realize any given Nth order transmission function by a cascade of first and second order networks, building blocks. For ease of implementation it is desirable that these building blocks possess inherent isolating characteristics so that there is negligible interaction between them.¹

The design of a second order hybrid integrated filter building block consisting of tantalum thin film RC networks and semiconductor operational amplifiers has been performed by Department 5316. This building block, shown in Figure 8, contains two stabilized operational amplifiers, a twin-T network, and an RC filter network. Use of this building block takes advantage of the work already done by Department 5316 to stabilize and desensitize it; plus it has inherent isolating characteristics so that there is negligible interaction between building blocks in cascade.⁵ Additional parameters considered in the use of the building block are given in Figure 9.

Using the building block with some modifications, a variety of synthesis techniques for second order networks is available. Second order transfer functions with their poles and zeroes in the left-half plane are realized using the frequency emphasizing networks shown in Figures 10 and 11.¹ First and second order all pass filter sections are available using the networks shown in Figures 12 and 13.* Second order transfer functions with poles in the left-half plane and zeroes on the $j\omega$ -axis are realized using the frequency

*These configurations were suggested by W. Thelen.

rejection network in Figure 14.¹ Additional synthesis techniques applicable to the building block are shown in Figure 15.⁸ But these synthesis techniques are only applicable provided the calculated component values are within the range specified for the building block.

Filter Network

The Nth order transfer function of the s-plane plots in Figures 3 and 4 is realized by a cascade of first and second order networks. For low sensitivity, the transfer function is decomposed in Figures 16 and 17 into a product of second order functions whose complex poles are associated with zeroes in such a way that the distance from the upper half plane complex pole to the respective zeroes are made as far self-distant for each sub-network as possible.⁶ The resulting 40.8 KB and normalized first and second order transfer functions for the nonminimum phase sections are:

$$T_2(s) = \left[\frac{s^2 - 1.7544(10^5)s + 2.3602(10^{10})}{s^2 + 1.7544(10^5)s + 2.3602(10^{10})} \right], f_o = 40.8$$

$$T_2(s) = \left[\frac{s^2 - 4.300s + 14.170}{s^2 + 4.300s + 14.170} \right], f_o = 1.0(10^{-3})$$

and

$$T_4(s) = -1 \left[\frac{s - 1.0810(10^5)}{s + 1.0810(10^5)} \right], f_o = 40.8$$

$$T_4(s) = -1 \left[\frac{s - 2.647}{s + 2.647} \right], f_o = 1.0(10^{-3})$$

The first and second order transfer functions for the minimum phase sections are:

$$T_A(s) = \frac{s^2 + 114.43}{s^2 + .7042(10^5)s + 5.0735(10^{10})}, \quad f_o = 40.8$$

$$T_B(s) = \frac{s^2 + 114.43}{s^2 + 1.726s + 30.479}, \quad f_o = 1.0(10^{-3})$$

$$T_B(s) = \frac{s^2 + 7.2943(10^{10})}{s^2 + 2.3235(10^5)s + 2.6324(10^{10})}, \quad f_o = 40.8$$

$$T_B(s) = \frac{s^2 + 43.810}{s^2 + 5.695s + 15.814}, \quad f_o = 1.0(10^{-3})$$

$$T_C(s) = \frac{10^5}{s + 3.0089(10^5)}, \quad f_o = 40.8$$

$$T_C(s) = \frac{2.451}{s + 7.375}, \quad f_o = 1.0(10^{-3})$$

By frequency scaling for the required f_o (36 kHz), second order transfer functions are obtained which meet the filter specifications as verified by the amplitude response plotted in Figure 18. The nonminimum transfer functions $T_2(s)$ and $T_4(s)$ are realized using the synthesis technique shown in Figures 12 and 13. Similarly, the transfer functions $T_A(s)$ and $T_B(s)$ are obtained using the frequency rejection network shown in Figure 14 and the transfer function $T_C(s)$ is realized using the network in Figure 15C. These sections are then cascaded as shown in Figure 19 to produce a modified raised cosine filter.

Considering the limits imposed on the operational amplifier offset voltage and gain bandwidth product and on building block component values, calculated component values for the modified raised cosine filter for the

304 Data Set are given in Figure 20. A study of Figure 20 also indicates that with a slight modification of the presently available second order building block and the use of some discrete components the filter can be realized with three building block sections. Suggested modifications in the building block to include all the configurations required for the raised cosine filters are shown in Figure 21. Because of the limits imposed by the operational amplifier slew rate, the sections are cascaded with the frequency rejection networks, which effectively serve as low pass sections, followed by the all pass sections with loss and finally the last section with an adjustable gain.

To check the amplitude and phase response of this cascaded network, a computer program, ECAP, was run on the 7094. The model of the network used and the calculated amplitude and phase response are shown in Figures 22 to 24. Also, a breadboard model was constructed using discrete components and integrated operational amplifiers.⁷ The filter was built and tuned in five separate sections. The components in each section were adjusted to give the proper frequency response for the section, as shown in Figures 25 to 30. The five sections were then cascaded and the amplitude and phase response measured. The measured amplitude and phase response are plotted in Figures 31 and 32. Additional characteristics of the breadboard model are given in Figure 33. The measured and computed amplitude and phase response are well within the limits set for the 304 Data Set and the measured values of offset voltage, signal level, noise level, and power dissipation indicate that this filter should be acceptable for use in the 304 Data Set.

Conclusion

The results of this memorandum indicate that the modified raised cosine filter can be synthesized using a slightly modified version of the second

order filter section developed by Department 5316. Use of the second order filter section offers the advantages of a low sensitivity network plus the economical advantages of circuit standardization.

Acknowledgments

Mr. G. S. Moschytz's supervision of my rotational assignment is appreciated. In addition, the contributions and assistance of W. H. D'Zio in turning the breadboard model of the filter are acknowledged.

M. S. Lane

M. S. LANE

HO-5316-MSL-EMB

Att.

References

Appendices A-C

Figures 1 - 33

App A - fig 1
App B - fig 2

Appendix A

Lattice Conversion Techniques

For the symmetrical lattice network in Figure 1A, the open-circuit impedance parameters are

$$\underline{Z}_L = \begin{bmatrix} .5(Z_b + Z_a) & .5(Z_b - Z_a) \\ .5(Z_b - Z_a) & .5(Z_b + Z_a) \end{bmatrix}$$

For the symmetrical T in Figure 1B, the open-circuit impedance parameters are

$$\underline{Z}_T = \begin{bmatrix} (Z_1 + Z_2) & (Z_2) \\ (Z_2) & (Z_1 + Z_2) \end{bmatrix}$$

For the symmetrical T in Figure 1B to be equivalent to the lattice in Figure 1A the open-circuit impedance parameters must be equal. This condition is met if

$$\begin{aligned} Z_1 &= Z_a \\ Z_2 &= .5(Z_b - Z_a) \end{aligned}$$

The short-circuit admittance parameters for the lattice network in Figure 1C are

$$\underline{Y}_L = \begin{bmatrix} .5(Y_b + Y_{a1} + Y_{a2}) & .5(Y_b - Y_{a1} - Y_{a2}) \\ .5(Y_b - Y_{a1} - Y_{a2}) & .5(Y_b + Y_{a1} + Y_{a2}) \end{bmatrix}$$

$$\underline{Y}_L = \begin{bmatrix} .5(Y_b + Y_{a1}) + .5 Y_{a2} & .5(Y_b - Y_{a1}) - .5 Y_{a1} \\ .5(Y_b - Y_{a1}) - .5 Y_{a2} & .5(Y_b + Y_{a1}) + .5 Y_{a1} \end{bmatrix}$$

$$\underline{Y}_L = \begin{bmatrix} Y_{11}' + Y_{11}'' & Y_{12}' + Y_{12}'' \\ Y_{12}' + Y_{12}'' & Y_{11}' + Y_{11}'' \end{bmatrix}$$

Appendix A - 2

Considering the primed and double-primed parameters as referring to two parallel networks the equivalent parallel connection of these networks corresponds to the bridged T network shown in Figure 1D.

Similarly, the open circuit impedance parameters for the lattice network in Figure 1E are

$$\underline{Z}_L = \begin{bmatrix} .5(Z_{b1} + Z_{b2} + Z_a) & .5(Z_{b1} + Z_{b2} - Z_a) \\ .5(Z_{b1} + Z_{b2} - Z_a) & .5(Z_{b1} + Z_{b2} + Z_a) \end{bmatrix}$$

$$\underline{Z}_L = \begin{bmatrix} .5(Z_{b1} + Z_a) + .5 Z_{b2} & .5(Z_{b1} - Z_a) + .5 Z_{b2} \\ .5(Z_{b1} - Z_a) + .5 Z_{b2} & .5(Z_{b1} - Z_a) + .5 Z_{b2} \end{bmatrix}$$

$$\underline{Z}_L = \begin{bmatrix} Z_{11}' + Z_{11}'' & Z_{12}' + Z_{12}'' \\ Z_{12}' + Z_{12}'' & Z_{11}' + Z_{11}'' \end{bmatrix}$$

Considering the primed and double-primed parameters as referring to two networks in a series-series configuration, the equivalent series connection of these networks is shown in Figure 1F.

Finally the combination of all these conversions results in the two equivalent networks shown in Figures 1G and 1H.

Appendix B

The loaded transfer function of the constant resistance lattice in Figure 2B is

$$T(s) = \frac{Z_{21}R}{\Delta Z + Z_{11}R}$$

$$\underline{Z} = \begin{bmatrix} .5(Z_b + Z_a) & .5(Z_b - Z_a) \\ .5(Z_b - Z_a) & .5(Z_b + Z_a) \end{bmatrix}$$

$$\Delta Z = Z_a Z_b = R^2$$

$$T(s) = \frac{.5(Z_b - Z_a)R}{R^2 + .5(Z_b + Z_a)R}$$

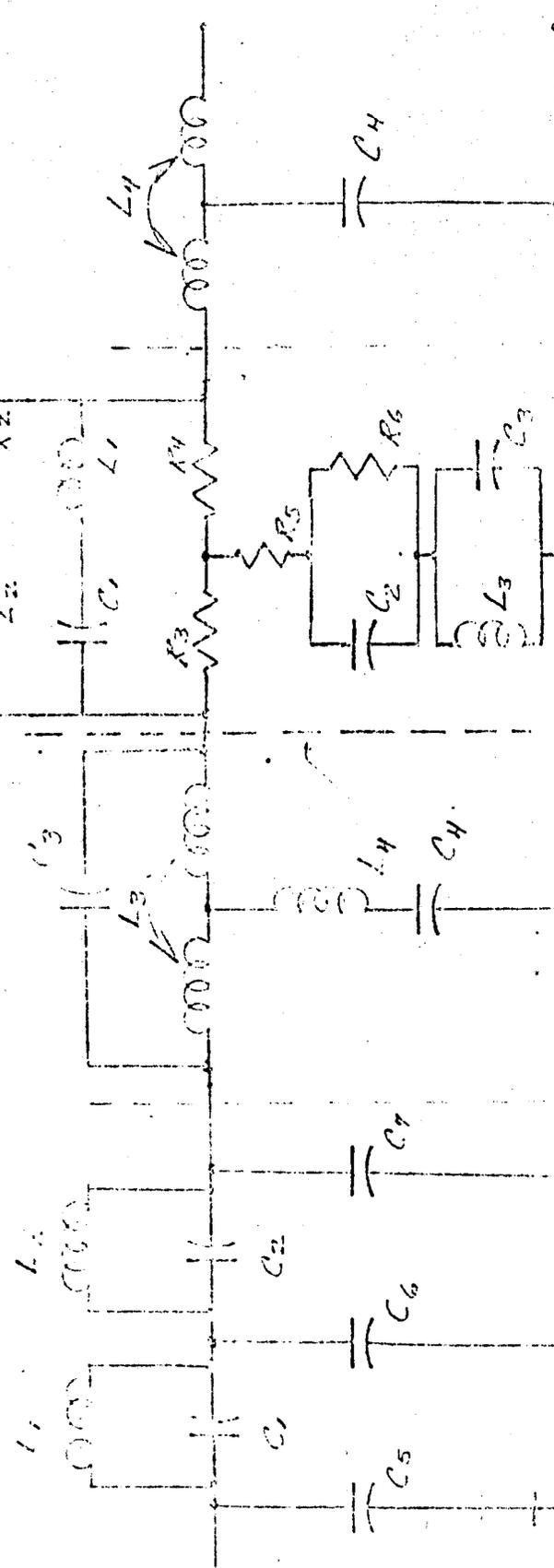
$$T(s) = \frac{.5\left(\frac{R^2}{Z_a} - Z_a\right)}{R + .5\left(\frac{R^2}{Z_a} + Z_a\right)} = \frac{R^2 - Z_a^2}{R^2 + 2RZ_a + Z_a^2}$$

$$T(s) = \left(\frac{R - Z_a}{R + Z_a}\right)$$

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7. Kolibaba, M. C., Medill, D. G., and D'Zio, W. H., "Active Building Blocks Using Frequency Emphasizing Networks," Memorandum for File, Case File 38763, September 11, 1967.
8. O'Neill, J. F., Technical Report 400-159 Active RC Delay Networks Using Operational Amplifiers," New York University School of Engineering and Science, Department of Electrical Engineering, May 1967.

100% Roll off Raised Cosine Filter
 10.8 KB Data



$f_0 = 600 \text{ Hz}$ $f_{\text{band}} = 4018 \text{ kHz}$ $b = 1.75$ $f_{\text{cut}} = 24.5 \text{ kHz}$ $f_{\text{roll}} = 17.1 \text{ kHz}$

- $L_1 = 4.095 \text{ mh.}$
- $L_2 = 2.75 \text{ mh.}$
- $L_3 = 2.92 \text{ mh.}$
- $L_4 = 1.15 \text{ mh.}$
- $C_1 = 1282 \text{ pf}$
- $C_2 = 4695 \text{ pf}$
- $C_3 = 4750 \text{ pf}$
- $C_4 = 24805 \text{ pf}$
- $C_5 = 3728 \text{ pf}$
- $C_6 = 1437 \text{ pf}$
- $C_7 = 1305 \text{ pf}$
- $L_1 = 2.95 \text{ mh.}$
- $L_2 = 2.60 \text{ mh.}$
- $L_3 = 2.194 \text{ mh.}$
- $L_4 = 11.15 \text{ mh.}$
- $C_1 = 6105 \text{ pf}$
- $C_2 = 7470 \text{ pf}$
- $C_3 = 8190 \text{ pf}$
- $C_4 = 30950 \text{ pf}$
- $R_1 = 510 \text{ } \Omega$
- $R_2 = 116.4 \text{ } \Omega$
- $R_3 = 600 \text{ } \Omega$
- $R_4 = 600 \text{ } \Omega$
- $R_5 = 706 \text{ } \Omega$
- $R_6 = 5090 \text{ } \Omega$

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TITLE
 Figure 1
 100% Roll off Raised Cosine Filter

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 16
 NO. OF SHEETS PER SET

5-103

Specifications
for
Modified Raised Cosine Filter
304 Data Set

Amplitude Response (36 KB Data)	0-36 KHz	$\frac{\pi f}{7200} \cos \frac{\pi f}{7200} \pm 0.03$
	36-61 KHz	< - 30 db.
	61-97 KHz	< - 40 db.
	f > 97 KHz	< - 50 db.
Phase Response	0-36 KHz	Linear
Input Impedance	600-Ω	
Output Impedance	600-Ω	
Temperature Range	40° F - 140° F	

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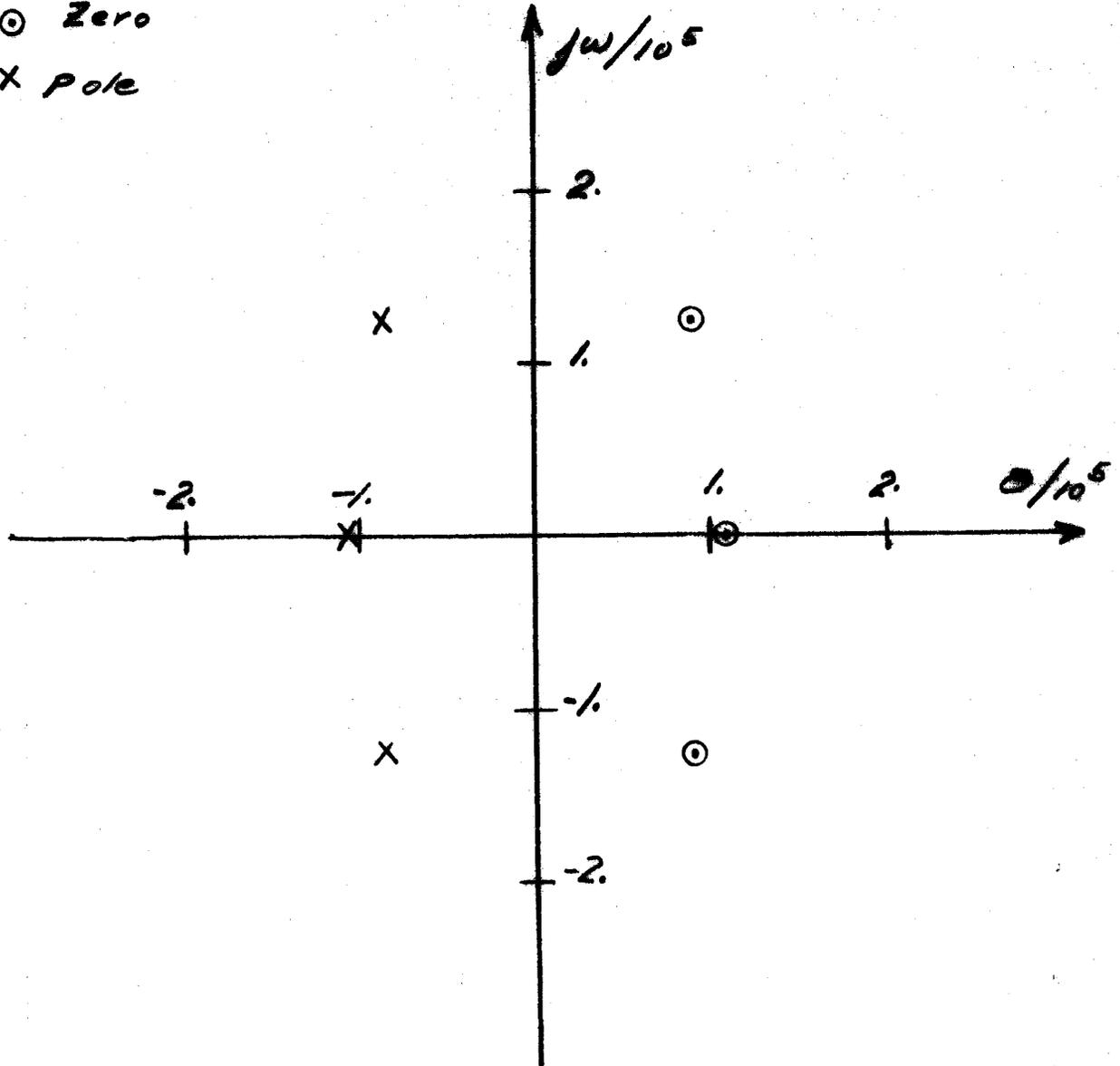
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TITLE
FIGURE 2
Specifications

BELL TELEPHONE LABORATORIES
INCORPORATED
NO. OF SHEETS PER SET
SHEET
17

NONMINIMUM PHASE SECTIONS
 MODIFIED RAISED COSINE FILTER
 40.8 KB Data

⊙ Zero
 × Pole



9/14/67

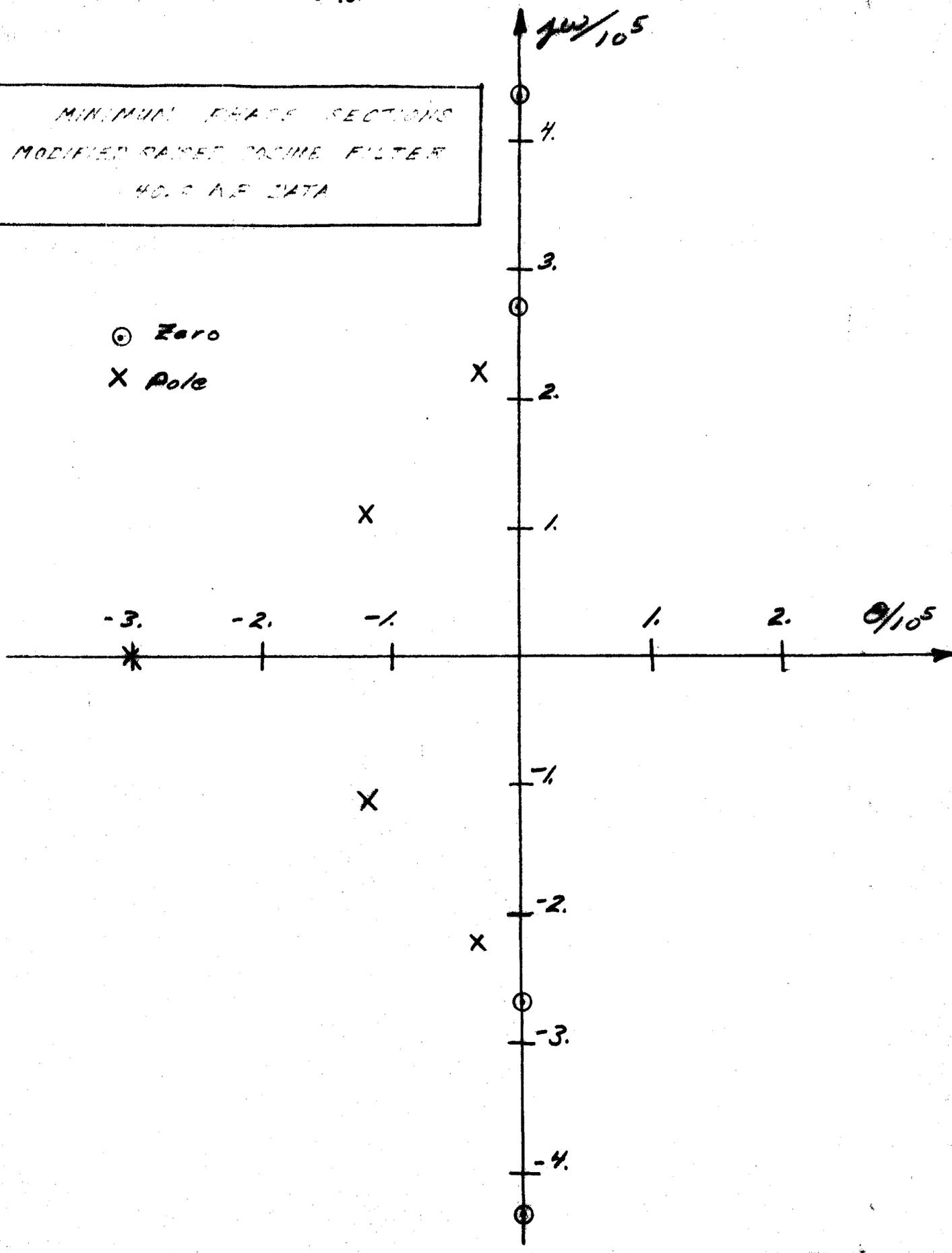
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TITLE
 FIGURE 3

DELL TELEPHONE LABORATORIES
 NO. OF SHEETS PER SET
 18

MINIMUM PHASE SECTIONS
 MODIFIED SAISED COSINE FILTER
 40.0 A.F. DATA

⊙ Zero
 X Pole



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ISSUE 9/14/67	DESIGNED BY M.S. Sch.	TITLE FIGURE 4	BELL TELEPHONE LABORATORIES NO. OF SHEETS PER SET 19
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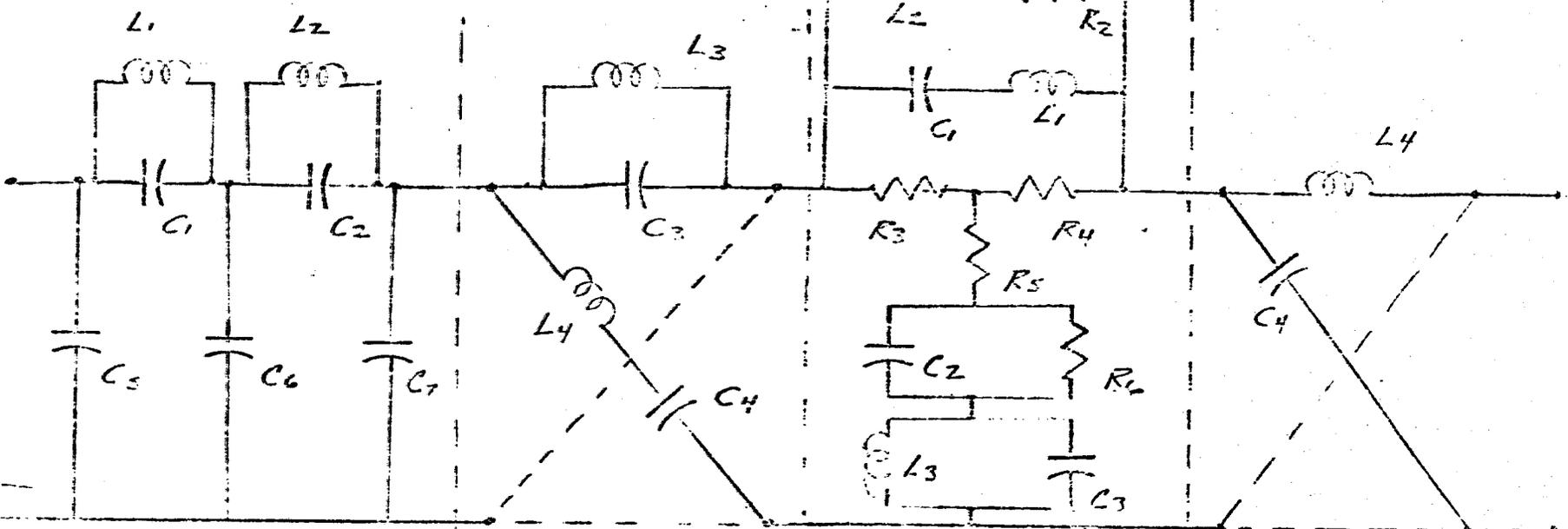
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TITLE
 Figure 5
 NORMALIZED
 RAISED COSINE FILTER

FREQUENCY SCALING: $\omega/10^5$
 IMPEDANCE SCALING: $Z/600$



1st SECTION

2nd SECTION

3rd SECTION

4th SECTION

$L_1 = .6825h$ $C_1 = .0769f$
 $L_2 = .4266h$ $C_2 = .2817f$
 $L_3 = .7433h$ $C_3 = .5400f$
 $L_4 = .5686h$ $C_4 = .7400f$
 $C_5 = .0783f$ $C_6 = .2237f$
 $C_7 = .8622f$

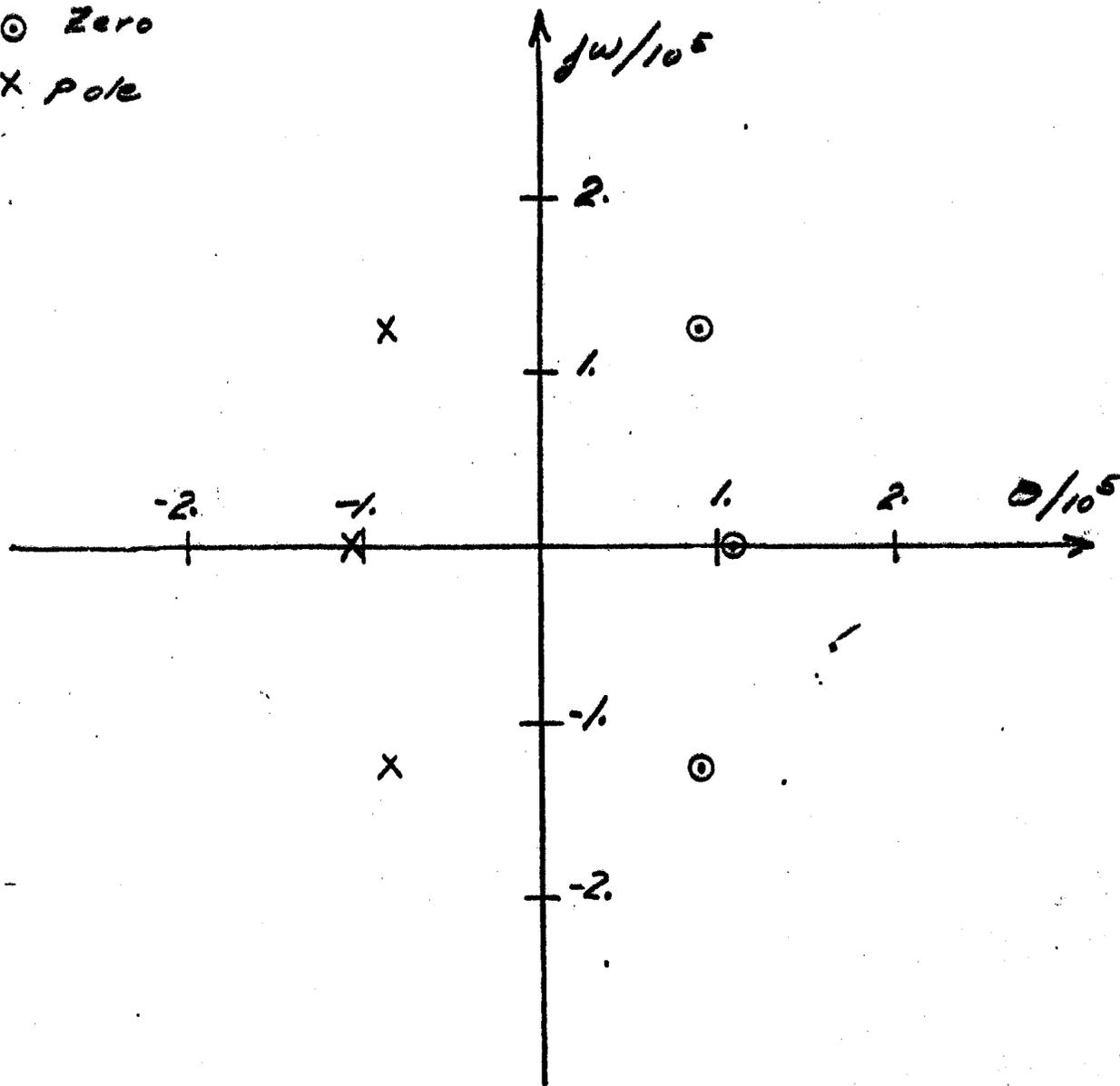
$L_1 = .4917h$ $C_3 = .4914f$ $R_5 = 1.176\Omega$
 $L_2 = .4333h$ $C_4 = .9285f$ $R_6 = 5.150\Omega$
 $L_3 = .3656h$ $R_1 = .8500\Omega$
 $L_4 = .9250h$ $R_2 = .1940\Omega$
 $C_1 = .3663f$ $R_3 = 1.0\Omega$
 $C_2 = .4344f$ $R_4 = 1.0\Omega$

NORMALIZED 100% ROLL OFF RAISED COSINE FILTER
 40.8 KB Data

BELL TELEPHONE LABORATORIES
 INCORPORATED
 SHEET
 90

NONMINIMUM PHASE SECTIONS
 MODIFIED RAISED COSINE FILTER
 40.8 KB Data

⊙ Zero
 × pole



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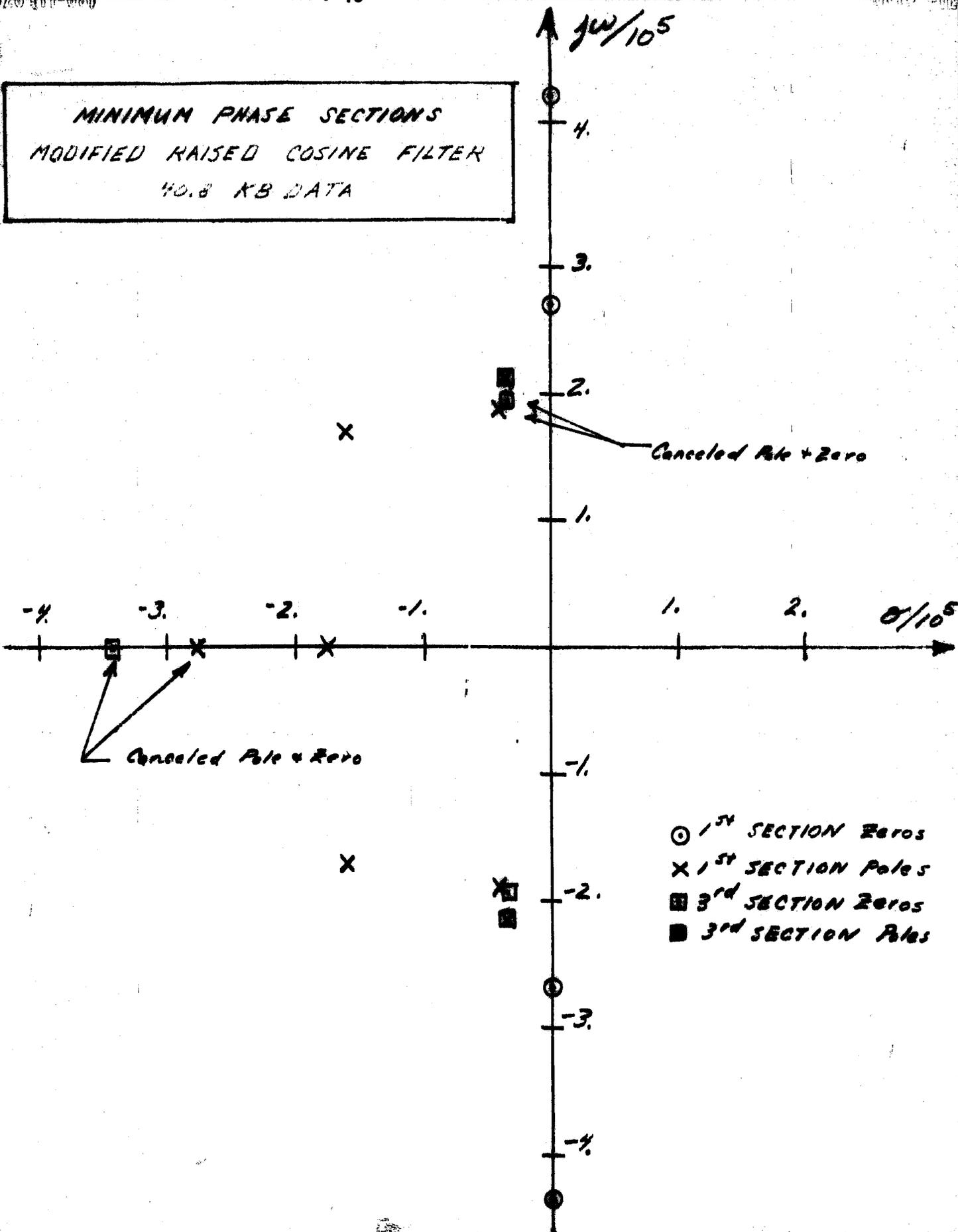
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TITLE
 FIGURE 6

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**MINIMUM PHASE SECTIONS
MODIFIED RAISED COSINE FILTER
40.8 KB DATA**



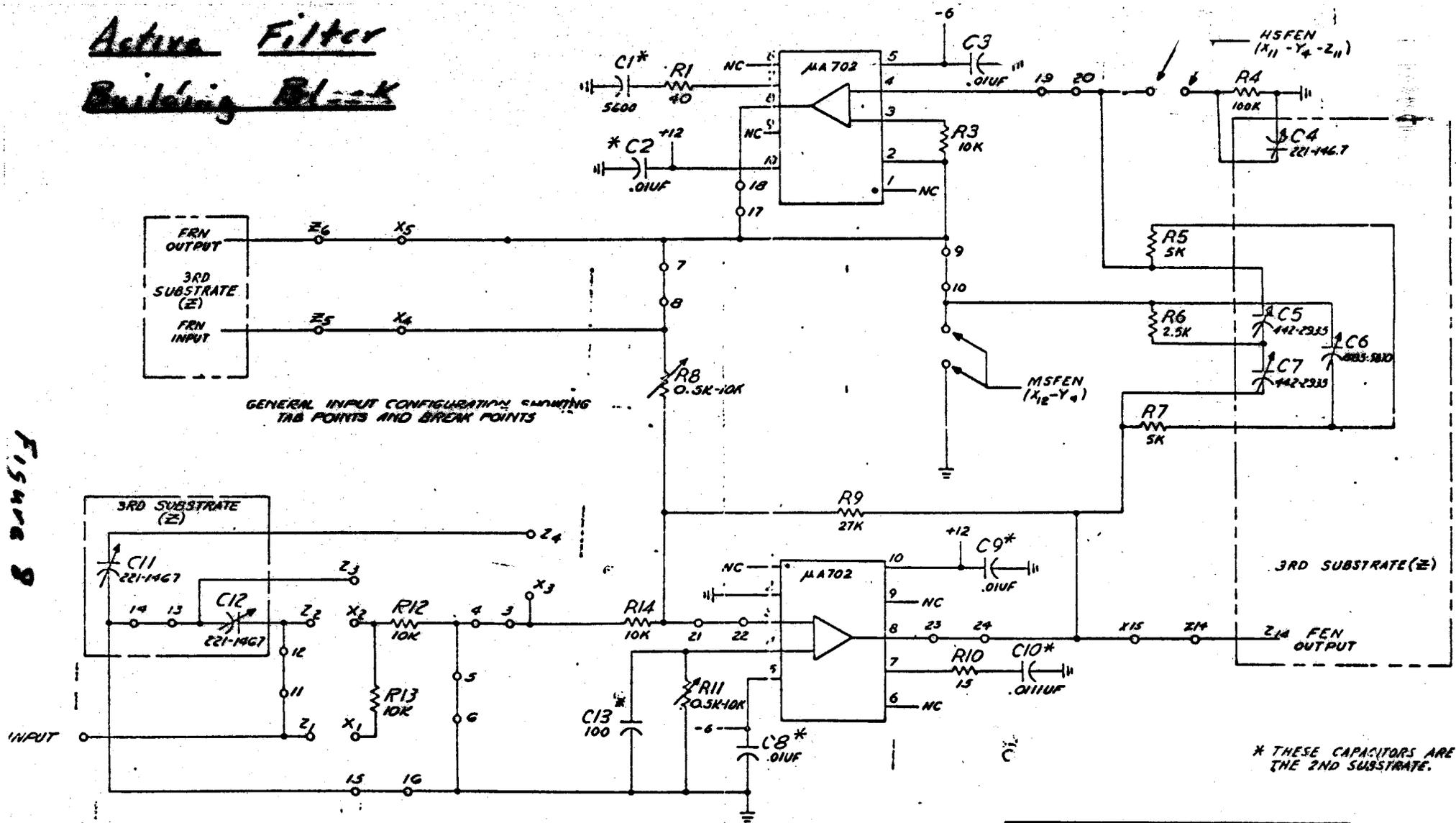
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7.5.6.

FIGURE 7

Active Filter Building Block

Figure 8



NOTES:

- UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN PICO FARADS
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.
- GROUND RETURN
- USE FOLLOWING COMBINATIONS FOR INPUT FILTERS
- ALL CAPACITORS $\pm 10\%$
R1, R4-R10 = 1% UNSHUNTED PORTION OF R8+R11 = $\pm 5\%$
R3, R11-R14 = 5%
R2 NOT TRIMMED
- BREAK NUMBERS 17-18 AND 19-20 EXIST DUE TO THE ABSENCE OF OPERATIONAL AMPLIFIER AND PRESENCE OF BEAM LEADED TRANSISTORS.
- HS - HIGH SELECTIVITY
MS - MEDIUM SELECTIVITY

FILTER	BREAK POINTS	TAB CONN
HPF	3-4, 15-16	Z4-X3, Z3-X2
LPF	11-12, 5+6	Z4-X3, Z1-X1, Z2-X2
BRF	13-14, 5+6	Z4-X3, Z3-X2
RES.	11-12, 5-6, 15-16	Z1-X1, Z4-X3, Z3-X2, GND Z2
FOR FRN BREAK	3-4	Z5-X4

	FEN	FRN
INPUT/OUTPUT OPEN BETWEEN TERMINALS	Z1 - Z14	Z5 / Z6 7 - 8
HS OPERATION OPEN BETWEEN TERMINALS CONNECT BETWEEN TABS	1-2 Z11-Y4 X10	1-2 Z11-Y4 X10
MS OPERATION OPEN BETWEEN TERMINALS CONNECT BETWEEN TABS	1-2 9-10 Y4-X12	

Figure 8

M.S.L.
01/15/60
R3

Building Block Parameters

	Min.	Typical	Max.
Frequency Range	7.2 KHz		72 KHz
Signal Level (Sinusoidal)		.6 Vp-p	1.0 Vp-p
Input off set voltage		2-5 mv	
Positive Supply Voltage		+12.0 v	
Negative Supply Voltage		-6.0 v	
Temperature Range	10°C		70°C
Power Dissipation		270 mw	

Component Values

Resistors	Routine	Available
Range	See Figure 8	
Max R/Substrate	0.5 mΩ	5 mΩ
Initial Precision	0.1%	0.02%
Aging (20 years)	1%	.05%
Temp Coef $\frac{dR/R}{^\circ C}$	-50 to -100 ± 15 $\frac{ppm}{^\circ C}$	0 to -1000 $\frac{ppm}{^\circ C}$
Tunability	+20% ± 1%	+50% ± .1%
Tracking	± 5 ppm/°C	
Capacitors	Au/NiCr/Ta ₂ O ₅ /Ta	
Range	See Figure 8	
Max C/Substrate	.05 μf	
Initial Precision	1-5 %	
Stability (Humidity & aging)	0.2%	
Temp. Coeff. $\frac{dc/c}{^\circ C}$ (25°C-65°C)	+235 ± 20 $\frac{ppm}{^\circ C}$	
Tracking	± 15 ppm/°C	
Dielectric Loss	.003 to .006	
Guaranteed Matching of RC Temp Coeff.		± 25 ppm/°C

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FIGURE 9
Building Block Parameters

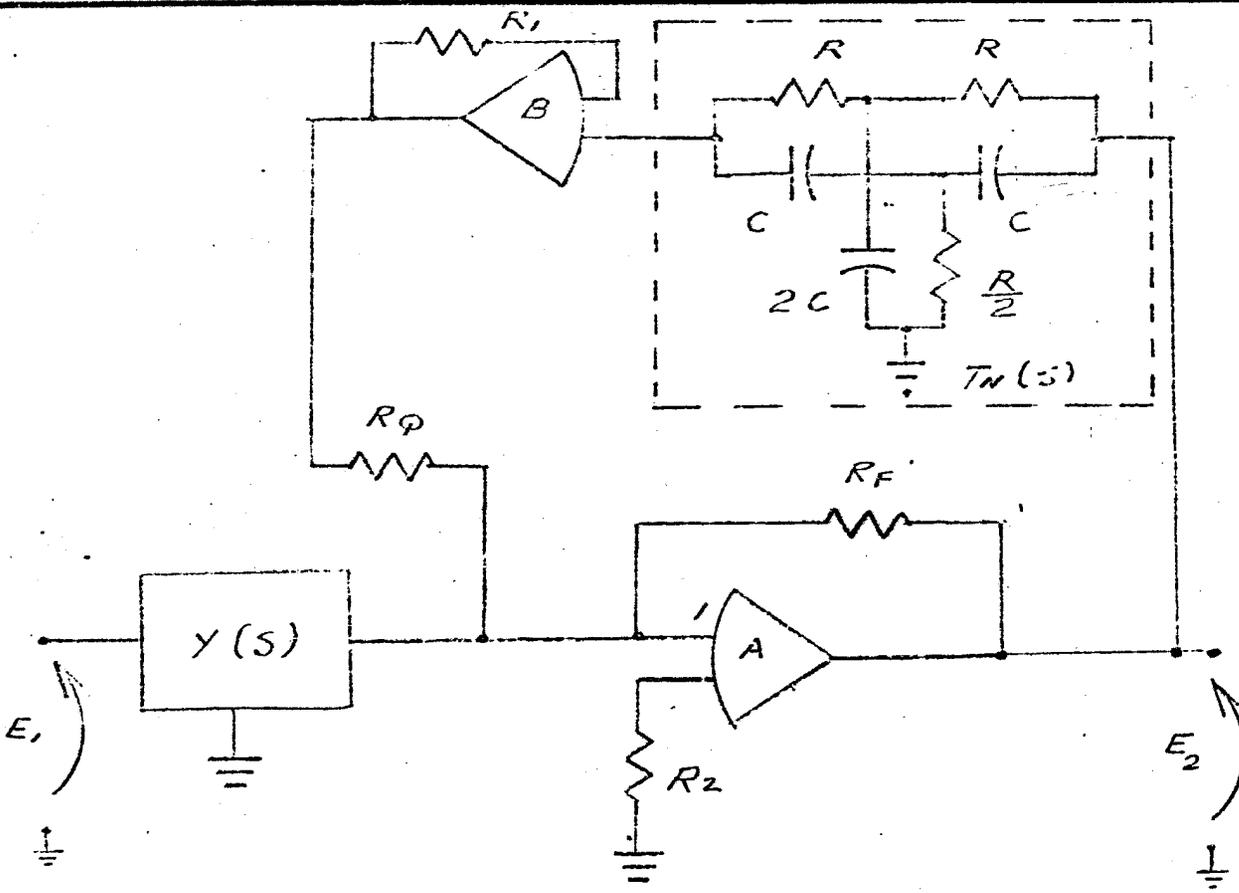
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INCORPORATED

NO. OF SHEETS PER SET

SHEET

24

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FREQUENCY EMPHASIZING NETWORK
BELL TELEPHONE LABORATORIES
INCORPORATED
NO. OF SHEETS PER SET
25 SHEET



* Does not include Amp. biasing circuits

$$\frac{E_2}{E_1} = \frac{-R_F}{1 + \frac{R_F}{R_Q}} \left[Y(s) \frac{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}{S^2 + \frac{\omega_1}{Q_1 (1 + \frac{R_F}{R_Q})} S + \omega_1^2} \right]$$

$$T_N(s) = \frac{S^2 + \omega_1^2}{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}$$

$$\omega_1 = \frac{1}{RC}$$

$$Q_1 = .25$$

$$R_1 = 2R$$

$R_2 =$ Resistance to Grd on Terminal 1 of A op. Amp.

Low Pass Filter:

$$Y(s) = \frac{K_r}{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}$$

High Pass Filter:

$$Y(s) = \frac{K_r \cdot S^2}{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}$$

Band Pass Filter:

$$Y(s) = \frac{K_r \cdot S}{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}$$

Resonator:

$$Y(s) = \frac{K_r (S + \omega_1)}{S^2 + \frac{\omega_1}{Q_1} S + \omega_1^2}$$

Conjugate-Complex Poles & Zeros:

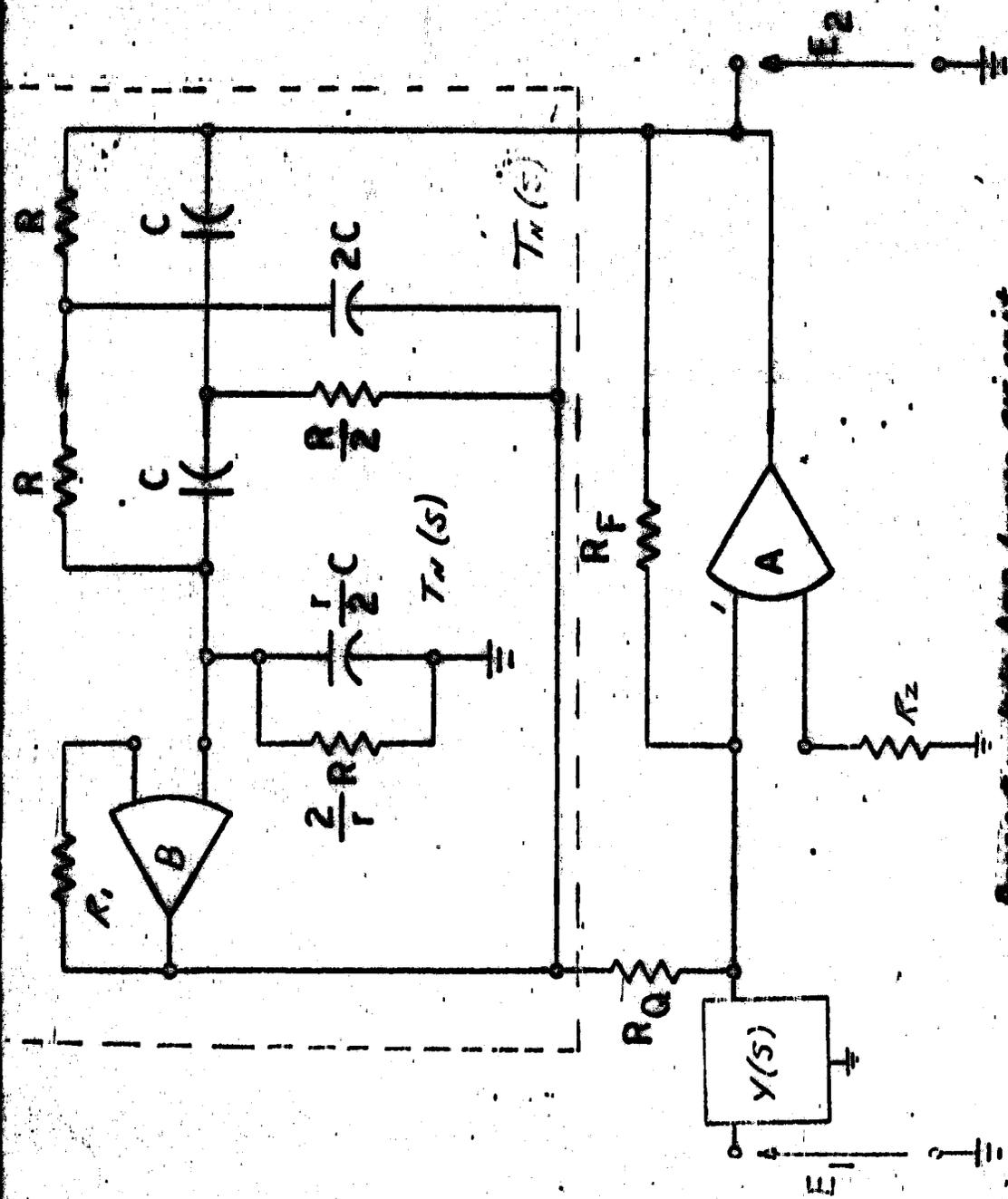
$$Y(s) = \frac{K_r (s^2 + \frac{\omega_1^2}{Q_1^2} s + \omega_1^2)}{(s^2 + \frac{\omega_1}{Q_1} s + \omega_1^2)}$$

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FIG. 1

FIGURE 1
 TRANSDUCER MODELING

ROLL TECHNICAL INFORMATION
 SERIALIZED
 FILED
 26



Does not include Amp. loading circuit

$$\frac{E_2}{E_1} = -Y(S) \left[\frac{R_F}{R_1 + \frac{R_F}{s^2 + \omega_n^2}} \right] \left[\frac{R_2}{R_2 + \frac{R_F}{s^2 + \omega_n^2}} \right] \left[\frac{R_2}{R_2 + \frac{R_F}{s^2 + \omega_n^2}} \right] \left[\frac{R_2}{R_2 + \frac{R_F}{s^2 + \omega_n^2}} \right]$$

$$T_N(S) = \frac{K_{TR}}{s^2 + \omega_n^2}$$

$$K_{TR} = \frac{1}{A}$$

$$\omega_n = \frac{1}{RC}$$

$$R_1 = \frac{1}{2} \left(\frac{R_F}{A} \right)$$

$$R_2 = 2R \parallel \frac{1}{A}$$

R_2 = Resistance to Grid
 on Terminal 1

at A of Amp

Load Res. Effect:

$$Y(S) = \frac{K}{s^2 + \omega_n^2}$$

High Res. Effect:

$$Y(S) = \frac{K}{s^2 + \omega_n^2}$$

Load Res. Effect:

$$Y(S) = \frac{K}{s^2 + \omega_n^2}$$

Remember:

$$Y(S) = \frac{K}{s^2 + \omega_n^2}$$

Remember:

$$Y(S) = \frac{K}{s^2 + \omega_n^2}$$

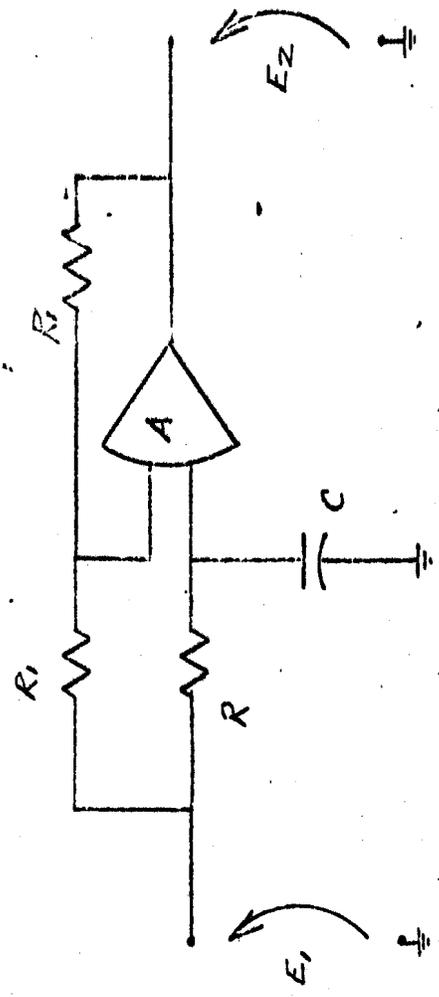
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FIGURE 12
1st Order All Pass Network

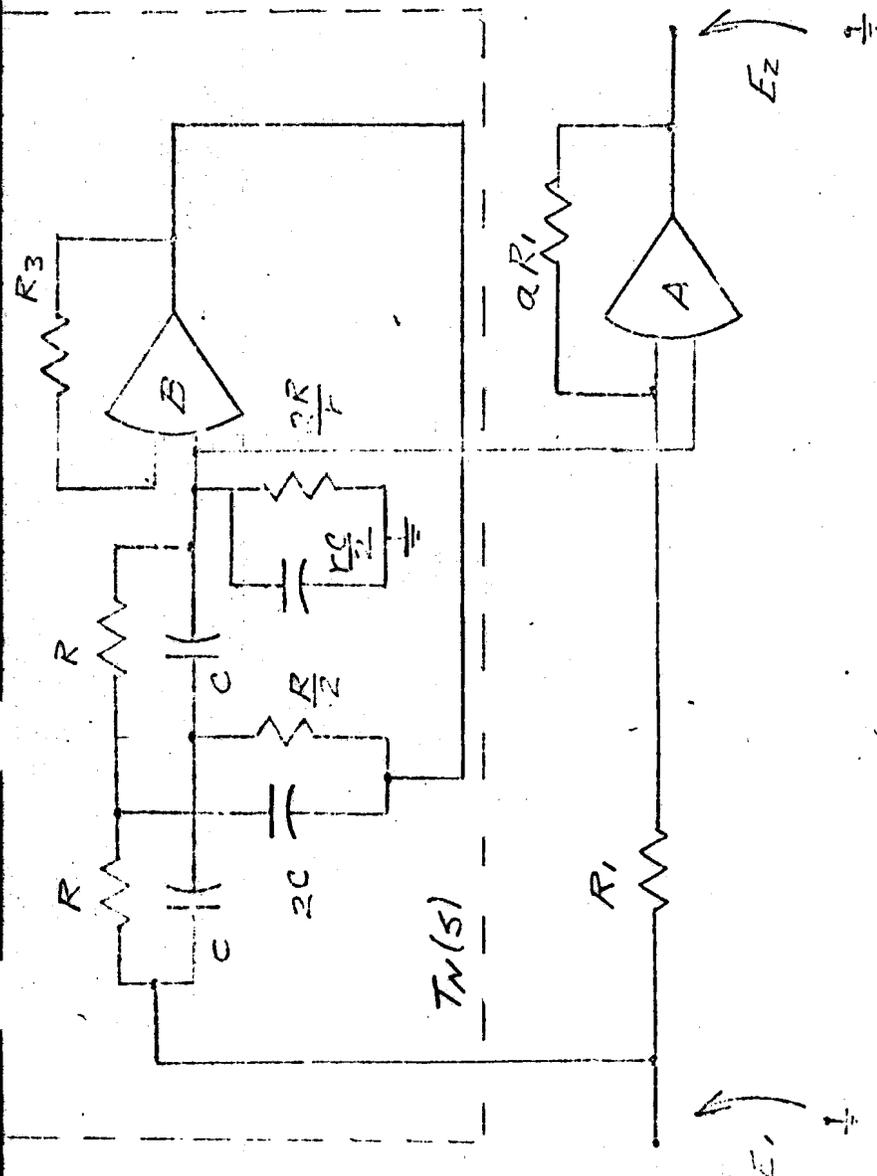
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INCORPORATED
NO. OF SHEETS PER SET
SHEET
27

$\omega_0 = \frac{1}{RC}$
 $R_1 = 2R$



* Does not include Amp. biasing circuits

$$\frac{E_2}{E_1} = \frac{-1 [s - \omega_0]}{[s + \omega_0]}$$



* Does not include Amp. biasing circuit

$$T_N(s) = \frac{K_{po} (s^2 + \omega_n^2)}{(s^2 + \frac{\omega_n}{Q}s + \omega_n^2)}$$

$$K_{po} = \frac{1+r}{1-r}$$

$$\omega_n = \frac{1}{RC}$$

$$Q_{nR} = \frac{1+r}{2r}$$

$$R_3 = 2R \parallel \frac{2R}{r}$$

$$aR_1 // R_1 = 2R \parallel \frac{2R}{r}$$

$$\frac{E_2}{E_1} = -a \left[1 - \left(1 + \frac{1}{a} \right) T_N(s) \right]$$

$$\frac{E_2}{E_1} = -a \left[\frac{s^2 [K_{po} (1+r) - 1] - \frac{\omega_n}{Q} (1+r) (s + \omega_n^2) - 1}{(s^2 + \frac{\omega_n}{Q} s + \omega_n^2)} \right]$$

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FIGURE 13
2nd Order All Pass Network

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SHEET

28

$$\omega M = X_e$$

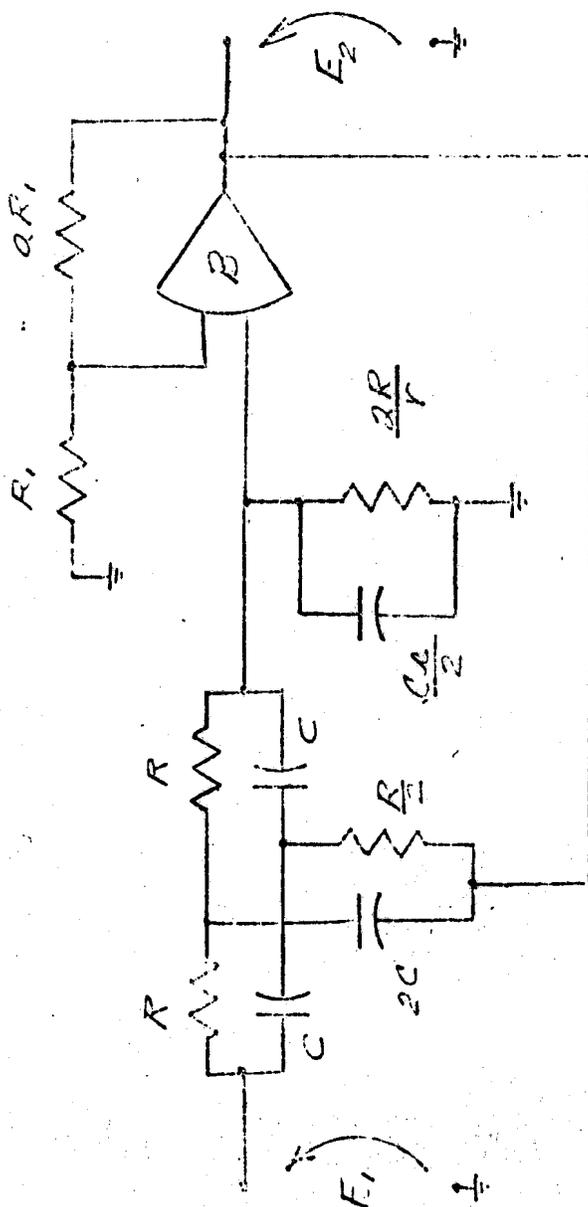
$$\omega M^2 = \omega M \sqrt{L_e}$$

$$X_{m2} = \frac{R}{\omega L_e}$$

$$Y_{m2} = \frac{25[(\omega X_e)^2]}{1 - B + 6\omega L_e X_e}$$

$$B = 1 + a$$

$$\frac{2R}{a+1} = 2R // \frac{R}{a}$$



* Does not include Amp. biasing circuit

$$\frac{E_2}{E_1} = \frac{R_{m2} (S^2 + \omega M^2)}{(S^2 + \omega M^2 + \omega M^2)}$$

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FIGURE 14
Frequency Rejection Network

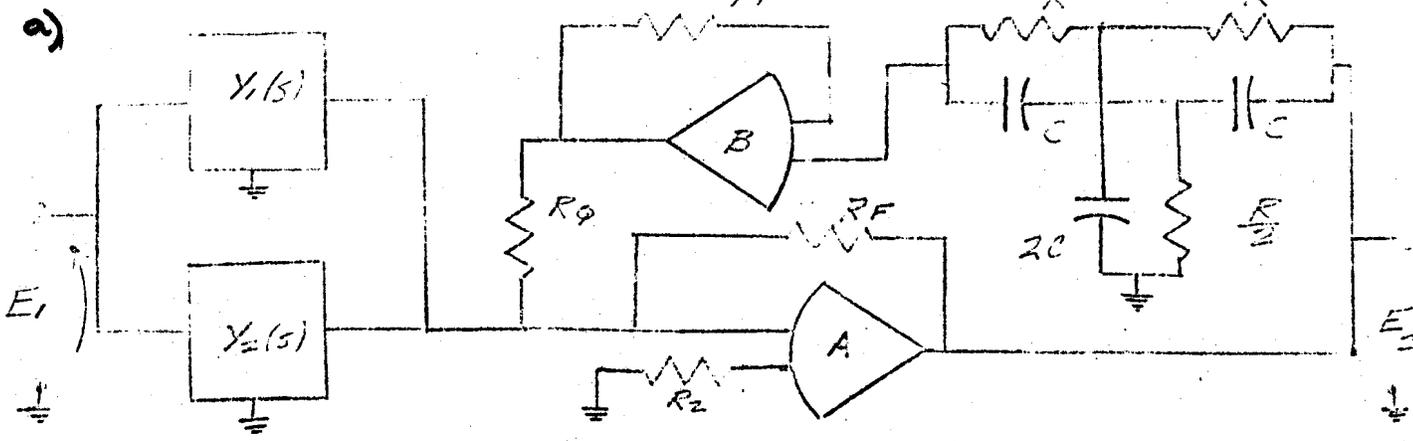
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SHEET

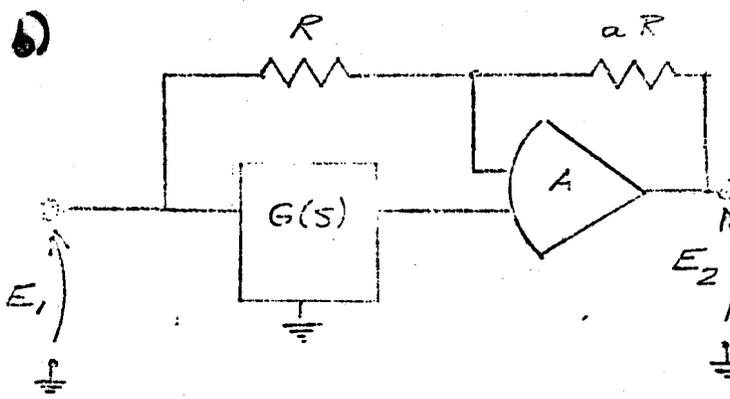
NO. OF SHEETS PER SET

29

10 8

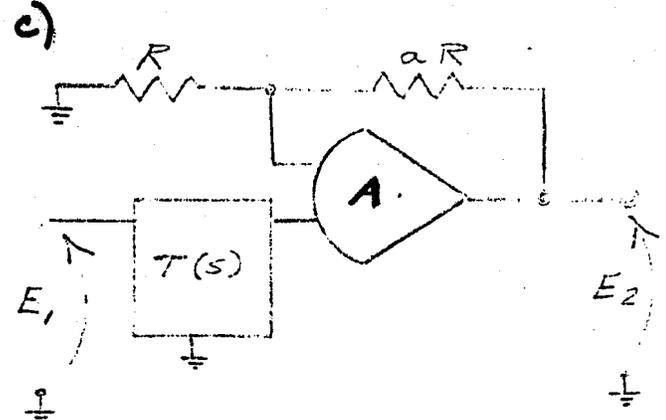


$$\frac{E_2}{E_1} = [Y_1(s) + Y_2(s)] \left[\frac{-R_1}{1 + \frac{R_1}{R_2}} \right] \frac{s^2 + \frac{R_1}{R_2} s + \omega_1^2}{s^2 + \frac{R_1}{R_2} s + \omega_1^2}$$

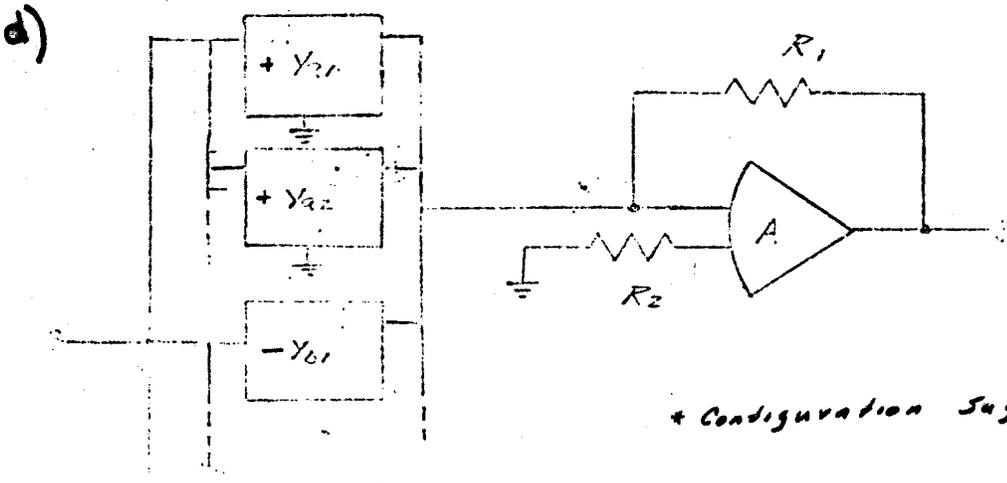


$$\frac{E_2}{E_1} = -a [1 - (1 + \frac{a}{R}) G(s)]$$

* Configuration Suggested by W. Thelen



$$\frac{E_2}{E_1} = +a T(s)$$



* Configuration Suggested by O'Neill

$$\frac{E_2}{E_1} = -R \left[\sum_{i=1}^n Y_{1i} - \sum_{j=1}^m Y_{2j} \right]$$

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FIGURE 15

Additional Synthesis Techniques

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SHEET

80

NONMINIMUM PHASE SECTIONS
 MODIFIED
 RAISED COSINE FILTER
 40 & 42 DATA

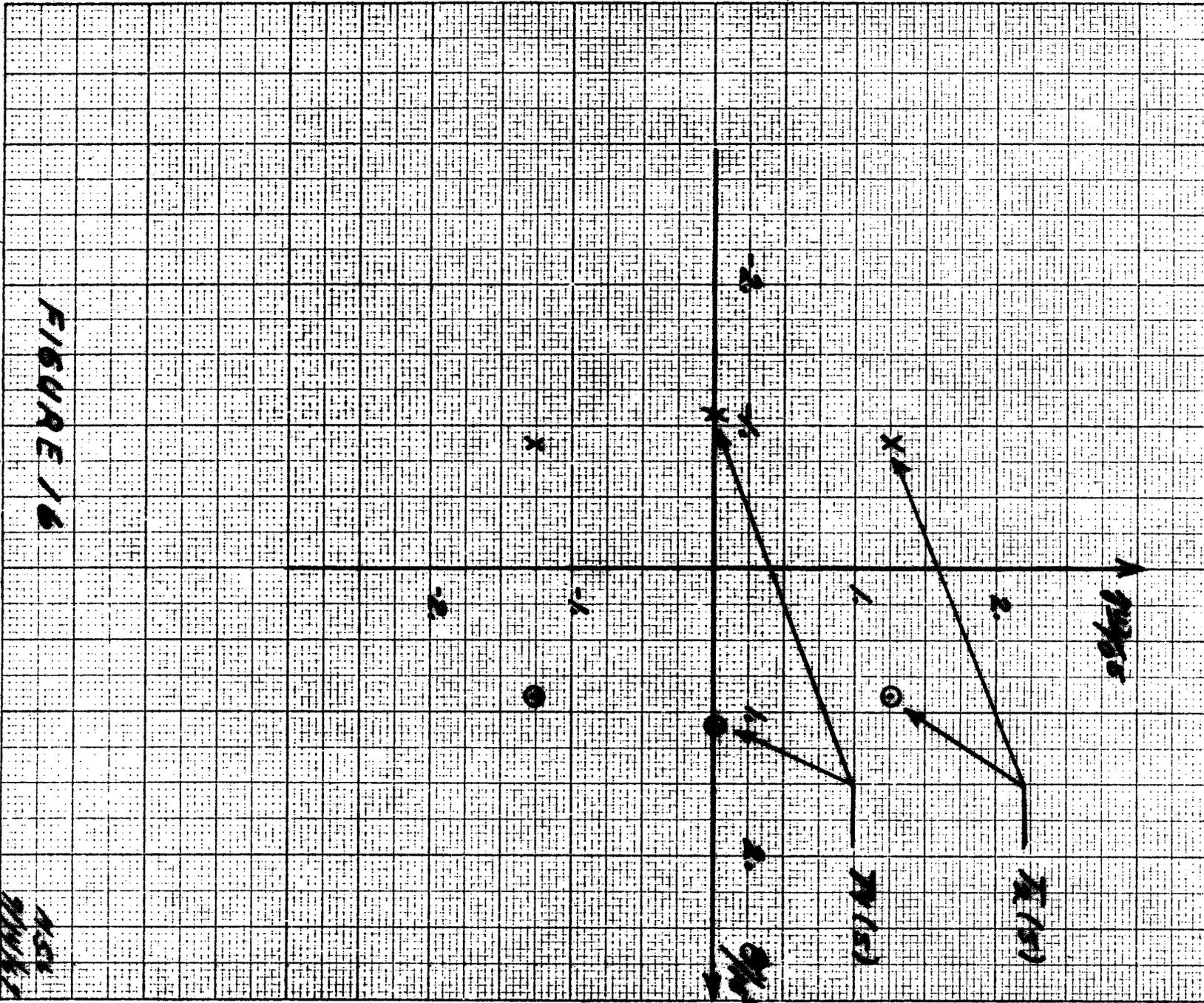


FIGURE 16

MINIMUM PHASE SECTIONS
 MODIFIED
 RAISED COSINE FILTER

409 KB Data

○ ZERO
 X POLE



X

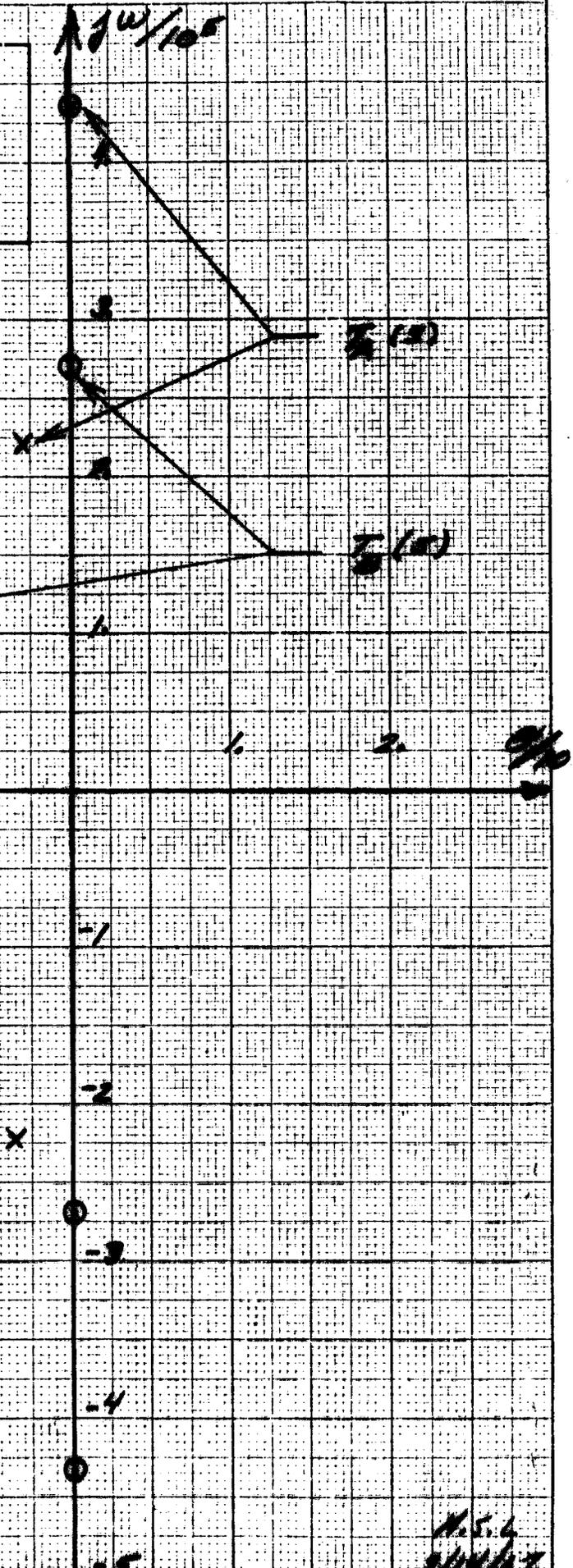


FIGURE 17

M.S.G.
 1/1/67

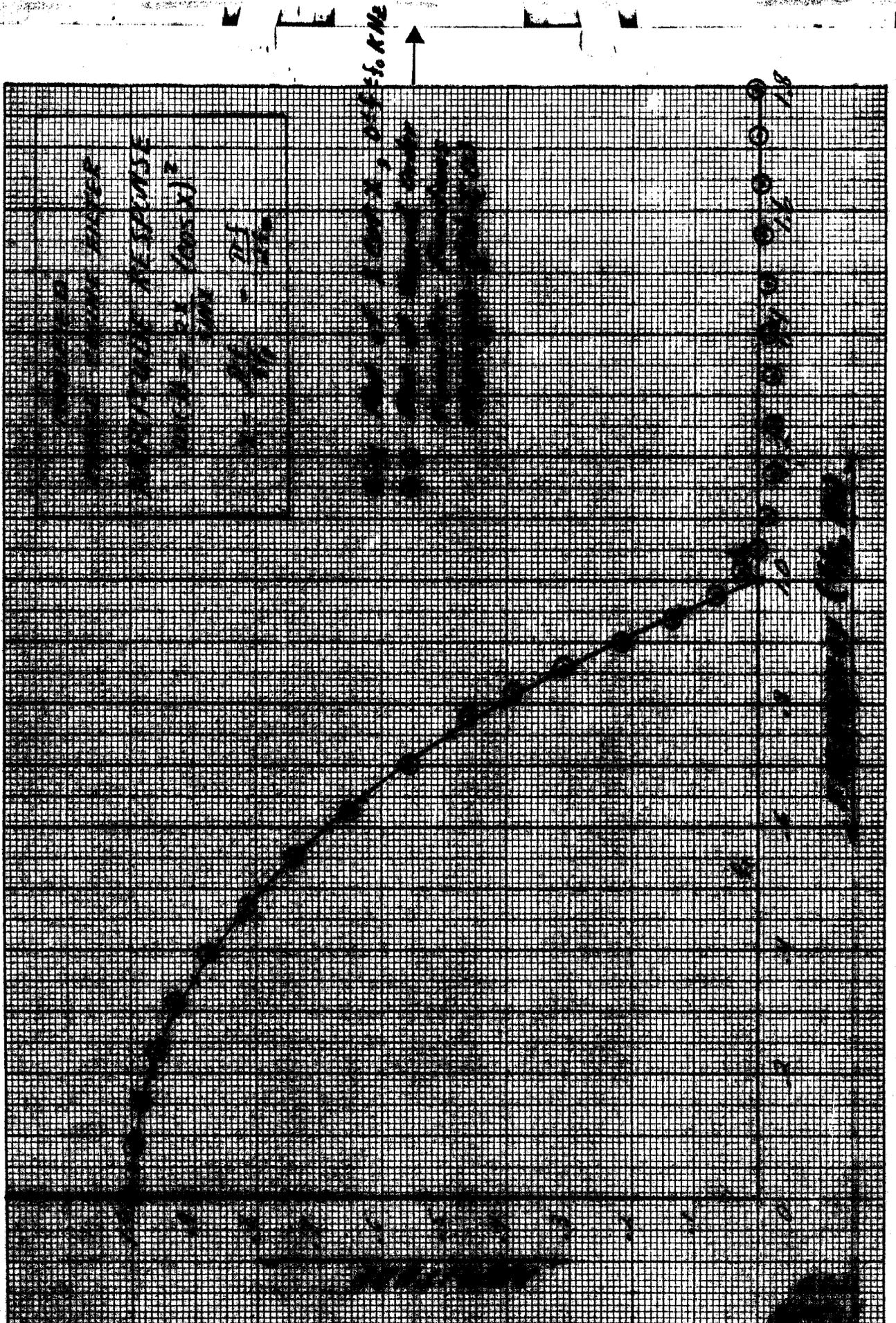
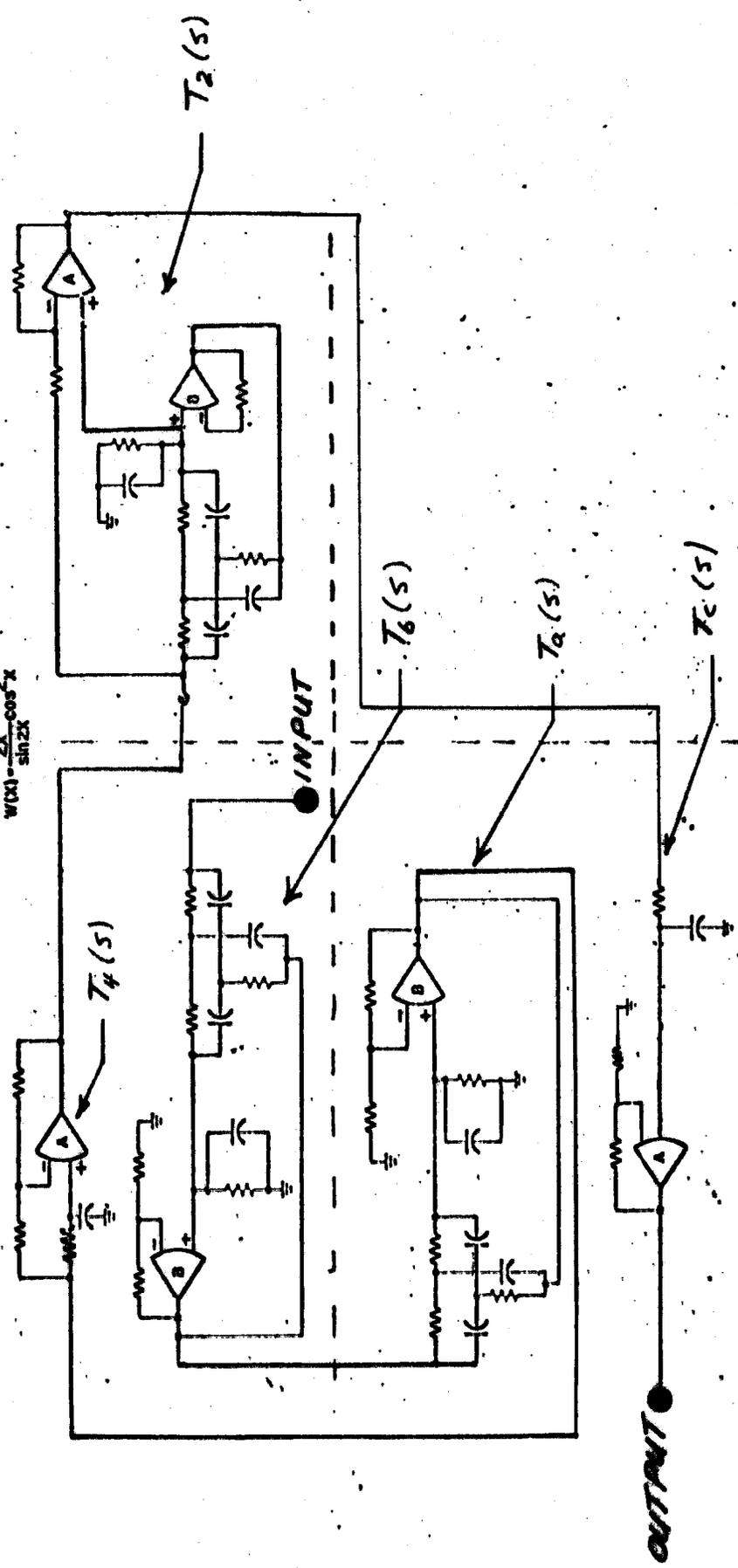


FIGURE 18

MODIFIED RAISED COSINE FILTER

$$W(x) = \frac{2x}{\sin 2x} \cos^2 x$$



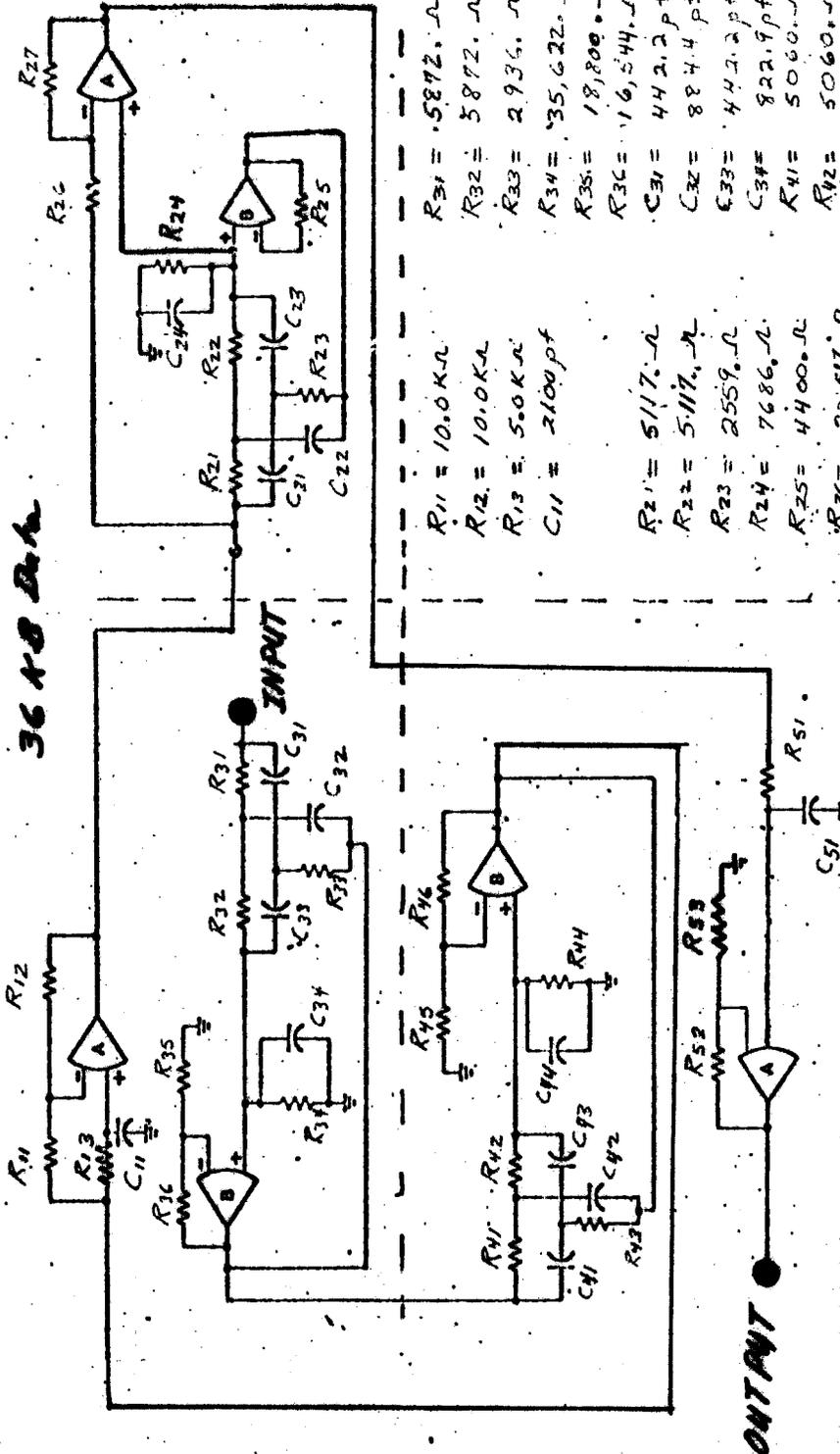
* These are not included in the original circuit

FIGURE 19

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9/14/69
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MODIFIED RAISED COSINE FILTER

36 K8 Data



- C41 = 829.4 pf
- C42 = 1658.8 pf
- C43 = 829.4 pf
- C44 = 1866.1 pf
- R51 = 4100 Ω
- R52 = 3,221 Ω
- R53 = 17.8 KΩ
- C51 = 1165.8 pf

- R31 = 5872 Ω
- R32 = 5872 Ω
- R33 = 2936 Ω
- R34 = 35,622 Ω
- R35 = 18,800 Ω
- R36 = 16,544 Ω
- C31 = 442.2 pf
- C32 = 884.4 pf
- C33 = 442.2 pf
- C34 = 822.9 pf
- R41 = 5060 Ω
- R42 = 5060 Ω
- R43 = 2580 Ω
- R44 = 10,276 Ω
- R45 = 31,554 Ω
- R46 = 5,972 Ω

- R11 = 10.0 KΩ
- R12 = 10.0 KΩ
- R13 = 5.0 KΩ
- C11 = 2100 pf
- R21 = 5117 Ω
- R22 = 5117 Ω
- R23 = 2559 Ω
- R24 = 7686 Ω
- R25 = 4400 Ω
- R26 = 20,517 Ω
- R27 = 5,601 Ω
- C21 = 1442 pf
- C22 = 2884 pf
- C23 = 1442 pf
- C24 = 960 pf

* Does not include Amp biasing circuit

FIGURE 20

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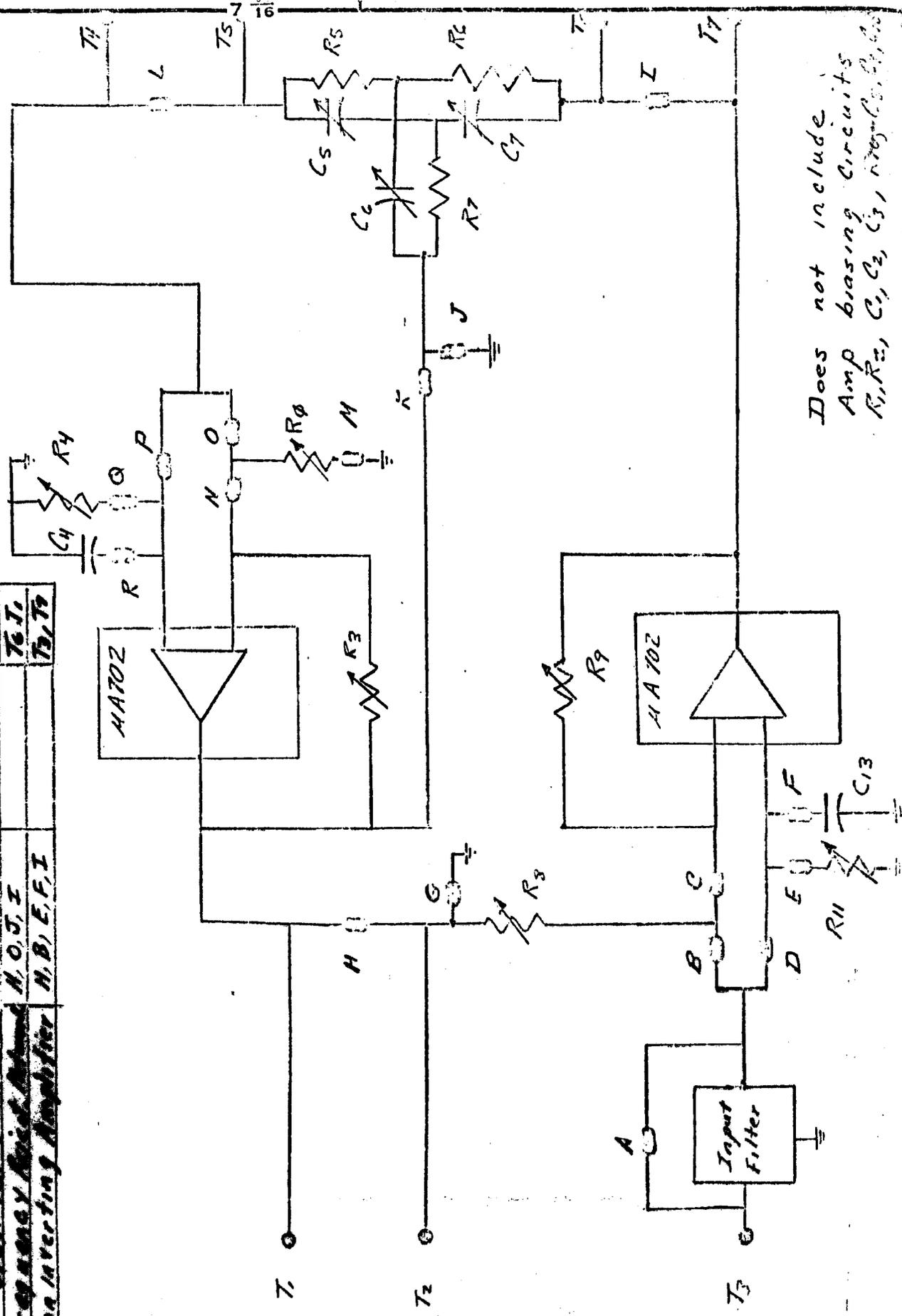
FIGURE 21
 Modified Active Filter

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 36

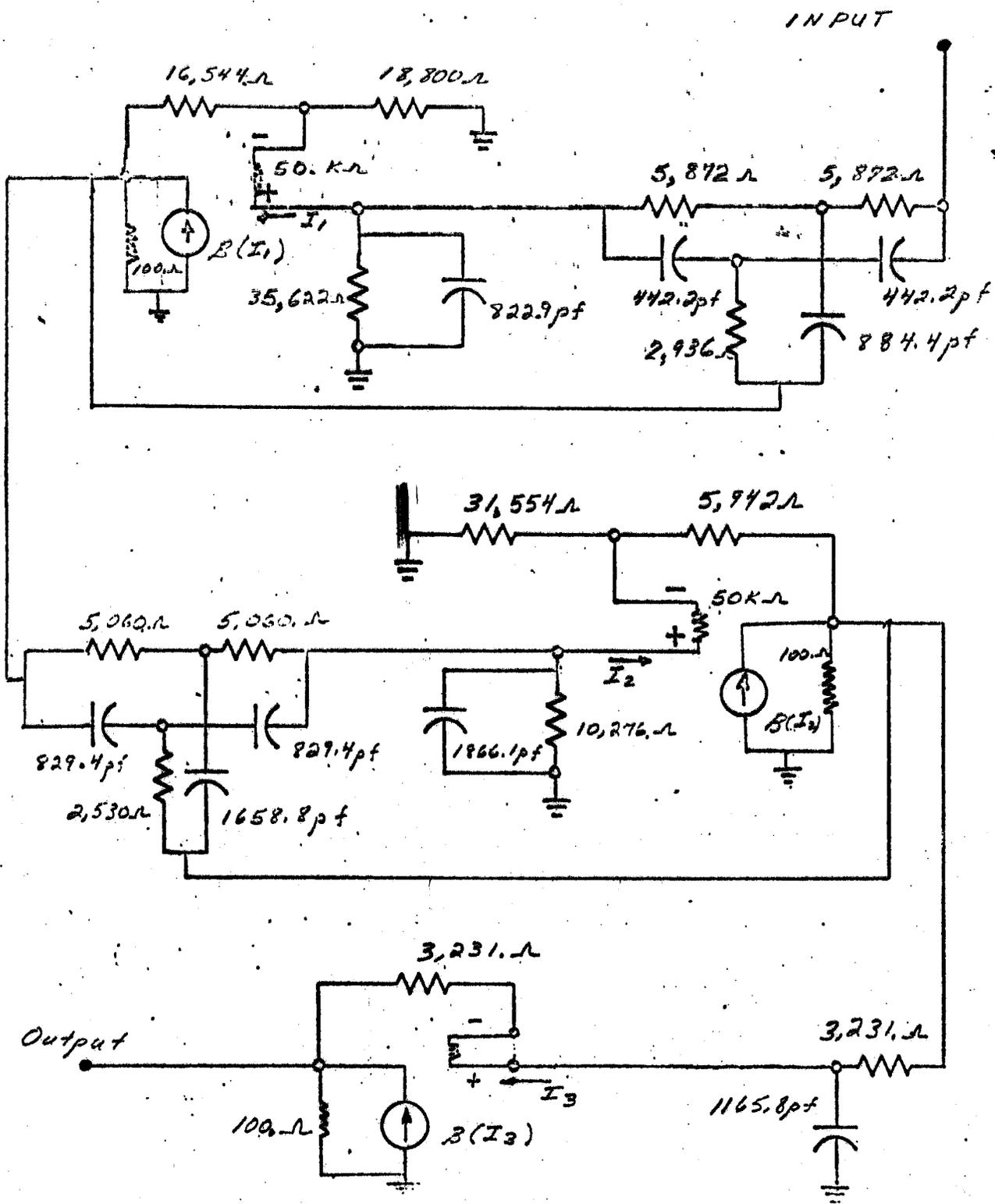
Filter Sections	Open Terminals	Short Terminals	Input Terminals
Freq. Equalizing Network	D, K, J, N, O, A	(T ₅ , T ₇) (T ₅ , T ₆)	T ₅ , T ₆
2nd order All Pass	B, E, F, I, M, N	(T ₅ , T ₇) (T ₅ , T ₆)	T ₅ , T ₇
1st order All Pass	A, B, E, F, G, I	(T ₅ , T ₇)	T ₅ , T ₇
Frequency Invert. Network	H, O, J, I		T ₆ , T ₇
Non Inverting Amplifier	M, B, E, F, I		T ₅ , T ₆

Modified Active Filter



Does not include
 Amp biasing circuits
 R₁, R₂, C₁, C₂, C₃, R₇₀, C₁, C₂, C₃

36 KB Data
 Modified Raised Cosine Filter
 ECAP CIRCUIT MODEL
 of
 ... Minimum Phase Sections



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FIGURE 22
ECAP CIRCUIT MODEL

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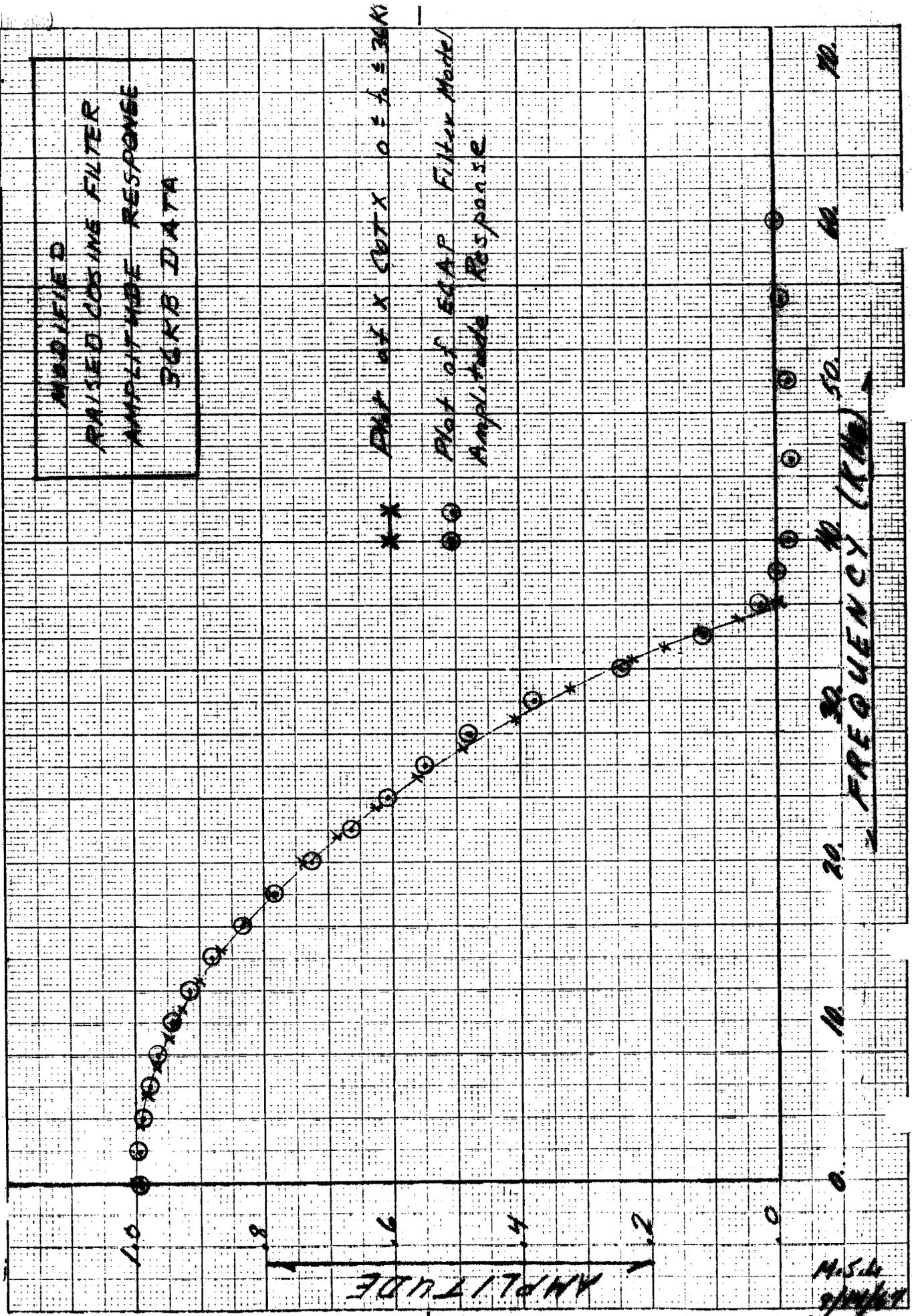
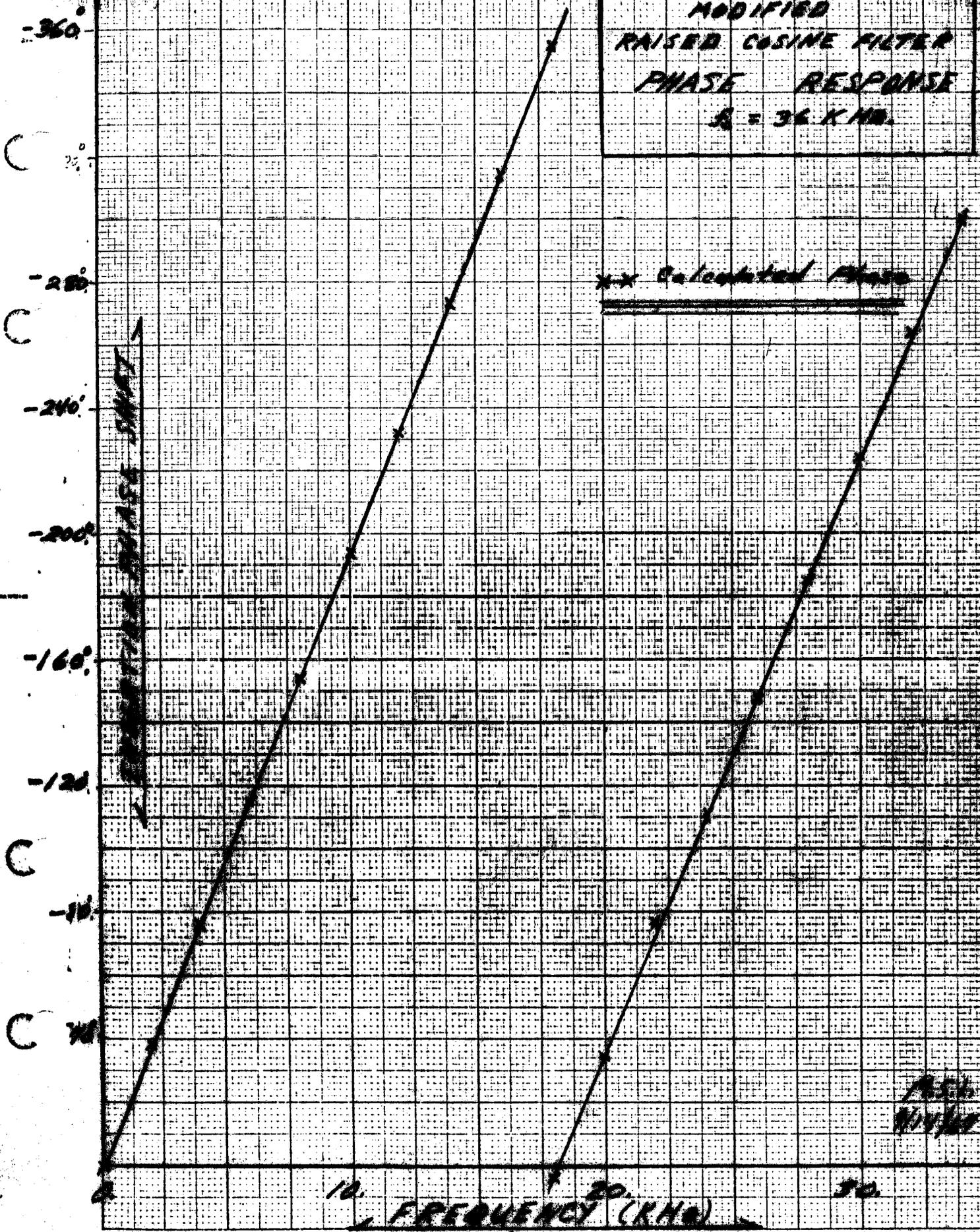


FIGURE 23

MODIFIED
RAISED COSINE FILTER
PHASE RESPONSE
 $B = 36 \text{ KHz}$



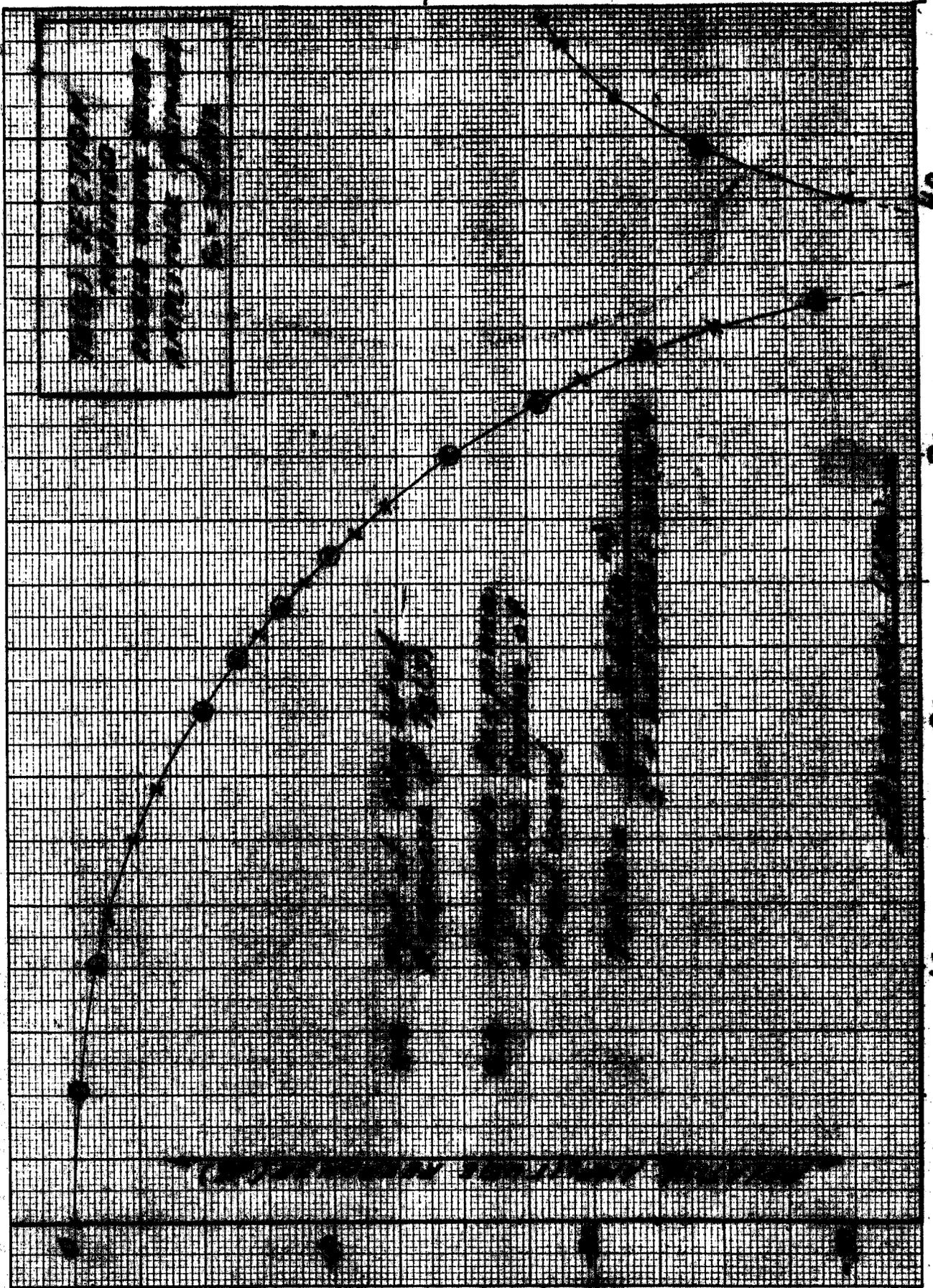
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FIGURE 24



FIGURE 25

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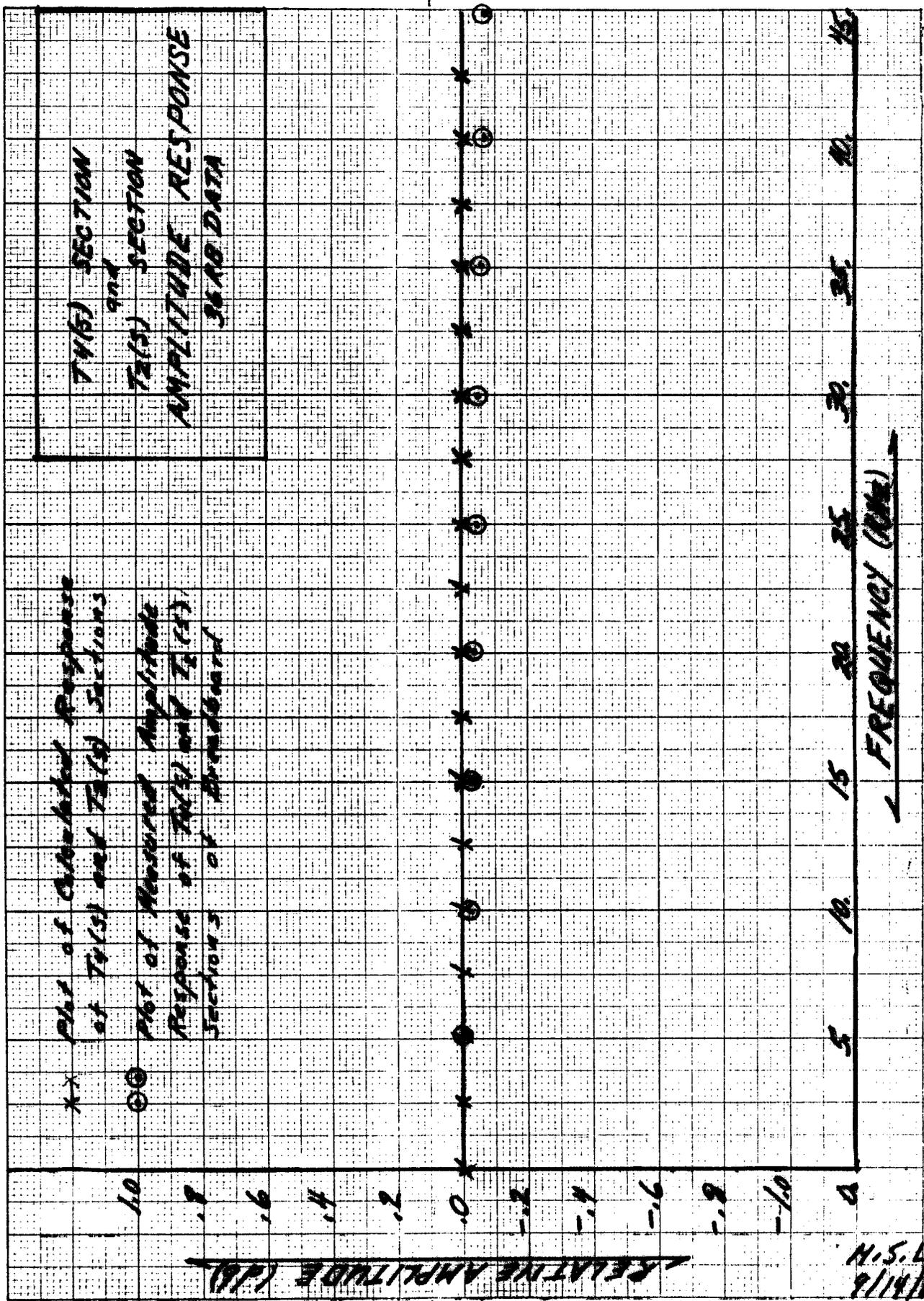


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FIGURE 26

K & E CO. 8115 N.Y.

M.S.L.



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N.S.L.

FIGURE 27

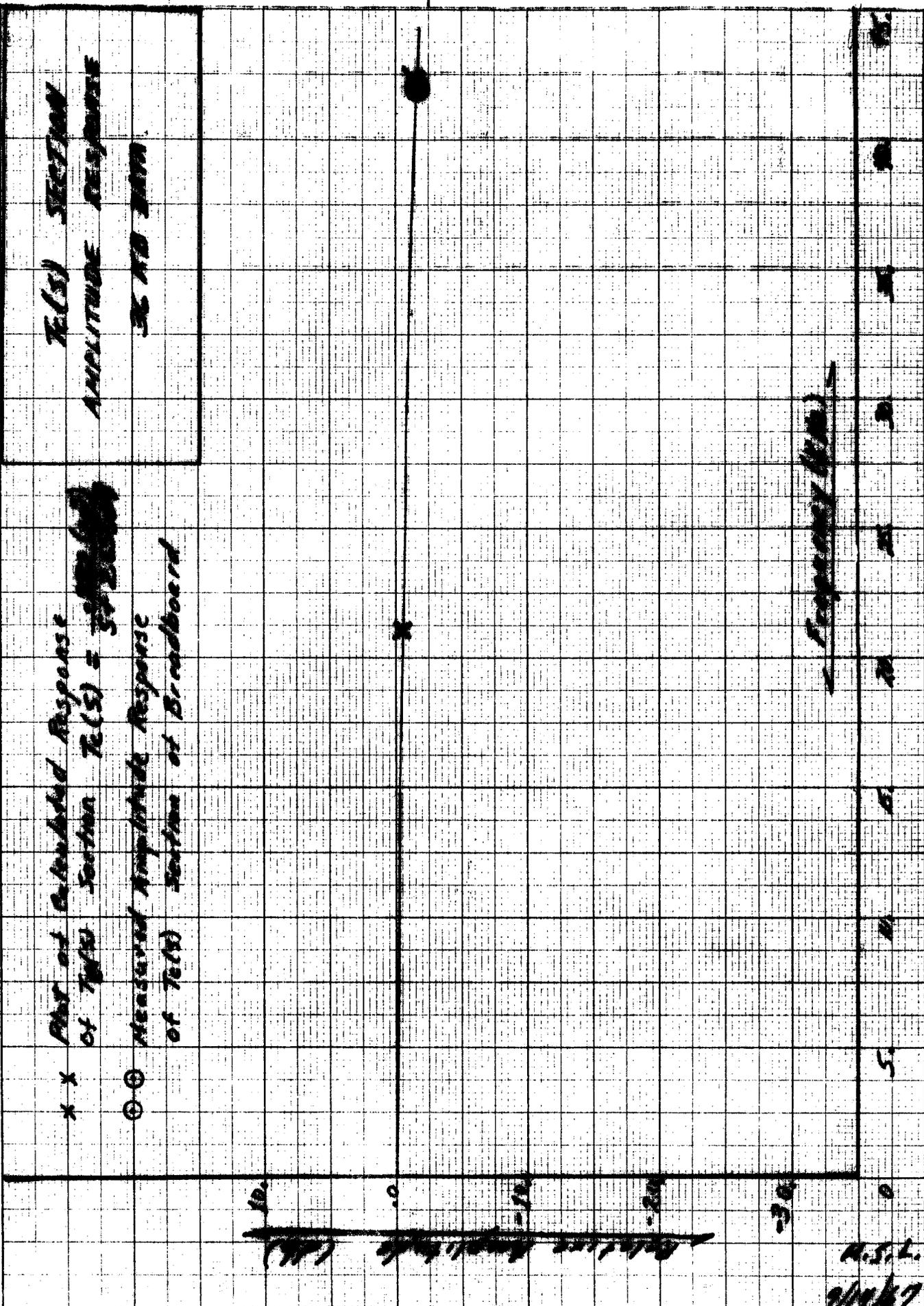
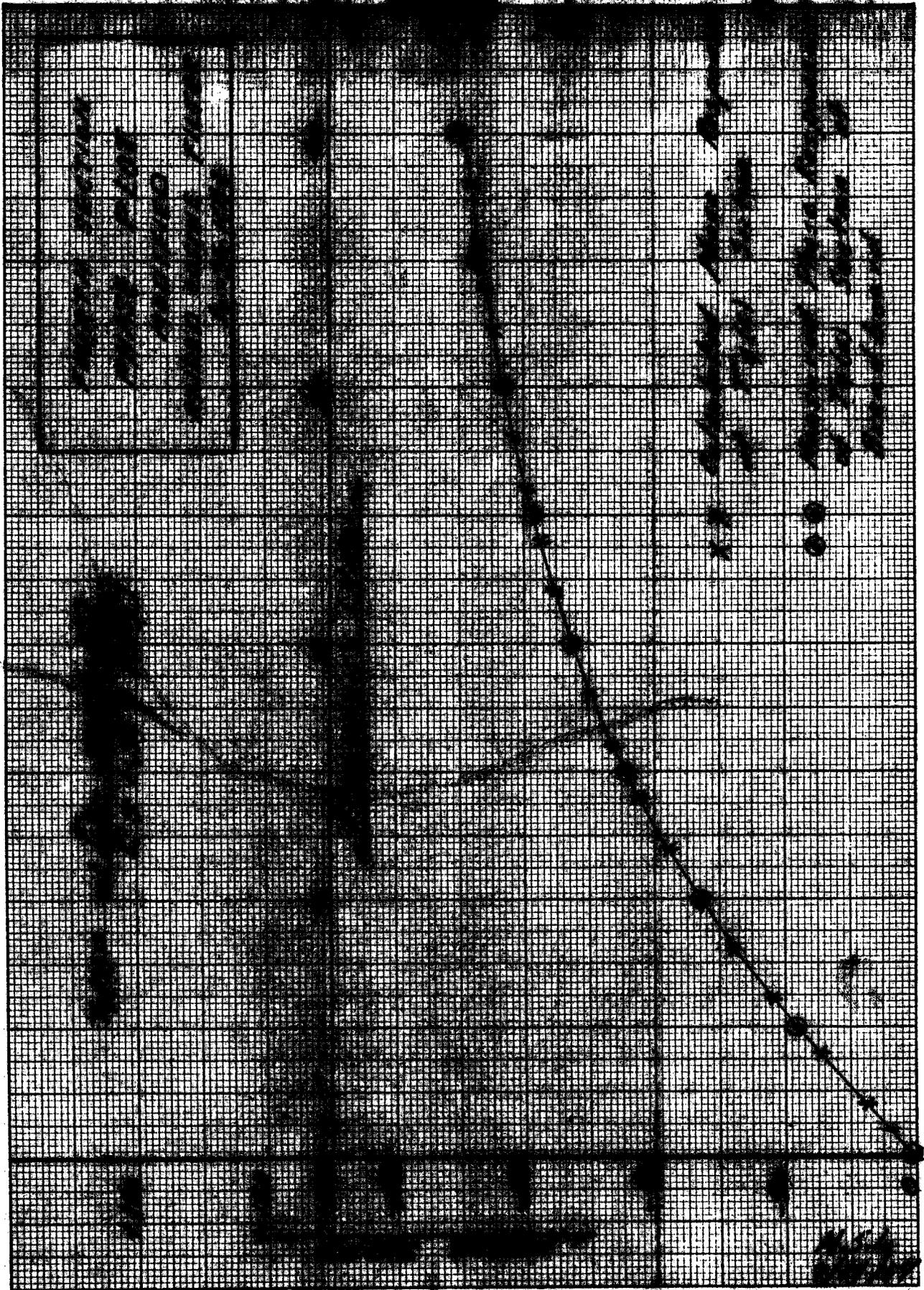


Figure 28



E-1863 (3-58)
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FIGURE 27 B & CO. 8118 N.Y.



E. 1563 (3-58)
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FIGURE 30

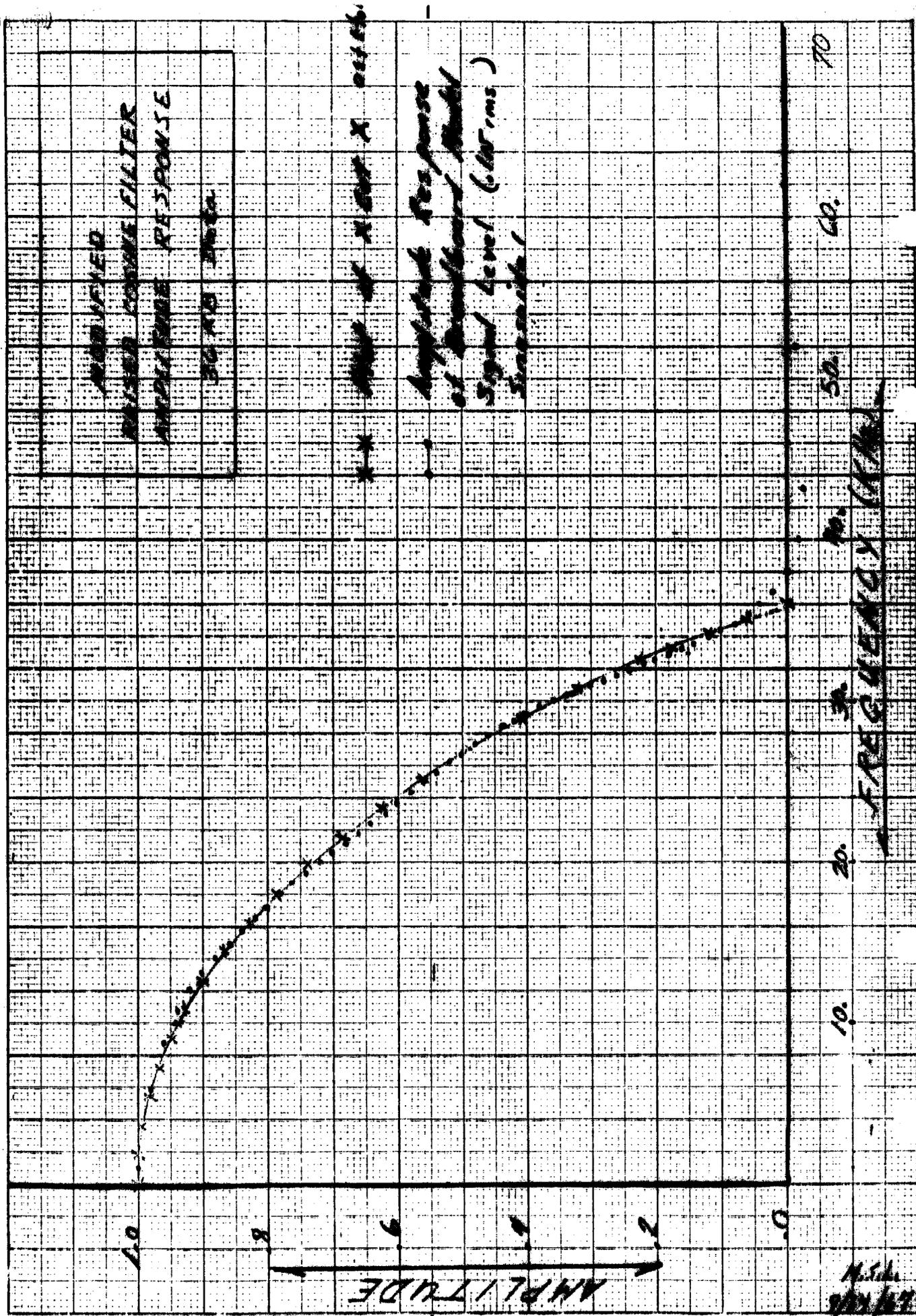
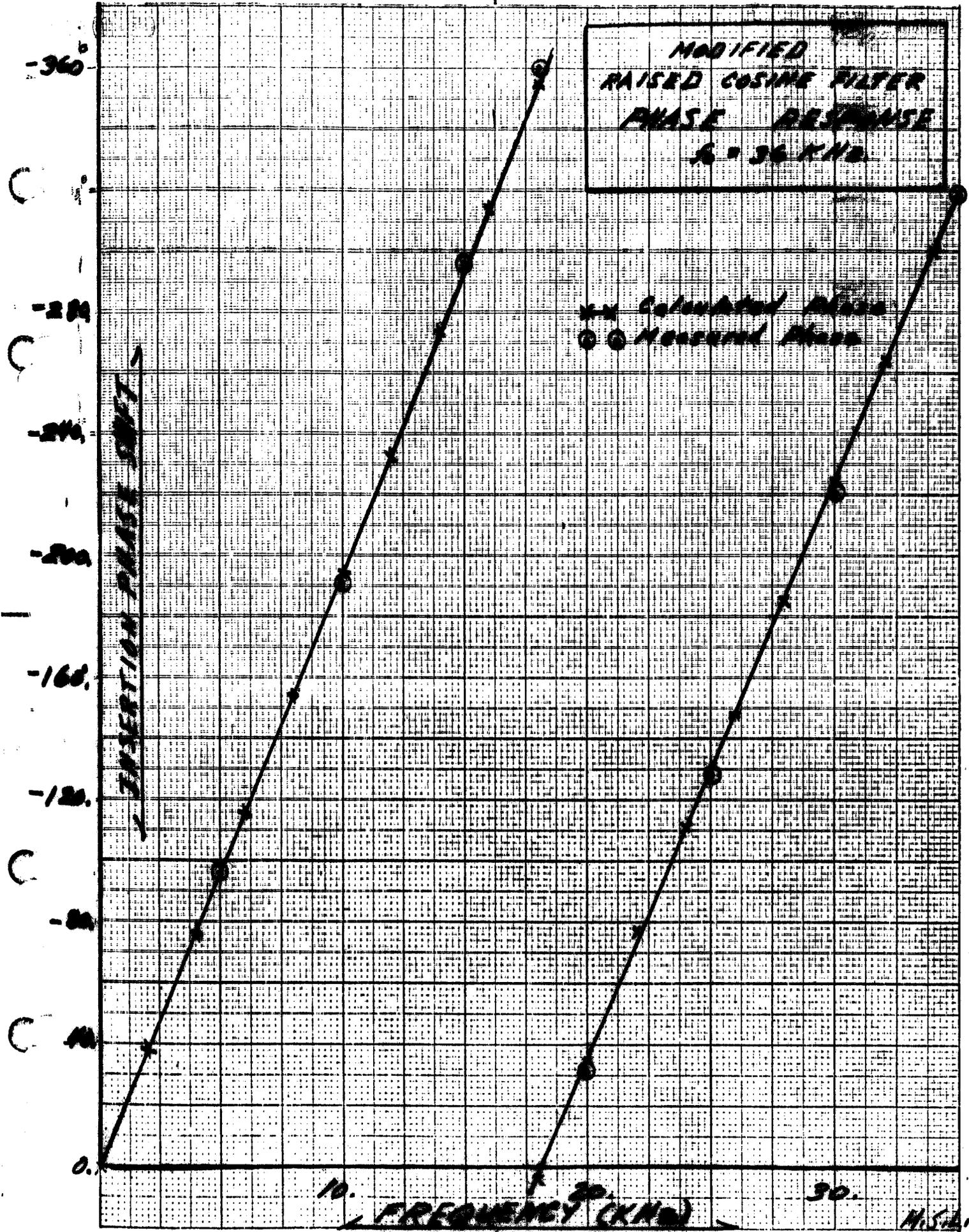


FIGURE 31



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FIGURE 32

MSA
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Measured Characteristics
 Breadboard Model 36KB Data
 Modified Raised Cosine Filter
 for 304 Data Set

D.C. Gain	1.0	
Offset Voltage	40mV	
Max. Signal Level (sinusoidal)	10V p-p	
Current Drain	15ma	
Power Dissipation	450mw	
Negative Supply Voltage	-6.0V	
Positive Supply Voltage	+12.5	
Noise Level ($V_{in}=0$)	1.0 mV	V_{out}

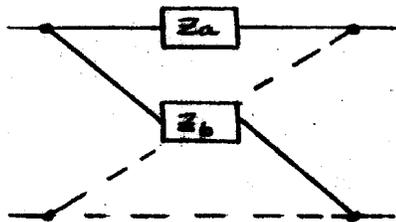


Fig 1A

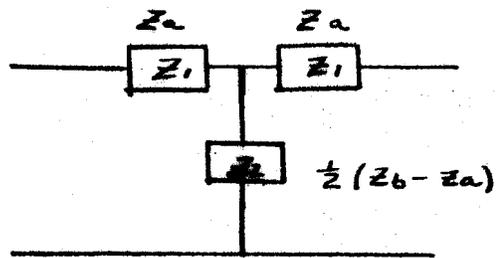


Fig 1B

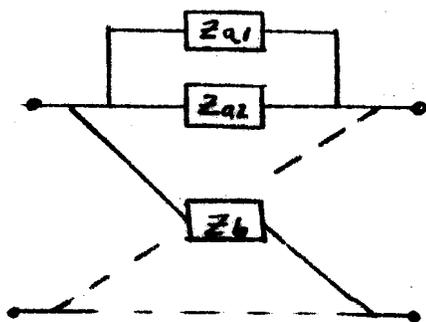


Fig 1C

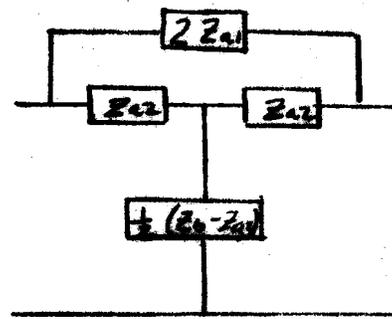


Fig 1D

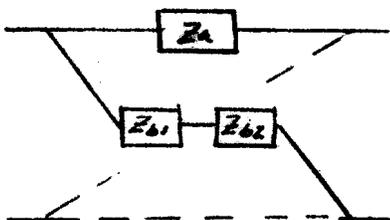


Fig 1E

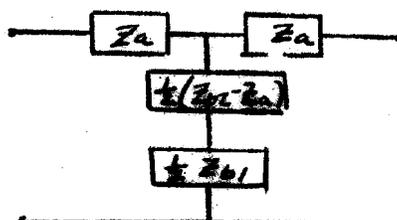


Fig 1F

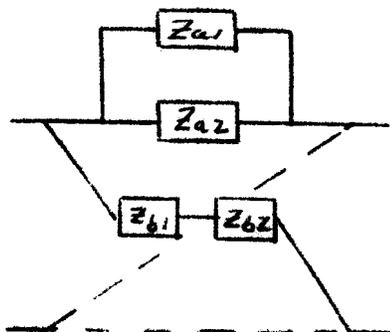


Fig 1G

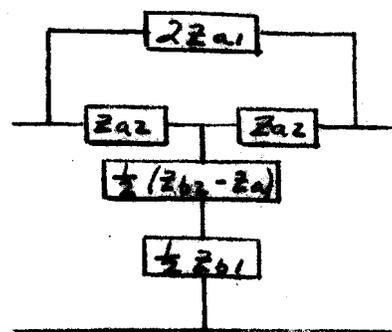
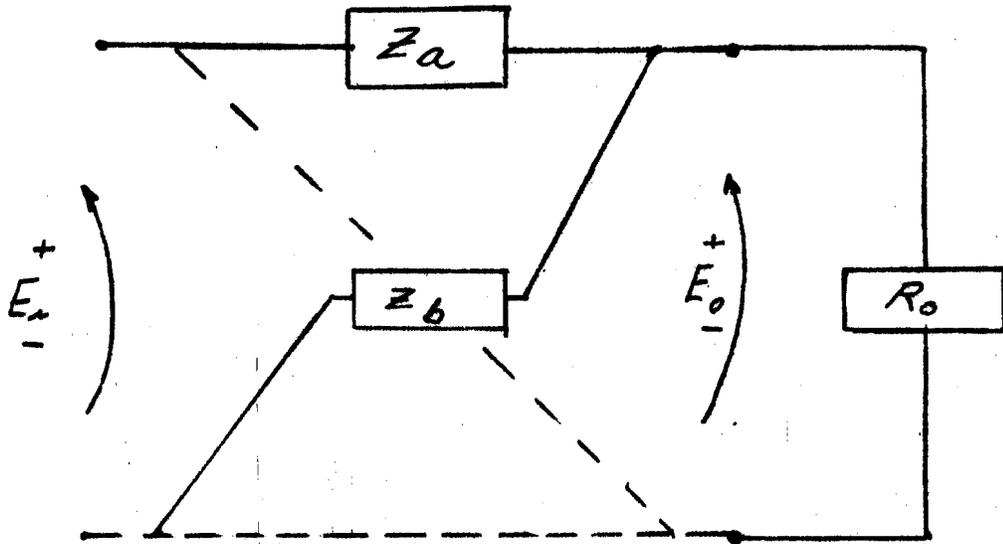


Fig 1H

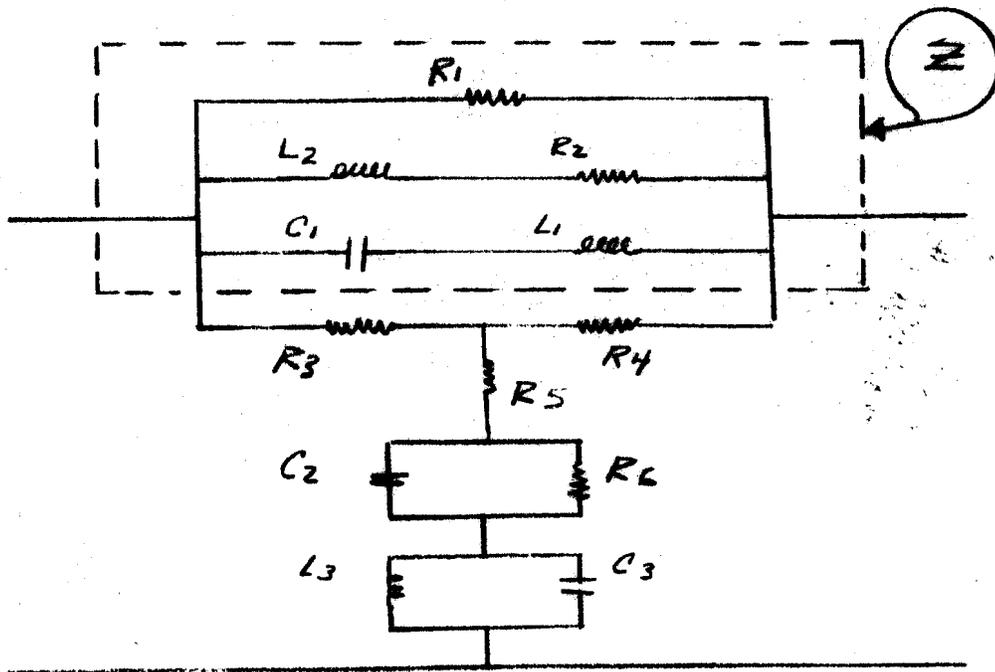


$$R_o = \sqrt{Z_a \cdot Z_b}$$

Constant Resistance
Symmetrical Lattice

$$T(s) = \frac{E_o}{E_i} = \frac{R - Z_a}{R + Z_a}$$

Appendix C



$$Z = R_1 \parallel (sL_2 + R_2) \parallel (L_1 s + \frac{1}{C_1 s})$$

$$Z = \frac{R_1 L_2 L_1 s^2 + R_1 R_2 L_1 s + \frac{R_1 L_2}{C_1} + \frac{R_2 R_1 L_1}{C_1}}{L_2 L_1 s^2 + [R_2 L_1 + R_1 (L_2 + L_1)] s + (R_2 R_1 + \frac{L_2}{C_1}) + \frac{(R_1 + R_2)}{C_1} (\frac{1}{s})}$$

$$T(s) = \frac{1}{1 + \frac{s}{R_0}}$$

$$T(s) = \frac{L_2 L_1 s^2 + [R_2 L_1 + R_1 (L_2 + L_1)] s + (R_2 R_1 + \frac{L_2}{C_1}) + \frac{R_1 + R_2}{C_1} (\frac{1}{s})}{(1 + \frac{R_1}{R_0}) L_2 L_1 s^2 + [R_2 L_1 (1 + \frac{R_1}{R_0}) + R_1 (L_2 + L_1)] s + [R_2 R_1 + \frac{L_2}{C_1} (1 + \frac{R_1}{R_0})] + [\frac{R_2}{C_1} (1 + \frac{R_1}{R_0}) + \frac{R_1}{C_1}]}$$