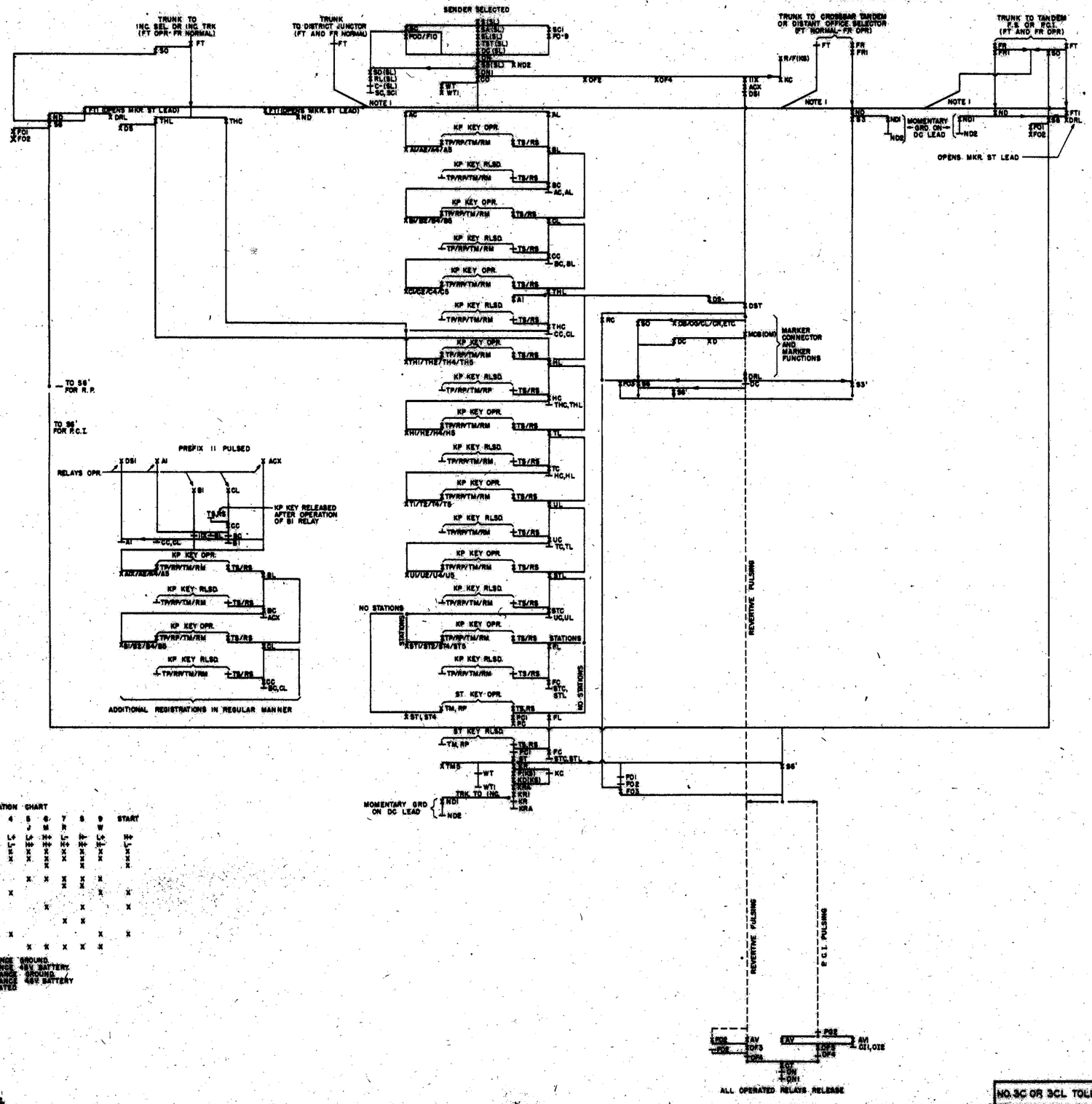
MP-11501

ISSUE	1	W.D.M.
DATE	3-6-50	



KEY REGISTRATION CHART

	KEY	0	1	2	3	4	5	6	7	8	9	START
							J	M	R		W	
LEAD	T		L+	H+	L-	L+	L+	H+	L+	H+	L+	H+
	R				L-	L-	H+	H+	H+	H+	H+	L+
	T5	X	X	X	X	X	X	X	X	X	X	X
	R5			X	X	X	X	X	X	X	X	X
	T4		X		X			X		X		X
PULSING RELAYS	R4						X	X	X	X	X	
	T3			X	X				X	X		
	R3					X					X	X
A1-B1- C1 ETC			X		X			X		X		X
A2-B2- C2 ETC				X	X				X	X		
REGISTER RELAYS A3-B3- C3 ETC						X					X	X
							X	X	X	X	X	

H+ INDICATES LOW RESISTANCE GROUND.
L+ INDICATES LOW RESISTANCE 48V BATTERY.
L- INDICATES HIGH RESISTANCE GROUND.
H- INDICATES HIGH RESISTANCE 48V BATTERY
X INDICATES RELAY OPERATED

NOTES:
1. PATH CLOSED FOR OPERATING AC AND AL RELAYS WHEN THE CO RELAY OPERATES.

* KEY PULSING SENDER CIRCUIT	SD-25015-01	ISS. 34-D
ORIGINATING MARKER CIRCUIT	SD-25016-01	ISS. 41-D
DISTRICT JUNCTOR CIRCUIT	SD-25021-01	ISS. 8-D
LINK AND CONTROLLER CIRCUIT	SD-25028-01	ISS. 32-D
ORIGINATING MARKER CONNECTOR	SD-25035-01	ISS. 30
KEY SET CIRCUIT	SD-56051-01	ISS. 8-B

KEY PULSING SENDER CIRCUIT RECEIVES D-C KEY PULSES REVERTIVE AND P.C.I. PULSING

NO. 3C OR 3CL TOLL SWBD.

SC 102-1

BELL TELEPHONE LABORATORIES, INC.
MADE IN U.S.A.

ORDER AS BSP. ITEM MP-11501