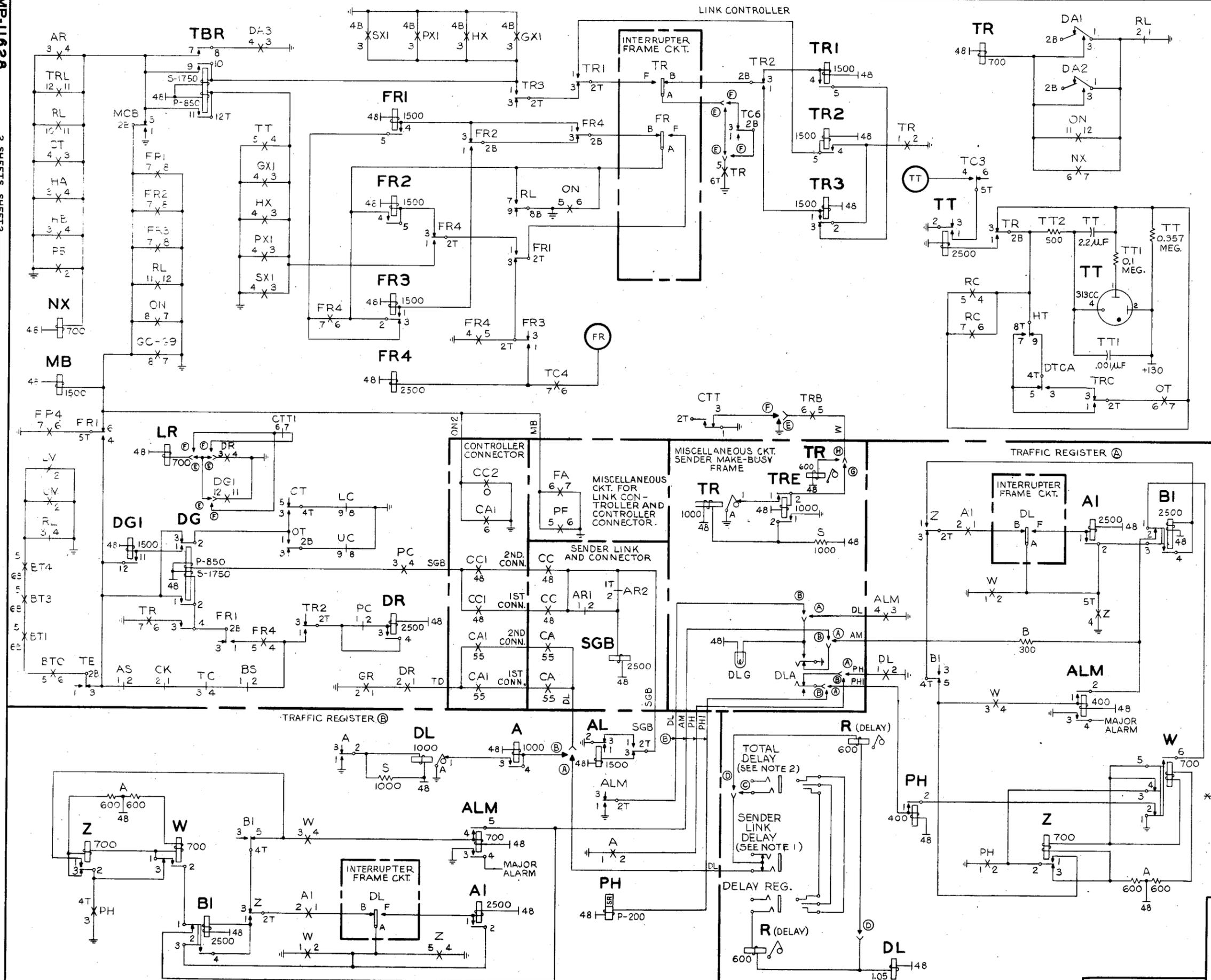


REVISION	DATE
1	7-20-51
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- NOTES:
1. SENDER LINK DELAY JACKS FURNISHED ONE PER PAIR OF SENDER LINK FRAMES IN A GROUP. THIS PROVIDES INDIVIDUAL SENDER LINK DELAY REGISTRATION.
 2. TOTAL DELAY JACK FURNISHED ONE PER 2 GROUPS OF SENDER LINK FRAMES. THIS PROVIDES COMBINED SENDER LINK DELAY REGISTRATION.
 3. IF NO SUBSEQUENT DELAY, REGISTRATION OCCURS.
 4. IF 2ND DELAY, REGISTRATION OCCURS.

OS OPTION	FEATURE OR OPTION	SD OPTION	SD
A	TRAFFIC REGISTER WITH 14-TYPE REGISTERS		68412-01
B	TRAFFIC REGISTER WITH 12-TYPE REGISTERS		68430-01
C	MFR. DISC.	FIG. 22	68412-01
D	STANDARD		68412-01
E	LINK CONTROLLER TEST CIRCUIT	NOT PROVIDED PROVIDED	YD YC, FIG. 7
G	A & M	FIG. 3	
H	STD	FIG. 21	68386-01

- CONTROLLER CONNECTOR CKT. SD-68336-01, ISS. 7
- INTERRUPTER FRAME CKT. SD-65053-01, ISS. 15
- * LINK CONTROLLER CKT. SD-68028-01, ISS. 24
- MISCELLANEOUS CKT. FOR SENDER MAKE-BUSY FRAME SD-68386-01, ISS. 3
- MISCELLANEOUS CKT. LINK CONTROLLER AND CONTROLLER CONNECTOR FRAME SD-68087-01, ISS. 7
- SENDER LINK AND CONNECTOR CKT. SD-68334-01, ISS. 11
- TRAFFIC REGISTER CKT. SD-68060-01, ISS. 23
- TRAFFIC REGISTER CKT. SD-68412-01, ISS. 5

**LINK CONTROLLER
OVERALL TIMING
AND
RECYCLING TUBE TIMER**

NO. 4A TOLL 2 SHEETS, SHEET 2 OS 113-1