



- NOTES:
1. NOTCH TOP EDGE OF BOARD AT LOCATIONS SHOWN.
 2. HOLES TO BE DRILLED THROUGH PRINTED WIRING PATHS AS SHOWN.
 3. MOUNT TWO 0.01 mf CAPACITORS PER KS 16048 L4 (C3, C4) AS CLOSE AS POSSIBLE TO TRANSISTOR Q1 AND TOP SURFACE OF BOARD (SEE VIEW B). FORM AND SOLDER LEADS OF CAPACITORS TO PRINTED WIRING PATHS OF THE BOARD AS SHOWN IN VIEW C.
 4. RUBBER STAMP ASTERISK (*) PER WL 2151. LOCATE APPROXIMATELY AS SHOWN.

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 Delete Material Not Refer-
 ing To R.F.
 Suppression
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