

APR 28 1981

BELL TELEPHONE LABORATORIES, INCORPORATED

9-246

COMMON SYSTEMS
FOR USE WITH NO. 3 ESS
ARRANGED WITH 2-WIRE FEATURES
STORE CONTROLLER DATA REGISTER TEST
(CDGSDF)

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AT&TCO
SPCS

PR-1C935-50
246 PAGES

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SECTION DICTIONARY

NUMBER	NAME	MAXIMUM	ORIGIN	TYP	ADDRESS-MODIFICATION
01	CSTDF1	0000504		C	
02	CSTDF2	0000577		C	
03	CSTDF3	0000370		C	
04	CSTDF4	0000600		C	
05	CPATCH	0000000		C	
06	CPATCH1	0000000		C	
07	CPATCH2	0000000		C	
08	CPATCH3	0000203		C	
09	CPATCH7	0000000		C	
0A	TFAPCH2	0000000		C	
0B	TPATCH	0000000		C	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 02/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSD F ISSUE 05 PAGE A1

0027662
0027662
0027662

```

01 DBNAM NO33E3I3.OFF.DATABASE
02 RELNAM NO3ESS
03 GETLIB LIBRARY, MEMBER=MOPTBL
04 GETLIB LIBRARY, MEMBER=SYSMAC
05 GETLIB LIBRARY, MEMBER=SYSSYM
06 GETLIB LIBRARY, MEMBER=PATCH2
07 FIRSTOPTSS
08 VERSION 3ACC, FIELD, ESS2E
09 PATCHINIT
10 NO33E3I3.OFF.OPMS(O) DECK
11 DSQUAL NO33E3I3
12 LISTING N, CRUNCH=Y, SYSOUT=N
13 #####
14 ##
15 ## UNIX ID = NO3PAG
16 ##
17 #####
18 #####
19 ###
20 ### pid date time rel
21 ###
22 ### cdgsdf 4/23/79 14:07:29 4.1.1.1
23 ###
24 #####

```

31 # THIS DATA SET EDITED FOR UNIX ON 05/25/78

```
01 GETLIB MEMBER=MEMDGM
02 GETLIB MEMBER=TTYMAC
03 GETLIB MEMBER=BSAIMAC
04 GETLIB MEMBER=CTVTAB
05 GETLIB MEMBER=DGNMAC # SELECT FIELD MACROS
06 NAME CDGSDF
```

10 ## The following is a list of the valid csects for program CDGSDF

0027662
0030366
0031165
0031555

```
12 CSTDF1 CSECT
13 CSTDF2 CSECT
14 CSTDF3 CSECT
15 CSTDF4 CSECT
```

18 ## The following csects are necessary for patching

0111432
0112562
0114107
0115073
0121236

```
20 CPATCH CSECT
21 CPATCH1 CSECT
22 CPATCH2 CSECT
23 CPATCH3 CSECT
24 CPATCH7 CSECT
```

0116774
0600000

```
26 TFAPCH2 CSECT
28 TPATCH CSECT
```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

0600000

```

01      BEGIN DIAG DGSDF,3ACC      # RUN ON 3ACC
-001- 02 CDGNN EXTERN NMICRO
-001- 03 CDGNN EXTERN NRETRN
-001- 04 CDGNN EXTERN N2RETRN
-001- 05 CDGNN EXTERN NSTRT
-001- 06 CDGNN EXTERN NSTOP
-001- 07 CDGNN EXTERN NDISABA
-001- 08 CDGNN EXTERN NDISABB
-001- 09 CDGNN EXTERN NSTRTCLK
-001- 10 CDGNN EXTERN NSTPCLK
-001- 11 CDGNN EXTERN NTGCLK
-001- 12 CDGNN EXTERN NRESCLK
-001- 13 CDGNN EXTERN NZERO_ER
-001- 14 CDGNN EXTERN NPROS
-001- 15 CDGNN EXTERN NTESTSEG
-001- 16 CDGNN EXTERN NFAILTST
-001- 17 CDGNN EXTERN NTTY
-001- 18 CDGNN EXTERN NMASK
-001- 19 CDGNN EXTERN NPASSTST
-001- 20 CDGNN EXTERN NSEND
-001- 21 CDGNN EXTERN N2SEND
-001- 22 CDGNN EXTERN NADD_OFF
-001- 23 CDGNN EXTERN NADMIC
-001- 24 CDGNN EXTERN NLDMIR
-001- 25 CDGNN EXTERN NEXECUTE
-001- 26 CDGNN EXTERN NCOMPARE
-001- 27 CDGNN EXTERN NFRZ
-001- 28 CDGNN EXTERN NBEGIN
-001- 29 CDGNN EXTERN NLP_END
-001- 30 CDGNN EXTERN NSKIP
-001- 31 CDGNN EXTERN NABT
-001- 32 CDGNN EXTERN NRDMICRO
-001- 33 CDGNN EXTERN NCLRMS
-001- 34 CDGNN EXTERN NNO3CD
-001- 35 CDGNN EXTERN NTMTEST
-001- 36 CDGNN EXTERN NZERPT
-001- 37 CDGNN EXTERN NFLBITS
-001- 38 CDGNN DIRECT INTPPNO3
-001- 39 CDGNN EXTERN NBN
-001- 40 CDGNOF EXTERN PASSOFF
-001- 41 CDGNOF EXTERN FAILOFF
-001- 42 CDGNN EXTERN SLDMCHBF
-001- 43 CDGNN EXTERN SLDMIRLF
-001- 44 CDGNN EXTERN SMCHF
-001- 45 CDGNN EXTERN NRTNBLM
-001- 46 CDGNN EXTERN NWDST

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 ****

CDGSDF W77D

-001- 01 # THIS DIAGNOSTIC PROGRAM IS ALL OR PARTIALLY WRITTEN IN A MACRO
-001- 02 # LANGUAGE WHICH PRODUCES TABLES WHICH ARE INTERPRETED BY THE
-001- 03 # DIAGNOSTIC MONITOR PROGRAM. FOR INFORMATION REGARDING THE
-001- 04 # STRUCTURE OF THE TABLES, SEE THE SECTION OF PROGRAM CDGNM (PR-1C910-01)
-001- 05 # ENTITLED "DIAGNOSTIC TABLE INTERPRETER". FOR A GENERAL DESCRIPTION
-001- 06 # OF THE DIAGNOSTIC PHILOSOPHY AND THE DETAILED REPAIR PROCEDURE,
-001- 07 # SEE THE TROUBLE LOCATION MANUAL (TLM-1C900) SECTION B.

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 4

23:27:57 2/05/81 ****

CDGSDF W77D

01 CDGSFA EXTERN FOBNDX1
02 TDATA EXTERN MASTYPE

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

0600000 ,

01 SYMLIB

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PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf. ISSUE 05 PAGE 6

23:27:57 2/05/81 ****

CDGSDF W77D

-002- 01 # EXTERNS REQUIRED BY STORE DIAGNOSTIC TESTS:

-002- 04 MASACS EXTERN MASCIOCC
-002- 06 CBLM EXTERN PRMASER
-002- 07 CBLM EXTERN WAIT
-002- 09 CDGNM EXTERN LOOPIND
-002- 10 CDGNM EXTERN NOSTPMES
-002- 11 CDGNM EXTERN PASSTST
-002- 12 CTSD EXTERN DGNLIM
-002- 14 CDGSBS EXTERN MEMOP
-002- 15 CDGSBS EXTERN MEMOPI
-002- 17 CDGSFA EXTERN FOBNDX
-002- 19 CDGSIO DIRECT MASION
-002- 20 CDGSIO EXTERN MSELMSK
-002- 21 CDGSIO EXTERN MODSIZE
-002- 22 CDGSIO EXTERN STRSIZE
-002- 23 CDGSIO EXTERN STSELMSK
-002- 24 CDGSIO EXTERN FINAL
-002- 25 CDGSIO EXTERN CONTPREP
-002- 28 CSYSUB EXTERN EXCOFLPG
-002- 29 CSYSUB EXTERN INIT_OCC
-002- 30 CSYSUB EXTERN INITOST
-002- 31 CSYSUB EXTERN INSYNC
-002- 32 CSYSUB EXTERN RGCHKADR
-002- 33 CSYSUB EXTERN UPD_OTS
-002- 35 CTSD EXTERN MASERCNT
-002- 36 CTSD EXTERN PAGSCR
-002- 37 CTSD EXTERN DGREQ
-002- 39 MASACS EXTERN MASONOFF
-002- 41 TDATA EXTERN STRLIM
-002- 42 TDATA EXTERN WDSTR
-002- 43 TDATA EXTERN WPTBL

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 7

23:27:57 2/05/81 ****

CDGSDF W77D

-002- 01 # STORE DIAGNOSTIC REGISTER DEFINITIONS

0000010	-002- 04	CHKNDX	EQR	8	# INDEX REGISTER FOR REFRESH SELECT CHECK
0000010	-002- 05	F0BCNT	EQR	8	# SCRATCH REG FOR CURRENT FANOUT BOARD COUNT
0000011	-002- 07	RANGE	EQR	9	# NUMBER OF OFF-LINE WORDS TO BE TESTED
-002- 11 # REGISTER SYMBOLS USED IN THE MEMORY MODULE TEST:					
0000000	-002- 12	PATBAK	EQR	R0	# DATA READ/WRITTEN VIA OTHER-STORE
0000001	-002- 13	HIFATBAK	EQR	R1	# HIGH DATA READ/WRITTEN VIA OTHER-STORE
	-002- 14	HIFATBAKXT	SYN	R1XT	# DEFINE FOR IGNORANT MIMODE MACRO
0000002	-002- 15	BLKMSK	EQR	R2	# MASK TO BLOCK INTERRUPTS & CHECKS
0000003	-002- 16	STRSTAT	EQR	R3	# SAVE (WIDE) STORE STATUS
0000004	-002- 17	LSTPAT	EQR	R4	# PATTERN PREVIOUSLY WRITTEN
0000005	-002- 18	NXTPAT	EQR	R5	# PATTERN CURRENTLY BEING WRITTEN
0000006	-002- 19	PATNDX	EQR	R6	# NUMBER OF TEST PATTERN BEING MATCHED
0000007	-002- 20	MNRNDX	EQR	R7	# MINOR LOOP INDEX
0000010	-002- 21	ERSAVE	EQR	R8	# ERROR REGISTER AFTER OTHER-STORE READ
0000010	-002- 22	PARSLT	EQR	R8	# PARITY TEST RESULTS - FIELD ONLY
0000014	-002- 23	MEMADDR0	EQR	R12	# UPPER ADDRESS BITS OF MEMORY TEST AREA
0000015	-002- 24	MEMADDR1	EQR	R13	# LOWER ADDRESS BITS OF MEMORY TEST AREA
0000014	-002- 25	MEMADDR	EQURA	RA0	# BASE ADDRESS OF MEMORY TEST AREA
0000016	-002- 26	PATADDR	EQURA	RA1	# ADDRESS OF TEST PATTERN TABLE

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 8

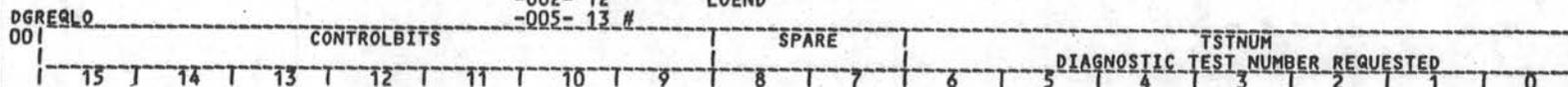
-002- 01 # FUNCTION OF BITS WITHIN I/O MESSAGE SENT TO STORE:
 -002- 02 CIOBUF LAYOUT

-002- 05 CINIT	ITEM	1	# MAIN STORE CONTROLLER INITIALIZATION
-002- 06 CKSTP	ITEM	1	# CONTROLLER CLOCK STEP
-002- 07 CKHLT	ITEM	1	# CONTROLLER CLOCK HALT
-002- 08 REFST	ITEM	1	# STORE REFRESH STOP
-002- 09 STORDER	ITEM	8	# 4/8 MAINTENANCE STATE CODE
-002- 10 O10TM	ITEM	1	# 0-10 BIT TIMING SHIFTER FAKE
-002- 11 LDFG	ITEM	1	# ORDER CHECKER EXCLUSIVE-OR LOAD
-002- 12 FSEL	ITEM	1	# STORE SELECT FORCE ON TIMING BOARD A
-002- 13 FBT09	ITEM	1	# FORCE BIT 15 ACTIVE IN MAIN SHIFTER
-002- 14	LOEND		
-002- 15	#		

CIOBUF				STORDER								REFST	CKHLT	CKSTP	CINIT	
00	FBT09	FSEL	LDFG	O10TM	4/8 MAINTENANCE STATE CODE											
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

-002- 01 # LAYOUT OF DIAGNOSTIC REQUEST WORD (DGREQ) WITH TEST NUMBER REQUESTED
 -002- 02 # NOTES: 1. REST OF BITS (CONTROLBITS) ARE DEFINED BY DGREQIP LAYOUT
 -002- 03 # FOUND IN PROLOGUE OF DIAGNOSTIC MONITOR (CDGNM)
 -002- 04 # 2. IF TEST NUMBER (TSTNUM) IS ZERO, THE ENTIRE DIAGNOSTIC
 -002- 05 # TEST SEQUENCE HAS BEEN REQUESTED
 -002- 06 # 3. STORAGE SPACE FOR DGREQ IS ALLOCATED IN CTSD

-002- 09 TSTNUM ITEM 7 # DIAGNOSTIC TEST NUMBER REQUESTED
 -002- 10 SPARE ITEM 2
 -002- 11 CONTROLBITS ITEM 7
 -002- 12 LOEND
 -005- 13 #



STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

-002- 01 # STORAGE FORMAT FOR MAIN STORE (MY & OTHER) SUBCHANNEL ADDRESS:

-002- 04	MSSCO	ITEM	16	#	ADDRESS OF MY STORE SUBCHANNEL 0
-002- 05	OSSCO	ITEM	16	#	ADDRESS OF OTHER STORE SUBCHANNEL 0
-002- 06	MSSC1	ITEM	16	#	ADDRESS OF MY STORE SUBCHANNEL 1
-002- 07	OSSC1	ITEM	16	#	ADDRESS OF OTHER STORE SUBCHANNEL 1
-002- 08	MSSC2	ITEM	16	#	ADDRESS OF MY STORE SUBCHANNEL 2
-002- 09	OSSC2	ITEM	16	#	ADDRESS OF OTHER STORE SUBCHANNEL 2
-002- 10	MSSC3	ITEM	16	#	ADDRESS OF MY STORE SUBCHANNEL 3
-002- 11	OSSC3	ITEM	16	#	ADDRESS OF OTHER STORE SUBCHANNEL 3
-002- 12		LOEND			
-005- 13		#			

MASCIOSC

00	MSSCO	ADDRESS OF MY STORE SUBCHANNEL 0
01	OSSCO	ADDRESS OF OTHER STORE SUBCHANNEL 0
02	MSSC1	ADDRESS OF MY STORE SUBCHANNEL 1
03	OSSC1	ADDRESS OF OTHER STORE SUBCHANNEL 1
04	MSSC2	ADDRESS OF MY STORE SUBCHANNEL 2
05	OSSC2	ADDRESS OF OTHER STORE SUBCHANNEL 2
06	MSSC3	ADDRESS OF MY STORE SUBCHANNEL 3
07	OSSC3	ADDRESS OF OTHER STORE SUBCHANNEL 3

15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

-002- 01 # WORDS IN PAGING BUFFER USED BY STORE
-002- 02 LOOPSUB ITEM 16
-002- 04 IOADDR ITEM 16
-002- 06 FOBNUM ITEM 16
-002- 08 TIMSAV ITEM 16
-002- 10 UPADDR ITEM ADDR
-002- 12 BASADDR ITEM ADDR

```

DIAGNOSTIC TESTS:
ADDRESS OF SUBSTITUTE FOR 'LOOP' IN TTY TROUBLE MESSAGE
I/O SUBCHANNEL ADDRESS FOR CURRENT STORE CONTROLLER
NUMBER OF MEMORY MODULES (OR FANOUT BOARDS) IN STORE CURRENTLY UNDER TEST
SAVE TIMING CONSTANT FOR TIMEOUT SUBROUTINE
ADDRESS OF NEXT BLOCK OF MEMORY TO BE UPDATED AFTER MEMORY PATTERN TEST
BASE ADDRESS OF STORE/MOD UNDER TEST

```

-002- 15 LOEND
-005- 16 #

```

PAGSCR

24	LOOPSUB
25	ADDRESS OF SUBSTITUTE FOR 'LOOP' IN TTY TROUBLE MESSAGE
26	I/O SUBCHANNEL ADDRESS FOR CURRENT STORE CONTROLLER
27	NUMBER OF MEMORY MODULES (OR FANOUT BOARDS) IN STORE CURRENTLY UNDER TEST
28	SAVE TIMING CONSTANT FOR TIMEOUT SUBROUTINE
29	UPADDR
30	BASADDR
31	

15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 ****

CDGSDF W77D

-002- 01 # STORE ERROR REGISTER (SER) BIT ASSIGNMENT:
 -002- 02 # NOTE: THESE INPUTS ARE ENABLED BY BIT09 OF THE MAIN SHIFTER

-002- 05 SER LAYOUT
 -002- 06 FOBTERB ITEM 1 # STORE TIMING ERROR FROM UNSELECTED FOB
 -002- 07 MPXER ITEM 1 # MULTIPLEXOR ERROR DETECTED BY CHECK
 BOARD
 -002- 09 CHDIF ITEM 1 # ERROR INDICATED BY STORE COMMAND
 CHECKER
 -002- 11 SCMER ITEM 1 # PREMATURE STORE COMPLETE(SCM)
 -002- 12 SHER ITEM 1 # ERROR IN MAIN SHIFTER OPERATION
 -002- 13 MODER ITEM 1 # ALL-SEEMS-WELL SIGNAL NOT RECEIVED FROM
 MODULE(FOB) ACCESSED DURING NORMAL CYCL
 -002- 15 RSELER ITEM 1 # ALL MEMORY MODULES(FOBS) NOT SELECTED
 DURING REFRESH
 -002- 17 FOBTERA ITEM 1 # STORE TIMING ERROR FROM SELECTED FOB
 -002- 18 FOBPARER ITEM 1 # PARITY ERROR DETECTED IN ADDRESS
 REACHING FANOUT BOARD(S)
 -004- 20 ##### FOBPARER WILL APPEAR AS FOBPARE IN PICTURES
 -002- 21 DHPER ITEM 1 # ERROR IN PARITY OVER HIGH DATA BITS
 -002- 22 PWER ITEM 1 # ERROR IN PARITY FOR WIDE STORE
 -002- 23 TBER ITEM 1 # ERROR DETECTED ON TIMING BOARD
 -002- 24 ITEM 1 # SPARE
 -002- 25 DLPER ITEM 1 # ERROR IN PARITY OVER LOW DATA BITS
 -002- 26 ITEM 1 # SPARE
 -002- 27 ITEM 1 # RESERVED
 -002- 28 LOEND
 -005- 29 #

SER	DLPER	TBER	PWER	DHPER	FOBPARE	FOBTERA	RSELER	MODER	SHER	SCMER	CHDIF	MPXER	FOBTERB			
001	SPARE	SPARE														
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 13

-002- 01 # STORE BUS CHECK BIT ASSIGNMENTS IN RESPONSE MESSAGE:
 -002- 02 # NOTE: THESE INPUTS ARE ENABLED BY ISOLATE CHECK (ISOCHK) I/O ORDER
 -002- 03 # W/ LOAD F & G (LDFG) OPTION SET

-002- 06 ISOBTS ITEM 7 # SD0-6,SD8-14 ISOLATE TEST RESULTS
 -002- 07 R^VDO ITEM 1 # SD7,SD15 ISOLATE TEST RESULT - ONLY
 GATED INTO BY LOAD I/O DECODER CHECK
 (LOADCHK) OPTION
 -002- 10 WDISOL ITEM 4 # RESULTS OF WIDE DATA (SD16-23) ISOLATE
 -002- 11 SDPCK ITEM 1 # RESULT OF STORE DATA PARITY ISOLATION
 -002- 12 ZEPCK ITEM 1 # RESULT OF EXTENDED (3 BIT) PARITY
 ISOLATE
 -002- 14 CTRLCK ITEM 1 # RESULT OF STORE CONTROL LEADS ISOLATION
 -002- 15 FXORG ITEM 1 # I/O ORDER CHECKER EXCLUSIVE-OR RESULT
 -002- 16 LOEND
 -005- 17 #

CHKWRD	FXORG	CTRLCK	ZEPCK	SDPCK	WDISOL	RSVDO	ISOBTS	SD0-6,SD8-14	ISOLATE TEST RESULTS							
001	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

STORE CONTROLLER DATA REGISTER TEST

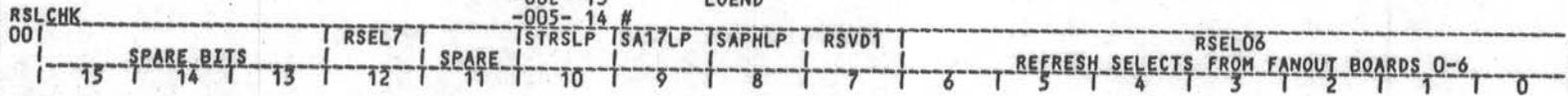
PR-1C935-50

23:27:57 2/05/81 ****

CDGSD F W77D

-002- 01 # REFRESH SELECT CHECK BIT ASSIGNMENTS IN STORE ERROR REGISTER
 -002- 02 # NOTE: THESE INPUTS ARE ENABLED BY EXTERNAL-ERROR-LOAD(EXERLD) ORDER

-002- 05	RSEL06	ITEM	7	# REFRESH SELECTS FROM FANOUT BOARDS 0-6
-002- 06	RSVD1	ITEM	1	# RESERVED - FOBTERA IS SET AFTER INIT
-002- 07	SAPHLP	ITEM	1	# STORE ADDRESS PARITY HIGH LOOP RESULT
-002- 08	SA17LP	ITEM	1	# STORE ADDRESS BIT 17 LOOP RESULT
-002- 09	STRSLP	ITEM	1	# STORE SELECT ADDRESS BITS LOOP RESULT
-002- 10		ITEM	1	# SPARE
-002- 11	RSEL7	ITEM	1	# REFRESH SELECT FROM FANOUT BOARD 7
-002- 12		ITEM	3	# SPARE BITS
-002- 13		LOEND		
-005- 14	#			



0010177 -002- 22 RSELBTS EQU M(RSEL06)IM(RSEL7) # MASK FOR REFRESH SELECT BITS
 0167600 -002- 23 RSELBTSN EQU (~RSELBTS)&B(1111111111111111)

```

-002- 01 # STORE STATUS REGISTER LAYOUT -- INCL BITS USED IN FIELD VERSION(S) OF TESTS
-002- 02 PLBAK ITEM 1 # PARITY LOW VALUE READ BACK
-002- 03 PHBAK ITEM 1 # PARITY HIGH VALUE READ BACK
-002- 04 ITEM 2 # UNUSED
-002- 05 OWDSTRO ITEM 1 # WIDE STORE INDICATOR BIT
-002- 06 SNGBTER ITEM 1 # SINGLE BIT ERROR FLAG
-002- 07 P/SBT ITEM 1 # REPEAT PASS THRU TEST INDICATOR
-002- 08 ITEM 9 # UNUSED
-002- 09 LOEND
-005- 10 #
    
```

STRSTAT

001	UNUSED										PASBT	SNGBTER	OWDSTRO	UNUSED		PHBAK	PLBAK
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 ****

CDGSDF W77D

-002- 01 # STORE DIAGNOSTIC SYMBOL LIBRARY:

0000000	-002- 05 BIT0	EQU	0		
0000001	-002- 06 BIT1	EQU	1		
0000002	-002- 07 BIT2	EQU	2		
0000003	-002- 08 BIT3	EQU	3		
0000004	-002- 09 BIT4	EQU	4		
0000005	-002- 10 BIT5	EQU	5		
0000006	-002- 11 BIT6	EQU	6		
0000007	-002- 12 BIT7	EQU	7		
0000010	-002- 13 BIT8	EQU	8		
0000011	-002- 14 BIT9	EQU	9		
0000012	-002- 15 BIT10	EQU	10		
0000013	-002- 16 BIT11	EQU	11		
0000014	-002- 17 BIT12	EQU	12		
0000015	-002- 18 BIT13	EQU	13		
0000016	-002- 19 BIT14	EQU	14		
0000017	-002- 20 BIT15	EQU	15		
0000020	-002- 21 BIT16	EQU	16		
0000021	-002- 22 BIT17	EQU	17		
0000022	-002- 23 BIT18	EQU	18		
0000023	-002- 24 BIT19	EQU	19		
0000004	-002- 25 FOUR	EQU	4		
	-002- 26 IOBRK	TEXT	' &'	# DELIMITERS FOR I/O ORDER STRINGS	
0000000	-002- 27 MOD0	EQU	0		
0000001	-002- 28 MOD1	EQU	1		
0000002	-002- 29 MOD2	EQU	2		
0000003	-002- 30 MOD3	EQU	3		
0000004	-002- 31 MOD4	EQU	4		
0000005	-002- 32 MOD5	EQU	5		
0000006	-002- 33 MOD6	EQU	6		
0000007	-002- 34 MOD7	EQU	7		
0000001	-002- 35 ONE	EQU	1		
0177777	-002- 36 ONES	EQU	0(177777)	# ALL ONE'S REGISTER	
0000200	-002- 37 OFLNCHNK	EQU	B(10000000)	# ON-LINE TESTS 128 WORDS/MINOR LOOP	
0007760	-002- 38 OFLNDONE	EQU	B(111111110000)	# SIGNAL THAT OFF-LINE MEMORY TEST IS DONE	
	-002- 41 #	NOTE: THE FOLLOWING CONSTANTS ALLOW GENERATION OF ARBITRARY PARITY. APPLICABLE			
	-002- 42 #	BITS ARE 0 & 1 SUCH THAT BIT 0 = VALUE FOR PARITY LOW			
	-002- 43 #		BIT 1 = VALUE FOR PARITY HIGH		
0600000	-002- 44 #			# DATA BITS (23-16)(15-8) (7-0)	
	0000020	-002- 45 PRHOLD	EQU	B(10000)	# SET PARITY (WIDE=)HIGH=0 LOW=0
	0000005	-002- 46 PRHOL1	EQU	B(00101)	# SET PARITY (WIDE=)HIGH=0 LOW=1
	0000012	-002- 47 PRH1LO	EQU	B(01010)	# SET PARITY (WIDE=)HIGH=1 LOW=0
	0000017	-002- 48 PRH1L1	EQU	B(01111)	# SET PARITY (WIDE=)HIGH=1 LOW=1
	0000002	-002- 49 PRWH1LO	EQU	B(00010)	# SET PARITY WIDE=0 HIGH=1 LOW=0

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 ****

CDGSDF W77D

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0000007 -002- 01 PRWOH1L1 EQU B(00111) # SET PARITY WIDE=0 HIGH=1 LOW=1
0000010 -002- 02 PRW1HOLD EQU B(01000) # SET PARITY WIDE=1 HIGH=0 LOW=0
0000015 -002- 03 PRW1HOL1 EQU B(01101) # SET PARITY WIDE=1 HIGH=0 LOW=1

0000003 -002- 05 THREE EQU 3
0000002 -002- 06 TWO EQU 2
0010000 -002- 07 WPPLKSIZE EQU 0(10000) # WRITE PROTECT BLOCK SIZE = 4K (4096)
0000000 -002- 08 ZERO EQU 0
0000000 -002- 09 ZEROS EQU 0 # ZERO CONSTANT
0000000 -002- 10 OTEST EQU 0 # VALUES FOR
0000001 -002- 11 1TEST EQU 1 # TEST MACRO

```

```

-002- 18 # MAIN MEMORY STATUS CONSTANTS ASSUMING FOLLOWING CONDITIONS:
-002- 19 # 1. MY STORE BUS PORT IS ISOLATED FROM INPUT FROM OTHER PROCESSOR
-002- 20 # 2. MAIN MEMORY CONTROLLER IS NOT UPDATING OTHER STORE
-002- 21 # 3. MY STORE BUS PORT IS ACCESSING MY STORE, E. NOT REVERSED

```

```

0106263 -002- 24 MTCRD EQU 0(106263) # MAIN MEMORY STATUS FOR MAINTENANCE READ
# OPERATION
0106203 -002- 26 MTCWR EQU 0(106203) # MAIN MEMORY STATUS FOR MAINTENANCE
# WRITE OPERATION
0106260 -002- 28 NMLRD EQU 0(106260) # MAIN MEMORY STATUS FOR NORMAL READ
# OPERATION
0106200 -002- 30 NMLWR EQU 0(106200) # MAIN MEMORY STATUS FOR NORMAL WRITE
# OPERATION
0106274 -002- 32 SPRRD EQU 0(106274) # MAIN MEMORY STATUS FOR SPARE READ
# OPERATION
0106214 -002- 34 SPRWR EQU 0(106214) # MAIN MEMORY STATUS FOR SPARE WRITE
# OPERATION

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 18

23:27:57 2/05/81 ****

CDGSDF W77D

-002- 01 # MAIN STORE I/O ORDERS:

0020016	-002- 04 ALLZERO EQU	0(20016)
	-002- 05 ALLZEROT TEXT	'APPLY ERRONEOUS ORDER(0/8) TO CHECKER'
0063476	-002- 06 BADCODE EQU	0(63476)
	-002- 07 BADCODET TEXT	'APPLY ERRONEOUS ORDER(5/8) TO CHECKER'
0047214	-002- 08 BUSCHK EQU	0(47214)
	-002- 09 BUSCHKT TEXT	'CHECK BUS ISOLATION USING SER'
0040374	-002- 10 CKTINIT EQU	0(40374) # STORE CONTROLLER INITIALIZATION (W/O WRITE PROTECT CLEAR)
	-002- 12 CKTINITT TEXT	'INITIALIZE ALL STORE CONTROLLER CIRCUITS'
0043310	-002- 13 CLRSER EQU	0(43310)
	-002- 14 CLRSERT TEXT	'CLEAR STORE ERROR REGISTER'
0046074	-002- 15 CLKSTOP EQU	0(46074)
	-002- 16 CLKSTOPT TEXT	'DISABLE STORE CONTROLLER CLOCK OUTPUT'
0046070	-002- 17 CLKSTRT EQU	0(46070)
	-002- 18 CLKSTRTT TEXT	'ENABLE STORE CONTROLLER CLOCK OUTPUT'
0146074	-002- 19 ERCOLL EQU	0(146074)
	-002- 20 ERCOLLT TEXT	'COLLECT ERRORS IN STORE ERROR REG'
0145454	-002- 21 EXERLD EQU	0(145454) # FAST REFRESH WITH REFRESH & CLOCK STOPPED
	-002- 23 EXERLDT TEXT	'ENABLE EXTERNAL LOAD OF STORE ERROR REG'
0145454	-002- 24 EXLDB15 EQU	0(145454)
	-002- 25 EXLDB15T TEXT	'ENABLE EXTERNAL LOAD OF STORE ERROR REG W/ ERROR COLLECT OPTION'
0005440	-002- 26 FASTREF EQU	0(5440)
	-002- 27 FASTREFT TEXT	'PERFORM FAST REFRESH ON STORE'
0045454	-002- 28 FASRNC EQU	0(45454)
	-002- 29 FASRNC T TEXT	'FORCE FAST REFRESH W/O CLOCK'
0145454	-002- 30 FFRMB15 EQU	0(145454)
	-002- 31 FFRMB15T TEXT	'FORCE FAST REFRESH & BIT 15'
0146074	-002- 32 FBIT09 EQU	0(146074) # FORCE BIT 09 (COMMON ERROR STROBE) ACTIVE
	-002- 34 FBIT09T TEXT	'COLLECT ERRORS IN STORE ERROR REGISTER'
0146064	-002- 35 FBIT09R EQU	0(146064)
	-002- 36 FBIT09RT TEXT	'COLLECT ERRORS IN SER W/ REFRESH ON'
0146076	-002- 37 FBRMB15 EQU	0(146076)
	-002- 38 FBRMB15T TEXT	'FANOUT BOARD REFRESH W/ ERROR COLLECT'
0046314	-002- 39 FCHKBT EQU	0(46314) # FORCE MAIN SHIFTER CHECK BIT ACTIVE
	-002- 40 FCHKBT T TEXT	'ACTIVATE MAIN SHIFTER CHECK BIT'
0046304	-002- 41 FCHKBTR EQU	0(46304)
	-002- 42 FCHKBTR T TEXT	'ACTIVATE SHIFTER CHECK BIT W/ REFRESH ENABLED'
0040574	-002- 43 FCHKOS EQU	0(40574)
	-002- 44 FCHKOST TEXT	'FORCE ZEROS TO MODULE SELECT XOR CHECK & WRITE PROTECT SELECT XOR CHECK CIRCUITS'
0041274	-002- 46 FCHK1S EQU	0(41274)
	-002- 47 FCHK1ST TEXT	'FORCE ONES TO MODULE SELECT XOR CHECK & WRITE PROTECT SELECT XOR CHECK CIRCUITS'
0140574	-002- 49 FCKOS15 EQU	0(140574)

STORE CONTROLLER DATA REGISTER TEST

PR-1c935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 19

0141274	-002- 01	FKK0S15T TEXT	'FORCE ZEROS TO CHECK CKTS W/ ERROR COLLECT'
	-002- 02	FKK1S15 EQU	0(141274)
0043074	-002- 03	FKK1S15T TEXT	'FORCE ONES TO CHECK CKTS W/ ERROR COLLECT'
	-002- 04	FDISIDLE EQU	0(43074)
0046156	-002- 05	FDISIDLET TEXT	'FORCE DISABLE OF IDLE FLOP'
	-002- 06	FLSHD EQU	0(46156) # FORCE LOAD THE SHIFTER WITH ZERO (W/CLOCK)
0046154	-002- 08	FLSHOT TEXT	'START ZERO INTO MAIN SHIFTER'
	-002- 09	FLSHONC EQU	0(46154) # PUT ZERO ON INPUT OF SHIFTER (W/O CLOCK)
0045434	-002- 11	FLSHONCT TEXT	'PRESENT ZERO TO INPUT OF MAIN SHIFTER'
	-002- 12	FNOWRT EQU	0(45434)
0045430	-002- 13	FNOWRTT TEXT	'FORCE WRITING OF ALL-ONES DATA'
	-002- 14	FNOWRTC EQU	0(45430)
0044474	-002- 15	FNOWRTCT TEXT	'FORCE WRITING OF ONES W/ CLOCK RUNNING'
	-002- 16	FNOTIM EQU	0(44474) # DISABLE TRANSMISSION OF TIMING SIGNALS TO FANOUT BOARDS
0044470	-002- 18	FNOTIMT TEXT	'INHIBIT TIMING SIGNALS TO FANOUT BOARDS'
	-002- 19	FNOTIMC EQU	0(44470)
0041550	-002- 20	FNOTIMCT TEXT	'BLOCK TIMING TO FANOUT BOARDS(W/CLOCK)'
	-002- 21	FOBMT EQU	0(41550)
0041474	-002- 22	FOBMTT TEXT	'FANOUT BOARD MAINTENANCE TEST'
	-002- 23	FRFSELO EQU	0(41474) # FORCE REFRESH SELECT SIGNAL FROM FOB0
0042334	-002- 24	FRFSELOT TEXT	'CANCEL SELECT FROM FOB0 DURING REFRESH'
	-002- 25	FRFSEL1 EQU	0(42334) # FORCE REFRESH SELECT SIGNAL FROM FOB1
0042534	-002- 26	FRFSEL1T TEXT	'CANCEL SELECT FROM FOB1 DURING REFRESH'
	-002- 27	FRFSEL2 EQU	0(42534) # FORCE REFRESH SELECT SIGNAL FROM FOB2
0043234	-002- 28	FRFSEL2T TEXT	'CANCEL SELECT FROM FOB2 DURING REFRESH'
	-002- 29	FRFSEL3 EQU	0(43234) # FORCE REFRESH SELECT SIGNAL FROM FOB3
0043434	-002- 30	FRFSEL3T TEXT	'CANCEL SELECT FROM FOB3 DURING REFRESH'
	-002- 31	FRFSEL4 EQU	0(43434) # FORCE REFRESH SELECT SIGNAL FROM FOB4
0044354	-002- 32	FRFSEL4T TEXT	'CANCEL SELECT FROM FOB4 DURING REFRESH'
	-002- 33	FRFSEL5 EQU	0(44354) # FORCE REFRESH SELECT SIGNAL FROM FOB5
0044554	-002- 34	FRFSEL5T TEXT	'CANCEL SELECT FROM FOB5 DURING REFRESH'
	-002- 35	FRFSEL6 EQU	0(44554) # FORCE REFRESH SELECT SIGNAL FROM FOB6
0045254	-002- 36	FRFSEL6T TEXT	'CANCEL SELECT FROM FOB6 DURING REFRESH'
	-002- 37	FRFSEL7 EQU	0(45254) # FORCE REFRESH SELECT SIGNAL FROM FOB7
0044334	-002- 38	FRFSEL7T TEXT	'CANCEL SELECT FROM FOB7 DURING REFRESH'
	-002- 39	FRREQA EQU	0(44334) # SET REFRESH REQUEST FLOP (DFFRREQ) ON TIMING BOARD (FA1071)
0040674	-002- 41	FRREQAT TEXT	'SET REFRESH REQUEST ON TIMING BOARD (FA1071)'
	-002- 42	FRREQB EQU	0(40674) # SET REFRESH REQUEST FLOP (RREQB) ON CHECK BOARD A (FA1062)
0040664	-002- 44	FRREQBT TEXT	'SET REFRESH REQ COPY ON CHECK BOARD A (FA1062)'
	-002- 45	FRREQBR EQU	0(40664)
0046074	-002- 46	FRREQBRT TEXT	'SET REFRESH REQUEST ON FA1062 W/ REFRESH ENABLED'
	-002- 47	FSELA EQU	0(46074)
0006074	-002- 48	FSELAT TEXT	'FORCE STORE SELECT(ISOLATE) SIGNAL ON FA1071'
	-002- 49	NSELA EQU	0(6074)

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 20

23:27:57 2/05/81 ****

CDGSDF W77D

0004714	-002- 01 NSELAT TEXT	'RESET STORE SELECT(ISOLATE) SIGNAL ON FA1071'
	-002- 02 FSELB EQU	0(4714) # FORCE STORE SELECT (TBBSEL) FROM CHECK BOARD A (FA1062)
0004710	-002- 04 FSELBT TEXT	'FORCE DUPLICATED STORE SELECT(SELB) ACTIVE'
	-002- 05 FSELBC EQU	0(4710)
0046516	-002- 06 FSELBCT TEXT	'FORCE DUPLICATED STORE SELECT(SELB) W/ CLOCK'
	-002- 07 FSTFBRM EQU	0(46516)
0042354	-002- 08 FSTFBRMT TEXT	'FORCE CONTROLLER & FANOUT BOARDS TO REFRESH'
	-002- 09 FSTNM EQU	0(42354) # FORCE MODE FLOP TO NORMAL STATE
0052354	-002- 10 FSTNMT TEXT	'FORCE MODE FLOP TO NORMAL STATE'
	-002- 11 FSTNMFR EQU	0(52354)
0042344	-002- 12 FSTNMFRT TEXT	'FORCE NORMAL MODE IN CONTROLLER & REFRESH TO FANOUT BOARDS'
	-002- 13 FSTNMR EQU	0(42344)
0046514	-002- 14 FSTNMRT TEXT	'FORCE MODE TO NORMAL W/ REFRESH ENABLED'
	-002- 15 FSTRM EQU	0(46514) # FORCE MODE FLOP TO REFRESH STATE
0046234	-002- 16 FSTRMT TEXT	'FORCE MODE FLOP TO REFRESH STATE'
	-002- 17 FTINWP EQU	0(46234)
0006220	-002- 18 FTINWPT TEXT	'FORCE IN-WRITE-PROTECT INHIBIT'
	-002- 19 IOGO EQU	0(6220)
0125252	-002- 20 IOGOT TEXT	'ENABLE ON-LINE I/O ACCESS TO OFF-LINE STORE'
	-002- 21 IOTSTA EQU	0(125252)
0052525	-002- 22 IOTSTAT TEXT	'I/O TEST PATTERN #1'
	-002- 23 IOTST5 EQU	0(52525)
0067214	-002- 24 IOTST5T TEXT	'I/O TEST PATTERN #2'
	-002- 25 ISOCHK EQU	0(67214) # CHECK ISOLATION FROM HIGHER STORES - PUT RESULTS OF TEST INTO STORE ERROR REGISTER (SER)
0027214	-002- 28 ISOCHKT TEXT	'ISOLATE FROM FAR STORES & VERIFY VIA SER'
	-002- 29 ISOCHKU EQU	0(27214)
0043454	-002- 30 ISOCHKUT TEXT	'APPLY ONES TO STORE DATA BUS(W/O ISOLATE)'
	-002- 31 KILL12 EQU	0(43454) # WIPE OUT SHIFTER BIT AT POSITION 12
0046071	-002- 32 KILL12T TEXT	'CLEAR SHIFTER BIT IN POSITION 12'
	-002- 33 MASCINT EQU	0(46071)
0006071	-002- 34 MASCINTT TEXT	'INITIALIZE STORE CONTROLLER'
	-002- 35 MSCITNS EQU	0(6071)
0046076	-002- 36 MSCITNST TEXT	'INITIALIZE STORE CONTROLLER W/O FORCE SELECT'
	-002- 37 MASCLK EQU	0(46076) # ACTIVATE STORE CONTROLLER CLOCK (CK) SIGNAL
0046074	-002- 39 MASCLKT TEXT	'FAKE FIRST HALF OF STORE CLOCK CYCLE'
	-002- 40 MASCLKN EQU	0(46074)
0047410	-002- 41 MASCLKNT TEXT	'FAKE SECOND HALF OF STORE CLOCK CYCLE'
	-002- 42 RDAD EQU	0(47410)
0047414	-002- 43 RDADT TEXT	'GATE STORE ADDRESS BUS(SA0-16,PL) TO STORE DATA BUS(SD0-15,PL,PH) W/ CLOCK'
	-002- 45 RDADNC EQU	0(47414)
0006070	-002- 46 RDADNCT TEXT	'GATE STORE ADDRESS BITS TO STORE DATA BUS'
	-002- 47 REFSTOP EQU	0(6070)
0006060	-002- 48 REFSTOPT TEXT	'INHIBIT REFRESHING OF STORE'
	-002- 49 REFSTRT EQU	0(6060)

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 21

23:27:57 2/05/81 ****

CDGSDF W77D

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0046060 -002- 01 REFSTRTT TEXT 'RESUME REGULAR REFRESHING OF STORE'
-002- 02 RFWISOL EQU 0(46060)
0047214 -002- 03 RFWISOLT TEXT 'RESUME REGULAR REFRESH W/ STORE ISOLATED'
-002- 04 RTNADHI EQU 0(47214) # RETURN UPPER ADDRESS (SA16-19) BITS &
# SELECTED CONTROL SIGNALS VIA I/O CHANNE

0044714 -002- 06 PTNADHIT TEXT 'LOAD SA17 & SAPH INTO SER'
-002- 07 SPAREA EQU 0(44714)
0047114 -002- 08 SPAREAT TEXT 'SPARE I/O ORDER'
-002- 09 SERREAD EQU 0(47114) # STORE ERROR REGISTER (SER) READ
-002- 10 SERREADT TEXT 'READ BACK STORE ERROR REGISTER(SER)'
0041634 -002- 11 STSBY EQU 0(41634)
-002- 12 STSBY TEXT 'SET STORE BUSY(SBY) SIGNAL'
0041630 -002- 13 STSBYCL EQU 0(41630)
-002- 14 STSBYCLT TEXT 'SET STORE BUSY(SBY) W/ CLOCK'
0007114 -002- 15 USERRED EQU 0(7114)
-002- 16 USERREDT TEXT 'READ STORE ERROR REG (W/O ISOLATE)'
0046061 -002- 17 WPCLR EQU 0(46061)
-002- 18 WPCLRT TEXT 'STORE INITIALIZE, INCL. WRITE PROTECT'
0052354 -002- 19 O1ONOR EQU 0(52354) # FORCE 0-10 (O1OTIM) SIGNAL & RESET
# REFRESH REQUEST FLOP
-002- 21 O1ONORT TEXT '0-10 TIMING(O1OTIM)THEN REFRESH REQUEST CLEAR'
0054334 -002- 22 O1OREF EQU 0(54334) # FORCE 0-10 (O1OTIM) SIGNAL & SET
# REFRESH REQUEST FLOP
-002- 24 O1OLEFT TEXT '0-10 TIMING(O1OTIM)THEN REFRESH REQUEST SET'
0056074 -002- 25 O1OTIM EQU 0(56074)
-002- 26 O1OTIMT TEXT 'FORCE 0-10 TIMING SIGNAL ACTIVE'

```

-002- 33 # STORE MAINTENANCE ORDER OPTIONS:

```

0010000 -002- 36 FAK010 EQU 0(10000) # FAKE 0-10 TIMING SIGNAL
-002- 37 # NOTE: NEXT OPTION BIT IS SHARED
0020000 -002- 38 FBUSRDN EQU 0(20000) # FORCE BUS READ INACTIVE TO BIT-SLICES
0020000 -002- 39 LOADCHK EQU 0(20000) # LOAD I/O ORDER CHECK RESULTS
0040000 -002- 40 ISOLATE EQU 0(40000) # ISOLATE THIS STORE FROM HIGHER STORES
0100000 -002- 41 FSBIT09 EQU 0(100000) # FORCE SHIFTER BIT 09 ACTIVE
0000001 -002- 42 INITBIT EQU 0(1) # INITIALIZE STORE CONTROLLER
-002- 43 # NOTE: NEXT OPTION BIT IS SHARED
0000002 -002- 44 FFOBRM EQU 0(2) # FORCE FANOUT BOARDS TO REFRESH MODE
0000002 -002- 45 STEPCLK EQU 0(2) # HALF-CYCLE OF STORE CLOCK
0000004 -002- 46 HALTCLK EQU 0(4) # DISABLE STORE CONTROLLER CLOCK
0000010 -002- 47 STOPREF EQU 0(10) #DISABLE AUTONOMOUS REFRESH

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 22

004

23:27:57 2/05/81 ****

CDGSDF W77D

0177771 -002- 01 CLOCKON EQU 0(177771)
0020016 -002- 02 FGLDNCFR EQU 0(20016)

MASK OUT CLOCK OFF OPTIONS
LOAD I/O ORDER CHECK RESULTS WITH CLOCK
STOPPED AND FANOUT BOARDS IN REFRESH
MODE

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 23

TEST 53

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *****
-001- 03 # **
-001- 04 # ** CU DIAGNOSTIC TEST NO. 53, MAIN_STORE_CONTROLLER_DATA_REGISTER
-001- 05 # **
-001- 06 # *****
-001- 07 # *****

```

0027662

-001- 10 CSTDF1 CSECT

0027662 01 000000 000145 ----- CDGNM

-001- 14 VFD 1,0 2,0 2,0 2,0 3,1 6,NBGN

```

18 # SEGMENT 1
19 # THIS SEGMENT CHECKS THE OUTPUTS AND INPUTS OF THE STORE DATA BITS THAT
20 # WERE NOT TESTED DURING THE STORE DATA BUS CHECK. THE INPUTS AND OUTPUTS TO
21 # THE STORE DATA FLOPS FROM THE MEMORY PLANES ARE DIVIDED INTO THOSE TO/FROM
22 # EVEN MEMORY MODULES ('A' SIGNALS) AND THOSE TO/FROM ODD MEMORY MODULES ('B'
23 # SIGNALS). THE FIRST PART OF THIS SEGMENT VERIFIES THAT ALL ZEROS AND ALL ONES
24 # CAN BE WRITTEN AND READ SUCCESSFULLY THRU THE STORE DATA FLOP AND THE FIRST
25 # MEMORY MODULE(MOD 0). THE SECOND PART OF THIS SEGMENT VERIFIES WRITES AND
26 # READS THRU THE SECOND MEMORY MODULE(MOD 1) ONLY IF THAT MODULE IS EQUIPPED.
27 # BOTH THE ALL-ZEROS AND THE ALL-ONES TESTS ARE DONE BY LOOPING THROUGH
28 # THE SAME CODE WITH DIFFERENT CONSTANTS AND TRANSFERRING TO A FAULT RESOLUTION
29 # ROUTINE OF DIAGNOSTIC MACROS IF A MISMATCH OCCURS.
30 # NOTES: 1. STORE OPERATIONS ARE PRESENTLY CONTROLLED BY THE ON-LINE
31 # PROCESSOR VIA THE MAINTENANCE CHANNEL. MANY WORDS COULD BE SAVED BY CONVERTING
32 # THE MEMORY OPERATION(MEMOP) MACROS AND SUBROUTINE INTO 'OTHER STORE' COMMANDS.
33 # 2. IF THE STORE UNDER TEST IS 26 DATA BITS WIDE, THE ADDITIONAL
34 # EIGHT(8) DATA FLOPS WILL BE CHECKED AS WELL.
35 # 3. THE ALL-ONES PATTERN IS CHECKED FIRST, BECAUSE THEN THE ALL-
36 # ZEROS TEST WILL ALSO COVER THE NORMAL READ(NRD) ENABLES ON THE OUTPUTS OF THE
37 # DATA FLOPS FOR STUCK-ACTIVE FAULTS. IF ANY OF THESE ENABLES IS STUCK ACTIVE,
38 # NORMAL LOAD WILL PRESERVE ONE(S) IN THE DATA FLOP(S), BECAUSE THEY WILL BE
39 # GATED ONTO THE BI-DIRECTIONAL BUS WHENEVER THAT STORE IS SELECTED(SEL).
40 # 4. THE ALL-ZEROS PATTERN ALSO CHECKS THAT NO MEMORY PLANE HAS ITS
41 # SENSE AMPLIFIER ENABLE (MEN1) STUCK ACTIVE (HIGH). IF THE ENABLE IS STUCK HIGH
42 # IT WILL PULL THE INTRASTORE DATA BUS LOW AND CAUSE A ONE TO BE LOADED INTO
43 # THE CORRESPONDING DATA FLOP IN THE CONTROLLER. THE FIRST PASS CHECKS THAT
44 # THIS FAULT DOES NOT EXIST ON ANY MEMORY PLANES EXCEPT FOR THOSE IN MODULE 0,
45 # AND THE SECOND PASS VERIFIES THAT THIS FAULT DOES NOT EXIST ON ANY MEMORY
46 # PLANES IN MODULE 0.

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 24

TEST 53

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # SEGMENT 2
02 # THIS SEGMENT CHECKS FOR A DATA STROBE ENABLE(DSTB) STUCK ACTIVE WITHIN
03 # A BIT-SLICED BOARD. A NORMAL READ IS ATTEMPTED WITH THE FORCE-NO-TIME(FNOTIM)
04 # ORDER ACTIVE. IF THIS ENABLE IS STUCK THEN THE NORMAL READ(NRD) AND REFRESH
05 # INHIBIT(REFI) WILL SUFFICE TO CREATE A READ DATA STROBE(RDDSTB). IN THIS CASE
06 # ALL ONES WILL BE READ BACK DIRECTLY FROM THE INACTIVE MEMORY PLANES. OTHERWISE
07 # ALL ZEROS WILL BE READ BACK FROM THE INITIALIZED STORE DATA FLOPS.
08 # NOTE: THIS TEST ENSURES THAT THE NORMAL LOAD AT THE BEGINNING OF THE READ
09 # CYCLE WILL NOT LOAD GARBAGE INTO THE INITIALIZED STORE DATA FLOPS.

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 25

TEST 53

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSD F W770

```

-001- 01 CSTDF1 0W      0(1),0(16)      # 12707
-001- 02      NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000001 *****

0027663
0027663 01 000001 000144 ----- CDGNM
0027664 01 000002 047707 ----- CDGNM
0027665 01 000003 000040 000001
0027667 01 000005 020005 ----- CDGNM
0027670 01 000006 106631 -----
0027671 01 000007 040043 000000 CDGNM
0027672 01 000010 000000 027674 0027674
0027674 01 000012 103501 000151 TTYTBL
0027676 01 000014 034500 043132 CTSD
0027700 01 000016 103240 -----
0027701 01 000017 004257 -----
0027702 01 000020 072652 ----- CDGNM
0027703 01 000021 037020 012757 CDGSIO
0027705 01 000023 172652 ----- CDGNM
0027706 01 000024 137020 012667 CDGSIO
0027710 01 000026 046071 -----
0027711 01 000027 171400 -----
0027712 01 000030 031000 027716 0027716
0027714 01 000032 037000 011162 CDGNM

05 DF1INIT PREPARE
-002- 06 # RTN_BLM
-002- 07 # TURN OFF AUTOMATIC RETURNS TO BLM EVERY 20 TABLE ENTRIES.
-002- 08 VFD 1,0 2,0 6,0 1,1 6,NRTNBLM

-002- 10 # SEND
-002- 11 # SEND 0(0000001) TO OFF-LINE MCHB.
-002- 12 # PARITY IS 0(2).
-002- 13 VFD 1,0 2,2 7,B(0111111) 6,N2SEND
-002- 14 ADDR 0(0000001),0(2)

-002- 16 # MICRO
-002- 17 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-002- 18 # TO FIELD = MSXT FROM FIELD = MCHBXF
-002- 19 VFD 1,0 2,1 7,0 6,NMICRO
-002- 20 VFD 8,MSXT 8,MCHBXF

-002- 22 # NO3_CODE
-002- 23 # PROCESS 3ACC CODED ROUTINE AT ADDRESS **2.
-002- 24 VFD 1,0 2,2 7,0 6,NN03CD
-002- 25 ADDR **2

-001- 27 # SUBSTITUTE 'MAS' FOR 'LOOP' IN TROUBLE PRINTOUT
-002- 28 LI R4,MXAS
-001- 29 STL R4,PAGSCR+24
-001- 30 # USE THE DIAGNOSTIC LOOP INDEX(LOOPIND) TO ACCESS THE PROPER I/Q
-001- 31 # SUBCHANNEL ADDRESS IN THE MAIN STORE CONTROLLER I/Q SUBCHANNEL TABLE
-001- 32 # (MASCIO SC) AND STORE IT IN THE ASSIGNED SLOT(IOADDR) IN PAGING BUFFER(PAGSCR)

-001- 34 ZR LOOPIND
-001- 35 SN LOOPIND,1 #ASSURE THAT LOOPIND IS ZERO BEFORE
# PROCEEDING WITH TEST

-001- 37 HN LOOP1.D,LOOPIND
-002- 38 BSA CONTPREP
-001- 39 HN LOOPIND,LOOPIND
-003- 40 BSA MASI0N
-002- 41 DATA MASCINT # INITIALIZE STORE CONTROLLER

-002- 43 GA
-002- 44 LAL RD,**4,RA0
-002- 45 BL INTRPN03

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSD F ISSUE 05 PAGE 26

TEST 53

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0027716 ,

01
-001- 02

EOW
NOTE

12707

***** THE LAST ADDRESS OVERWRITTEN IS 000033 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 27

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 53 SEGMENT 1
-001- 04 # *
-001- 05 # *****

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0027716 01 000034 000100 ----- CDGMM -001- 08 VFD 1,0 2,0 2,0 5,D(1) 6,NTESTSEG

```

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11 # THIS SEGMENT CHECKS THE OUTPUTS AND INPUTS OF THE STORE DATA BITS THAT
12 # WERE NOT TESTED DURING THE STORE DATA BUS CHECK. THE INPUTS AND OUTPUTS TO
13 # THE STORE DATA FLOPS FROM THE MEMORY PLANES ARE DIVIDED INTO THOSE TO/FROM
14 # EVEN MEMORY MODULES ('A' SIGNALS) AND THOSE TO/FROM ODD MEMORY MODULES ('B'
15 # SIGNALS). THE FIRST PART OF THIS SEGMENT VERIFIES THAT ALL ZEROS AND ALL ONES
16 # CAN BE WRITTEN AND READ SUCCESSFULLY THRU THE STORE DATA FLOP AND THE FIRST
17 # MEMORY MODULE(MOD 0). THE SECOND PART OF THIS SEGMENT VERIFIES WRITES AND
18 # READS THRU THE SECOND MEMORY MODULE(MOD 1) ONLY IF THAT MODULE IS EQUIPPED.
19 # BOTH THE ALL-ZEROS AND THE ALL-ONES TESTS ARE DONE BY LOOPING THROUGH
20 # THE SAME CODE WITH DIFFERENT CONSTANTS AND TRANSFERRING TO A FAULT RESOLUTION
21 # ROUTINE OF DIAGNOSTIC MACROS IF A MISMATCH OCCURS.
22 # NOTES: 1. STORE OPERATIONS ARE PRESENTLY CONTROLLED BY THE ON-LINE
23 # PROCESSOR VIA THE MAINTENANCE CHANNEL. MANY WORDS COULD BE SAVED BY CONVERTING
24 # THE MEMORY OPERATION(MEMOP) MACROS AND SUBROUTINE INTO 'OTHER STORE' COMMANDS.
25 # 2. IF THE STORE UNDER TEST IS 26 DATA BITS WIDE, THE ADDITIONAL
26 # EIGHT(8) DATA FLOPS WILL BE CHECKED AS WELL.
27 # 3. THE ALL-ONES PATTERN IS CHECKED FIRST, BECAUSE THEN THE ALL-
28 # ZEROS TEST WILL ALSO COVER THE NORMAL READ(NRD) ENABLES ON THE OUTPUTS OF THE
29 # DATA FLOPS FOR STUCK-ACTIVE FAULTS. IF ANY OF THESE ENABLES IS STUCK ACTIVE,
30 # NORMAL LOAD WILL PRESERVE ONE(S) IN THE DATA FLOP(S), BECAUSE THEY WILL BE
31 # GATED ONTO THE BI-DIRECTIONAL BUS WHENEVER THAT STORE IS SELECTED(SEL).
32 # 4. THE ALL-ZEROS PATTERN ALSO CHECKS THAT NO MEMORY PLANE HAS ITS
33 # SENSE AMPLIFIER ENABLE (MEM1) STUCK ACTIVE (HIGH). IF THE ENABLE IS STUCK HIGH
34 # IT WILL PULL THE INTRASTORE DATA BUS LOW AND CAUSE A ONE TO BE LOADED INTO
35 # THE CORRESPONDING DATA FLOP IN THE CONTROLLER. THE FIRST PASS CHECKS THAT
36 # THIS FAULT DOES NOT EXIST ON ANY MEMORY PLANES EXCEPT FOR THOSE IN MODULE 0,
37 # AND THE SECOND PASS VERIFIES THAT THIS FAULT DOES NOT EXIST ON ANY MEMORY
38 # PLANES IN MODULE 0.

```

```

-001- 41 # NO3 CODE
-001- 42 # PROCESS 3ACC CODED ROUTINE AT ADDRESS CKSDAO.
0027717 01 000035 040043 000000 CDGMM -001- 43 VFD 1,0 2,2 7,0 6,NN03CD
0027720 01 000036 000000 027722 0027722 -001- 44 ADDR .CKSDAO

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

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23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 28

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TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # CAN ONES & ZEROS BE WRITTEN/READ THRU STORE DATA FF AND FANOUT GATE A(000A)?

0027722					03	CKSDAO					
0027722	01	000040	103741	177777	04	LI	R14,ONES		# SDO-15 = ALL-ONES		
0027724	01	000042	003761	177777	05	LI	R15,ONES		# IF WIDE STORE, SD16-23 = ALL-ONES		
0027726	01	000044	003157	-----	06	LN	R6,PRH1L1		# PARITY HIGH & LOW = ONE		
0027727					08	CKSDALP					
0027727					09	SIOM	MASCINT				
0027727	01	000045	137020	012667	-002-	10	BSA	MASIO			
0027731	01	000047	046071	-----	-001-	11	DATA	MASCINT		# INITIALIZE STORE CONTROLLER	
0027732	01	000050	103561	106200		12	LI	R7,NMLWR		# MAIN MEMORY STATUS FOR NORMAL WRITE	
0027734	01	000052	031040	043140		13	LAL	R2,BASADDR,RAO			
0027736	01	000054	040061	-----		14	L	R3,1(RAO)		# ADDRESS FIRST MEMORY MODULE	
0027737						15	CALL	MEMOP			
0027737	01	000055	037020	013027	-001-	16	BSA	MEMOP			
0027741						17	NMLREAD	BASADDR,NOSPAR			
0027741	01	000057	103561	106260	-001-	18	LI	R7,B(1000110010110000)		# MAIN MEMORY STATUS	
0027743	01	000061	031040	043140		-001-	19	LAL	R2,BASADDR,RAO		
0027745	01	000063	041061	-----		-001-	20	LA	R3,1(RAO)		
0027746	01	000064	037020	013027	-002-	21	BSA	MEMOP			
0027750	01	000066	112403	-----		22	UNPK	MCHB			
0027751	01	000067	015076	-----		23	XR	R3,R14		# DATA SENT = DATA BACK?	
0027752	01	000070	055042	-----		24	BNC	RSFLTA		# IF NOT, GO TO FAULT RESOLUTION ROUTINE	
0027753	01	000071	003000	-----		25	ZR	RO		# CLEAR FOR PARITY RESULTS	
0027754	01	000072	057403	-----		26	TSRPL	MCHB			
0027755	01	000073	030000	-----		27	ICF	RO,0		# PUT PARITY LOW RECEIVED IN REG 0,BIT 0	
0027756	01	000074	057423	-----		28	TSRPH	MCHB			
0027757	01	000075	030001	-----		29	ICF	RO,1		# PUT PARITY HIGH RECEIVED IN REG 0,BIT 1	
0027760	01	000076	016546	000003		30	LRM	R6,R6,X(3)		# EXTRACT PH & PL	
0027762	01	000100	015006	-----		31	XR	RO,R6		# PARITY SENT = PARITY BACK ?	
0027763	01	000101	055031	-----		32	BNC	RSFLTA		# IF NOT, GO TO FAULT RESOLUTION ROUTINE	

34 # IF THIS IS A WIDE(26-BIT) STORE, RETRIEVE STORE DATA REG 1(SDR1) & CHECK

0027764	01	000102	030402	021637		36	LL	RO,STRLIM			
0027766	01	000104	024004	-----		37	TBN	RO,S(WDSTR)		# IS THIS A WIDE STORE ?	
0027767	01	000105	055016	-----		38	BNC	CKSDAND		# IF NOT, GO TO LOOP END	
0027770	01	000106	003401	114455		39	LI	RO,MCHBXT*(E(8))+SDR1XF		# RETRIEVE SDR1	
0027772	01	000110	007400	-----		40	LSR	MCHTR,RO			
0027773						41	LMCH	LDMIRL			
0027773					-001-	42	CALL	SLDMIRL			
0027773	01	000111	037020	111075	-002-	43	BSA	SLDMIRL			
0027775						44	LMCH	RTNMCHB			
0027775	01	000113	103401	000261	-001-	45	LI	RO,RTNMCHB			
0027777					-001-	46	CALL	SMCH			
0027777	01	000115	073027	-----	-002-	47	BSAI	SXMCH		# SUBROUTINE SMCH IS IN PROGRAM CSYSUB	
0030000	01	000116	112403	-----		48	UNPK	MCHB			
0030001	01	000117	016077	000377		49	IRM	R3,R15,X(00FF)		# DUPLICATE LOWER BITS(SDR1,BITS 0-7)	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 29

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0030003 01 000121 015077 -----
0030004 01 000122 055037 ----- 0030043
                                01 XR R3,R15
                                02 BNC RSHDFA # HIGH DATA SENT = HIGH DATA BACK?
                                                # IF NOT, GO TO FAULT RESOLUTION ROUTINE

0030005
0030005 01 000123 114356 -----
0030006 01 000124 054050 ----- 0030056
0030007 01 000125 003340 -----
0030010 01 000126 003360 -----
0030011 01 000127 003541 000020
0030013 01 000131 053714 ----- 0027727
                                04 CKSDAND
                                05 TZ R14 # WAS THIS ALL-ZEROS TEST?
                                06 BC CKSDBO # IF SO, TEST IS DONE
                                07 ZR R14 # SDO-15 = ALL-ZEROS
                                08 ZR R15 # SD16-23 = ALL-ZEROS
                                09 LI R6,PRHOLD # PARITY LOW & HIGH = ZERO
                                10 B CKSDALP # REPEAT FOR ALL-ZEROS PATTERN

```

14 # THESE ROUTINES RESOLVE FAILURE TO A PARTICULAR DATA/PARITY BIT

```

0030014
0030014 01 000132 112060 -----
0030015
0030015 01 000133 013400 ----- -001- 17 RSFLTA
0030016
0030016 01 000134 070523 ----- -001- 18 PACK MCHB # STORE COMPARE RESULTS
0030017
0030017 01 000135 025730 ----- -001- 19 MIMODE
0030020
0030020 01 000136 026750 ----- -001- 20 MI 0
                                21 L BR,RO
                                22 VFD 8,BRXT 8,ROXF # LOAD BR BITS 0&1 INTO PL&PH OF MTC CHAN
                                23 LDPAR DATA LDPARX
                                24 ZMINT
                                25 ZMINTX

0030021
0030021 01 000137 071400 ----- -001- 27 RTN_TBL *+4
0030022 01 000140 031000 030026 0030026 -001- 28 GA
0030024 01 000142 037000 011162 CDGNM -001- 29 LAL RO,*+4,RAO
                                30 BL INTRPN03

-001- 32 # COMPARE
-001- 33 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 34 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030026 01 000144 050014 ----- CDGNM -001- 35 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0030027 01 000145 000000 000000 -001- 36 ADDR 0(0000000)

-001- 38 # FAILBIT
-001- 39 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS STKSDA
-001- 40 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030031 01 000147 020020 ----- CDGNM -001- 41 VFD 1,0 2,1 5,0 2, 6,NFLBITS
0030032 01 000150 000002 ----- -001- 42 VFD 16,STKSDA

```

44 # STORE DATA BIT IS STUCK-AT-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD(S)

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 30

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # COMPARE
-001- 02 # COMPARE ON-LINE MCHB BIT PL TO 0.
-001- 03 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030033 01 000151 022414 ----- CDGNM -001- 04 VFD 1,LPINDX 2,1 1,0 1,SUCCES 5,PL 6,NCOMPARE
0030034 01 000152 000000 ----- -001- 05 VFD 15,0 1,0

-001- 07 # FAILTST
-001- 08 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS STKSDAPR.
0030035 01 000153 020017 ----- CDGNM -001- 09 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030036 01 000154 000022 ----- -001- 10 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 11 VFD 16,STKSDAPR

-001- 13 # COMPARE
-001- 14 # COMPARE ON-LINE MCHB BIT PH TO 0.
-001- 15 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030037 01 000155 022514 ----- CDGNM -001- 16 VFD 1,LPINDX 2,1 1,0 1,SUCCES 5,PH 6,NCOMPARE
0030040 01 000156 000000 ----- -001- 17 VFD 15,0 1,0

-001- 19 # FAILTST
-001- 20 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS STKSDAPR.
0030041 01 000157 020017 ----- CDGNM -001- 21 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030042 01 000160 000022 ----- -001- 22 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 23 VFD 16,STKSDAPR

```

25 # STORE DATA PARITY IS STUCK-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD(S)

```

0030043 29 RSWDFA
0030043 01 000161 112060 ----- 30 PACK MCHB # STORE COMPARE RESULTS
0030044 31 RTN_TBL **4
0030044 01 000162 071400 ----- -001- 32 GA
0030045 01 000163 031000 030051 0030051 -001- 33 LAL RO,**4,RAD
0030047 01 000165 037000 011162 CDGNM -001- 34 BL INTRPN03

-001- 36 # COMPARE
-001- 37 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 38 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030051 01 000167 050014 ----- CDGNM -001- 39 VFD 1,LPINDX 2,2 1,1 1,SUCCES 5,0 6,NCOMPARE
0030052 01 000170 000000 000000 -001- 40 ADDR 0(0000000)

-001- 42 # FAILBIT
-001- 43 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS STKSDA+9XX
-001- 44 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030054 01 000172 020020 ----- CDGNM -001- 45 VFD 1,0 2,1 5,0 2, 6,NFLBITS
0030055 01 000173 000013 137020 -001- 46 VFD 16,STKSDA+9

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 31

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # HIGH DATA BIT IS STUCK-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD(S)

08 # CAN ONES & ZEROS BE WRITTEN/READ THRU STORE DATA FLOP(SDFF) & FANOUT GATE B?

	10	CSTDF1	OW	0(174)	# 12582	
-001-	11		NOTE	*****	THE FIRST ADDRESS	OVERWRITTEN IS 000174 *****
0030056	14	CKSDBO				
0030056	15		CALL	FOBNDX1		
0030056 01 000174 137020 013171 CDGSFA	-001-	16	BSA	FOBNDX1		
0030060	17		EOW		# 12582	
	-001-	18		NOTE	*****	THE LAST ADDRESS OVERWRITTEN IS 000175 *****
0030060 01 000176 104217 -----	21		SN	F0BCNT,1		
0030061 01 000177 014210 -----	22		TZ	F0BCNT	# WAS THERE ONLY ONE FANOUT BOARD?	
0030062 01 000200 054137 ----- 0030221	23		BC	CKSDXT	# IF SO, SKIP NEXT TEST	
0030063 01 000201 003741 177777	25		LI	R14,ONES	# SD0-15 = ALL-ONES	
0030065 01 000203 003761 177777	26		LI	R15,ONES	# IF WIDE STORE, SD16-23 = ALL-ONES	
0030067 01 000205 003157 -----	27		LN	R6,PRH1L1	# PARITY HIGH & LOW = ONE	
0030070	29	CKSDBLP				
0030070	30		SIOM	MASCINT		
0030070 01 000206 137020 012667 CDGSIO	-002-	31	BSA	MASION		
0030072 01 000210 046071 -----	-001-	32	DATA	MASCINT	# INITIALIZE STORE CONTROLLER	
0030073 01 000211 130700 043140 CTSD	33		LL	R12,BASADDR		
0030075 01 000213 030720 012754 CDGSIO	34		LL	R13,MODSIZE	# POINT TO SECOND MEMORY MODULE	
	35	CSTDF1	OW	0(215)	# 12582	
	-001-	36		NOTE	*****	THE FIRST ADDRESS OVERWRITTEN IS 000215 *****
0030077 01 000215 037000 115262 0000167	39	CPATCH3	BGNP	0(167)	# 12582	
	-001-	40	BL	XXX353		
	-001-	41		NOTE	CPATCH3 'CSECT'	
	-001-	42		NOTE	*****	PATCH AREA BEGINS AT 000167 *****
0115262	-001-	45	XXX353	PATCHAREA		
0115262 10 000167 116414 000017	46		LRM	RO,R12,0(17)		
0115264	47				# LOAD BASADDR BITS 0 THRU 3	
0115264 10 000171 010402 -----	48		RRN	RO,2		
0115265	49				# SHIFT BITS TO POINT TO PROPER STORE IN	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 32

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

						MASTYPE			
0115265	10	000172	030422	020616	TDATA	02	LL	R1,MASTYPE	
0115267	10	000174	024420	-----		03	TBR	R1,RO	
0115270	10	000175	055002	-----	0115272	04			# TEST TYPE OF MEMORY THIS STORE
0115271	10	000176	004702	-----		05	BNC	CONTINUE	# SKIP IF NOT 16K TYPE
0115272	10	000177	103561	106200		06	AN	R12,2	# IF 16K TYPE INC ADDR TO GET TO ODD MOD
0115274	10	000201	037000	030101	0000217	07			
						08			
						09	CONTINUE		
						10	LI	R7,NMLWR	
						11			# MAIN MEMORY STATUS FOR NORMAL WRITE
						12	ENDP		# 12582
						13	BL	XXX355	
						14	NOTE	***** LAST PATCH ADDRESS USED IS 000202 *****	
						15	NOTE	***** NUMBER OF PATCH WORDS USED IS 12 (DECIMAL) *****	
0030101						-001- 18	XXX355	OWCONTINUE	
0030101	01	000217	106054	-----		19	LR	R2,R12	
0030102						20	EOW		# 12582
						-001- 21	NOTE	***** THE LAST ADDRESS OVERWRITTEN IS 000217 *****	
0030102	01	000220	006075	-----		24	LR	R3,R13	
0030103						25	CALL	MEMOP	
0030103	01	000221	037020	013027	CDGSBS	-001- 26	BSA	MEMOP	
0030105						27	NMLREAD	RA0,NOSPAR	
0030105	01	000223	103561	106260		-001- 28	LI	R7,B(1000110010110000)	# MAIN MEMORY STATUS
0030107	01	000225	006054	-----		-001- 29	LR	R2,R12	
0030110	01	000226	006075	-----		-001- 30	LR	R3,R13	
0030111	01	000227	037020	013027	CDGSBS	-002- 31	BSA	MEMOP	
0030113	01	000231	112403	-----		32	UNPK	MCHB	
0030114	01	000232	015076	-----		33	XR	R3,R14	# DATA SENT = DATA BACK?
0030115	01	000233	055042	-----	0030157	34	BNC	RSFLTB	# IF NOT, GO TO FAULT RESOLUTION ROUTINE
0030116	01	000234	003000	-----		35	ZR	RO	# CLEAR FOR PARITY RESULTS
0030117	01	000235	057403	-----		36	TSRPL	MCHB	
0030120	01	000236	030000	-----		37	ICF	RO,0	# PUT PARITY LOW RECEIVED IN REG 0,BIT 0
0030121	01	000237	057423	-----		38	TSRPH	MCHB	
0030122	01	000240	030001	-----		39	ICF	RO,1	# PUT PARITY HIGH RECEIVED IN REG 0,BIT 1
0030123	01	000241	016546	000003		40	LRM	R6,R6,X(3)	# EXTRACT PH & PL
0030125	01	000243	015006	-----		41	XR	RO,R6	# PARITY SENT = PARITY BACK ?
0030126	01	000244	055031	-----	0030157	42	BNC	RSFLTB	# IF NOT, GO TO FAULT RESOLUTION ROUTINE
0030127	01	000245	030402	021637	TDATA	45	LL	RO,STRLIM	
0030131	01	000247	024004	-----	TDATA	46	TBN	RO,S(WDSTR)	# IS THIS A WIDE STORE ?
0030132	01	000250	055016	-----	0030150	47	BNC	CKSDBND	# IF NOT, GO TO LOOP END
0030133	01	000251	003401	114455		48	LI	RO,MCHBXT*(E(8))+SDR1XF	# RETRIEVE SDR1
0030135	01	000253	007400	-----		49	LSR	MCHTR,RO	

STORE CONTROLLER DATA REGISTER TEST

PR-10935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 33

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

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0030136          01          01          LMCH  LDMIRL
0030136          01          02          CALL  SLDMIRL
0030136 01 000254 037020 111075 CSYSUB -001- 03          BSA   SLDMIRL
0030140          01          04          LMCH  RTNMCHB
0030140 01 000256 103401 000261 -001- 05          LI    RO,RTNMCHB
0030142          01          06          CALL  SMCH
0030142 01 000260 073027 ----- -002- 07          BSAI  SXMCH
0030143 01 000261 112403 -----          08          UNPK  MCHB
0030144 01 000262 016077 000377          09          IRM   R3,R15,X(OOFF)
0030146 01 000264 015077 -----          10          XR    R3,R15
0030147 01 000265 055037 ----- 0030206 11          BNC   RSWDFB
                                     # SUBROUTINE SMCH IS IN PROGRAM CSYSUB
                                     # DUPLICATE LOWER BITS(SDR1,BITS 0-7)
                                     # HIGH DATA SENT = HIGH DATA BACK?
                                     # IF NOT, GO TO FAULT RESOLUTION ROUTINE

0030150          01          13          CKSDBND
0030150 01 000266 114356 -----          14          TZ    R14
0030151 01 000267 054050 ----- 0030221 15          BC    CKSDXT
0030152 01 000270 003340 -----          16          ZR    R14
0030153 01 000271 003360 -----          17          ZR    R15
0030154 01 000272 003541 000020          18          LI    R6,PRHOLD
0030156 01 000274 053712 ----- 0030070 19          B     CKSDBLP
                                     # WAS THIS ALL-ZEROS TEST?
                                     # IF SO, TEST IS DONE
                                     # SDO-15 = ALL-ZEROS
                                     # SD16-23 = ALL-ZEROS
                                     # PARITY HIGH & LOW = ZERO
                                     # REPEAT FOR ALL-ZEROS PATTERN

                                     23 # THESE ROUTINES RESOLVE FAILURE TO A PARTICULAR DATA/PARITY BIT

0030157          01          25          RSFLTB
0030157 01 000275 112060 -----          26          PACK  MCHB
0030160          01          27          MIMODE
0030160 01 000276 013400 ----- -001- 28          MI    O
0030161          01          29          L     BR,RO
0030161 01 000277 070523 ----- -001- 30          VFD   8,BRXT 8,ROXF
0030162          01          31          LDPAR
0030162 01 000300 025730 ----- -001- 32          DATA LDPARX
0030163          01          33          ZMINT
0030163 01 000301 026750 ----- -001- 34          DATA ZMINTX
0030164          01          35          RTN_TBL **4
0030164 01 000302 071400 ----- -001- 36          GA
0030165 01 000303 031000 030171 0030171 -001- 37          LAL   RD,**4,RAD
0030167 01 000305 037000 011162 CDGNM -001- 38          BL    INTRPN03
                                     # STORE COMPARE RESULTS
                                     # LOAD BR BITS 0&1 INTO PL&PH OF MTC CHAN

                                     -001- 40 # COMPARE
                                     -001- 41 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
                                     -001- 42 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030171 01 000307 050014 ----- CDGNM -001- 43          VFD   1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0030172 01 000310 000000 000000 -001- 44          ADDR  0(0000000)

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 34

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # FAILBIT
-001- 02 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS STKSDBX
-001- 03 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030174 01 000312 020020 ----- CDGNM -001- 04 # VFD 1,0 2,1 5,0 2, 6,NFLBITS
0030175 01 000313 000033 ----- -001- 05 # VFD 16,STKSDB

07 # STORE DATA BIT IS STUCK-AT-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD 0(2,4,6)
-001- 08 # COMPARE
-001- 09 # COMPARE ON-LINE MCHB BIT PL TO 0.
0030176 01 000314 022414 ----- CDGNM -001- 10 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030177 01 000315 000000 ----- -001- 11 # VFD 1,LPINDX 2,1 1,0 1,SUCCES 5,PL 6,NCOMPARE
-001- 12 # VFD 15,0 1,0

-001- 14 # FAILTST
-001- 15 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS STKSDBPR.
0030200 01 000316 020017 ----- CDGNM -001- 16 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030201 01 000317 000053 ----- -001- 17 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 18 # VFD 16,STKSDBPR

-001- 20 # COMPARE
-001- 21 # COMPARE ON-LINE MCHB BIT PH TO 0.
0030202 01 000320 022514 ----- CDGNM -001- 22 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030203 01 000321 000000 ----- -001- 23 # VFD 1,LPINDX 2,1 1,0 1,SUCCES 5,PH 6,NCOMPARE
-001- 24 # VFD 15,0 1,0

-001- 26 # FAILTST
-001- 27 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS STKSDBPR.
0030204 01 000322 020017 ----- CDGNM -001- 28 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030205 01 000323 000053 ----- -001- 29 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 30 # VFD 16,STKSDBPR

32 # STORE DATA PARITY IS STUCK-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD 0(2,4,6)

0030206 36 RSWDFB
0030206 01 000324 112060 ----- 37 PACK MCHB
0030207 38 RTN_TBL **4
0030207 01 000325 071400 ----- -001- 39 # GA
0030210 01 000326 031000 030214 0030214 -001- 40 # LAL RD,**4,RAO
0030212 01 000330 037000 011162 CDGNM -001- 41 # BL INTRPNO3

-001- 43 # COMPARE
-001- 44 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
0030214 01 000332 050014 ----- CDGNM -001- 45 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030215 01 000333 000000 000000 -001- 46 # VFD 1,LPINDX 2,2 1,1 1,SUCCES 5,0 6,NCOMPARE
-001- 47 # ADDR 0(0000000)

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 35

TEST 53 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # FAILBIT
-001- 02 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS STKSDB+9XX
-001- 03 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030217 01 000335 020020 ----- CDGNM -001- 04 VFD 1,0 2,1 5,0 2, 6,NFLBITS
0030220 01 000336 000044 ----- -001- 05 VFD 16,STKSDB+9
07 HIGH DATA BIT IS STUCK-ONE/ZERO BETWEEN CONTROLLER & FANOUT BOARD 0(2,4,6)

```

0030221

12 CKSDXT

```

0030221 15 RTN_TBL **4
0030221 01 000337 171400 ----- -001- 16 GA
0030222 01 000340 031000 030226 0030226 -001- 17 LAL RD,**4,RAO
0030224 01 000342 037000 011162 CDGNM -001- 18 BL INTRPN03

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 36

TEST 53 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W770

```

-001- 01 # *****
-001- 02 # *
-011- 03 # * TEST 53 SEGMENT 2
-001- 04 # *
-001- 05 # *****

```

```

0030226 01 000344 000200 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(2) 6,NTESTSEG

```

```

11 # THIS SEGMENT CHECKS FOR A DATA STROBE ENABLE(DSTB) STUCK ACTIVE WITHIN
12 # A BIT-SLICED BOARD. A NORMAL READ IS ATTEMPTED WITH THE FORCE-NO-TIME(FNOTIM)
13 # ORDER ACTIVE. IF THIS ENABLE IS STUCK THEN THE NORMAL READ(NRD) AND REFRESH
14 # INHIBIT(REFI) WILL SUFFICE TO CREATE A READ DATA STROBE(RDDSTB). IN THIS CASE
15 # ALL ONES WILL BE READ BACK DIRECTLY FROM THE INACTIVE MEMORY PLANES. OTHERWISE
16 # ALL ZEROS WILL BE READ BACK FROM THE INITIALIZED STORE DATA FLOPS.
17 # NOTE: THIS TEST ENSURES THAT THE NORMAL LOAD AT THE BEGINNING OF THE READ
18 # CYCLE WILL NOT LOAD GARBAGE INTO THE INITIALIZED STORE DATA FLOPS.

```

```

-001- 21 # NO3_CODE
-001- 22 # PROCESS 3ACC CODED ROUTINE AT ADDRESS CKSDDS.
0030227 01 000345 040043 000000 CDGNM -001- 23 VFD 1,0 2,2 7,0 6,NNO3CD
0030230 01 000346 000000 030232 0030232 -001- 24 ADDR CKSDDS

```

```

0030232 27 CKSDDS
0030232 28 SIOM MASCINT
0030232 01 000350 137020 012667 CDGSIO -002- 29 BSA MASIOM
0030234 01 000352 046071 103401 -001- 30 DATA MASCINT # INITIALIZE STORE CONTROLLER

```

```

32 # SET UP MAINTENANCE STATE THAT FORCES THE STORE DATA BUS INTO THE READ
33 # DIRECTION TO PRESERVE ZEROS IN THE CONTROLLER DATA FLOPS DURING NORMAL LOAD
34 # NOTE: ALSO REQUIRED FOR WIDE STORES TO GUARANTEE ZERO PARITY IS CALCULATED

```

```

0030235 01 000353 103401 114615 36 LI RO,MCHBXT*(E(8))+MSXF # RETRIEVE MAINTENANCE STATE REG
0030237 01 000355 007400 ----- 37 LSR MCHTR,RO
0030240 38 LMCH LDMIRL
0030240 -001- 39 CALL SLDMIRL
0030240 01 000356 037020 111075 CSYSUB -002- 40 BSA SLDMIRL
0030242 41 LMCH RTNMCHB
0030242 01 000360 103401 000261 -001- 42 LI RO,RTNMCHB
0030244 -001- 43 CALL SMCH
0030244 01 000362 073027 ----- -002- 44 BSAI SXMCH # SUBROUTINE SMCH IS IN PROGRAM CSYSUB
0030245 01 000363 112403 ----- 45 UNPK MCHB
0030246 01 000364 026077 ----- 46 SBN R3,S(STR1S) # JAM STORE COMMAND BIT(SC3) ACTIVE(LOW)
0030247 01 000365 012000 ----- 47 PACK MCHTR
0030250 -001- 48 LMCH LDMCHB
-001- 49 CALL SLDMCHB

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 37

```

TEST 53 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W770

0030250	01	000366	037020	111072	CSYSUB	-002-	01	BSA	SLDMCHB	
0030252	01	000370	103401	106631			02	LI	RO,MSXT*(8)+MCHB*F	
0030254	01	000372	007400	-----			03	LSR	MCHTR,RO	
0030255							04	LMCH	LDMIRL	
0030255	01	000373	037020	111075	CSYSUB	-001-	05	CALL	SLDMIRL	
0030257							06	BSA	SLDMIRL	
0030257	01	000375	137020	012667	CDGSIO	-002-	07	SIOM	FNOTIMC	
0030261	01	000377	044470	-----			08	BSA	MASIOB	
0030262							09	DATA	FNOTIMC	# BLOCK TIMING TO FANOUT BOARDS(W/CLOCK)
0030262	01	000400	103561	106260			10	NMLREAD	BASADDR	
0030264	01	000402	031040	043140	CTSD	-001-	11	LI	R7,B(1000110010110000)	# MAIN MEMORY STATUS
0030266	01	000404	041061	-----			12	LAL	R2,BASADDR,RAO	
0030267	01	000405	003140	-----			13	LA	R3,1(RAO)	
0030270	01	000406	037020	013027	CDGSBS	-001-	14	ZR	R6	# NO SPECIAL PARITY
0030272							15	BSA	MEMOP	
0030272	01	000410	171400	-----			16	RTN_TBL	**4	
0030273	01	000411	031000	030277	0030277	-001-	17	GA		
0030275	01	000413	037000	011162	CDGNM	-001-	18	LAL	RO,**4,RAO	
							19	BL	INTRPN03	
							21	#	COMPARE	
							22	#	COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(000000).	
0030277	01	000415	050014	-----	CDGNM	-001-	23	#	IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.	
0030300	01	000416	000000	000000		-001-	24	VFD	1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE	
						-001-	25	ADDR	0(0000000)	
							27	#	FAILBIT	
							28	#	FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS DSS1SDXX	
0030302	01	000420	020020	-----	CDGNM	-001-	29	#	WHERE XX IS THE LOWEST FAILING BIT NUMBER.	
0030303	01	000421	000064	-----		-001-	30	VFD	1,0 2,1 5,0 2, 6,NFLBITS	
						-001-	31	VFD	16,DSS1SD	
							33	#	COMPARE	
							34	#	COMPARE ON-LINE MCHB BIT PL TO 0.	
0030304	01	000422	022414	-----	CDGNM	-001-	35	#	IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.	
0030305	01	000423	000000	-----		-001-	36	VFD	1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE	
						-001-	37	VFD	15,0 1,0	
							39	#	FAILTST	
							40	#	FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS DSS1SDPL.	
0030306	01	000424	020017	-----	CDGNM	-001-	41	#	DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.	
0030307	01	000425	0C0104	-----		-001-	42	VFD	1,0 2,1 5,0 2,0 6,NFAILTST	
						-001-	43	VFD	16,DSS1SDPL	
							45	#	COMPARE	
							46	#	COMPARE ON-LINE MCHB BIT PH TO 0.	
0030310	01	000426	022514	-----	CDGNM	-001-	47	#	IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.	
0030311	01	000427	000000	-----		-001-	48	VFD	1,LPINDX 2,1 1,0 1,SUCCESS 5,PH 6,NCOMPARE	
						-001-	49	VFD	15,0 1,0	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 38

TEST 53 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDf W77D

```

-001- 02 #      FAILTST
-001- 03 #      FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS DSS1SDPH.
-001- 04 #      DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030312 01 000430 020017 ----- CDGNM
0030313 01 000431 000105 -----
-001- 05 #      VFD      1,0 2,1 5,0 2,0 6,NFAILTST
-001- 06 #      VFD      16,DSS1SDPH

      08 # STORE DATA INPUT GATING IS STUCK ACTIVE ON THE BIT-SLICED BOARD INDICATED

-001- 11 #      WIDE_TST
-001- 12 #      IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS CKSDDSXT.
0030314 01 000432 040046 000000 CDGNM
0030315 01 000433 000000 030332 0030332
-001- 13 #      VFD      1,0 2,2 7,0 6,NWDTST
-001- 14 #      ADDR      CKSDDSXT

-001- 16 #      MICRO
-001- 17 #      PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 18 #      TO FIELD = MCHBXT FROM FIELD = SDR1XF
0030317 01 000435 020005 ----- CDGNM
0030320 01 000436 114455 -----
-001- 19 #      VFD      1,0 2,1 7,0 6,NMICRO
-001- 20 #      VFD      8,MCHBXT 8,SDR1XF

-001- 22 #      RETRN
-001- 23 #      RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0030321 01 000437 007710 ----- CDGNM
-001- 24 #      VFD      1,0 2,0 7,B(0111111) 6,NRETRN

-001- 26 #      MASK
-001- 27 #      MASK CONTENTS OF ON-LINE MCHB WITH 0(0177400).
0030322 01 000440 040013 ----- CDGNM
0030323 01 000441 000000 177400
-001- 28 #      VFD      1,0 2,2 7,0 6,NMASK
-001- 29 #      ADDR      0(0177400)

-001- 31 #      COMPARE
-001- 32 #      COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 33 #      IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030325 01 000443 050014 ----- CDGNM
0030326 01 000444 000000 000000
-001- 34 #      VFD      1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 35 #      ADDR      0(0000000)

-001- 37 #      FAILBIT
-001- 38 #      FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS DSS1SD+8XX
-001- 39 #      WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030330 01 000446 020020 ----- CDGNM
0030331 01 000447 000074 -----
-001- 40 #      VFD      1,0 2,1 5,0 2, 6,NFLBITS
-001- 41 #      VFD      16,DSS1SD+8

      43 # DATA STROBE(DST) GATING SIGNAL IS STUCK ACTIVE ON A STORE DATA FLOP(FFSD16-23)

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 39

TEST 53 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF

W77D

01 # CLEAR THE MAINTENANCE STATE BEFORE EXITTING

0030332

					03 CKSDDSXT			
					-001- 04 #	NO3_CODE		
					-001- 05 #	PROCESS	3ACC	CODED ROUTINE AT ADDRESS CLRSC3.
0030332	01	000450	040043	000000	CDGMM	-001- 06	VFD	1,0 2,2 7,0 6,NN03CD
0030333	01	000451	000000	030335	0030335	-001- 07	ADDR	CLRSC3
0030335						09	CLRSC3	
0030335	01	000453	103401	114615		10	LI	RO,MCHBXT+E(8)+MSXF
0030337	01	000455	007400	-----		11	LSR	MCHTR,RO
0030340						12	LMCH	LDMIRL
0030340						-001- 13	CALL	SLDMIRL
0030340	01	000456	037020	111075	CSYSUB	-002- 14	BSA	SLDMIRL
0030342						15	LMCH	RTNMCHB
0030342	01	000460	103401	000261		-001- 16	LI	RO,RTNMCHB
0030344						-001- 17	CALL	SMCH
0030344	01	000462	073027	-----		-002- 18	BSAI	SXMCH
0030345	01	000463	112403	-----		19	UNPK	MCHB
0030346	01	000464	022077	-----		20	ZBN	R3,S(STR1S)
0030347	01	000465	012000	-----		21	PACK	MCHTR
0030350						22	LMCH	LDMCHB
0030350						-001- 23	CALL	SLDMCHB
0030350	01	000466	037020	111072	CSYSUB	-002- 24	BSA	SLDMCHB
0030352	01	000470	103401	106631		25	LI	RO,MSXT+E(8)+MCHBXF
0030354	01	000472	007400	-----		26	LSR	MCHTR,RO
0030355						27	LMCH	LDMIRL
0030355						-001- 28	CALL	SLDMIRL
0030355	01	000473	037020	111075	CSYSUB	-002- 29	BSA	SLDMIRL

SUBROUTINE SMCH IS IN PROGRAM CSYSUB

UNJAM STORE COMMAND BIT(SC3)

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 40

TEST 53 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

				01	CSTDF1	OM	0(475),0(477)	# 12707	
			-001-	02		NOTE	***** THE FIRST ADDRESS OVERWRITTEN IS 000475 *****		
0030357									
0030357	01	000475	137020	012771	CDGSIO	-002-	06	FINALE	DF1INIT
0030361	01	000477	137000	027705	0027705	-001-	07	BSA	FINAL
0030363	01	000501	171400	-----		-001-	08	BL	BKCSTDF1
0030364	01	000502	037000	011725	CDGNM	-001-	09	GA	
0030364							10	BL	PASSTST
							11	EOW	# TEST COMPLETED
								NOTE	# 12707
									***** THE LAST ADDRESS OVERWRITTEN IS 000503 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 41

TEST 54

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *****
-001- 03 # **
-001- 04 # ** CU DIAGNOSTIC TEST NO. 54, MAIN_STORE_CONTROLLER_DATA_REGISTER
-001- 05 # **
-001- 06 # *****
-001- 07 # *****

```

0030366

-001- 10 CSTDF2 CSECT

0030366 U2 000000 000145 ----- CDGNM

-001- 14 VFD 1,0 2,0 2,0 2,0 3,1 6,NBGN

```

18 # SEGMENT 1
19 # THIS SEGMENT VERIFIES THAT THE WRITE MODE ENABLES(WRTMD) AND REFRESH MODE
20 # INHIBITS(REFI) TO STORE DATA FLOPS(FFSD) ARE NOT STUCK ACTIVE. IN THE FIRST
21 # CASE, A SPECIAL MAINTENANCE STATE(FNOWRTC) SHOULD FORCE ONES TO BE WRITTEN IN
22 # THOSE DATA BITS WHICH DO NOT HAVE THEIR WRITE MODE ENABLES STUCK ACTIVE.
23 # THE REFRESH INHIBITS ARE CHECKED BY STEPPING THRU A STORE READ WITH THE MODE
24 # FLOP JAMMED TO THE REFRESH STATE. AT THE END OF THE CYCLE THE NORMAL MODE IS
25 # RESUMED SO THAT A STORE COMPLETE(SCM) WILL ALLOW THE CONTENTS OF THE STORE
26 # DATA FLOPS TO BE RETURNED. ANY DATA FLOPS WITH REFRESH INHIBITS STUCK ACTIVE
27 # WILL CONTAIN THE ONES ORIGINALLY WRITTEN, OTHERWISE THE INFORMATION READ BACK
28 # SHOULD BE ZEROS LOADED INTO THE DATA FLOPS FROM A REVERSED(READ DIRECTION)
29 # STORE BUS BY THE NORMAL LOAD PULSE(NLD) AT THE BEGINNING OF THE READ CYCLE.

```

```

32 # SEGMENT 2
33 # THIS SEGMENT CONSISTS OF A FUNCTIONAL TEST OF THE SELECT ENABLES ON THE
34 # STORE DATA FLOPS(FFSD) OF THE NEXT STORE CLOSEST TO THE PROCESSOR. THIS TEST
35 # IS PERFORMED ON ALL EQUIPPED STORES (INCLUDING THE FIRST ONE EVEN THOUGH IT
36 # IS NOT MEANINGFUL IN THIS CASE). ONE LOCATION IN THE STORE IS WRITTEN WITH AN
37 # ALL-ZEROS TEST PATTERN. AFTER THIS ANOTHER LOCATION IS WRITTEN TO ALL ONES,
38 # SO THAT THE DATA FLOPS IN THE NEXT LOWER STORE ARE ALL SET IN THIS PROCESS.
39 # THEN THE FIRST (ALL-ZEROS) PATTERN IS READ BACK TO DETERMINE IF ANY OF THE
40 # ZEROS COMING BACK OVER THE BUS WILL BE MASKED BY ONES IN LOWER STORES WHICH
41 # GET GATED ONTO THE BUS BY A STORE SELECT SIGNAL (SEL1) WHICH IS STUCK ACTIVE.
42 # THERE IS A RACE INVOLVED IN KEEPING THE ONES IN THE DATA FLOPS OF THE LOWER
43 # STORE DURING THE READ OPERATION. THEREFORE, THIS FUNCTIONAL TEST ONLY CHECKS
44 # FOR STORE SELECTS STUCK ACTIVE ON THOSE BITS THAT WOULD INTERFERE WITH STORE
45 # DATA BUS TRANSMISSION BY 'ORING' ONES ONTO THE BUS DURING A READ OF A HIGHER
46 # STORE.

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 42

TEST 54

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

01 CSTDF2 OW 0(1),0(16) # 12707
-001- 02 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000001 *****

0030367 05 DF2INIT PREPARE
-002- 06 # RTN_BLM
-002- 07 # TURN OFF AUTOMATIC RETURNS TO BLM EVERY 20 TABLE ENTRIES.
0030367 02 000001 000144 ----- CDGNM -002- 08 VFD 1,0 2,0 6,0 1,1 6,NRTNBLM
-002- 10 # SEND
-002- 11 # SEND 0(000001) TO OFF-LINE MCHB.
-002- 12 # PARITY IS 0(2).
0030370 02 000002 047707 ----- CDGNM -002- 13 VFD 1,0 2,2 7,B(0111111) 6,N2SEND
0030371 02 000003 000040 000001 -002- 14 ADDR 0(000001),0(2)
-002- 16 # MICRO
-002- 17 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-002- 18 # TO FIELD = MSXT FROM FIELD = MCHBXF
0030373 02 000005 020005 ----- CDGNM -002- 19 VFD 1,0 2,1 7,0 6,NMICRO
0030374 02 000006 106631 ----- -002- 20 VFD 8,MSXT 8,MCHBXF
-002- 22 # NO3_CODE
-002- 23 # PROCESS 3ACC CODED ROUTINE AT ADDRESS **2.
0030375 02 000007 040043 000000 CDGNM -002- 24 VFD 1,0 2,2 7,0 6,NN03CD
0030376 02 000010 000000 030400 0030400 -002- 25 ADDR **2
-001- 27 # SUBSTITUTE 'MAS' FOR 'LOOP' IN TROUBLE PRINTOUT
0030400 02 000012 103501 000151 TTYTBL -002- 28 LI R4,MXAS
0030402 02 000014 034500 043132 CTSD -001- 29 STL R4,PAGSCR+24
-001- 30 # USE THE DIAGNOSTIC LOOP INDEX(LOOPIND) TO ACCESS THE PROPER I/Q
-001- 31 # SUBCHANNEL ADDRESS IN THE MAIN STORE CONTROLLER I/Q SUBCHANNEL TABLE
-001- 32 # (MASCIOSC) AND STORE IT IN THE ASSIGNED SLOT(IOADDR) IN PAGING BUFFER(PAGSCR)

0030404 02 000016 103240 ----- -001- 34 ZR LOOPIND
0030405 02 000017 004257 ----- -001- 35 SN LOOPIND,1 #ASSURE THAT LOOPIND IS ZERO BEFORE
#PROCEEDING WITH TEST

0030406 02 000020 072652 ----- CDGNM -001- 37 HN LOOPIND,LOOPIND
0030407 02 000021 037020 012757 CDGSIO -002- 38 BSA CONTPREP
0030411 02 000023 172652 ----- CDGNM -001- 39 HN LOOPIND,LOOPIND
0030412 02 000024 137020 012667 CDGSIO -003- 40 BSA MASIOM
0030414 02 000026 046071 ----- -002- 41 DATA MASCINT # INITIALIZE STORE CONTROLLER

0030415 02 000027 171400 ----- -002- 43 GA
0030416 02 000030 031000 030422 0030422 -002- 44 LAL RD,**4,RAD
0030420 02 000032 037000 011162 CDGNM -002- 45 BL INTRPNO3

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 43

TEST 54

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0030422

01
-001- 02

EOW
NOTE

12707

***** THE LAST ADDRESS OVERWRITTEN IS 000033 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 44

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 54 SEGMENT 1
-001- 04 # *
-001- 05 # *****

0030422 02 000034 000100 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(1) 6,NTESTSEG

11 # THIS SEGMENT VERIFIES THAT THE WRITE MODE ENABLES(WRTMD) AND REFRESH MODE
12 # INHIBITS(REFI) TO STORE DATA FLOPS(FFSD) ARE NOT STUCK ACTIVE. IN THE FIRST
13 # CASE, A SPECIAL MAINTENANCE STATE(FNOWRTC) SHOULD FORCE ONES TO BE WRITTEN IN
14 # THOSE DATA BITS WHICH DO NOT HAVE THEIR WRITE MODE ENABLES STUCK ACTIVE.
15 # THE REFRESH INHIBITS ARE CHECKED BY STEPPING THRU A STORE READ WITH THE MODE
16 # FLOP JAMMED TO THE REFRESH STATE. AT THE END OF THE CYCLE THE NORMAL MODE IS
17 # RESUMED SO THAT A STORE COMPLETE(SCM) WILL ALLOW THE CONTENTS OF THE STORE
18 # DATA FLOPS TO BE RETURNED. ANY DATA FLOPS WITH REFRESH INHIBITS STUCK ACTIVE
19 # WILL CONTAIN THE ONES ORIGINALLY WRITTEN, OTHERWISE THE INFORMATION READ BACK
20 # SHOULD BE ZEROS LOADED INTO THE DATA FLOPS FROM A REVERSED(READ DIRECTION)
21 # STORE BUS BY THE NORMAL LOAD PULSE(NLD) AT THE BEGINNING OF THE READ CYCLE.

-001- 24 # NO3_CODE
-001- 25 # PROCESS 3ACC CODED ROUTINE AT ADDRESS CKSDNW.
0030423 02 000035 040043 000000 CDGNM -001- 26 VFD 1,0 2,2 7,0 6,NNO3CD
0030424 02 000036 000000 030426 0030426 -001- 27 ADDR CKSDNW

30 # CHECK THAT WRITE MODE ENABLE ON BIT-SLICED DATA BITS IS NOT STUCK ACTIVE

0030426 32 CKSDNW
0030426 33 SION MASCINT
0030426 02 000040 137020 012667 CDGSIO -002- 34 BSA MASION
0030430 02 000042 046071 137020 -001- 35 DATA MASCINT # INITIALIZE STORE CONTROLLER
0030431 36 SION FNOWRTC
0030431 02 000043 137020 012667 CDGSIO -002- 37 BSA MASION
0030433 02 000045 045430 ----- -001- 38 DATA FNOWRTC # FORCE WRITING OF ONES W/ CLOCK RUNNING
0030434 39 NMLWRIT BASADDR,ZEROS,PRHOLO
0030434 02 000046 103561 106200 -001- 40 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0030436 02 000050 003741 000000 -001- 41 LI R14,ZEROS # TO BE WRITTEN - SD0-15
0030440 02 000052 003761 000000 -001- 42 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 43 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0030442 02 000054 031040 043140 CTSD -001- 44 LAL R2,BASADDR,RAO
0030444 02 000056 041061 ----- -001- 45 LA R3,1(RAO)
0030445 02 000057 003541 000020 -001- 46 LI R6,PRHOLO # SETUP SPECIAL PARITY
0030447 -001- 47 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0030447 02 000061 037020 013027 CDGSBS -002- 48 BSA MEMOP
0030451 49 SION CLKSTRT # GET RID OF FORCE NO WRITE

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 45

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0030451	02	000063	137020	012667	CDGSIO	-002-	01	BSA	MASION	
0030453	02	000065	046070	-----		-001-	02	DATA	CLKSTRT	# ENABLE STORE CONTROLLER CLOCK OUTPUT
0030454							03	NMLREAD	BASADDR	
0030454	02	000066	103561	106260		-001-	04	LI	R7,B(1G00110010110000)	# MAIN MEMORY STATUS
0030456	02	000070	031040	043140	CTSD	-001-	05	LAL	R2,BASADDR,RAO	
0030460	02	000072	041061	-----		-001-	06	LA	R3,1(RAO)	
0030461	02	000073	003140	-----		-001-	07	ZR	R6	# NO SPECIAL PARITY
0030462	02	000074	037020	013027	CDGSBS	-002-	08	BSA	MEMOP	
							09	# NOTE: MAINTENANCE STATE (FNOWRTC) SHOULD FORCE ALL ONES TO BE WRITTEN INTO		
							10	# THE MEMORY UNLESS THE WRITE MODE ENABLE(WRTMD) IS STUCK ACTIVE. THIS TEST		
							11	# WILL RESULT IN ZEROS BEING WRITTEN INTO THOSE BITS WITH THE ENABLE STUCK.		
0030464							12	RTN_TBL	**4	
0030464	02	000076	171400	-----		-001-	13	GA		
0030465	02	000077	031000	030471	0030471	-001-	14	LAL	RO,**4,RAO	
0030467	02	000101	037000	011162	CDGNM	-001-	15	BL	INTRPN03	
							17	# COMPARE		
							18	# COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).		
							19	# IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.		
0030471	02	000103	050014	-----	CDGNM	-001-	20	VFD	1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE	
0030472	02	000104	000000	177777		-001-	21	ADDR	0(0177777)	
							23	# FAILBIT		
							24	# FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS 1XX		
							25	# WHERE XX IS THE LOWEST FAILING BIT NUMBER.		
0030474	02	000106	020220	-----	CDGNM	-001-	26	VFD	1,0 2,1 5,0 2,ENS1SD 6,NFLBITS	
0030475	02	000107	000001	-----		-001-	27	VFD	16,1	
							29	# COMPARE		
							30	# COMPARE ON-LINE MCHB BIT PL TO 1.		
							31	# IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.		
0030476	02	000110	022414	-----	CDGNM	-001-	32	VFD	1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE	
0030477	02	000111	000001	-----		-001-	33	VFD	15,0 1,1	
							35	# FAILTST		
							36	# FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS ENS1SDPL.		
							37	# DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.		
0030500	02	000112	020017	-----	CDGNM	-001-	38	VFD	1,0 2,1 5,0 2,0 6,NFAILTST	
0030501	02	000113	000022	-----		-001-	39	VFD	16,ENS1SDPL	
							41	# COMPARE		
							42	# COMPARE ON-LINE MCHB BIT PH TO 1.		
							43	# IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.		
0030502	02	000114	022514	-----	CDGNM	-001-	44	VFD	1,LPINDX 2,1 1,0 1,SUCCESS 5,PH 6,NCOMPARE	
0030503	02	000115	000001	-----		-001-	45	VFD	15,0 1,1	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 46

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

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-001- 01 #   FAILTST
-001- 02 #   FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS ENS1SDPH.
-001- 03 #   DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030504 02 000116 020017 ----- CDGNM
0030505 02 000117 000023 -----
-001- 04     VFD  1,0 2,1 5,0 2,0 6,NFAILTST
-001- 05     VFD  16,ENS1SDPH

                                07 # STORE DATA(SD0-15,PL,PH) WRITE ENABLE ON BIT-SLICED BOARD(S) IS STUCK ACTIVE

-001- 10 #   WIDE_TST
-001- 11 #   IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS CKSDNWXT.
0030506 02 000120 040046 000000 CDGNM
0030507 02 000121 000000 030535 0030535
-001- 12     VFD  1,0 2,2 7,0 6,NWDTST
-001- 13     ADDR  CKSDNWXT

-001- 15 #   MICRO
-001- 16 #   PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 17 #   TO FIELD = MCHBXT FROM FIELD = SDR1XF
0030511 02 000123 020005 ----- CDGNM
0030512 02 000124 114455 -----
-001- 18     VFD  1,0 2,1 7,0 6,NMICRO
-001- 19     VFD  8,MCHBXT 8,SDR1XF

-001- 21 #   RETRNL
-001- 22 #   RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0030513 02 000125 007710 ----- CDGNM
-001- 23     VFD  1,0 2,0 7,B(0111111) 6,NRETRN

-001- 25 #   NO3_CODE
-001- 26 #   PROCESS 3ACC CODED ROUTINE AT ADDRESS MSKBTS.
0030514 02 000126 040043 000000 CDGNM
0030515 02 000127 000000 030517 0030517
-001- 27     VFD  1,0 2,2 7,0 6,NNO3CD
-001- 28     ADDR  MSKBTS

0030517                                30 MSKBTS
0030517 02 000131 112403 -----
0030520 02 000132 003463 000377
0030522 02 000134 012060 -----
0030523
0030523 02 000135 071400 -----
0030524 02 000136 031000 030530 0030530
0030526 02 000140 037000 011162 CDGNM
-001- 31     UNPK  MCHB
-001- 32     OI    R3,X(OOFF) # MASK UNUSED DATA BITS
-001- 33     PACK  MCHB
-001- 34     RTN_TBL  ++4
-001- 35     GA
-001- 36     LAL  R0,++4,RA0
-001- 37     BL   INTRPN03

-001- 39 #   COMPARE
-001- 40 #   COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).
-001- 41 #   IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030530 02 000142 050014 ----- CDGNM
0030531 02 000143 000000 177777
-001- 42     VFD  1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 43     ADDR  0(0177777)

-001- 45 #   FAILBIT
-001- 46 #   FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS ENS1SD+8XX
-001- 47 #   WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030533 02 000145 020120 ----- CDGNM
0030534 02 000146 000012 -----
-001- 48     VFD  1,0 2,1 5,0 2,1 6,NFLBITS
-001- 49     VFD  16,ENS1SD+8

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 47

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

02 # STORE DATA(SD16-23) WRITE ENA3LE ON UPPER BIT-SLICED BOARD(S) IS STUCK ACTIVE

0030535

05 CKSDNWXT
 06 CSTDF2 OW 0(147),0(150) # 13267
 -001- 07 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000147 *****

10 CSTDF2 OW 0(147),0(150) # 780315
 -001- 11 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000147 *****

-001- 14 # NO3 CODE
 -001- 15 # PROCESS 3ACC CODED ROUTINE AT ADDRESS CKSDRM.
 -001- 16 VFD 1,0 2,2 7,0 6,NN03CD
 -001- 17 ADDR CKSDRM

0030535 02 000147 040043 000000 CDGNM
 0030536 02 000150 000000 030540 0030540

0030540

19 EOW # 780315
 -001- 20 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000151 *****

0030540

23 EOW # 13267
 -001- 24 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000151 *****

29 # CHECK THAT REFRESH INHIBIT(REFI) TO STORE DATA BITS(FFSD) IS NOT STUCK HI

0030540

0030540
 0030540 02 000152 137020 012667 CDGSIO
 0030542 02 000154 046071 -----

31 CKSDRM
 32 SIOM MASCINT
 -002- 33 BSA MASIOM
 -001- 34 DATA MASCINT # INITIALIZE STORE CONTROLLER
 35 NMLWRIT BASADDR,ONES,PRH1L1
 -001- 36 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
 -001- 37 LI R14,ONES # TO BE WRITTEN - SDO-15
 -001- 38 LI R15,ONES # TO BE WRITTEN - SD16-23
 -001- 39 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
 -001- 40 LAL R2,BASADDR,RAO
 -001- 41 LA R3,1(RAO)
 -001- 42 LI R6,PRH1L1 # SETUP SPECIAL PARITY
 -001- 43 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
 -002- 44 BSA MEMOP
 45 SIOM CLKSTOP
 -002- 46 BSA MASIOM
 -001- 47 DATA CLKSTOP # DISABLE STORE CONTROLLER CLOCK OUTPUT

0030543
 0030543 02 000155 103561 106200
 0030545 02 000157 003741 177777
 0030547 02 000161 003761 177777

0030551 02 000163 031040 043140 CTSD

0030553 02 000165 041061 -----

0030554 02 000166 003541 000017

0030556
 0030556 02 000170 037020 013027 CDGSBS

0030560

0030560 02 000172 137020 012667 CDGSIO

0030562 02 000174 046074 103401

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 48

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # SET UP MAINTENANCE STATE THAT FORCES THE STORE DATA BUS INTO THE READ
 02 # DIRECTION TO PRESERVE ZEROS IN THE CONTROLLER DATA FLOPS DURING NORMAL LOAD
 03 # NOTE: ALSO REQUIRED FOR WIDE STORES TO GUARANTEE ZERO PARITY IS CALCULATED

0030563	02	000175	103401	114615		05	LI	RD,MCHBXT*(8)+MSXF	# RETRIEVE MAINTENANCE STATE REG
0030565	02	000177	007400	-----		06	LSR	MCHTR,RO	
0030566						07	LMCH	LDMIRL	
0030566					-01-	08	CALL	SLDMIRL	
0030566	02	000200	037020	111075	CSYSUB	-002-	BSA	SLDMIRL	
0030570						10	LMCH	RTNMCHB	
0030570	02	000202	103401	000261		-001-	LI	RD,RTNMCHB	
0030572						-001-	CALL	SMCH	
0030572	02	000204	073027	-----		-002-	BSAI	SXMCH	# SUBROUTINE SMCH IS IN PROGRAM CSYSUB
0030573	02	000205	112403	-----		14	UNPK	MCHB	
0030574	02	000206	026077	-----		15	SBN	R3,S(STR1S)	# JAM STORE COMMAND BIT(SC3) ACTIVE(LOW)
0030575	02	000207	012000	-----		16	PACK	MCHTR	
0030576						17	LMCH	LDMCHB	
0030576					-001-	18	CALL	SLDMCHB	
0030576	02	000210	037020	111072	CSYSUB	-002-	BSA	SLDMCHB	
0030600	02	000212	103401	106631		20	LI	RD,MSXT*(8)+MCHBZF	
0030602	02	000214	007400	-----		21	LSR	MCHTR,RO	
0030603						22	LMCH	LDMIRL	
0030603					-001-	23	CALL	SLDMIRL	
0030603	02	000215	037020	111075	CSYSUB	-002-	BSA	SLDMIRL	
0030605						25	NMLREAD	BASADDR	
0030605	02	000217	103561	106260		-001-	LI	R7,B(1000110010110000)	# MAIN MEMORY STATUS
0030607	02	000221	031040	043140	CTSD	-001-	LAL	R2,BASADDR,RAO	
0030611	02	000223	041061	-----		-001-	LA	R3,1(RAO)	
0030612	02	000224	003140	-----		-001-	ZR	R6	# NO SPECIAL PARITY
0030613	02	000225	037020	013027	CDGSBS	-002-	BSA	MEMOP	
						31	OW	O(227)	# 780315
					-001-	32	NOTE	***** THE FIRST ADDRESS OVERWRITTEN IS 000227 *****	
0030615						35	SIOM	FSTRM	
0030615	02	000227	137020	012667	CDGSIO	-002-	BSA	MASIOM	
0030617	02	000231	046514	137020		-001-	DATA	FSTRM	# FORCE MODE FLOP TO REFRESH STATE
0030620						38	SIOM	CLKSTOP	
0030620	02	000232	137020	012667	CDGSIO	-002-	BSA	MASIOM	
0030622	02	000234	046074	137020		-001-	DATA	CLKSTOP	# DISABLE STORE CONTROLLER CLOCK OUTPUT
0030623						41	SIOM	CLKSTRT	
0030623	02	000235	137020	012667	CDGSIO	-002-	BSA	MASIOM	
0030625	02	000237	046070	137020		-001-	DATA	CLKSTRT	# ENABLE STORE CONTROLLER CLOCK OUTPUT
0030626						44	SIOM	FNOTIMC	
0030626	02	000240	137020	012667	CDGSIO	-002-	BSA	MASIOM	
0030630	02	000242	044470	-----		-001-	DATA	FNOTIMC	# BLOCK TIMING TO FANOUT BOARDS(W/CLOCK)
0030631						47	NMLREAD	BASADDR	
0030631	02	000243	103561	106260		-001-	LI	R7,B(1000110010110000)	# MAIN MEMORY STATUS
0030633	02	000245	031040	043140	CTSD	-001-	LAL	R2,BASADDR,RAO	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 49

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

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0030635 02 000247 041061 ----- -001- 01 LA R3,1(RAO)
0030636 02 000250 003140 ----- -001- 02 ZR R6 # NO SPECIAL PARITY
0030637 02 000251 037020 013027 CDGSBS -002- 03 BSA MEMOP
0030641 02 000253 106000 ----- -001- 04 TAKEOUT 16
0030642 02 000254 006000 ----- -001- 05 LR 0,0
0030643 02 000255 006000 ----- -001- 06 LR 0,0
0030644 02 000256 006000 ----- -001- 07 LR 0,0
0030645 02 000257 006000 ----- -001- 08 LR 0,0
0030646 02 000260 006000 ----- -001- 09 LR 0,0
0030647 02 000261 006000 ----- -001- 10 LR 0,0
0030650 02 000262 006000 ----- -001- 11 LR 0,0
0030651 02 000263 006000 ----- -001- 12 LR 0,0
0030652 02 000264 006000 ----- -001- 13 LR 0,0
0030653 02 000265 006000 ----- -001- 14 LR 0,0
0030654 02 000266 006000 ----- -001- 15 LR 0,0
0030655 02 000267 006000 ----- -001- 16 LR 0,0
0030656 02 000270 006000 ----- -001- 17 LR 0,0
0030657 02 000271 006000 ----- -001- 18 LR 0,0
0030660 02 000272 006000 ----- -001- 19 LR 0,0
0030661 02 000272 006000 ----- -001- 20 LR 0,0
0030661 21 EOW # 780315
-001- 22 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000272 *****

0030661 02 000273 071400 ----- 25 RTN_TBL **4
0030661 02 000273 071400 ----- -001- 26 GA
0030662 02 000274 031000 030666 0030666 -001- 27 LAL R0,**4,RAD
0030664 02 000276 037000 011162 CDGNM -001- 28 BL INTRPN03

-001- 30 # MICRO
-001- 31 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 32 # TO FIELD = MCHBXT FROM FIELD = SDRXF
0030666 02 000300 020005 ----- CDGNM -001- 33 VFD 1,0 2,1 7,0 6,NMICRO
0030667 02 000301 114456 ----- -001- 34 VFD 8,MCHBXT 8,SDRXF

-001- 36 # RETRN
-001- 37 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0030670 02 000302 007710 ----- CDGNM -001- 38 VFD 1,0 2,0 7,B(0111111) 6,NRETRN

40 CSTDF2 OW 0(303),0(305) # 780315
-001- 41 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000303 *****

44 CSTDF2 OW 0(303),0(305) # 790350
-001- 45 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000303 *****

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 50

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

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-001- 01 # COMPARE
-001- 02 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 03 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030671 02 000303 050014 ----- CDGNM
0030672 02 000304 000000 000000 -001- 04 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 05 ADDR 0(0000000)
0030674 -001- 07 EOW # 790350
-001- 08 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000305 *****

0030674 -001- 11 EOW # 780315
-001- 12 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000305 *****

-001- 15 # FAILBIT
-001- 16 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS ENS1SDXX
-001- 17 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030674 02 000306 020020 ----- CDGNM
0030675 02 000307 000002 ----- -001- 18 VFD 1,0 2,1 5,0 2, 6,NFLBITS
-001- 19 VFD 16,ENS1SD
-001- 21 CSTDF2 OW 0(310),0(311) # 780315
-001- 22 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000310 *****

-001- 25 CSTDF2 OW 0(310),0(311) # 790350
-001- 26 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000310 *****

-001- 29 # COMPARE
-001- 30 # COMPARE ON-LINE MCHB BIT PL TO 0.
-001- 31 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030676 02 000310 022414 ----- CDGNM
0030677 02 000311 000000 ----- -001- 32 VFD 1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE
-001- 33 VFD 15,0 1,0
0030700 -001- 35 EOW # 790350
-001- 36 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000311 *****

0030700 -001- 39 EOW # 780315
-001- 40 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000311 *****

-001- 43 # FAILTST
-001- 44 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS ENS1SDPL.
-001- 45 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030700 02 000312 020017 ----- CDGNM
0030701 02 000313 000022 ----- -001- 46 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 47 VFD 16,ENS1SDPL

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 51

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

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-001- 01 CSTDF2 OW 0(314),0(315) # 780315
-001- 02 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000314 *****

-001- 05 CSTDF2 OW 0(314),0(315) # 790350
-001- 06 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000314 *****

-001- 09 # COMPARE
-001- 10 # COMPARE ON-LINE MCHB BIT PH TO 0.
-001- 11 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030702 02 000314 022514 ----- CDGNM
0030703 02 000315 000000 -----
-001- 12 VFD 1,LPINDX 2,1 1,0 1,SUCCE 5,PH 6,NCOMPARE
-001- 13 VFD 15,0 1,0

0030704
-001- 15 EOW # 790350
-001- 16 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000315 *****

0030704
-001- 19 EOW # 780315
-001- 20 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000315 *****

-001- 23 # FAILTST
-001- 24 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS ENS1SDPH.
-001- 25 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0030704 02 000316 020017 ----- CDGNM
0030705 02 000317 000023 -----
-001- 26 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 27 VFD 16,ENS1SDPH

29 # STORE DATA INPUT ENABLE(REFI) IS STUCK ACTIVE ON BIT-SLICED BOARD INDICATED

-001- 32 # WIDE_TST
-001- 33 # IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS CKENXT.
0030706 02 000320 040046 000000 CDGNM
0030707 02 000321 000000 030724 0030724
-001- 34 VFD 1,0 2,2 7,0 6,NWDTST
-001- 35 ADDR CKENXT

-001- 37 # MICRO
-001- 38 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 39 # TO FIELD = MCHBXT FROM FIELD = SDR1XF
0030711 02 000323 020005 ----- CDGNM
0030712 02 000324 114455 -----
-001- 40 VFD 1,0 2,1 7,0 6,NMICRO
-001- 41 VFD 8,MCHBXT 8,SDR1XF

-001- 43 # RETRN
-001- 44 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0030713 02 000325 007710 ----- CDGNM
-001- 45 VFD 1,0 2,0 7,B(0111111) 6,NRETRN

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 52

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # MASK
-001- 02 # MASK CONTENTS OF ON-LINE MCHB WITH 0(0177400).
0030714 02 000326 040013 ----- CDGNM -001- 03 VFD 1,0 2,2 7,0 6,NMASK
0030715 02 000327 000000 177400 -001- 04 ADDR 0(0177400)

-001- 06 CSTDF2 OW 0(331),0(333) # 780315
-001- 07 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000331 *****

-001- 10 CSTDF2 OW 0(331),0(333) # 790350
-001- 11 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000331 *****

-001- 14 # COMPARE
-001- 15 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(C000000).
-001- 16 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0030717 02 000331 050014 ----- CDGNM -001- 17 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0030720 02 000332 000000 000000 -001- 18 ADDR 0(000000)

0030722 -001- 20 EOW # 790350
-001- 21 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000333 *****

0030722 -001- 24 EOW # 780315
-001- 25 NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000333 *****

-001- 28 # FAILBIT
-001- 29 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS ENS1SD+8XX
-001- 30 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0030722 02 000334 020020 ----- CDGNM -001- 31 VFD 1,0 2,1 5,0 2, 6,NFLBITS
0030723 02 000335 000012 ----- -001- 32 VFD 16,ENS1SD+8

34 # REFRESH INHIBIT(REFI) SIGNAL STUCK ACTIVE ON A STORE DATA FLOP(FFSD16-23)

0030724 -001- 37 CKENXT
-001- 38 # NO3 CODE
-001- 39 # PROCESS 3ACC CODED ROUTINE AT ADDRESS CLRMTc.
0030724 02 000336 040043 000000 CDGNM -001- 40 VFD 1,0 2,2 7,0 6,NNO3CD
0030725 02 000337 000000 030727 0030727 -001- 41 ADDR CLRMTc

0030727 43 CLRMTc
0030727 02 000341 103401 114615 44 LI RD,MCHBXT*(8)+MSXF
0030731 02 000343 007400 ----- 45 LSR MCHTR,RD
0030732 46 LMCH LDMIRL
0030732 -001- 47 CALL SLDMIRL
0030732 02 000344 037020 111075 CSYSUB -002- 48 BSA SLDMIRL
0030734 49 LMC.I RTNMCHB

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 53

TEST 54 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0030734	02	000346	103401	000261	-001-	01	LI	RD,RTNMCHB	
0030736					-001-	02	CALL	SMCH	
0030736	02	000350	073027	-----	-002-	03	BSAI	SXMCH	# SUBROUTINE SMCH IS IN PROGRAM CSYSUB
0030737	02	000351	112403	-----		04	UNPK	MCHB	
0030740	02	000352	022077	-----		05	ZBN	R3,S(STR1S)	# UNJAM STORE COMMAND BIT(SC3)
0030741	02	000353	012000	-----		06	PACK	MCHTR	
0030742						07	LMCH	LDMCHB	
0030742					-001-	08	CALL	SLDMCHB	
0030742	02	000354	037020	111072 CSYSUB	-002-	09	BSA	SLDMCHB	
0030744	02	000356	103401	106631		10	LI	RO,MSXT*E(8)+MCHBXF	
0030746	02	000360	007400	-----		11	LSR	MCHTR,RO	
0030747						12	LMCH	LDMIRL	
0030747					-001-	13	CALL	SLDMIRL	
0030747	02	000361	037020	111075 CSYSUB	-002-	14	BSA	SLDMIRL	
0030751						15	RTN_TBL	**4	
0030751	02	000363	171400	-----	-001-	16	GA		
0030752	02	000364	031000	030756 0030756	-001-	17	LAL	RO,**4,RAO	
0030754	02	000366	037000	011162 CDGMM	-001-	18	BL	INTRPN03	

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 54

TEST 54 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 54 SEGMENT 2
-001- 04 # *
-001- 05 # *****

```

```

0030756 02 000370 000200 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(2) 6,NTESTSEG

```

```

11 # THIS SEGMENT CONSISTS OF A FUNCTIONAL TEST OF THE SELECT ENABLES ON THE
12 # STORE DATA FLOPS(FFSD) OF THE NEXT STORE CLOSEST TO THE PROCESSOR. THIS TEST
13 # IS PERFORMED ON ALL EQUIPPED STORES (INCLUDING THE FIRST ONE EVEN THOUGH IT
14 # IS NOT MEANINGFUL IN THIS CASE). ONE LOCATION IN THE STORE IS WRITTEN WITH AN
15 # ALL-ZEROS TEST PATTERN. AFTER THIS ANOTHER LOCATION IS WRITTEN TO ALL ONES,
16 # SO THAT THE DATA FLOPS IN THE NEXT LOWER STORE ARE ALL SET IN THIS PROCESS.
17 # THEN THE FIRST (ALL-ZEROS) PATTERN IS READ BACK TO DETERMINE IF ANY OF THE
18 # ZEROS COMING BACK OVER THE BUS WILL BE MASKED BY ONES IN LOWER STORES WHICH
19 # GET GATED ONTO THE BUS BY A STORE SELECT SIGNAL (SEL1) WHICH IS STUCK ACTIVE.
20 # THERE IS A RACE INVOLVED IN KEEPING THE ONES IN THE DATA FLOPS OF THE LOWER
21 # STORE DURING THE READ OPERATION. THEREFORE, THIS FUNCTIONAL TEST ONLY CHECKS
22 # FOR STORE SELECTS STUCK ACTIVE ON THOSE BITS THAT WOULD INTERFERE WITH STORE
23 # DATA BUS TRANSMISSION BY 'ORING' ONES ONTO THE BUS DURING A READ OF A HIGHER
24 # STORE.

```

```

-001- 27 # NO3 CODE
-001- 28 # PROCESS 3ACC CODED ROUTINE AT ADDRESS BUSELCK.
0030757 02 000371 040043 000000 CDGNM -001- 29 VFD 1,0 2,2 7,0 6,NN03CD
0030760 02 000372 000000 030762 0030762 -001- 30 ADDR BUSELCK

```

```

0030762 32 BUSELCK
0030762 33 SIOM MASCINT
0030762 02 000374 137020 012667 CDGSIO -002- 34 BSA MASIOM
0030764 02 000376 046071 ----- -001- 35 DATA MASCINT # INITIALIZE STORE CONTROLLER
0030765 02 000377 130700 043140 CTSD 36 LL R12,BASADDR
0030767 02 000401 003320 ----- 37 ZR R13 # POINT TO FIRST ADDRESS IN STORE
0030770 38 NMLWRIT RAO,ZEROS,PRHOLD # STORE ALL-ZEROS IN FIRST LOCATION
0030770 02 000402 003561 106200 -001- 39 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0030772 02 000404 003741 000000 -001- 40 LI R14,ZEROS # TO BE WRITTEN - SDO-15
0030774 02 000406 003761 000000 -001- 41 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 42 # IF ADDRESS(RAO) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0030776 02 000410 006054 ----- -001- 43 LR R2,R12
0030777 02 000411 006075 ----- -001- 44 LR R3,R13
0031000 02 000412 003541 000020 -001- 45 LI R6,PRHOLD # SETUP SPECIAL PARITY
0031002 -001- 46 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031002 02 000414 037020 013027 CDGSBS -002- 47 BSA MEMOP
0031004 02 000416 104721 ----- 48 AN R13,1 # NEXT LOCATION
0031005 49 NMLWRIT RAO,ONES # LOAD ALL-ONES INTO STORE DATA FLOPS

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 55

```

TEST 54 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031005 02 000417 003561 106200          -001- 01      LI      R7,B(1000110010000000) # MAIN MEMORY STATUS
0031007 02 000421 003741 177777          -001- 02      LI      R14,ONES # TO BE WRITTEN - SD0-15
0031011 02 000423 003761 177777          -001-;03      LI      R15,ONES # TO BE WRITTEN - SD16-23
-001- 04 # IF ADDRESS(RAO) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031013 02 000425 006054 -----          -001- 05      LR      R2,R12
0031014 02 000426 006075 -----          -001-;06      LR      R3,R13
0031015 02 000427 003140 -----          -001- 07      ZR      R6 # NO SPECIAL PARITY
0031016          -001- 08      CALL   MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031016 02 000430 037020 013027 CDGSBS -002- 09      BSA    MEMOP
0031020 02 000432 104337 -----          -001- 10      SN      R13,1 # BACK TO FIRST LOCATION
0031021          -001- 11      NMLREAD RAO # READ BACK ALL-ZEROS PATTERN
0031021 02 000433 003561 106260          -001- 12      LI      R7,B(1000110010110000) # MAIN MEMORY STATUS
0031023 02 000435 006054 -----          -001- 13      LR      R2,R12
0031024 02 000436 006075 -----          -001-;14      LR      R3,R13
0031025 02 000437 003140 -----          -001- 15      ZR      R6 # NO SPECIAL PARITY
0031026 02 000440 037020 013027 CDGSBS -002- 16      BSA    MEMOP
0031030          -001- 17      RTN_TBL **4
0031030 02 000442 171400 -----          -001- 18      GA
0031031 02 000443 031000 031035 0031035 -001- 19      LAL    RD,**4,RAO
0031033 02 000445 037000 011162 CDGNM -001- 20      BL     INTRPN03

-001- 22 # COMPARE
-001- 23 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 24 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031035 02 000447 050014 ----- CDGNM -001- 25      VFD    1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0031036 02 000450 000000 000000 -001- 26      ADDR  0(0000000)

-001- 28 # FAILBIT
-001- 29 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS SELSTKXX
-001- 30 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031040 02 000452 020020 ----- CDGNM -001- 31      VFD    1,0 2,1 5,0 2, 6,NFLBITS
0031041 02 000453 000034 ----- -001- 32      VFD    16,SELSTK

34 # SELECT ENABLE (SEL1) ON STORE DATA FLOP (FFSD) OF LOWER STORE IS STUCK ACTIVE

-001- 37 # COMPARE
-001- 38 # COMPARE ON-LINE MCHB BIT PL TO 0.
-001- 39 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031042 02 000454 022414 ----- CDGNM -001- 40      VFD    1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE
0031043 02 000455 000000 ----- -001- 41      VFD    15,0 1,0

-001- 43 # FAILTST
-001- 44 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS SELSTKPL.
-001- 45 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031044 02 000456 020017 ----- CDGNM -001- 46      VFD    1,0 2,1 5,0 2,0 6,NFAILTST
0031045 02 000457 000054 ----- -001- 47      VFD    16,SELSTKPL

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 56

TEST 54 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # SELECT ENABLE (SEL1) ON DATA PARITY LOW (SDPL) OF LOWER STORE IS STUCK ACTIVE

```

-001- 04 # COMPARE
-001- 05 # COMPARE ON-LINE MCHB BIT PH TO 0.
-001- 06 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031046 02 000460 022514 ----- CDGMM
0031047 02 000461 000000 -----
-001- 07 VFD 1,LPINDX 2,1 1,0 1,SUCCESS 5,PH 6,NCOMPARE
-001- 08 VFD 15,0 1,0

-001- 10 # FAILTST
-001- 11 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS SELSTKPH.
-001- 12 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031050 02 000462 020017 ----- CDGMM
0031051 02 000463 000055 -----
-001- 13 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 14 VFD 16,SELSTKPH

```

16 # SELECT ENABLE (SEL1) ON DATA PARITY HIGH (SDPH) OF LOWER STORE IS STUCK ACTIVE

```

-001- 19 # WIDE_TST
-001- 20 # IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS BUSELXT.
0031052 02 000464 040046 000000 CDGMM
0031053 02 000465 000000 031153 0031153
-001- 21 VFD 1,0 2,2 7,0 6,NWDTST
-001- 22 ADDR BUSELXT

-001- 24 # MICRO
-001- 25 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 26 # TO FIELD = MCHBXT FROM FIELD = SDR1XF
0031055 02 000467 020005 ----- CDGMM
0031056 02 000470 114455 -----
-001- 27 VFD 1,0 2,1 7,0 6,NMICRO
-001- 28 VFD 8,MCHBXT 8,SDR1XF

-001- 30 # RETRN
-001- 31 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0031057 02 000471 007710 ----- CDGMM
-001- 32 VFD 1,0 2,0 7,B(01111111) 6,NRETRN

-001- 34 # MASK
-001- 35 # MASK CONTENTS OF ON-LINE MCHB WITH 0(0177400).
0031060 02 000472 040013 ----- CDGMM
0031061 02 000473 000000 177400
-001- 36 VFD 1,0 2,2 7,0 6,NMASK
-001- 37 ADDR 0(0177400)

-001- 39 # TTY_DATA
-001- 40 # STORE CONTENTS OF ON-LINE MCHB AS A DATA WORD FOR TTY FAILURE MESSAGE.
0031063 02 000475 000016 ----- CDGMM
-001- 41 VFD 1,0 2,0 4,0 1,0 1,1-1 1,0 6,NTTY

-001- 43 # COMPARE
-001- 44 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 45 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031064 02 000476 050014 ----- CDGMM
0031065 02 000477 000000 000000
-001- 46 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 47 ADDR 0(0000000)

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 57

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # FAILBIT
-001- 02 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS SELSTK+8XX.
-001- 03 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031067 02 000501 020020 ----- CDGNM -001- 04 # VFD 1,0 2,1 5,0 2, 6,NFLBITS
0031070 02 000502 000044 ----- -001- 05 # VFD 16,SELSTK+8

07 # SELECT ENABLE (SEL1) ON WIDE DATA FLOP (FFSD) OF LOWER STORE IS STUCK ACTIVE

-001- 10 # NO3 CODE
-001- 11 # PROCESS 3ACC CODED ROUTINE AT ADDRESS EPSELTST.
0031071 02 000503 040043 000000 CDGNM -001- 12 # VFD 1,0 2,2 7,0 6,NN03CD
0031072 02 000504 000000 031074 0031074 -001- 13 # ADDR EPSELTST

0031074 15 EPSELTST
0031074 16 SIOM MASCINT
0031074 02 000506 137020 012667 CDGSIO -002- 17 # BSA MASIOM
0031076 02 000510 046071 ----- -001- 18 # DATA MASCINT # INITIALIZE STORE CONTROLLER
0031077 02 000511 130700 043140 CTSD 19 # LL R12,BASADDR
0031101 02 000513 003320 ----- 20 # ZR R13 # POINT TO FIRST ADDRESS IN STORE
0031102 21 NMLWRIT RAO,ZEROS,PRHOLD,HIDATA=B(10000000)
0031102 02 000514 003561 106200 -001- 22 # LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031104 02 000516 003741 000000 -001- 23 # LI R14,ZEROS # TO BE WRITTEN - SD0-15
0031106 02 000520 003761 000200 -001- 24 # LI R15,B(10000000) # TO BE WRITTEN - SD16-23
-001- 25 # IF ADDRESS(RAO) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031110 02 000522 006054 ----- -001- 26 # LR R2,R12
0031111 02 000523 006075 ----- -001- 27 # LR R3,R13
0031112 02 000524 003541 000020 -001- 28 # LI R6,PRHOLD # SETUP SPECIAL PARITY
0031114 02 000526 037020 013027 CDGSBS -001- 29 # CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031116 02 000530 104721 ----- -002- 30 # BSA MEMOP
0031117 31 # AN R13,1 # DIFFERENT MEMORY LOCATION
0031117 02 000531 003561 106200 -001- 32 # NMLWRIT RAO,ONES # LOAD ALL-ONES INTO STORE DATA FLOPS
0031121 02 000533 003741 177777 -001- 33 # LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031123 02 000535 003761 177777 -001- 34 # LI R14,ONES # TO BE WRITTEN - SD0-15
-001- 35 # LI R15,ONES # TO BE WRITTEN - SD16-23
-001- 36 # IF ADDRESS(RAO) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031125 02 000537 006054 ----- -001- 37 # LR R2,R12
0031126 02 000540 006075 ----- -001- 38 # LR R3,R13
0031127 02 000541 003140 ----- -001- 39 # ZR R6 # NO SPECIAL PARITY
0031130 -001- 40 # CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031130 02 000542 037020 013027 CDGSBS -002- 41 # BSA MEMOP
0031132 02 000544 104337 ----- 42 # SN R13,1 # BACK TO ORIGINAL LOCATION
0031133 43 # NMLREAD RAO # READ BACK ALL-ZEROS PATTERN
0031133 02 000545 003561 106260 -001- 44 # LI R7,B(1000110010110000) # MAIN MEMORY STATUS
0031135 02 000547 006054 ----- -001- 45 # LR R2,R12
0031136 02 000550 006075 ----- -001- 46 # LR R3,R13
0031137 02 000551 003140 ----- -001- 47 # ZR R6 # NO SPECIAL PARITY
0031140 02 000552 037020 013027 CDGSBS -002- 48 # BSA MEMOP
0031142 49 # RTN_TBL **4
    
```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

TEST 54 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031142 02 000554 171400 ----- -001- 01      GA
0031143 02 000555 031000 031147 0031147 -001- 02      LAL      RO,++4,RAD
0031145 02 000557 037000 011162 CDGNM -001- 03      BL      INTRPN03

                                -001- 05 #      COMPARE
                                -001- 06 #      COMPARE ON-LINE MCHB BIT PH TO 0.
                                -001- 07 #      IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031147 02 000561 022514 ----- CDGNM -001- 08      VFD      1,L,PINDX 2,1 1,0 1,SUCCE 5,PH 6,NCOMPARE
0031150 02 000562 000000 ----- -001- 09      VFD      15,0 1,0

                                -001- 11 #      FAILTST
                                -001- 12 #      FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS SELSTKEP.
                                -001- 13 #      DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031151 02 000563 020017 ----- CDGNM -001- 14      VFD      1,0 2,1 5,0 2,0 6,NFAILTST
0031152 02 000564 000066 ----- -001- 15      VFD      16,SELSTKEP

```

17 # SELECT ENABLE (SEL1) ON EXTENDED PARITY (SDEPH) OF LOWER STORE IS STUCK ACTIVE

0031153

```

                                20 BUSELXT
                                -001- 21 #      NO3_CODE
                                -001- 22 #      PROCESS 3ACC CODED ROUTINE AT ADDRESS ++2.
0031153 02 000565 040043 000000 CDGNM -001- 23      VFD      1,0 2,2 7,0 6,NN03CD
0031154 02 000566 000000 031156 0031156 -001- 24      ADDR      ++2

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 59

TEST 54 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

		01	CSTDF2	OW	0(570),0(572)	# 12707	
		-001-	02	NOTE	***** THE FIRST ADDRESS OVERWRITTEN IS 000570 *****		
0031156			05	FINALE	DF2INIT		
0031156	02	000570	137020	012771	CD6SIO	-002-	06
0031160	02	000572	137000	030411	0030411	-001-	07
0031162	02	000574	171400	-----		-001-	08
0031163	02	000575	037000	011725	CD6NM	-001-	09
0031163							10
		-001-	11	NOTE	***** THE LAST ADDRESS OVERWRITTEN IS 000576 *****		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 60

TEST 55

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSD F W77D

```

-001- 01 # *****
-001- 02 # *****
-001- 03 # **
-001- 04 # ** CU DIAGNOSTIC TEST NO. 55, MAIN_STORE_CONTROLLER_DATA_REGISTER
-001- 05 # **
-001- 06 # *****
-001- 07 # *****

```

0031165

-001- 10 CSTDF3 CSECT

0031165 03 000000 000145 ----- CDGNM

-001- 14 VFD 1,0 2,0 2,0 2,0 3,1 6,NBGN

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

TEST 55

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 CSTDF3 OW 0(1),0(16) # 12707
-001- 02 NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000001 *****

0031166 05 DF3INIT PREPARE
-002- 06 # RTN_BLM
-002- 07 # TURN OFF AUTOMATIC RETURNS TO BLM EVERY 20 TABLE ENTRIES.
0031166 03 000001 000144 ----- CDGNM -002- 08 VFD 1,0 2,0 6,0 1,1 6,NRTNBLM

-002- 10 # SEND
-002- 11 # SEND 0(0000001) TO OFF-LINE MCHB.
-002- 12 # PARITY IS 0(2).
0031167 03 000002 047707 ----- CDGNM -002- 13 VFD 1,0 2,2 7,B(0111111) 6,N2SEND
0031170 03 000003 000040 000001 -002- 14 ADDR 0(0000001),0(2)

-002- 16 # MICRO
-002- 17 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-002- 18 # TO FIELD = MSXT FROM FIELD = MCHBZF
0031172 03 000005 020005 ----- CDGNM -002- 19 VFD 1,0 2,1 7,0 6,NMICRO
0031173 03 000006 106631 ----- -002- 20 VFD 8,MSXT 8,MCHBZF

-002- 22 # NO3_CODE
-002- 23 # PROCESS 3ACC CODED ROUTINE AT ADDRESS **2.
0031174 03 000007 040043 000000 CDGNM -002- 24 VFD 1,0 2,2 7,0 6,NNO3CD
0031175 03 000010 000000 031177 0031177 -002- 25 ADDR **2

-001- 27 # SUBSTITUTE 'MAS' FOR 'LOOP' IN TROUBLE PRINTOUT
0031177 03 000012 103501 000151 TTYTBL -002- 28 LI R4,MXAS
0031201 03 000014 034500 043132 CTSD -001- 29 STL R4,PAGSCR+24
-001- 30 # USE THE DIAGNOSTIC LOOP INDEX (LOOPIND) TO ACCESS THE PROPER I/Q
-001- 31 # SUBCHANNEL ADDRESS IN THE MAIN STORE CONTROLLER I/Q SUBCHANNEL TABLE
-001- 32 # (MASCIOSC) AND STORE IT IN THE ASSIGNED SLOT (IOADDR) IN PAGING BUFFER (PAGSCR)

0031203 03 000016 103240 ----- -001- 34 ZR LOOPIND
0031204 03 000017 004257 ----- -001- 35 SN LOOPIND,1 #ASSURE THAT LOOPIND IS ZERO BEFORE
# PROCEEDING WITH TEST

0031205 03 000020 072652 ----- CDGNM -001- 37 HN LOOPIND,LOOPIND
0031206 03 000021 037020 012757 CDGSIO -002- 38 BSA CONTPREP
0031210 03 000023 172652 ----- CDGNM -001- 39 HN LOOPIND,LOOPIND
0031211 03 000024 137020 012667 CDGSIO -003- 40 BSA MASIOH
0031213 03 000026 046071 ----- -002- 41 DATA MASCINT # INITIALIZE STORE CONTROLLER

0031214 03 000027 171400 ----- -002- 43 GA
0031215 03 000030 031000 031221 0031221 -002- 44 LAL 'R0,**4,RAO
0031217 03 000032 037000 011162 CDGNM -002- 45 BL INTRPNO3

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

C01

TEST 55

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0031221

01
-001- 02

EOM
NOTE

12707

***** THE LAST ADDRESS OVERWRITTEN IS 000033 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-5D

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 63

TEST 55 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 55 SEGMENT 1
-001- 04 # *
-001- 05 # *****

0031221 03 000034 000100 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(1) 6,NTESTSEG

11 # THIS SEGMENT ENSURES THAT NONE OF THE NORMAL LOAD (NLDB1) SIGNALS WHICH
12 # ENABLE THE FAST SET INPUTS(TSTSD0A0/TSTSD1A0) OF THE BIT-SLICED DATA FLOPS
13 # (FFSD) ARE STUCK ACTIVE (HIGH). THIS TEST IS SIMPLIFIED THRU THE USE OF THE
14 # WRITE COMPLEMENT STORE BUS SIGNAL WHICH DISABLES THE NORMAL LOAD SIGNAL
15 # IN THE STORE CONTROLLER BEFORE IT GETS TO THE DATA FLOPS. AFTER THE STORE
16 # INITIALIZE ORDER CLEARS ALL OF THE DATA FLOPS, A WRITE IS PERFORMED WITH
17 # THE COMPLEMENT WRITE OPTION ACTIVE (CW=1) WHICH SHOULD PREVENT ANY OF THE
18 # ONES ON THE STORE BUS FROM THE PROCESSOR FROM REGISTERING IN THE DATA FLOPS.
19 # A READ IS DONE WITH THE READ DATA STROBE ENABLE (RDDSTB1) TO THE DATA FLOP
20 # DEACTIVATED BY A MAINTENANCE I/O ORDER (FNOTIMC) TO ELIMINATE INTERFERENCE
21 # FROM MEMORY PLANE DATA. IF ONE OF THE NORMAL LOAD SIGNALS IS STUCK-AT-ONE,
22 # THAT BIT WILL BE READ AS AN OVERWRITTEN 'ONE' INSTEAD OF THE 'ZERO' THAT
23 # SHOULD HAVE BEEN PRESERVED FROM THE FIRST INITIALIZE.

-001- 26 # NO3_CODE
-001- 27 # PROCESS 3ACC CODED ROUTINE AT ADDRESS SDNLDC.
0031222 03 000035 040043 000000 CDGNM -001- 28 VFD 1,0 2,2 7,0 6,NNO3CD
-0031223 03 000036 000000 031225 0031225 -001- 29 ADDR SDNLDC

0031225 31 SDNLDC
0031225 32 NMLWRIT BASADDR,ZEROS
0031225 03 000040 103561 106200 -001- 33 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031227 03 000042 003741 000000 -001- 34 LI R14,ZEROS # TO BE WRITTEN - SD0-15
0031231 03 000044 003761 000000 -001- 35 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 36 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031233 03 000046 031040 043140 CTSD -001- 37 LAL R2,BASADDR,RAO
0031235 03 000050 041061 ----- -001- 38 LA R3,1(RAO)
0031236 03 000051 003140 ----- -001- 39 ZR R6 # NO SPECIAL PARITY
0031237 -001- 40 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031237 03 000052 037020 013027 CDGSBS -002- 41 BSA MEMOP
42 # INITIAL NMLWRITE OF ZEROES IS DONE TO ALLOW NORMAL LOAD
43 # TO OCCURE. THIS IS NECESSARY FOR STORE 1 SELECT.
0031241 44 SIOM MASCINT
0031241 03 000054 137020 012667 CDGSIO -002- 45 BSA MASIOM
0031243 03 000056 046071 137020 -001- 46 DATA MASCINT # INITIALIZE STORE CONTROLLER
0031244 47 SIOM FNOTIMC
0031244 03 000057 137020 012667 CDGSIO -002- 48 BSA MASIOM
0031246 03 000061 044470 ----- -001- 49 DATA FNOTIMC # BLOCK TIMING TO FANOUT BOARDS(W/CLOCK)

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 64

TEST 55 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031247          01          NMLWRIT BASADDR,ONES,CW=1          # ATTEMPT TO OVERWRITE INITIAL ZEROS
0031247 03 000062 103561 146200          -001- 02          LI          R7,B(1100110010000000) # MAIN MEMORY STATUS
0031251 L3 000064 003741 177777          -001- 03          LI          R14,ONES          # TO BE WRITTEN - SD0-15
0031253 03 000066 003761 177777          -001-;04          LI          R15,ONES          # TO BE WRITTEN - SD16-23
          -001- 05 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031255 03 000070 031040 043140 CTSD          -001- 06          LAL         R2,BASADDR,RAO
0031257 03 000072 041061 -----          -001-;07          LA          R3,1(RAO)
0031260 03 000073 003140 -----          -001- 08          ZR          R6          # NO SPECIAL PARITY
0031261          -001- 09          CALL        MEMOP          # INITIATE NORMAL WRITE(OFF-LINE)
0031261 03 000074 037020 013027 CDGSBS          -002- 10          BSA         MEMOP
0031263          -001- 11          NMLREAD    BASADDR,CW=1          # CHECK STORE CONTROLLER DATA FLOPS
0031263 03 000076 103561 146260          -001- 12          LI          R7,B(1100110010110000) # MAIN MEMORY STATUS
0031265 03 000100 031040 043140 CTSD          -001- 13          LAL         R2,BASADDR,RAO
0031267 03 000102 041061 -----          -001-;14          LA          R3,1(RAO)
0031270 03 000103 003140 -----          -001- 15          ZR          R6          # NO SPECIAL PARITY
0031271 03 000104 037020 013027 CDGSBS          -002- 16          BSA         MEMOP
0031273          -001- 17          RTN_TBL    **4
0031273 03 000106 171400 -----          -001- 18          GA
0031274 03 000107 031000 031300 0031300          -001- 19          LAL         R0,**4,RAO
0031276 03 000111 037000 011162 CDGNM          -001- 20          BL          INTRPN03

          -001- 22 # COMPARE
          -001- 23 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
          -001- 24 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031300 03 000113 050014 ----- CDGNM          -001- 25          VFD         1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0031301 03 000114 000000 000000          -001- 26          ADDR        0(0000000)

          -001- 28 # FAILBIT
          -001- 29 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS NLDBSXX
          -001- 30 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031303 03 000116 020020 ----- CDGNM          -001- 31          VFD         1,0 2,1 5,0 2, 6,NFLBITS
0031304 03 000117 000001 -----          -001- 32          VFD         16,NLDBS

          34 # NORMAL LOAD (NLDB1) ON INPUT TO STORE DATA FLOP (FFSD) IS STUCK-AT-ONE (HIGH)

          -001- 37 # COMPARE
          -001- 38 # COMPARE ON-LINE MCHB BIT PL TO 0.
          -001- 39 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031305 03 000120 022414 ----- CDGNM          -001- 40          VFD         1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE
0031306 03 000121 000000 -----          -001- 41          VFD         15,0 1,0

          -001- 43 # FAILTST
          -001- 44 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDBSPR.
          -001- 45 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031307 03 000122 020017 ----- CDGNM          -001- 46          VFD         1,0 2,1 5,0 2,0 6,NFAILTST
0031310 03 000123 000021 -----          -001- 47          VFD         16,NLDBSPR

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 65

TEST 55 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # NORMAL LOAD (NLDB1) ON INPUT OF DATA PARITY LOW FLOP IS STUCK-AT-ONE (HIGH)

```

-001- 04 # COMPARE
-001- 05 # COMPARE ON-LINE MCHB BIT PH TO 0.
-001- 06 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031311 03 000124 022514 ----- CDGNM
0031312 03 000125 000000 -----
-001- 07 # VFD 1,LPINDX 2,1 1,0 1,SUCCESS 5,PH 6,NCOMPARE
-001- 08 # VFD 15,0 1,0

-001- 10 # FAILTST
-001- 11 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDBSPR.
-001- 12 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031313 03 000126 020017 ----- CDGNM
0031314 03 000127 000021 -----
-001- 13 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 14 # VFD 16,NLDBSPR

```

16 # NORMAL LOAD (NLDB1) ON INPUT OF DATA PARITY HIGH FLOP IS STUCK-AT-ONE (HIGH)

```

-001- 19 # WIDE_TST
-001- 20 # IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS SDNLDT.
0031315 03 000130 040046 000000 CDGNM
0031316 03 000131 000000 031333 0031333
-001- 21 # VFD 1,0 2,2 7,0 6,NWDTST
-001- 22 # ADDR SDNLDT

-001- 24 # MICRO
-001- 25 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 26 # TO FIELD = MCHBXT FROM FIELD = SDR1XF
0031320 03 000133 020005 ----- CDGNM
0031321 03 000134 114455 -----
-001- 27 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 28 # VFD 8,MCHBXT 8,SDR1XF

-001- 30 # RETRN
-001- 31 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0031322 03 000135 007710 ----- CDGNM
-001- 32 # VFD 1,0 2,0 7,B(0111111) 6,NRETRN

-001- 34 # MASK
-001- 35 # MASK CONTENTS OF ON-LINE MCHB WITH 0(0177400).
0031323 03 000136 040013 ----- CDGNM
0031324 03 000137 000000 177400
-001- 36 # VFD 1,0 2,2 7,0 6,NMASK
-001- 37 # ADDR 0(0177400)

-001- 39 # COMPARE
-001- 40 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 41 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031326 03 000141 050014 ----- CDGNM
0031327 03 000142 000000 000000
-001- 42 # VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 43 # ADDR 0(0000000)

-001- 45 # FAILBIT
-001- 46 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS NLDBS+9XX
-001- 47 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031331 03 000144 020020 ----- CDGNM
0031332 03 000145 000012 -----
-001- 48 # VFD 1,0 2,1 5,0 2, 6,NFLBITS
-001- 49 # VFD 16,NLDBS+9

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 66

TEST 55 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

02 # NORMAL LOAD (NLDB1) ON INPUT OF WIDE DATA FLOP (FFSD) IS STUCK-AT-ONE (HIGH)

0031333

05 SDNLDXT

STORE CONTROLLER DATA REGISTER TEST

PR-109-5-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 67

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 55 SEGMENT 2
-001- 04 # *
-001- 05 # *****

```

```

0031333 03 000146 000200 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(2) 6,NTESTSEG

```

```

11 # THIS SEGMENT ENSURES THAT NONE OF THE NORMAL LOAD SIGNALS (NLDB1) THAT
12 # CONTROL THE GATED INPUTS OF THE STORE ADDRESS FLOPS (FFSAD) ARE STUCK ACTIVE.
13 # AFTER THE STORE INITIALIZE ORDER CLEARS ALL OF THE ADDRESS FLOPS, A
14 # COMPLEMENT WRITE IS DONE USING AN ALL-ONES STORE ADDRESS. THE STORE
15 # COMPLEMENT WRITE SIGNAL (SCW) SHOULD DISABLE THE NORMAL LOAD ENABLES THAT
16 # ARE USUALLY SENT TO ALL OF THE ADDRESS FLOPS. IF ANY OF THESE NLDB1 SIGNALS
17 # ARE STUCK-AT-ONE (HIGH), THEN EITHER THE SUBSEQUENT BUS LOOPAROUND READ OR
18 # THE STORE ERROR REGISTER READ WILL FIND A 'ONE' HAS BEEN GATED INTO THE
19 # FAULTY BIT BY THE PRECEDING WRITE. IF NONE OF THE NORMAL LOAD ENABLES ARE
20 # STUCK, THE ADDRESS FLOPS SHOULD CONTAIN THE 'ZEROS' THAT WERE LOADED BY THE
21 # INITIALIZE ORDER.

```

```

-001- 24 # NO3_CODE
-001- 25 # PROCESS 3ACC CODED ROUTINE AT ADDRESS SANLDCKL.
0031334 03 000147 040043 000000 CDGNM -001- 26 VFD 1,0 2,2 7,0 6,NN03CD
0031335 03 000150 000000 031337 0031337 -001- 27 ADDR SANLDCKL
0031337 29 SANLDCKL
0031337 30 SIOM MASCINT
0031337 03 000152 137020 012667 CDGSIO -002- 31 BSA MASIOM
0031341 03 000154 046071 137020 -001- 32 DATA MASCINT # INITIALIZE STORE CONTROLLER
0031342 33 SIOM RDAD
0031342 03 000155 137020 012667 CDGSIO -002- 34 BSA MASIOM
0031344 03 000157 047410 ----- -001- 35 DATA RDAD # GATE STORE ADDRESS BUS(SAO-16,PL) TO
# STORE DATA BUS(SDO-15,PL,PH) W/ CLOCK
0031345 03 000160 130700 043140 CTSD 37 LL R12,BASADDR # UPPER ADDRESS BITS OF STORE UNDER TEST
0031347 03 000162 003721 177777 38 LI R13,0(177777) # SET SAO-15=1
0031351 39 NMLWRIT RAO,DATA=ZEROS,CW=0,BEC=1 # COMPLEMENT WRITE W/ ALL-ONES ADDRESS
0031351 03 000164 003561 106200 -001- 40 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031353 03 000166 003741 000000 -001- 41 LI R14,ZEROS # TO BE WRITTEN - SDO-15
0031355 03 000170 003761 000000 -001- 42 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 43 # IF ADDRESS(RAO) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
-001- 44 LR R2,R12
-001- 45 LR R3,R13
0031357 03 000172 006054 ----- -001- 46 ZR R6 # NO SPECIAL PARITY
0031360 03 000173 006075 ----- -001- 47 CALL MEMCP # INITIATE NORMAL WRITE(OFF-LINE)
0031361 03 000174 003140 ----- -002- 48 BSA MEMOP
0031362 0031362 -001- 49 LL R12,BASADDR # UPPER ADDRESS BITS OF STORE UNDER TEST
0031362 03 000175 037020 013027 CDGSBS
0031364 03 000177 130700 043140 CTSD

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

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23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 68

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TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031366,03 000201 003320 ----- 01 ZR R13 # RESET SA0-15=0
0031367 02 MTCREAD RAD,PRH1L1,CW=1 # RETRIEVE ADDRESS BITS (SA0-16,PL)
0031367 03 000202 003561 146263 -001- 03 LI R7,B(1100110010110011) # MAIN MEMORY STATUS
0031371 03 000204 006054 ----- -001- 04 LR R2,R12
0031372 03 000205 006075 ----- -001-;05 LR R3,R13
0031373 03 000206 003541 000017 -001- 06 LI R6,PRH1L1 # SETUP SPECIAL PARITY
0031375 03 000210 037020 013027 CDGSBS -002- 07 BSA MEMOP
0031377 08 RTN_TBL **4
0031377 03 000212 171400 ----- -001- 09 GA
0031400 03 000213 031000 031404 0031404 -001- 10 LAL RD,**4,RAD
0031402 03 000215 037000 011162 CDGNM -001- 11 BL INTRPN03

-001- 13 # COMPARE
-001- 14 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).
-001- 15 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031404 03 000217 050014 ----- CDGNM -001- 16 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0031405 03 000220 000000 177777 -001- 17 ADDR 0(0177777)

-001- 19 # FAILBIT
-001- 20 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS NLDSAXX
-001- 21 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031407 03 000222 020020 ----- CDGNM -001- 22 VFD 1,0 2,1 5,0 2, 6,NFLBITS
0031410 03 000223 000032 ----- -001- 23 VFD 16,NLDSA

25 # NORMAL LOAD (NLDB1) ON GATED INPUT OF STORE ADDRESS FLOP (FFSAD) IS STUCK HIGH

-001- 28 # NO3 CODE
-001- 29 # PROCESS 3ACC CODED ROUTINE AT ADDRESS SANLDCKM.
0031411 03 000224 040043 000000 CDGNM -001- 30 VFD 1,0 2,2 7,0 6,NN03CD
0031412 03 000225 000000 031414 0031414 -001- 31 ADDR SANLDCKM

0031414 33 SANLDCKM
0031414 34 SIOM MASCINT
0031414 03 000227 137020 012667 CDGSIO -002- 35 BSA MASIOH
0031416 03 000231 046071 137020 -001- 36 DATA MASCINT # INITIALIZE STORE CONTROLLER
0031417 37 SIOM RDAD&-ISOLATE
0031417 03 000232 137020 012667 CDGSIO -002- 38 BSA MASIOH
0031421 03 000234 007410 ----- -001- 39 DATA RDAD&-ISOLATE # GATE STORE ADDRESS BUS(SA0-16,PL) TO
STORE DATA BUS(SD0-15,PL,PH) W/ CLOCK
0031422 03 000235 130700 043140 CTSD 41 LL R12,BASADDR # UPPER ADDRESS BITS OF STORE UNDER TEST
0031424 03 000237 003721 000401 42 LI R13,0(401) # PUT ODD PARITY IN BOTH HALVES OF SA0-15
0031426 43 NMLWRIT RAD,DATA=0(401),HIDATA=0,BEC=1
0031426 03 000241 003561 106200 -001- 44 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031430 03 000243 003741 000401 -001- 45 LI R14,0(401) # TO BE WRITTEN - SD0-15
0031432 03 000245 003761 000000 -001-;46 LI R15,0 # TO BE WRITTEN - SD16-23
-001- 47 # IF ADDRESS(RAD) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAD
0031434 03 000247 006054 ----- -001- 48 LR R2,R12
0031435 03 000250 006075 ----- -001-;49 LR R3,R13

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 69

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0031436	03	000251	003140	-----	-001- 01	ZR	R6	# NO SPECIAL PARITY
0031437					-001- 02	CALL	MEMOP	# INITIATE NORMAL WRITE(OFF-LINE)
0031437	03	000252	037020	013027 CDGSBS	-002- 03	BSA	MEMOP	
0031441	03	000254	130700	043140 CTSD	04	LL	R12,BASADDR	# UPPER ADDRESS BITS OF STORE UNDER TEST
0031443	03	000256	015714	-----	05	COM	R12	# COMPLEMENT HIGH ADDRESS BITS (SA16-19)
0031444	03	000257	003320	-----	06	ZR	R13	# PUT EVEN PARITY IN BOTH HALVES OF SA0-15
0031445					08	MTCREAD	RA0,CH=1	# ATTEMPT READ WITH INCORRECT ADDRESS BITS
0031445	03	000260	003561	146263	-001- 10	LI	R7,B(1100110010110011)	# MAIN MEMORY STATUS
0031447	03	000262	006054	-----	-001- 11	LR	R2,R12	
0031450	03	000263	006075	-----	-001-;12	LR	R3,R13	
0031451	03	000264	003140	-----	-001- 13	ZR	R6	# NO SPECIAL PARITY
0031452	03	000265	037020	013027 CDGSBS	-002- 14	BSA	MEMOP	
0031454					15	RTN_TBL	DRTST	
0031454	03	000267	171400	-----	-001- 16	GA		
0031455	03	000270	031000	031461 0031461	-001- 17	LAL	RO,DRTST,RA0	
0031457	03	000272	037000	011162 CDGNM	-001- 18	BL	INTRPN03	
0031461					20 DRTST			
					-001- 21 #	MICRO		
					-001- 22 #	PERFORM MICRO INSTRUCTION IN OFF-LINE CU.		
					-001- 23 #	TO FIELD = MCHBXT FROM FIELD = MCSXF		
0031461	03	000274	020005	----- CDGNM	-001- 24	VFD	1,0 2,1 7,0 6,NMICRO	
0031462	03	000275	114564	-----	-001- 25	VFD	8,MCHBXT 8,MCSXF	
					-001- 27 #	RETRN		
					-001- 28 #	RETURN OFF-LINE MCHB TO ON-LINE MCHB.		
0031463	03	000276	007710	----- CDGNM	-001- 29	VFD	1,0 2,0 7,B(0111111) 6,NRETRN	
					-001- 31 #	COMPARE		
					-001- 32 #	COMPARE ON-LINE MCHB BIT S(DRO) TO 1.		
					-001- 33 #	IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.		
0031464	03	000277	021014	----- CDGNM	-001- 34	VFD	1,LPINDX 2,1 1,0 1,SUCCESS 5,S(DRO) 6,NCOMPARE	
0031465	03	000300	000001	-----	-001- 35	VFD	15,0 1,1	
					-001- 37 #	FAILTST		
					-001- 38 #	FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDSA89.		
					-001- 39 #	DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.		
0031466	03	000301	020017	----- CDGNM	-001- 40	VFD	1,0 2,1 5,0 2,0 6,NFAILTST	
0031467	03	000302	000055	-----	-001- 41	VFD	16,NLDSA89	

43 # NORMAL LOAD (NLDB1) ON FAST SET INPUT OF ADDRESS BIT 18 OR 19 IS STUCK HIGH

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 70

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # NO3_CODE
-001- 02 # PROCESS 3ACC CODED ROUTINE AT ADDRESS SANLDCNT.
0031470 03 000303 040043 000000 CDGNM -001- 03 VFD 1,0 2,2 7,0 6,NN03CD
0031471 03 000304 000000 031473 0031473 -001- 04 ADDR SANLDCNT

0031473 06 SANLDCNT
0031473 07 SIOM EXERLD
0031473 03 000306 137020 012667 CDGSIO -002- 08 BSA MASIOM
0031475 03 000310 145454 137020 -001- 09 DATA EXERLD # ENABLE EXTERNAL LOAD OF STORE ERROR REG
0031476 10 SRIO SERREAD
0031476 03 000311 137020 012667 CDGSIO -002- 11 BSA MASIOM
0031500 03 000313 047114 ----- -001- 12 DATA SERREAD # READ BACK STORE ERROR REGISTER(SER)
0031501 03 000314 130420 043140 CTSD 13 LL R1,BASADDR # UPPER ADDRESS BITS OF STORE UNDER TEST
0031503 03 000316 016421 000017 14 LRM R1,R1,0(17) # EXTRACT SA16-19
0031505 03 000320 026024 ----- 15 SBN R1,4
16 # COMPENSATE FOR SINGLE BIT (SA8) SET IN ORIGINAL DATA PATTERN

0031506 03 000321 057001 ----- 18 TRPL R1 # EXPECTED PARITY OVER THESE BITS
0031507 03 000322 003000 ----- 19 ZR RD # CLEAR BEFORE PARITY RESULT IS INSERTED
0031510 03 000323 030010 ----- 20 ICF RO,S(SAPHL P) # PREPARE FOR COMPARE WITH PARITY OBSERVED
0031511 03 000324 015260 ----- 22 XR R11,RO # COMPARE PARITY HIGH OBSERVED WITH EXPECTED
0031512 03 000325 007473 ----- 24 LSR MCHB,R11 # PREPARE FOR DIAGNOSTIC COMPARE
0031513 25 RTN_TBL SANLDCKH
0031513 03 000326 071400 ----- -001- 26 GA
0031514 03 000327 031000 031520 0031520 -001- 27 LAL RO,SANLDCKH,RAO
0031516 03 000331 037000 011162 CDGNM -001- 28 BL INTRPN03

0031520 30 SANLDCKH
-001- 31 # COMPARE
-001- 32 # COMPARE ON-LINE MCHB BIT S(SAPHL P) TO 0.
-001- 33 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031520 03 000333 021014 ----- CDGNM -001- 34 VFD 1,LPINDX 2,1 1,0 1,SUCCESS 5,S(SAPHL P) 6,NCOMPARE
0031521 03 000334 000000 ----- -001- 35 VFD 15,0 1,0

-001- 37 # FAILTST
-001- 38 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDSAPH.
-001- 39 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031522 03 000335 020017 ----- CDGNM -001- 40 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
0031523 03 000336 000056 ----- -001- 41 VFD 16,NLDSAPH

43 # NORMAL LOAD (NLDB1) ON FAST SET INPUT OF ADDRESS PARITY HIGH IS STUCK HIGH

```

STORE CONTROLLER DATA REGISTER TEST

PR-10935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 71

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # COMPARE
-001- 02 # COMPARE ON-LINE MCHB BIT S(SA17LP) TO 0.
-001- 03 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031524 03 000337 021114 ----- CDGNM -001- 04 # VFD 1,LPINDX 2,1 1,0 1,SUCCE 5,S(SA17LP) 6,NCOMPARE
0031525 03 000340 000000 ----- -001- 05 # VFD 15,0 1,0

-001- 07 # FAILTST
-001- 08 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDSA17.
-001- 09 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031526 03 000341 020017 ----- CDGNM -001- 10 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
0031527 03 000342 000054 ----- -001- 11 # VFD 16,NLDSA17

13 # NORMAL LOAD (NLDB1) ON FAST SET INPUT OF ADDRESS BIT 17 IS STUCK HIGH

-001- 18 # MICRO
-001- 19 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 20 # TO FIELD = MCHBXT FROM FIELD = SDRXF
0031530 03 000343 020005 ----- CDGNM -001- 21 # VFD 1,0 2,1 7,0 6,NMICRO
0031531 03 000344 114456 ----- -001- 22 # VFD 8,MCHBXT 8,SDRXF

-001- 24 # RETRN
-001- 25 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0031532 03 000345 007710 ----- CDGNM -001- 26 # VFD 1,0 2,0 7,B(0111111) 6,NRETRN

-001- 28 # COMPARE
-001- 29 # COMPARE ON-LINE MCHB BIT PL TO 0.
-001- 30 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031533 03 000346 022414 ----- CDGNM -001- 31 # VFD 1,LPINDX 2,1 1,0 1,SUCCE 5,PL 6,NCOMPARE
0031534 03 000347 000000 ----- -001- 32 # VFD 15,0 1,0

-001- 34 # FAILTST
-001- 35 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDSAPL.
-001- 36 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031535 03 000350 020017 ----- CDGNM -001- 37 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
0031536 03 000351 000052 ----- -001- 38 # VFD 16,NLDSAPL

40 # NORMAL LOAD (NLDB1) ON FAST SET INPUT OF ADDRESS PARITY LOW IS STUCK HIGH

-001- 43 # COMPARE
-001- 44 # COMPARE ON-LINE MCHB BIT PH TO 0.
-001- 45 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031537 03 000352 022514 ----- CDGNM -001- 46 # VFD 1,LPINDX 2,1 1,0 1,SUCCE 5,PH 6,NCOMPARE
0031540 03 000353 000000 ----- -001- 47 # VFD 15,0 1,0

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 72

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 #   FAILTST
-001- 02 #   FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS NLDSA16.
-001- 03 #   DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031541 03 000354 020017 ----- CDGNN -001- 04   VFD   1,0 2,1 5,0 2,0 6,NFAILTST
0031542 03 000355 000053 ----- -001- 05   VFD   16,NLDSA16

```

07 # NORMAL LOAD (NLDB1) ON FAST SET INPUT OF ADDRESS BIT 16 IS STUCK HIGH

```

-001- 12 #   NO3_CODE
-001- 13 #   PROCESS 3ACC CODED ROUTINE AT ADDRESS SANLDXT.
0031543 03 000356 040043 000000 CDGNN -001- 14   VFD   1,0 2,2 7,0 6,NN03CD
0031544 03 000357 000000 031546 0031546 -001- 15   ADDR   SANLDXT
0031546                                17 SANLDXT

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 73

TEST 55 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

		01	CSTDF3	OM	0(361),0(363)	# 12707	
		-001-	02	NOTE	*****	THE FIRST ADDRESS	OVERWRITTEN IS 000361 *****
0031546				05	FINALE	DF3INIT	
0031546	03	000361	137020	012771	CDGSIO	-002-	06
0031550	03	000363	137000	031210	0031210	-001-	07
0031552	03	000365	171400	-----		-001-	08
0031553	03	000366	037000	011725	CDGNM	-001-	09
0031553							10
							11
					BSA	FINAL	
					BL	BKCSTDF3	
					GA		
					BL	PASSTST	# TEST COMPLETED
					EOW		# 12707
					NOTE	*****	THE LAST ADDRESS
							OVERWRITTEN IS 000367 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 74

TEST 56

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *****
-001- 03 # **
-001- 04 # ** CU DIAGNOSTIC TEST NO. 56, MAIN_STORE_CONTROLLER_DATA_REGISTER
-001- 05 # **
-001- 06 # *****
-001- 07 # *****

```

0031555

-001- 10 CSTDF4 CSECT

0031555 04 000000 000145 ----- CDGNM -001- 14 VFD 1,0 2,0 2,0 2,0 3,1 6,NBGN

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

TEST 56

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

01 CSTDF4 OW      0(1),0(16)      # 12707
-001- 02      NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000001 *****

0031556
0031556 04 000001 000144 ----- CDGNM
0031557 04 000002 047707 ----- CDGNM
0031560 04 000003 000040 000001
0031562 04 000005 020005 ----- CDGNM
0031563 04 000006 106631 -----
0031564 04 000007 040043 000000 CDGNM
0031565 04 000010 000000 031567 0031567
0031567 04 000012 103501 000151 TTYTBL
0031571 04 000014 034500 043132 CTSD
0031573 04 000016 103240 -----
0031574 04 000017 004257 -----
0031575 04 000020 072652 ----- CDGNM
0031576 04 000021 037020 012757 CDGSIO
0031600 04 000023 172652 ----- CDGNM
0031601 04 000024 137020 012667 CDGSIO
0031603 04 000026 046071 -----
0031604 04 000027 171400 -----
0031605 04 000030 031000 031611 0031611
0031607 04 000032 037000 011162 CDGNM

05 DF4INIT PREPARE
-002- 06 # RTN_BLM
-002- 07 # TURN OFF AUTOMATIC RETURNS TO BLM EVERY 20 TABLE ENTRIES.
-002- 08 VFD 1,0 2,0 6,0 1,1 6,NRTNBLM

-002- 10 # SEND
-002- 11 # SEND 0(0000001) TO OFF-LINE MCHB.
-002- 12 # PARITY IS 0(2).
-002- 13 VFD 1,0 2,2 7,B(0111111) 6,N2SEND
-002- 14 ADDR 0(0000001),0(2)

-002- 16 # MICRO
-002- 17 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-002- 18 # TO FIELD = MSXT FROM FIELD = MCHBXF
-002- 19 VFD 1,0 2,1 7,0 6,NMICRO
-002- 20 VFD 8,MSXT 8,MCHBXF

-002- 22 # NO3_CODE
-002- 23 # PROCESS 3ACC CODED ROUTINE AT ADDRESS **2.
-002- 24 VFD 1,0 2,2 7,0 6,NN03CD
-002- 25 ADDR **2

-001- 27 # SUBSTITUTE 'MAS' FOR 'LOOP' IN TROUBLE PRINTOUT
-002- 28 LI R4,MXAS
-001- 29 STL R4,PAGSCR+24
-001- 30 # USE THE DIAGNOSTIC LOOP INDEX(LOOPIND) TO ACCESS THE PROPER I/Q
-001- 31 # SUBCHANNEL ADDRESS IN THE MAIN STORE CONTROLLER I/Q SUBCHANNEL TABLE
-001- 32 # (MASCIOSC) AND STORE IT IN THE ASSIGNED SLOT(IOADDR) IN PAGING BUFFER(PAGSCR)

-001- 34 ZR LOOPIND
-001- 35 SN LOOPIND,1 #ASSURE THAT LOOPIND IS ZERO BEFORE
#PROCEEDING WITH TEST

-001- 37 HN LOOPIND,LOOPIND
-002- 38 BSA CONTPREP
-001- 39 HN LOOPIND,LOOPIND
-003- 40 BSA MASIOM
-002- 41 DATA MASCINT # INITIALIZE STORE CONTROLLER

-002- 43 GA
-002- 44 LAL R0,**4,RAO
-002- 45 BL INTRPN03

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 76

TEST 56

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSD F W77D

003161j

01
-001- 02

EOM
NOTE

12707

***** THE LAST ADDRESS OVERWRITTEN IS 000033 *****

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSD F ISSUE 05 PAGE 77

TEST 56 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF

W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 56 SEGMENT 1
-001- 04 # *
-001- 05 # *****

```

```

0031611 04 000034 000100 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(1) 6,NTESTSEG

```

```

11 # THIS SEGMENT CHECKS THE COMPLEMENT WRITE DISABLES (CPMWRTO) ON THE
12 # OUTPUTS OF ALL OF THE DATA FLOPS (FFSD) IN THE STORE CONTROLLER FOR STUCK-
13 # AT-ONE (HIGH) FAULTS, WITH THE EXCEPTION OF THE PARITY HIGH BIT WHICH HAS
14 # ITS COMPLEMENT WRITE ENABLE (COMWRT1) CHECKED FOR A STUCK-AT-ZERO (LOW) FAULT.
15 # THIS IS DONE BY WRITING AN ALL-ZEROS DATA PATTERN WITH AN INCORRECT PARITY
16 # LOW BIT AND SIMULATING THE ACTION THAT WOULD BE TAKEN BY THE MICROCODE
17 # IF THIS SINGLE BAD PARITY BIT WERE READ OUT WHILE THE PROCESSOR WAS IN THE
18 # COMPLEMENT CORRECTION (SIMPLEX) MODE OF OPERATION. AFTER THE COMPLEMENT WRITE
19 # OF THIS PATTERN, IT IS READ BACK WITHOUT ANY ERROR CORRECTION OPTIONS ACTIVE.
20 # IF THE COMPLEMENT CORRECTION WAS SUCCESSFUL, AN ALL-ONES PATTERN SHOULD BE
21 # READ OUT (WITH PARITY HIGH EQUAL TO ZERO). ANY BITS THAT DISAGREE WITH THIS
22 # EXPECTED RESULT MUST HAVE FAULTY COMPLEMENT WRITE CIRCUITRY.

```

```

-001- 25 # NO3_CODE
-001- 26 # PROCESS JACC CODED ROUTINE AT ADDRESS SCWCKO.
0031612 04 000035 040043 000000 CDGNM -001- 27 VFD 1,0 2,2 7,0 6,NN03CD
0031613 04 000036 000000 031615 0031615 -001- 28 ADDR SCWCKO

```

```

0031615 30 SCWCKO
0031615 31 SIOM MASCINT
0031615 04 000040 137020 012667 CDGSIO -002- 32 BSA MASIOM
0031617 04 000042 046071 ----- -001- 33 DATA MASCINT # INITIALIZE STORE CONTROLLER
0031620 34 NMLWRIT BASADDR,ZEROS,PRH1D # WRITE INITIAL PATTERN THRU SDR (& SDR1)
0031620 04 000043 103561 106200 -001- 35 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031622 04 000045 003741 000000 -001- 36 LI R14,ZEROS # TO BE WRITTEN - SDO-15
0031624 04 000047 003761 000000 -001- 37 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 38 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031626 04 000051 031040 043140 CTSD -001- 39 LAL R2,BASADDR,RAO
0031630 04 000053 041061 ----- -001- 40 LA R3,1(RAO)
0031631 04 000054 003541 000012 -001- 41 LI R6,PRH1D # SETUP SPECIAL PARITY
0031633 -001- 42 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031633 04 000056 037020 013027 CDGSBS -002- 43 BSA MEMOP
0031635 44 NMLREAD BASADDR,BEC=0,SAC=1 # READ BACK INTO SDR (& SDR1)
0031635 04 000060 103561 036260 -001- 45 LI R7,B(0011110010110000) # MAIN MEMORY STATUS
0031637 04 000062 031040 043140 CTSD -001- 46 LAL R2,BASADDR,RAO
0031641 04 000064 041061 ----- -001- 47 LA R3,1(RAO)
0031642 04 000065 003140 ----- -001- 48 ZR R6 # NO SPECIAL PARITY
0031643 04 000066 037020 013027 CDGSBS -002- 49 BSA MEMOP

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 78

```

TEST 56 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031645          01      NHLWRT BASADDR,ZEROS,PRH10,CW=1,BEC=0,SAC=1 # COMPLEMENT WRITE
0031645 04 000070 103561 076200      -001- 02      LI      R7,B(0111110010000000) # MAIN MEMORY STATUS
0031647 04 000072 003741 000000      -001- 03      LI      R14,ZEROS # TO BE WRITTEN - SDO-15
0031651 04 000074 003761 000000      -001- 04      LI      R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 05 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031653 04 000076 031040 043140 CTSD      -001- 06      LAL     R2,BASADDR,RAO
0031655 04 000100 041061 -----      -001- 07      LA      R3,1(RAO)
0031656 04 000101 003541 000012      -001- 08      LI      R6,PRH10 # SETUP SPECIAL PARITY
0031660          -001- 09      CALL    MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031660 04 000103 037020 013027 CDGSBS      -002- 10      BSA     MEMOP
0031662          11      NHLREAD BASADDR,BEC=1,SAC=0 # READ BACK COMPLEMENT PATTERN
0031662 04 000105 103561 106260      -001- 12      LI      R7,B(1000110010110000) # MAIN MEMORY STATUS
0031664 04 000107 031040 043140 CTSD      -001- 13      LAL     R2,BASADDR,RAO
0031666 04 000111 041061 -----      -001- 14      LA      R3,1(RAO)
0031667 04 000112 003140 -----      -001- 15      ZR      R6 # NO SPECIAL PARITY
0031670 04 000113 037020 013027 CDGSBS      -002- 16      BSA     MEMOP
0031672          -001- 17      RTN_TBL **4
0031672 04 000115 171400 -----      -001- 18      GA
0031673 04 000116 031000 031677 0031677      -001- 19      LAL     RD,**4,RAO
0031675 04 000120 037000 011162 CDGNM      -001- 20      BL      INTRPN03

-001- 22 # COMPARE
-001- 23 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).
-001- 24 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031677 04 000122 050014 ----- CDGNM      -001- 25      VFD     1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0031700 04 000123 090000 177777      -001- 26      ADDR    0(0177777)

-001- 28 # FAILBIT
-001- 29 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS CWSTKXX
-001- 30 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0031702 04 000125 020020 ----- CDGNM      -001- 31      VFD     1,0 2,1 5,0 2, 6,NFLBITS
0031703 04 000126 000002 -----      -001- 32      VFD     16,CWSTK

34 # COMPLEMENT WRITE DISABLE (COMWRTO) ON DATA FLOP (FFSD) OUTPUT IS STUCK-AT-ONE

-001- 37 # COMPARE
-001- 38 # COMPARE ON-LINE MCHB BIT PL TO 1.
-001- 39 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0031704 04 000127 022414 ----- CDGNM      -001- 40      VFD     1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE
0031705 04 000130 000001 -----      -001- 41      VFD     15,0 1,1

-001- 43 # FAILTST
-001- 44 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS CWSTKPR.
-001- 45 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0031706 04 000131 020017 ----- CDGNM      -001- 46      VFD     1,0 2,1 5,0 2,0 6,NFAILTST
0031707 04 000132 000022 -----      -001- 47      VFD     16,CWSTKPR

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 79

TEST 56 SEGMENT 1

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDf

W77D

02 # COMPLEMENT WRITE DISABLE (COMWRTO) ON WIDE DATA (FFSD) OUTPUT IS STUCK-AT-ONE

0031732

05 SCHCKOXT

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 81

TEST 56 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF

W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 56 SEGMENT 2
-001- 04 # *
-001- 05 # *****

```

```

0031732 04 000155 000200 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(2) 6,NTESTSEG

```

```

11 # THIS SEGMENT CHECKS THE COMPLEMENT WRITE ENABLES (COMWRT1) ON THE OUTPUTS
12 # OF ALL OF THE DATA FLOPS (FFSD) IN THE STORE CONTROLLER FOR STUCK-AT-ZERO
13 # (LOW) FAULTS, WITH THE EXCEPTION OF THE PARITY HIGH BIT WHICH HAS ITS
14 # COMPLEMENT WRITE DISABLE (COMWRT0) CHECKED FOR A STUCK-AT-ONE (HIGH) FAULT.
15 # THIS IS DONE BY WRITING AN ALL-ONES DATA PATTERN WITH AN INCORRECT PARITY
16 # HIGH BIT AND SIMULATING THE ACTION THAT WOULD BE TAKEN BY THE MICROCODE IF
17 # THIS BAD PARITY BIT WERE READ OUT WHILE THE PROCESSOR WAS IN THE COMPLEMENT
18 # CORRECTION (SIMPLEX) MODE OF OPERATION. AFTER THIS PATTERN IS COMPLEMENT
19 # CORRECTED, IT IS READ BACK WITHOUT ANY ERROR CORRECTION OPTIONS ACTIVE.
20 # IF THE COMPLEMENT CORRECTION WAS SUCCESSFUL, AN ALL-ZEROS PATTERN SHOULD BE
21 # READ OUT (WITH PARITY HIGH EQUAL TO ONE). ANY BITS THAT DISAGREE WITH THIS
22 # EXPECTED RESULT MUST HAVE FAULTY COMPLEMENT WRITE CIRCUITRY.

```

```

-001- 25 # NO3 CODE
-001- 26 # PROCESS 3ACC CODED ROUTINE AT ADDRESS SCWCK1.
0031733 04 000156 040043 000000 CDGNM -001- 27 VFD 1,0 2,2 7,0 6,NNO3CD
0031734 04 000157 000000 031736 0031736 -001- 28 ADDR SCWCK1

```

```

0031736 30 SCWCK1
0031736 31 SIOM MASCINT
0031736 04 000161 137020 012667 CDGSIO -002- 32 BSA MASIO
0031740 04 000163 046071 ----- -001- 33 DATA MASCINT # INITIALIZE STORE CONTROLLER
0031741 34 NMLWRIT BASADDR,ONES,PRHOL1 # WRITE INITIAL PATTERN THRU SDR (& SDR1)
0031741 04 000164 103561 106200 -001- 35 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0031743 04 000166 003741 177777 -001- 36 LI R14,ONES # TO BE WRITTEN - SDO-15
0031745 04 000170 003761 177777 -001- 37 LI R15,ONES # TO BE WRITTEN - SD16-23
-001- 38 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAD), LOAD R2 & R3 DIRECTLY FROM RAD
0031747 04 000172 031040 043140 CTSD -001- 39 LAL R2,BASADDR,RAD
0031751 04 000174 041061 ----- -001- 40 LA R3,1(RAD)
0031752 04 000175 003541 000005 -001- 41 LI R6,PRHOL1 # SETUP SPECIAL PARITY
0031754 -001- 42 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0031754 04 000177 037020 013027 CDGSBS -002- 43 BSA MEMOP
0031756 44 NMLREAD BASADDR,BEC=0,SAC=1 # READ BACK INTO SDR (& SDR1)
0031756 04 000201 103561 036260 -001- 45 LI R7,B(0011110010110000) # MAIN MEMORY STATUS
0031760 04 000203 031040 043140 CTSD -001- 46 LAL R2,BASADDR,RAD
0031762 04 000205 041061 ----- -001- 47 LA R3,1(RAD)
0031763 04 000206 003140 ----- -001- 48 ZR R6 # NO SPECIAL PARITY
0031764 04 000207 037020 013027 CDGSBS -002- 49 BSA MEMOP

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 82

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TEST 56 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0031766          01      NMLWRT BASADDR,ONES,PRHOL1,CW=1,BEC=0,SAC=1 # COMPLEMENT WRITE
0031766 04 000211 103561 076200      -001- 02      LI      R7,B(0111110010000000) # MAIN MEMORY STATUS
0031770 04 000213 003741 177777      -001- 03      LI      R14,ONES # TO BE WRITTEN - SDO-15
0031772 04 000215 003761 177777      -001- 04      LI      R15,ONES # TO BE WRITTEN - SD16-23
-001- 05 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0031774 04 000217 031040 043140 CTSD      -001- 06      LAL     R2,BASADDR,RAO
0031776 04 000221 041061 -----      -001- 07      LA      R3,1(RAO)
0031777 04 000222 003541 000005      -001- 08      LI      R6,PRHOL1 # SETUP SPECIAL PARITY
0032001          09      CALL     MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0032001 04 000224 037020 013027 CDGSBS      -002- 10      BSA     MEMOP
0032003          11      NMLREAD BASADDR,BEC=1,SAC=0 # READ BACK COMPLEMENTED PATTERN
0032003 04 000226 103561 106260      -001- 12      LI      R7,B(1000110010110000) # MAIN MEMORY STATUS
0032005 04 000230 031040 043140 CTSD      -001- 13      LAL     R2,BASADDR,RAO
0032007 04 000232 041061 -----      -001- 14      LA      R3,1(RAO)
0032010 04 000233 003140 -----      -001- 15      ZR      R6 # NO SPECIAL PARITY
0032011 04 000234 037020 013027 CDGSBS      -002- 16      BSA     MEMOP
0032013          17      RTN_TBL **4
0032013 04 000236 171400 -----      -001- 18      GA
0032014 04 000237 031000 032020 0032020      -001- 19      LAL     R0,**4,RAO
0032016 04 000241 037000 011162 CDGNM      -001- 20      BL      INTRPN03

-001- 22 # COMPARE
-001- 23 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 24 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032020 04 000243 050014 ----- CDGNM      -001- 25      VFD     1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0032021 04 000244 000000 000000      -001- 26      ADDR   0(0000000)

-001- 28 # FAILBIT
-001- 29 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS CWSTKXX
-001- 30 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0032023 04 000246 020020 ----- CDGNM      -001- 31      VFD     1,0 2,1 5,0 2, 6,NFLBITS
0032024 04 000247 000002 -----      -001- 32      VFD     16,CWSTK

34 # COMPLEMENT WRITE ENABLE (COMWRT1) ON OUTPUT OF DATA FLOP IS STUCK-AT-ZERO (LO)

-001- 37 # COMPARE
-001- 38 # COMPARE ON-LINE MCHB BIT PL TO 0.
-001- 39 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032025 04 000250 022414 ----- CDGNM      -001- 40      VFD     1,LPINDX 2,1 1,0 1,SUCCESS 5,PL 6,NCOMPARE
0032026 04 000251 000000 -----      -001- 41      VFD     15,0 1,0

-001- 43 # FAILTST
-001- 44 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS CWSTKPR.
-001- 45 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0032027 04 000252 020017 ----- CDGNM      -001- 46      VFD     1,0 2,1 5,0 2,0 6,NFAILTST
0032030 04 000253 000022 -----      -001- 47      VFD     16,CWSTKPR

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 83

TEST 56 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

01 # COMPLEMENT WRITE ENABLE (COMWRT1) ON PARITY LOW BIT (SDPL) IS STUCK-AT-ZERO

```

-001- 04 # COMPARE
-001- 05 # COMPARE ON-LINE MCHB BIT PH TO 1.
-001- 06 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032031 04 000254 022514 ----- CDGNM
0032032 04 000255 000001 -----
-001- 07 VFD 1,LPINDX 2,1 1,0 1,SUCCESS 5,PH 6,NCOMPARE
-001- 08 VFD 15,0 1,1

```

```

-001- 10 # FAILTST
-001- 11 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS CWSTKPR.
-001- 12 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0032033 04 000256 020017 ----- CDGNM
0032034 04 000257 000022 -----
-001- 13 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 14 VFD 16,CWSTKPR

```

16 # COMPLEMENT WRITE DISABLE (COMWRT0) ON PARITY HIGH BIT (SDPH) IS STUCK-AT-ONE

```

-001- 19 # WIDE_TST
-001- 20 # IF WIDE STORE IS NOT PRESENT, GO TO TABLE ENTRY AT ADDRESS SCWCK1XT.
0032035 04 000260 040046 000000 CDGNM
0032036 04 000261 000000 032053 0032053 -001- 21 VFD 1,0 2,2 7,0 6,NWDTST
-001- 22 ADDR SCWCK1XT

```

```

-001- 24 # MICRO
-001- 25 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 26 # TO FIELD = MCHBXT FROM FIELD = SDR1XF
0032040 04 000263 020005 ----- CDGNM
0032041 04 000264 114455 -----
-001- 27 VFD 1,0 2,1 7,0 6,NMICRO
-001- 28 VFD 8,MCHBXT 8,SDR1XF

```

```

-001- 30 # RETRN
-001- 31 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0032042 04 000265 007710 ----- CDGNM
-001- 32 VFD 1,0 2,0 7,B(0111111) 6,NRETRN

```

```

-001- 34 # MASK
-001- 35 # MASK CONTENTS OF ON-LINE MCHB WITH 0(0177400).
0032043 04 000266 040013 ----- CDGNM
0032044 04 000267 000000 177400 -001- 36 VFD 1,0 2,2 7,0 6,NMASK
-001- 37 ADDR 0(0177400)

```

```

-001- 39 # COMPARE
-001- 40 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0000000).
-001- 41 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032046 04 000271 050014 ----- CDGNM
0032047 04 000272 000000 000000 -001- 42 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 43 ADDR 0(0000000)

```

```

-001- 45 # FAILBIT
-001- 46 # FAILURE EXIT. SYMBOLIC TROUBLE NUMBER IS CWSTK+9XX
-001- 47 # WHERE XX IS THE LOWEST FAILING BIT NUMBER.
0032051 04 000274 020020 ----- CDGNM
0032052 04 000275 000013 -----
-001- 48 VFD 1,0 2,1 5,0 2, 6,NFLBITS
-001- 49 VFD 16,CWSTK+9

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 84

D04

TEST 56 SEGMENT 2

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

02 # COMPLEMENT WRITE ENABLE (COMWRT1) ON OUTPUT OF WIDE DATA BIT IS STUCK-AT-ZERO

0032053

05 SCHCK1XT

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 85

TEST 56 SEGMENT 3

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 56 SEGMENT 3
-001- 04 # *
-001- 05 # *****

```

```

0032053 04 000276 000300 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(3) 6,NTESTSEG

```

```

11 # THIS SEGMENT ENSURES THAT COMPLEMENT WRITES CAN BE PERFORMED ON AN
12 # INSTRUCTION, THAT IS, WITH THE 'ID' FLOP IN THE INSTRUCTION MODE. THE WRITE
13 # COMPLEMENT TEST THAT WAS DONE PREVIOUSLY VIA THE STORE DATA REGISTER(S)
14 # IS VIRTUALLY REPEATED (USING AN ALL-ZEROS TEST PATTERN) WITH ONE EXCEPTION.
15 # IN THIS CASE THE COMPLEMENT WRITE (CE=1) IS DONE IN THE INSTRUCTION MODE
16 # (MODE=I). IF THE PATTERN WHICH IS FINALLY READ BACK INTO THE STORE
17 # INSTRUCTION REGISTER IS THE COMPLEMENT OF THE ORIGINAL PATTERN, THEN THE
18 # COMPLEMENT WRITE OPERATION IN THE INSTRUCTION MODE WAS SUCCESSFUL.

```

```

-001- 21 # NO3_CODE
-001- 22 # PROCESS 3ACC CODED ROUTINE AT ADDRESS IDMDCHK.
0032054 04 000277 040043 000000 CDGNM -001- 23 VFD 1,0 2,2 7,0 6,NN03CD
0032055 04 000300 000000 032057 0032057 -001- 24 ADDR IDMDCHK

0032057 26 IDMDCHK
0032057 27 SIOM MASCINT
0032057 04 000302 137020 012667 CDGSIO -002- 28 BSA MASIOM
0032061 04 000304 046071 ----- -001- 29 DATA MASCINT # INITIALIZE STORE CONTROLLER
0032062 30 NMLWRIT BASADDR,ZEROS,PRHOLO # WRITE INITIAL PATTERN OF ALL ZEROS
0032062 04 000305 103561 106200 -001- 31 LI R7,B(1000110010000000) # MAIN MEMORY STATUS
0032064 04 000307 003741 000000 -001- 32 LI R14,ZEROS # TO BE WRITTEN - SD0-15
0032066 04 000311 003761 000000 -001- 33 LI R15,ZEROS # TO BE WRITTEN - SD16-23
-001- 34 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0032070 04 000313 031040 043140 CTSD -001- 35 LAL R2,BASADDR,RAO
0032072 04 000315 041061 ----- -001- 36 LA R3,1(RAO)
0032073 04 000316 003541 000020 -001- 37 LI R6,PRHOLO # SETUP SPECIAL PARITY
0032075 -001- 38 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0032075 04 000320 037020 013027 CDGSBS -002- 39 BSA MEMOP
0032077 40 NMLREAD BASADDR,BEC=0,SAC=1,MODE=I # READ BACK INTO SIR (& SIR1)
0032077 04 000322 103561 036260 -001- 41 LI R7,B(0011110010110000) # MAIN MEMORY STATUS
0032101 04 000324 031040 043140 CTSD -001- 42 LAL R2,BASADDR,RAO
0032103 04 000326 041061 ----- -001- 43 LA R3,1(RAO)
0032104 04 000327 003140 ----- -001- 44 ZR R6 # NO SPECIAL PARITY
0032105 04 000330 037020 013034 CDGSBS -002- 45 BSA MEMOPI
0032107 46 NMLWRIT BASADDR,CW=1,BEC=0,SAC=1 # COMPLEMENT WRITE THE INSTRUCTION
0032107 04 000332 103561 076200 -001- 47 LI R7,B(0111110010000000) # MAIN MEMORY STATUS
0032111 04 000334 003741 000000 -001- 48 LI R14,0 # TO BE WRITTEN - SD0-15
0032113 04 000336 003761 000000 -001- 49 LI R15,0 # TO BE WRITTEN - SD16-23

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 86

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TEST 56 SEGMENT 3

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0032115 04 000340 031040 043140 CTSD -001- 01 # IF ADDRESS(BASADDR) IS ADDRESS REG PAIR(RAO), LOAD R2 & R3 DIRECTLY FROM RAO
0032117 04 000342 041061 ----- -001- 02 LAL R2,BASADDR,RAO
0032120 04 000343 003140 ----- -001- 03 LA R3,1(RAO)
0032121 -001- 04 ZR R6 # NO SPECIAL PARITY
0032121 04 000344 037020 013027 CDGSBS -001- 05 CALL MEMOP # INITIATE NORMAL WRITE(OFF-LINE)
0032123 -002- 06 BSA MEMOP
0032123 04 000346 103561 036260 -001- 07 NMLREAD BASADDR,BEC=0,SAC=1,MODE=I # READ BACK COMPLEMENTED INSTRUCTION
0032125 04 000350 031040 043140 CTSD -001- 08 LI R7,B(0011110010110000) # MAIN MEMORY STATUS
0032127 04 000352 041061 ----- -001- 09 LAL R2,BASADDR,RAO
0032130 04 000353 003140 ----- -001- 10 LA R3,1(RAO)
0032131 04 000354 037020 013034 CDGSBS -001- 11 ZR R6 # NO SPECIAL PARITY
0032133 -002- 12 BSA MEMOPI
0032133 04 000356 171400 ----- -001- 13 RTN_TBL **4
0032134 04 000357 031000 032140 0032140 -001- 14 GA
0032136 04 000361 037000 011162 CDGNM -001- 15 LAL R0,++4,RAO
-001- 16 BL INTRPN03

-001- 18 # COMPARE
-001- 19 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).
-001- 20 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032140 04 000363 050014 ----- CDGNM -001- 21 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0032141 04 000364 000000 177777 -001- 22 ADDR 0(0177777)

-001- 24 # FAILTST
-001- 25 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS INSTCOMP.
-001- 26 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0032143 04 000366 020017 ----- CDGNM -001- 27 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
0032144 04 000367 000033 ----- -001- 28 VFD 16,INSTCOMP

```

30 # COMPLEMENT WRITE IN INSTRUCTION MODE WAS UNSUCCESSFUL

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 87

TEST 56 SEGMENT 4

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 56 SEGMENT 4
-001- 04 # *
-001- 05 # *****

```

```

0032145 04 000370 000400 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(4) 6,NTESTSEG

```

```

11 # THIS SEGMENT CHECKS THAT THE STATE OF THE INSTRUCTION/DATA FLOP (ID)
12 # IS NOT AFFECTED BETWEEN MEMORY OPERATIONS, IF THE PROCESSOR IS IN THE
13 # COMPLEMENT WRITE (CW=1) MODE OF OPERATION. MORE SPECIFICALLY, THE ID FLOP
14 # SHOULD NOT AUTOMATICALLY FLIP BACK TO THE INSTRUCTION (I) STATE AFTER A
15 # STORE DATA (D) OPERATION. THIS CAN BE TESTED BY LOADING THE STORE ADDRESS
16 # (SAR) AND STORE DATA (SDR) REGISTERS WITH ALL ZEROS, IN THAT ORDER,
17 # AND PERFORMING THE COMPLEMENT WRITE OPERATION. THE DATA READY FLOP (DRD/1)
18 # IS CLEARED AND A LOAD OF THE STORE INSTRUCTION REGISTER (SIR) WITH ALL ONES IS
19 # ATTEMPTED, IN ORDER TO INDIRECTLY CHECK THE RESULTING STATE OF THE ID FLOP.
20 # IF THE ALL ONES PATTERN WAS LOADED INTO THE SIR AND CAN BE READ OUT AFTER
21 # THIS, THE ID FLOP MUST NOT HAVE BEEN FLIPPED BACK TO THE INSTRUCTION STATE.
22 # THAT IS, THE ID FLOP DETERMINES WHICH REGISTER POINTS TO THE STORE BUS, AND
23 # THUS CANNOT BE ACCESSED BY MICROINSTRUCTIONS. IF THE SIR CAN BE GATED INTO
24 # AND READ FROM, THE ID FLOP MUST NOT BE IN THE INSTRUCTION (I) STATE.

```

```

-001- 27 # SEND
-001- 28 # SEND 0(0000000) TO OFF-LINE MCHB.
-001- 29 # PARITY IS 0(0).
0032146 04 000371 047707 ----- CDGNM -001- 30 VFD 1,0 2,2 7,B(0111111) 6,N2SEND
0032147 04 000372 000000 000000 -001- 31 ADDR 0(0000000),0(0)
-001- 33 # MICRO
-001- 34 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 35 # TO FIELD = SARXT FROM FIELD = MCHBXF
0032151 04 000374 020005 ----- CDGNM -001- 36 VFD 1,0 2,1 7,0 6,NMICRO
0032152 04 000375 112631 ----- -001- 37 VFD 8,SARXT 8,MCHBXF
-001- 39 # MICRO
-001- 40 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 41 # TO FIELD = SDRXT FROM FIELD = MCHBXF
0032153 04 000376 020005 ----- CDGNM -001- 42 VFD 1,0 2,1 7,0 6,NMICRO
0032154 04 000377 027231 ----- -001- 43 VFD 8,SDRXT 8,MCHBXF
-001- 45 # MICRO
-001- 46 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 47 # MISCELLANEOUS CROSSPOINT SDRX
0032155 04 000400 020005 ----- CDGNM -001- 48 VFD 1,0 2,1 7,0 6,NMICRO
0032156 04 000401 015471 ----- -001- 49 VFD 16,SDRX

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

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23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 88

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TEST 56 SEGMENT 4

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0032157 04 000402 047706 ----- CDGNM
0032160 04 000403 000000 076217

-001- 02 # SET OFF-LINE DATA READY FLOP (DR) TO ALLOW ACTIVATION OF SARDXT CROSSPOINT
-001- 03 # SEND
-001- 04 # SEND 0(0076217) TO OFF-LINE MCHB.
-001- 05 # PARITY IS NORMAL.
-001- 06 # VFD 1,0 2,2 7,B(0111111) 6,NSEND
-001- 07 # ADDR 0(0076217)

-001- 09 # MICRO
-001- 10 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 11 # TO FIELD = BRXT FROM FIELD = MCHBXF
0032162 04 000405 020005 ----- CDGNM
0032163 04 000406 070631 -----
-001- 12 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 13 # VFD 8,BRXT 8,MCHBXF

-001- 15 # MICRO
-001- 16 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 17 # MISCELLANEOUS CROSSPOINT BRXMSX
0032164 04 000407 020005 ----- CDGNM
0032165 04 000410 145312 -----
-001- 18 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 19 # VFD 16,BRXMSX

-001- 21 # MICRO
-001- 22 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 23 # TO FIELD = SARDXT FROM FIELD = NOPXF
0032166 04 000411 020005 ----- CDGNM
0032167 04 000412 141760 -----
-001- 24 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 25 # VFD 8,SARDXT 8,NOPXF

-001- 27 # GATE INTO STORE ADDRESS REGISTER (SAR) IN PREPARATION FOR A DATA OPERATION
-001- 28 # MICRO
-001- 29 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 30 # MISCELLANEOUS CROSSPOINT ZDRX
0032170 04 000413 020005 ----- CDGNM
0032171 04 000414 015472 -----
-001- 31 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 32 # VFD 16,ZDRX

-001- 34 # SEND
-001- 35 # SEND 0(0177777) TO OFF-LINE MCHB.
-001- 36 # PARITY IS NORMAL.
0032172 04 000415 047706 ----- CDGNM
0032173 04 000416 000000 177777
-001- 37 # VFD 1,0 2,2 7,B(0111111) 6,NSEND
-001- 38 # ADDR 0(0177777)

-001- 40 # MICRO
-001- 41 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 42 # TO FIELD = SIRXT FROM FIELD = MCHBXF
0032175 04 000420 020005 ----- CDGNM
0032176 04 000421 017231 -----
-001- 43 # VFD 1,0 2,1 7,0 6,NMICRO
-001- 44 # VFD 8,SIRXT 8,MCHBXF

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 89

TEST 56 SEGMENT 4

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF

W77D

```

-001- 01 # MICRO
-001- 02 # PERFORM MICRO INSTRUCTION IN OFF-LINE CU.
-001- 03 # TO FIELD = MCHBXT FROM FIELD = SIRXF
0032177 04 000422 020005 ----- CDGNM -001- 04 # VFD 1,0 2,1 7,0 6,NMICRO
0032200 04 000423 114436 ----- -001- 05 # VFD 8,MCHBXT 8,SIRXF

-001- 07 # RETRN
-001- 08 # RETURN OFF-LINE MCHB TO ON-LINE MCHB.
0032201 04 000424 007710 ----- CDGNM -001- 09 # VFD 1,0 2,0 7,B(0111111) 6,NRETRN

-001- 11 # COMPARE
-001- 12 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(0177777).
-001- 13 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
0032202 04 000425 050014 ----- CDGNM -001- 14 # VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
0032203 04 000426 000000 177777 -001- 15 # ADDR 0(0177777)

-001- 17 # FAILTST
-001- 18 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS IDFFLT.
-001- 19 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
0032205 04 000430 020017 ----- CDGNM -001- 20 # VFD 1,0 2,1 5,0 2,0 6,NFAILTST
0032206 04 000431 000034 ----- -001- 21 # VFD 16,IDFFLT

```

23 # INSTRUCTION/DATA FLOP (ID) WAS INCORRECTLY FLIPPED TO 'I' STATE AFTER A
24 # COMPLEMENT WRITE OF A DATA WORD

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 90

TEST 56 SEGMENT 5

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

-001- 01 # *****
-001- 02 # *
-001- 03 # * TEST 56 SEGMENT 5
-001- 04 # *
-001- 05 # *****

```

```

0032207 04 000432 000500 ----- CDGNM -001- 08 VFD 1,0 2,0 2,0 5,D(5) 6,NTESTSEG

```

```

11 # THIS SEGMENT FUNCTIONALLY TESTS THE INVOCATION OF THE MICROCODED
12 # COMPLEMENT CORRECTION SUBROUTINE. THIS ROUTINE SHOULD BE INVOKED WHENEVER
13 # A SINGLE PARITY ERROR IS DETECTED IN INFORMATION READ FROM THE MAIN STORE,
14 # WHILE THE PROCESSOR IS IN THE COMPLEMENT WRITE MODE OF OPERATION.
15 # THE PROCEDURE FOR CHECKING THIS INVOLVES WRITING A FEW WORDS OF CODE AND
16 # ONE WORD WITH A SINGLE BAD PARITY BIT INTO THE OFF-LINE STORE. THE CODE IS
17 # EXECUTED AND IN THE PROCESS THE WORD WITH BAD PARITY SHOULD BE ENCOUNTERED.
18 # SINCE THE INTEGRITY OF THIS PARTICULAR WORD HAS BEEN CHECKED PREVIOUSLY,
19 # THE COMPLEMENT CORRECTION ROUTINE WILL BE UNABLE TO SUCCESSFULLY CORRECT
20 # THIS WORD VIA THE COMPLEMENT TECHNIQUE. THUS, THIS MICROINTERRUPT ROUTINE
21 # SHOULD INTERRUPT ITSELF EACH TIME THE SINGLE BAD PARITY BIT IS (RE)DISCOVERED
22 # IN THE 'CORRECTED' DATA THAT IS READ BACK. IF THE PROGRAM ADDRESS REG (PA)
23 # IN THE OFF-LINE PROCESSOR POINTS TO THE READ INSTRUCTION (WHERE COMPLEMENT
24 # CORRECTION ROUTINE IS REPEATEDLY BEING INVOKED) AFTER THIS OFF-LINE CODE
25 # HAS BEEN INITIATED, THEN THE ROUTINE IS WORKING PROPERLY. IF THE 'PA' IS
26 # POINTING ANYWHERE ELSE, THERE MUST BE A FAULT IN THE HARDWARE ASSOCIATED WITH
27 # THIS MICROINTERRUPT SUBROUTINE.

```

```

-001- 30 # NO3 CODE
-001- 31 # PROCESS 3ACC CODED ROUTINE AT ADDRESS MICROCK.
0032210 04 000433 040043 000000 CDGNM -001- 32 VFD 1,0 2,2 7,0 6,NN03CD
0032211 04 000434 000000 032213 0032213 -001- 33 ADDR MICROCK

```

```

0032213 35 MICROCK
0032213 36 CALL INIT_OCC # INITIALIZE OFF-LINE PROCESSOR &
# UNISOLATE OFF-LINE STORE BUS
0032213 04 000436 137020 111240 CSYSUB -001- 38 BSA INIT_OCC
0032215 39 SIOM REFSTRT
0032215 04 000440 137020 012667 CDGSIO -002- 40 BSA MASIOM
0032217 04 000442 006060 ----- -001- 41 DATA REFSTRT # RESUME REGULAR REFRESHING OF STORE

```

```

43 # LOAD OFF-LINE ADDRESS REGISTER PAIR (RAO) WITH BASE ADDRESS (BASADDR)

```

```

0032220 04 000443 131000 043140 CTSD 45 LAL RO,BASADDR,RAO
0032222 04 000445 007400 ----- 46 LSR MCHTR,RO
0032223 47 LMCH LDMCHB
0032223 -001- 48 CALL SLDMCHB
0032223 04 000446 037020 111072 CSYSUB -002- 49 BSA SLDMCHB

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

```

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 91

```

TEST 56 SEGMENT 5

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0032225	04	000450	103401	043631		01	LI	RO,R12XT*E(8)+MCHBXF	
0032227	04	000452	007400	-----		02	LSR	MCHTR,RO	
0032230						03	LMCH	LDMIRL	
0032230						-001- 04	CALL	SLDMIRL	
0032230	04	000453	037020	111075	CSYSUB	-002- 05	BSA	SLDMIRL	
0032232	04	000455	140001	-----		07	L	RO,1(RAD)	
0032233	04	000456	007400	-----		08	LSR	MCHTR,RO	
0032234						09	LMCH	LDMCHB	
0032234						-001- 10	CALL	SLDMCHB	
0032234	04	000457	037020	111072	CSYSUB	-002- 11	BSA	SLDMCHB	
0032236	04	000461	103401	045631		12	LI	RO,R13XT*E(8)+MCHBXF	
0032240	04	000463	007400	-----		13	LSR	MCHTR,RO	
0032241						14	LMCH	LDMIRL	
0032241						-001- 15	CALL	SLDMIRL	
0032241	04	000464	037020	111075	CSYSUB	-002- 16	BSA	SLDMIRL	
19 # LOAD 'NOP' COMMAND WITH SINGLE BAD PARITY BIT INTO OFF-LINE BASE ADDRESS									
0032243						21	NOPLD		
0032243	04	000466	106000	-----		22	NOP		
0032244	04	000467	030400	032243	0032243	23	LL	RO,NOPLD	# GET NOP COMMAND
0032246	04	000471	007400	-----		24	LSR	MCHTR,RO	
0032247	04	000472	057000	-----		25	TRPL	RO	# SAMPLE CORRECT PARITY LOW
0032250	04	000473	030000	-----		26	ICF	RO,BIT0	# PRESERVE PARITY LOW BIT
0032251	04	000474	057020	-----		28	TRPH	RO	# INVERT PARITY HIGH BIT
0032252	04	000475	055003	-----	0032255	29	BNC	SBNOP	# PARITY HIGH = 0?
0032253	04	000476	022001	-----		30	ZBN	RO,BIT1	# IF NOT, CLEAR INDICATOR BIT
0032254	04	000477	053002	-----	0032256	31	B	NOPST	
0032255						32	SBNOP		
0032255	04	000500	126001	-----		33	SBN	RO,BIT1	# IF SO, SET INDICATOR BIT
0032256						36	NOPST		
0032256	04	000501	103421	000006		37	LI	R1,M(BIN)IM(BHC)	
0032260	04	000503	007721	-----		38	LSR	SS_S,R1	# BLOCK INTERRUPTS & HARDWARE CHECKS
0032261						39	MIMODE		
0032261	04	000504	013400	-----		-001- 40	MI	0	
0032262						41	L	BR,RO	
0032262	04	000505	070523	-----		-001- 42	VFD	8,BRXT 8,ROXF	
0032263						43	LDPAR		
0032263	04	000506	025730	-----		-001- 44	DATA	LDPARX	# LOAD RO BITS 0&1 INTO PL&PH OF MCHTR
0032264						45	MCHTRXMCHB		
0032264	04	000507	026472	-----		-001- 46	DATA	MCHTRXMCHBX	
0032265						47	ZMINT		
0032265	04	000510	026750	-----		-001- 48	DATA	ZMINTX	
E0032266	04	000511	021003	-----		49	LRS	RO,MCHB	# PREPARE TO WRITE INTO OFF-LINE STORE

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 92

TEST 56 SEGMENT 5

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

0032267	04	000512	031340	043140	CTSD	01	LAL	R14,BASADDR,RAO	
0032271	04	000514	040361	-----		02	L	R15,1(RAO)	
0032272						03	STOS	O(RA1)	# WRITE 'NOP' COMMAND W/ BAD PARITY HIGH
E0032272	04	000515	000420	-----		-001- 04	MSTF	O(RA1)	
0032273	04	000516	136100	-----		-001- 05	DATA	B(1011110001000000)	
0032274	04	000517	003000	-----		07	ZR	RO	# CLEAR BAD PARITY
0032275	04	000520	013405	-----		-001- 08	MIMODE	DATA=S(ERRI)	
0032276						09	MI	S(ERRI)	
0032276	04	000521	025750	-----		-001- 10	ZER		
0032277						11	DATA	ZERX	
0032277	04	000522	033322	-----		-001- 12	L	IS_R,YT	# RESET ERROR INTERRUPT BIT
0032277	04	000522	033322	-----		-001- 13	VFD	8,IS_RXT 8,YTXF	
0032300						14	ZMINT		
0032300	04	000523	026750	-----		-001- 15	DATA	ZMINTX	
0032301	04	000524	003401	000006		16	LI	RO,M(BIN)IM(BHC)	
0032303	04	000526	007760	-----		17	LSR	SS_R,RO	# REENABLE INTERRUPTS & HARDWARE CHECKS
						18	FEND		
21 # ATTEMPT TO EXECUTE 'NOP' COMMAND WITH BAD PARITY HIGH IN OFF-LINE BASE ADDRESS									
0032304						23	CALL	EXCOFLPG	
0032304	04	000527	037020	111235	CSYSUB	-001- 24	BSA	EXCOFLPG	
27 # STOP OFF-LINE PROCESSOR AND RETRIEVE ITS PROGRAM ADDRESS REGISTER (PA)									
0032306						29	LMCH	MSTOP	
0032306	04	000531	103401	000321		-001- 30	LI	RO,MSTOP	
0032310						-001- 31	CALL	SMCH	
0032310	04	000533	073027	-----		-002- 32	BSAI	SXMCH	# SUBROUTINE SMCH IS IN PROGRAM CSYSUB
0032311	04	000534	103401	114626		33	LI	RO,MCHBXT+E(8)+PAZF	
0032313	04	000536	007400	-----		34	LSR	MCHTR,RO	
0032314						35	LMCH	LDMIRL	
0032314						-001- 36	CALL	SLDMIRL	
0032314	04	000537	037020	111075	CSYSUB	-002- 37	BSA	SLDMIRL	
0032316						38	LMCH	RTNMCHB	
0032316	04	000541	103401	000261		-001- 39	LI	RO,RTNMCHB	
0032320						-001- 40	CALL	SMCH	
0032320	04	000543	073027	-----		-002- 41	BSAI	SXMCH	# SUBROUTINE SMCH IS IN PROGRAM CSYSUB
0032321	04	000544	112403	-----		42	UNPK	MCHB	
0032322	04	000545	031000	043140	CTSD	43	LAL	RO,BASADDR,RAO	
0032324	04	000547	040021	-----		44	L	R1,1(RAO)	
0032325	04	000550	015040	-----		45	XR	R2,RO	# DO UPPER ADDRESS BITS COMPARE?
0032326	04	000551	055004	-----	0032332	46	BNC	FAILXTH	# IF NOT, TAKE FAILURE EXIT
0032327	04	000552	015061	-----		47	XR	R3,R1	# DO LOWER ADDRESS BITS COMPARE?
0032330	04	000553	055003	-----	0032333	48	BNC	FAILXTL	# IF NOT, TAKE FAILURE EXIT
0032331	04	000554	053015	-----	0032346	49	B	MICROXT	# EXIT IF PA POINTS TO BASE ADDRESS

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 93

TEST 56 SEGMENT 5

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

0032332
0032332 04 000555 115061 -----
0032333
0032333 04 000556 112060 -----
0032334
0032334 04 000557 071400 -----
0032335 04 000560 031000 032341 0032341
0032337 04 000562 037000 011162 CDGNM

0032341 04 000564 050014 ----- CDGNM
0032342 04 000565 000000 000000

0032344 04 000567 020017 ----- CDGNM
0032345 04 000570 000035 137020

0032346

03 # IF PROGRAM ADDRESS (PA) DOES NOT POINT TO BASE ADDRESS, THEN THE MICROCODE
05 FAILXTH
06 XR R3,R1 # PREPARE FOR PACK OF MTC CHANNEL
07 FAILXTL
08 PACK MCHB # PREPARE FOR COMPARE
09 RTN_TBL **4
-001- 10 GA
-001- 11 LAL RO,**4,RAO
-001- 12 BL INTRPN03

-001- 14 # COMPARE
-001- 15 # COMPARE ON-LINE MAINTENANCE CHANNEL BUFFER TO 0(000000).
-001- 16 # IF COMPARISON EXISTS, SKIP NEXT TABLE ENTRY.
-001- 17 VFD 1,LPINDX 2,2 1,1 1,SUCCESS 5,0 6,NCOMPARE
-001- 18 ADDR 0(000000)

-001- 20 # FAILTST
-001- 21 # FAILURE EXIT - SYMBOLIC TROUBLE NUMBER IS UCODFALT.
-001- 22 # DATA WORD IN FAILURE MESSAGE IS ON-LINE MCHB.
-001- 23 VFD 1,0 2,1 5,0 2,0 6,NFAILTST
-001- 24 VFD 16,UCODFALT

26 # COMPLEMENT CORRECTION MICROCODE IS NOT BEING INVOKED AFTER SINGLE PARITY ERROR

29 # DID NOT HANG IN A LOOP WHERE IT WAS CONTINUOUSLY COMPLEMENTING SINGLE
30 # PARITY BIT ERRORS ON A GOOD STORE LOCATION AS IT SHOULD HAVE

34 MICROXT

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 94

TEST 56 SEGMENT 5

23:27:57 2/05/81 ****

MAIN_STORE_CONTROLLER_DATA_REGISTER DIAGNOSTIC TEST

CDGSDF W77D

```

                                01 CSTDF4 OM      0(571),0(573)      # 12707
                                -001- 02  NOTE ***** THE FIRST ADDRESS OVERWRITTEN IS 000571 *****

0032346                                05          FINALE DF4INIT
0032346 04 000571 137020 012771 CDGSIO -002- 06          BSA          FINAL
0032350 04 000573 137000 031600 0031600 -001- 07          BL          BKCSTDF4
0032352 04 000575 171400 ----- -001- 08          GA
0032353 04 000576 037000 011725 CDGNM -001- 09          BL          PASSTST
0032353                                10          EOW          # TEST COMPLETED
                                -001- 11  NOTE ***** THE LAST ADDRESS OVERWRITTEN IS 000577 *****
                                                # 12707

14 CSTDF1 MAXSIZE 0,504
15 CSTDF2 MAXSIZE 0,577
16 CSTDF3 MAXSIZE 0,370
17 CSTDF4 MAXSIZE 0,600

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 95

PATCH HISTORY

23:27:57 2/05/81 ****

LISTING OF INDIVIDUAL PATCHES

CDGSDF W77D

```

    **/**/012707/**/**/
-002- 01      PBEGIN  PROG=CDGSDF
    02 # THIS  PATCH CORRECTS THE FOLLOWING PROBLEMS:
    03 #      1. INCORRECT STORE NUMBER IN FAIL MESSAGE
    04 #      WHEN MORE THAN STORE 0 DEFINED
    05 #      2. TESTS WOULD NOT DIAGNOSE ANY STORE
    06 #      HIGHER THAN STORE 1
    07      TCEND
-001- 08      NOTE *****
0032353

    **/**/013267/**/**/
-002- 15      PBEGIN  PROG=CDGSDF
    16 # THIS  PATCH BYPASSES A PORTION OF TEST 54 SEG 1 THAT
    17 # PUTS THE STORE IN A HIGH POWER STATE WHICH THEN
    18 # CAUSES MORE SENSITIVE CONVERTERS TO DROP POWER.
    19      TCEND
-001- 20      NOTE *****
0032353

    **/**/012582/**/**/
-002- 27      PBEGIN  PROG=CDGSDF
    28
    # TEST FOR 16K TYPE MEMORY AND STEP ADDR
    # BY 128K IF SO EQUIPPED
    TRNO=12582  SERNOB=50373
-001- 30      TCEND
    31      NOTE *****
0032353
0032353

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 96

TROUBLE LOCATION MANUAL
LISTING OF INDIVIDUAL PATCHES

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 03 #& TBL 5301
 -001- 04 #& TEST 53
 -001- 05 #& SEGMENT 0, 1 OR 2
 -001- 06 #& PR CDGSDF
 -001- 07 #
 08 # TWO SUCCESSIVE I/O TRANSMISSIONS TO THE STORE CONTROLLER UNDER TEST FAILED
 09 # TO COMPLETE PROPERLY.
 10 # DATA = I/O ORDER WHICH WAS UNSUCCESSFULLY TRANSMITTED
 11 # ADDRESS = FAILURE EXIT(FAILTST) IN 'MASIOM' SUBROUTINE
 12 #
 13 # MASCH 08-20 FA1064 MAIN STORE CONTROLLER - I/O ORDER DECODER
 14 # MASCH 08-22 FA1063 MAIN STORE CONTROLLER - I/O ORDER RECEIVER
 15 # MASCH 08-23 FC203 MAIN STORE CONTROLLER CLOCK & I/O INTERFACE

0032353

-001- 18 #& TBL 5302
 -001- 19 #& TEST 53
 -001- 20 #& SEGMENT 1
 -001- 21 #& PR CDGSDF
 -001- 22 #
 23 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 0 OF MEMORY MODULE 0 --
 24 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
 25 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 26 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
 27 #
 28 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 29 # MASCH 08-18 FA1071 MAIN STORE CONTROLLER - TIMING GENERATOR
 30 # FUNCTIONAL TEST OF STORE CONTROLLER TIMING CIRCUITRY
 31 # MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
 32 # STORE COMPLEMENT WRITE CIRCUITRY IS UNCHECKED
 33 # MASCH 03-02 JK3 MAIN STORE FANOUT BOARD FOR MODULE 0
 34 # MODTINO GATES MODULE ENABLE (MENB0) ON FANOUT BOARD
 35 # MASCH 03-04 JL2 MAIN STORE PLANE - MOD 0 - BITS 0 & 8
 36 # MASCH 03-31 JL2 MAIN STORE PLANE - MOD 1 - BITS 0 & 8
 37 # MASHA 03-04 JL2 MAIN STORE PLANE - MOD 2 - BITS 0 & 8
 38 # MASHA 03-31 JL2 MAIN STORE PLANE - MOD 3 - BITS 0 & 8
 39 # MASMB 03-04 JL2 MAIN STORE PLANE - MOD 4 - BITS 0 & 8
 40 # MASMB 03-31 JL2 MAIN STORE PLANE - MOD 5 - BITS 0 & 8
 41 # MASMC 03-04 JL2 MAIN STORE PLANE - MOD 6 - BITS 0 & 8
 42 # MASMC 03-31 JL2 MAIN STORE PLANE - MOD 7 - BITS 0 & 8
 43 #
 44 #
 45 # OPTION A
 46 #
 47 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 48 # MASCH 08-18 FA1071 MAIN STORE CONTROLLER - TIMING GENERATOR
 49 # FUNCTIONAL TEST OF STORE CONTROLLER TIMING CIRCUITRY

0032353
 0032353
 0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 97

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5302 STKSDA00

CDGSDF

W77D

```

01 #          MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
02 #          STORE COMPLEMENT WRITE CIRCUITRY IS UNCHECKED
03 #          MASCH 03-02 JK25  MAIN MEMORY FANOUT BOARD FOR MODULE 0
04 #          MODTIMO GATES MODULE ENABLE (MENBO) ON FANOUT BOARD
05 #          MASCH 03-04 JL16  MAIN STORE PLANE - MOD 0 BITS 0 & 8
06 #          MASCH 03-31 JL16  MAIN STORE PLANE - MOD 1 BITS 0 & 8
07 #
08 #
0032353
0032353

-001- 11 #& TBL 5303
-001- 12 #& TEST 53
-001- 13 #& SEGMENT 1
-001- 14 #& PR CDGSDF
-001- 15 #
16 #          UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 1 OF MEMORY MODULE 0 --
17 #          MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(ODDA) TO MODULE(S)
18 #          0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
19 #          NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
20 #
0032353
21 #          MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
22 #          MASCH 03-05 JL2  MAIN STORE PLANE - MOD 0 - BITS 1 & 9
23 #          MASCH 03-29 JL2  MAIN STORE PLANE - MOD 1 - BITS 1 & 9
24 #          MASMA 03-05 JL2  MAIN STORE PLANE - MOD 2 - BITS 1 & 9
25 #          MASMA 03-29 JL2  MAIN STORE PLANE - MOD 3 - BITS 1 & 9
26 #          MASMB 03-05 JL2  MAIN STORE PLANE - MOD 4 - BITS 1 & 9
27 #          MASMB 03-29 JL2  MAIN STORE PLANE - MOD 5 - BITS 1 & 9
28 #          MASMC 03-05 JL2  MAIN STORE PLANE - MOD 6 - BITS 1 & 9
29 #          MASMC 03-29 JL2  MAIN STORE PLANE - MOD 7 - BITS 1 & 9
30 #
0032353
0032353
31 #
0032353
32 # OPTION A
33 #
34 #          MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
35 #          MASCH 03-05 JL16  MAIN STORE PLANE - MOD 0 BITS 1 & 9
36 #          MASCH 03-29 JL16  MAIN STORE PLANE - MOD 1 BITS 1 & 9
37 #
0032353
0032353
38 #

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 98

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5304 STKSDA02

CDGSDF W77D

-001- 01 #& TBL 5304
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 2 OF MEMORY MODULE 0 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDOA) TO MODULE(S)
 08 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

10 #
 11 # MASCH 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 12 # MASCH 03-06 JL2 MAIN STORE PLANE - MOD 0 - BITS 2 & 10
 13 # MASCH 03-28 JL2 MAIN STORE PLANE - MOD 1 - BITS 2 & 10
 14 # MASMA 03-06 JL2 MAIN STORE PLANE - MOD 2 - BITS 2 & 10
 15 # MASMA 03-28 JL2 MAIN STORE PLANE - MOD 3 - BITS 2 & 10
 16 # MASMB 03-06 JL2 MAIN STORE PLANE - MOD 4 - BITS 2 & 10
 17 # MASMB 03-28 JL2 MAIN STORE PLANE - MOD 5 - BITS 2 & 10
 18 # MASMC 03-06 JL2 MAIN STORE PLANE - MOD 6 - BITS 2 & 10
 19 # MASMC 03-28 JL2 MAIN STORE PLANE - MOD 7 - BITS 2 & 10

0032353

0032353

22 # OPTION A

23 #
 24 # MASCH 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 25 # MASCH 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
 26 # MASCH 03-28 JL16 MAIN STORE PLANE - MOD 1 BITS 2 & 10

0032353

0032353

28

-001- 31 #& TBL 5305
 -001- 32 #& TEST 53
 -001- 33 #& SEGMENT 1
 -001- 34 #& PR CDGSDF
 -001- 35 #
 36 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 3 OF MEMORY MODULE 0 --
 37 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDOA) TO MODULE(S)
 38 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

40 #
 41 # MASCH 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
 42 # MASCH 03-07 JL2 MAIN STORE PLANE - MOD 0 - BITS 3 & 11
 43 # MASCH 03-27 JL2 MAIN STORE PLANE - MOD 1 - BITS 3 & 11
 44 # MASMA 03-07 JL2 MAIN STORE PLANE - MOD 2 - BITS 3 & 11
 45 # MASMA 03-27 JL2 MAIN STORE PLANE - MOD 3 - BITS 3 & 11
 46 # MASMB 03-07 JL2 MAIN STORE PLANE - MOD 4 - BITS 3 & 11
 47 # MASMB 03-27 JL2 MAIN STORE PLANE - MOD 5 - BITS 3 & 11
 48 # MASMC 03-07 JL2 MAIN STORE PLANE - MOD 6 - BITS 3 & 11
 49 # MASMC 03-27 JL2 MAIN STORE PLANE - MOD 7 - BITS 3 & 11

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 99

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5305 STKSDA03

CDGSDF W77D

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0032353 01 # MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353 02
03
04 # OPTION A
0032353 05
06 # MASCH 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
07 # MASCH 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
08 # MASCH 03-27 JL16 MAIN STORE PLANE - MOD 1 BITS 3 & 11
09 # MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353 10
0032353 11

-001- 14 #& TBL 5306
-001- 15 #& TEST 53
-001- 16 #& SEGMENT 1
-001- 17 #& PR CDGSDF
-001- 18 #
19 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 4 OF MEMORY MODULE 0 --
20 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DO0A) TO MODULE(S)
21 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
22 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353 23
24 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
25 # MASCH 03-08 JL2 MAIN STORE PLANE - MOD 0 - BITS 4 & 12
26 # MASCH 03-26 JL2 MAIN STORE PLANE - MOD 1 - BITS 4 & 12
27 # MASHA 03-08 JL2 MAIN STORE PLANE - MOD 2 - BITS 4 & 12
28 # MASHA 03-26 JL2 MAIN STORE PLANE - MOD 3 - BITS 4 & 12
29 # MASHB 03-08 JL2 MAIN STORE PLANE - MOD 4 - BITS 4 & 12
30 # MASHB 03-26 JL2 MAIN STORE PLANE - MOD 5 - BITS 4 & 12
31 # MASHC 03-08 JL2 MAIN STORE PLANE - MOD 6 - BITS 4 & 12
32 # MASHC 03-26 JL2 MAIN STORE PLANE - MOD 7 - BITS 4 & 12
33 # MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353 34
0032353 35
0032353 36 # OPTION A
37
38 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
39 # MASCH 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
40 # MASCH 03-26 JL16 MAIN STORE PLANE - MOD 1 BITS 4 & 12
41 # MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353 42
0032353 43

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 100

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5307 STKSDA05

CDGSDF W77D

-001- 01 #& TBL 5307
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 5 OF MEMORY MODULE 0 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
 08 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

10 #
 11 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 12 # MASCM 03-09 JL2 MAIN STORE PLANE - MOD 0 - BITS 5 & 13
 13 # MASCM 03-25 JL2 MAIN STORE PLANE - MOD 1 - BITS 5 & 13
 14 # MASMA 03-09 JL2 MAIN STORE PLANE - MOD 2 - BITS 5 & 13
 15 # MASMA 03-25 JL2 MAIN STORE PLANE - MOD 3 - BITS 5 & 13
 16 # MASMB 03-09 JL2 MAIN STORE PLANE - MOD 4 - BITS 5 & 13
 17 # MASMB 03-25 JL2 MAIN STORE PLANE - MOD 5 - BITS 5 & 13
 18 # MASMC 03-09 JL2 MAIN STORE PLANE - MOD 6 - BITS 5 & 13
 19 # MASMC 03-25 JL2 MAIN STORE PLANE - MOD 7 - BITS 5 & 13

0032353

0032353

20 #
 21 #
 22 # OPTION A

0032353

23 #
 24 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 25 # MASCM 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
 26 # MASCM 03-25 JL16 MAIN STORE PLANE - MOD 1 BITS 5 & 13

0032353

0032353

27 #
 28 #

-001- 31 #& TBL 5308
 -001- 32 #& TEST 53
 -001- 33 #& SEGMENT 1
 -001- 34 #& PR CDGSDF
 -001- 35 #
 36 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 6 OF MEMORY MODULE 0 --
 37 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
 38 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

40 #
 41 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 42 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
 43 # STORE COMPLEMENT WRITE CIRCUITRY IS UNCHECKED
 44 # MASCM 03-10 JL2 MAIN STORE PLANE - MOD 0 - BITS 6 & 14
 45 # MASCM 03-24 JL2 MAIN STORE PLANE - MOD 1 - BITS 6 & 14
 46 # MASMA 03-10 JL2 MAIN STORE PLANE - MOD 2 - BITS 6 & 14
 47 # MASMA 03-24 JL2 MAIN STORE PLANE - MOD 3 - BITS 6 & 14
 48 # MASMB 03-10 JL2 MAIN STORE PLANE - MOD 4 - BITS 6 & 14
 49 # MASMB 03-24 JL2 MAIN STORE PLANE - MOD 5 - BITS 6 & 14

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 101

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5308 STKSDA06

CDGSDF W77D

0032353
003235301 # MASC 03-10 JL2 MAIN STORE PLANE - MOD 6 - BITS 6 & 14
02 # MASC 03-24 JL2 MAIN STORE PLANE - MOD 7 - BITS 6 & 14

0032353

03
04
05 # OPTION A
06
07 # MASC 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
08 # MASC 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
09 # STORE COMPLEMENT WRITE CIRCUITRY IS UNCHECKED
10 # MASC 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
11 # MASC 03-24 JL16 MAIN STORE PLANE - MOD 1 BITS 6 & 140032353
003235312
13-001- 16 #& TBL 5309
-001- 17 #& TEST 53
-001- 18 #& SEGMENT 1
-001- 19 #& PR CDGSDF
-001- 20 #

0032353

21 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 7 OF MEMORY MODULE 0 --
22 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDDA) TO MODULE(S)
23 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
24 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.0032353
003235325
26 # MASC 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
27 # MASC 03-11 JL2 MAIN STORE PLANE - MOD 0 - BITS 7 & 15
28 # MASC 03-23 JL2 MAIN STORE PLANE - MOD 1 - BITS 7 & 15
29 # MASA 03-11 JL2 MAIN STORE PLANE - MOD 2 - BITS 7 & 15
30 # MASA 03-23 JL2 MAIN STORE PLANE - MOD 3 - BITS 7 & 15
31 # MASMB 03-11 JL2 MAIN STORE PLANE - MOD 4 - BITS 7 & 15
32 # MASMB 03-23 JL2 MAIN STORE PLANE - MOD 5 - BITS 7 & 15
33 # MASC 03-11 JL2 MAIN STORE PLANE - MOD 6 - BITS 7 & 15
34 # MASC 03-23 JL2 MAIN STORE PLANE - MOD 7 - BITS 7 & 15

0032353

35
36
37 # OPTION A0032353
003235338
39 # MASC 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
40 # MASC 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
41 # MASC 03-23 JL16 MAIN STORE PLANE - MOD 1 BITS 7 & 15
42
43

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 102

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5310 STKSDA08

CDGSDF W77D

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-001- 01 #& TBL 5310
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 8 OF MEMORY MODULE 0 --
07 #      MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
08 #      0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
09 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
10 #
0032353 11 #      MASCN 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
12 #      MASCN 03-04 JL2   MAIN STORE PLANE - MOD 0 - BITS 0 & 8
13 #      MASCN 03-31 JL2   MAIN STORE PLANE - MOD 1 - BITS 0 & 8
14 #      MASMA 03-04 JL2   MAIN STORE PLANE - MOD 2 - BITS 0 & 8
15 #      MASMA 03-31 JL2   MAIN STORE PLANE - MOD 3 - BITS 0 & 8
16 #      MASMB 03-04 JL2   MAIN STORE PLANE - MOD 4 - BITS 0 & 8
17 #      MASMB 03-31 JL2   MAIN STORE PLANE - MOD 5 - BITS 0 & 8
18 #      MASMC 03-04 JL2   MAIN STORE PLANE - MOD 6 - BITS 0 & 8
19 #      MASMC 03-31 JL2   MAIN STORE PLANE - MOD 7 - BITS 0 & 8
0032353 20 #
0032353 21 #
0032353 22 # OPTION A
23 #
24 #      MASCN 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
25 #      MASCN 03-04 JL16  MAIN STORE PLANE - MOD 0 BITS 0 & 8
26 #      MASCN 03-31 JL16  MAIN STORE PLANE - MOD 1 BITS 0 & 8
0032353 27 #
0032353 28 #

-001- 31 #& TBL 5311
-001- 32 #& TEST 53
-001- 33 #& SEGMENT 1
-001- 34 #& PR CDGSDF
-001- 35 #
36 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 9 OF MEMORY MODULE 0 --
37 #      MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
38 #      0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
39 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
40 #
0032353 41 #      MASCN 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
42 #      MASCN 03-05 JL2   MAIN STORE PLANE - MOD 0 - BITS 1 & 9
43 #      MASCN 03-29 JL2   MAIN STORE PLANE - MOD 1 - BITS 1 & 9
44 #      MASMA 03-05 JL2   MAIN STORE PLANE - MOD 2 - BITS 1 & 9
45 #      MASMA 03-29 JL2   MAIN STORE PLANE - MOD 3 - BITS 1 & 9
46 #      MASMB 03-05 JL2   MAIN STORE PLANE - MOD 4 - BITS 1 & 9
47 #      MASMB 03-29 JL2   MAIN STORE PLANE - MOD 5 - BITS 1 & 9
48 #      MASMC 03-05 JL2   MAIN STORE PLANE - MOD 6 - BITS 1 & 9
49 #      MASMC 03-29 JL2   MAIN STORE PLANE - MOD 7 - BITS 1 & 9

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 103

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5311 STKSDA09

CDGSDF W77D

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0032353 01
0032353 02
0032353 03 # OPTION A
04
05 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
06 # MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
07 # MASCM 03-29 JL16 MAIN STORE PLANE - MOD 1 BITS 1 & 9
08
0032353 09
0032353

-001- 12 #& TBL 5312
-001- 13 #& TEST 53
-001- 14 #& SEGMENT 1
-001- 15 #& PR CDGSDF
-001- 16 #
17 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 10 OF MEMORY MODULE 0 --
18 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D0DA) TO MODULE(S)
19 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
21
0032353 22 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
23 # MASCM 03-06 JL2 MAIN STORE PLANE - MOD 0 - BITS 2 & 10
24 # MASCM 03-28 JL2 MAIN STORE PLANE - MOD 1 - BITS 2 & 10
25 # MASMA 03-06 JL2 MAIN STORE PLANE - MOD 2 - BITS 2 & 10
26 # MASMA 03-28 JL2 MAIN STORE PLANE - MOD 3 - BITS 2 & 10
27 # MASMB 03-06 JL2 MAIN STORE PLANE - MOD 4 - BITS 2 & 10
28 # MASMB 03-28 JL2 MAIN STORE PLANE - MOD 5 - BITS 2 & 10
29 # MASMC 03-06 JL2 MAIN STORE PLANE - MOD 6 - BITS 2 & 10
30 # MASMC 03-28 JL2 MAIN STORE PLANE - MOD 7 - BITS 2 & 10
31
0032353 32
0032353 33 # OPTION A
0032353 34
35 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
36 # MASCM 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
37 # MASCM 03-28 JL16 MAIN STORE PLANE - MOD 1 BITS 2 & 10
0032353 38
0032353 39

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 104

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5313 STKSDA11

CDGSDF W77D

-001- 01 #& TBL 5313
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #

0032353

06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 11 OF MEMORY MODULE 0 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDOA) TO MODULE(S)
 08 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

10 #
 11 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
 12 # MASCM 03-07 JL2 MAIN STORE PLANE - MOD 0 - BITS 3 & 11
 13 # MASCM 03-27 JL2 MAIN STORE PLANE - MOD 1 - BITS 3 & 11
 14 # MASMA 03-07 JL2 MAIN STORE PLANE - MOD 2 - BITS 3 & 11
 15 # MASMA 03-27 JL2 MAIN STORE PLANE - MOD 3 - BITS 3 & 11
 16 # MASMB 03-07 JL2 MAIN STORE PLANE - MOD 4 - BITS 3 & 11
 17 # MASMB 03-27 JL2 MAIN STORE PLANE - MOD 5 - BITS 3 & 11
 18 # MASMC 03-07 JL2 MAIN STORE PLANE - MOD 6 - BITS 3 & 11
 19 # MASMC 03-27 JL2 MAIN STORE PLANE - MOD 7 - BITS 3 & 11

0032353
003235320 #
21 #
22 # OPTION A

0032353

23 #
 24 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
 25 # MASCM 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
 26 # MASCM 03-27 JL16 MAIN STORE PLANE - MOD 1 BITS 3 & 11

0032353
003235327 #
28 #

-001- 31 #& TBL 5314
 -001- 32 #& TEST 53
 -001- 33 #& SEGMENT 1
 -001- 34 #& PR CDGSDF
 -001- 35 #

0032353

36 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 12 OF MEMORY MODULE 0 --
 37 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDOA) TO MODULE(S)
 38 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

40 #
 41 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
 42 # MASCM 03-08 JL2 MAIN STORE PLANE - MOD 0 - BITS 4 & 12
 43 # MASCM 03-26 JL2 MAIN STORE PLANE - MOD 1 - BITS 4 & 12
 44 # MASMA 03-08 JL2 MAIN STORE PLANE - MOD 2 - BITS 4 & 12
 45 # MASMA 03-26 JL2 MAIN STORE PLANE - MOD 3 - BITS 4 & 12
 46 # MASMB 03-08 JL2 MAIN STORE PLANE - MOD 4 - BITS 4 & 12
 47 # MASMB 03-26 JL2 MAIN STORE PLANE - MOD 5 - BITS 4 & 12
 48 # MASMC 03-08 JL2 MAIN STORE PLANE - MOD 6 - BITS 4 & 12
 49 # MASMC 03-26 JL2 MAIN STORE PLANE - MOD 7 - BITS 4 & 12

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 105

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5314 STKSDA12

CDGSDF W77D

0032353
003235301
02
03 # OPTION A

0032353

04
05 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
06 # MASCH 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
07 # MASCH 03-26 JL16 MAIN STORE PLANE - MOD 1 BITS 4 & 120032353
003235308
09-001- 12 #& TBL 5315
-001- 13 #& TEST 53
-001- 14 #& SEGMENT 1
-001- 15 #& PR CDGSDF
-001- 16 #17 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 13 OF MEMORY MODULE 0 --
18 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
19 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

21
22 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
23 # MASCH 03-09 JL2 MAIN STORE PLANE - MOD 0 - BITS 5 & 13
24 # MASCH 03-25 JL2 MAIN STORE PLANE - MOD 1 - BITS 5 & 13
25 # MASMA 03-09 JL2 MAIN STORE PLANE - MOD 2 - BITS 5 & 13
26 # MASMA 03-25 JL2 MAIN STORE PLANE - MOD 3 - BITS 5 & 13
27 # MASMB 03-09 JL2 MAIN STORE PLANE - MOD 4 - BITS 5 & 13
28 # MASMB 03-25 JL2 MAIN STORE PLANE - MOD 5 - BITS 5 & 13
29 # MASMC 03-09 JL2 MAIN STORE PLANE - MOD 6 - BITS 5 & 13
30 # MASMC 03-25 JL2 MAIN STORE PLANE - MOD 7 - BITS 5 & 130032353
003235331
32
33 # OPTION A

0032353

34
35 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
36 # MASCH 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
37 # MASCH 03-25 JL16 MAIN STORE PLANE - MOD 1 BITS 5 & 130032353
003235338
39

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 106

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5316 STKSDA14

CDGSDF W77D

-001- 01 #& TBL 5316
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 14 OF MEMORY MODULE 0 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDDA) TO MODULE(S)
 08 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

10 #
 11 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 12 # MASCH 03-10 JL2 MAIN STORE PLANE - MOD 0 - BITS 6 & 14
 13 # MASCH 03-24 JL2 MAIN STORE PLANE - MOD 1 - BITS 6 & 14
 14 # MASMA 03-10 JL2 MAIN STORE PLANE - MOD 2 - BITS 6 & 14
 15 # MASMA 03-24 JL2 MAIN STORE PLANE - MOD 3 - BITS 6 & 14
 16 # MASMB 03-10 JL2 MAIN STORE PLANE - MOD 4 - BITS 6 & 14
 17 # MASMB 03-24 JL2 MAIN STORE PLANE - MOD 5 - BITS 6 & 14
 18 # MASMC 03-10 JL2 MAIN STORE PLANE - MOD 6 - BITS 6 & 14
 19 # MASMC 03-24 JL2 MAIN STORE PLANE - MOD 7 - BITS 6 & 14

0032353
0032353

20 #
 21 #
 22 # OPTION A

0032353

23 #
 24 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 25 # MASCH 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
 26 # MASCH 03-24 JL16 MAIN STORE PLANE - MOD 1 BITS 6 & 14

0032353
0032353

27 #
 28 #

-001- 31 #& TBL 5317
 -001- 32 #& TEST 53
 -001- 33 #& SEGMENT 1
 -001- 34 #& PR CDGSDF
 -001- 35 #
 36 # UNSUCCESSFUL WRITE/READ OF DATA INTO PIT 15 OF MEMORY MODULE 0 --
 37 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDDA) TO MODULE(S)
 38 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
 39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

0032353

40 #
 41 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
 42 # MASCH 03-11 JL2 MAIN STORE PLANE - MOD 0 - BITS 7 & 15
 43 # MASCH 03-23 JL2 MAIN STORE PLANE - MOD 1 - BITS 7 & 15
 44 # MASMA 03-11 JL2 MAIN STORE PLANE - MOD 2 - BITS 7 & 15
 45 # MASMA 03-23 JL2 MAIN STORE PLANE - MOD 3 - BITS 7 & 15
 46 # MASMB 03-11 JL2 MAIN STORE PLANE - MOD 4 - BITS 7 & 15
 47 # MASMB 03-23 JL2 MAIN STORE PLANE - MOD 5 - BITS 7 & 15
 48 # MASMC 03-11 JL2 MAIN STORE PLANE - MOD 6 - BITS 7 & 15
 49 # MASMC 03-23 JL2 MAIN STORE PLANE - MOD 7 - BITS 7 & 15

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 107

5317 STKSDA15

CDGSDF W77D

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0032353          01
0032353          02
0032353          03 # OPTION A
0032353          04
0032353          05 # MASC M 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
0032353          06 # MASC M 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
0032353          07 # MASC M 03-23 JL16 MAIN STORE PLANE - MOD 1 BITS 7 & 15
0032353          08
0032353          09

-001- 12 #& TBL 5318
-001- 13 #& TEST 53
-001- 14 #& SEGMENT 1
-001- 15 #& PR CDGSDF
-001- 16 #
17 # UNSUCCESSFUL WRITE/READ OF DATA INTO PL OR PH BIT OF MEMORY MODULE 0 --
18 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(000A) TO MODULE(S)
19 # 0(,2,4,6), OR A STUCK DATA LEAD AT THE(ANY) MEMORY MODULE(S).
20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353          21
0032353          22 # MASC M 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
0032353          23 # MASC M 03-17 JL2 MAIN STORE PLANE - MOD 0 - BITS PL&PH
0032353          24 # MASC M 03-18 JL2 MAIN STORE PLANE - MOD 1 - BITS PL&PH
0032353          25 # MASC M 03-17 JL2 MAIN STORE PLANE - MOD 2 - BITS PL&PH
0032353          26 # MASC M 03-18 JL2 MAIN STORE PLANE - MOD 3 - BITS PL&PH
0032353          27 # MASC M 03-17 JL2 MAIN STORE PLANE - MOD 4 - BITS PL&PH
0032353          28 # MASC M 03-18 JL2 MAIN STORE PLANE - MOD 5 - BITS PL&PH
0032353          29 # MASC M 03-17 JL2 MAIN STORE PLANE - MOD 6 - BITS PL&PH
0032353          30 # MASC M 03-18 JL2 MAIN STORE PLANE - MOD 7 - BITS PL&PH
0032353          31 # NOTE--1 MASC M 08-14 FA1067 MAIN STORE CONTROLLER - PARITY TRANSLATOR
0032353          32 # NOTE--1 MASC M 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
0032353          33 # NOTE--1 MASC M 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
0032353          34 # NOTE--1 MASC M 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353          35 # NOTE--1 MASC M 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
0032353          36 # MASC M 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353          37
0032353          38
0032353          39 # OPTION A
0032353          40
0032353          41 # MASC M 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
0032353          42 # MASC M 03-17 JL16 MAIN STORE PLANE - MOD 0 BITS PL & PH
0032353          43 # NOTE--1 MASC M 03-18 JL16 MAIN STORE PLANE - MOD 1 BITS PL & PH
0032353          44 # NOTE--1 MASC M 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
0032353          45 # NOTE--1 MASC M 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
0032353          46 # NOTE--1 MASC M 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353          47 # NOTE--1 MASC M 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
0032353          48 # MASC M 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
0032353          49

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 108

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5318 STKSDAPR

CDGSDF W77D

0032353

01

-001- 04 #8 TBL 5319
 -001- 05 #8 TEST 53
 -001- 06 #8 SEGMENT 1
 -001- 07 #8 PR CDGSDF
 -001- 08 #

09 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 16 OF MEMORY MODULE 0 --
 10 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DO0A) TO MODULE(S)
 11 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
 12 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)

0032353

13
 14 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
 15 # NOTE--1 MASCH 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20
 16 # NOTE--1 MASCH 03-22 JL2 MAIN STORE PLANE - MOD 1 - BITS 16&20
 17 # NOTE--1 MASMA 03-12 JL2 MAIN STORE PLANE - MOD 2 - BITS 16&20
 18 # NOTE--1 MASMA 03-22 JL2 MAIN STORE PLANE - MOD 3 - BITS 16&20
 19 # NOTE--1 MASMB 03-12 JL2 MAIN STORE PLANE - MOD 4 - BITS 16&20
 20 # NOTE--1 MASMB 03-22 JL2 MAIN STORE PLANE - MOD 5 - BITS 16&20
 21 # NOTE--1 MASMC 03-12 JL2 MAIN STORE PLANE - MOD 6 - BITS 16&20
 22 # NOTE--1 MASMC 03-22 JL2 MAIN STORE PLANE - MOD 7 - BITS 16&20

0032353

0032353

23
 24
 25 # OPTION A

0032353

26
 27 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
 28 # NOTE--1 MASCH 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20
 29 # NOTE--1 MASCH 03-22 JL16 MAIN STORE PLANE - MOD 1 BITS 16 & 20

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0032353

30
 31

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 109

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-001- 01 #& TBL 5320
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 17 OF MEMORY MODULE 0 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDDA) TO MODULE(S)
08 # 7(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10
0032353
11 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
12 # NOTE--1 MASCH 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21
13 # NOTE--1 MASCH 03-21 JL2 MAIN STORE PLANE - MOD 1 - BITS 17&21
14 # NOTE--1 MASMA 03-13 JL2 MAIN STORE PLANE - MOD 2 - BITS 17&21
15 # NOTE--1 MASMA 03-21 JL2 MAIN STORE PLANE - MOD 3 - BITS 17&21
16 # NOTE--1 MASMB 03-13 JL2 MAIN STORE PLANE - MOD 4 - BITS 17&21
17 # NOTE--1 MASMB 03-21 JL2 MAIN STORE PLANE - MOD 5 - BITS 17&21
18 # NOTE--1 MASMC 03-13 JL2 MAIN STORE PLANE - MOD 6 - BITS 17&21
19 # NOTE--1 MASMC 03-21 JL2 MAIN STORE PLANE - MOD 7 - BITS 17&21
20
0032353
0032353
21
0032353
22 # OPTION A
23
24 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
25 # NOTE--1 MASCH 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21
26 # NOTE--1 MASCH 03-21 JL16 MAIN STORE PLANE - MOD 1 BITS 17 & 21
27
0032353
0032353
28

-001- 31 #& TBL 5321
-001- 32 #& TEST 53
-001- 33 #& SEGMENT 1
-001- 34 #& PR CDGSDF
-001- 35 #
36 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 18 OF MEMORY MODULE 0 --
37 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(DDDA) TO MODULE(S)
38 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
40
0032353
41 # NOTE--1 MASCH 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
42 # NOTE--1 MASCH 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22
43 # NOTE--1 MASCH 03-20 JL2 MAIN STORE PLANE - MOD 1 - BITS 18&22
44 # NOTE--1 MASMA 03-14 JL2 MAIN STORE PLANE - MOD 2 - BITS 18&22
45 # NOTE--1 MASMA 03-20 JL2 MAIN STORE PLANE - MOD 3 - BITS 18&22
46 # NOTE--1 MASMB 03-14 JL2 MAIN STORE PLANE - MOD 4 - BITS 18&22
47 # NOTE--1 MASMB 03-20 JL2 MAIN STORE PLANE - MOD 5 - BITS 18&22
48 # NOTE--1 MASMC 03-14 JL2 MAIN STORE PLANE - MOD 6 - BITS 18&22
49 # NOTE--1 MASMC 03-20 JL2 MAIN STORE PLANE - MOD 7 - BITS 18&22

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 110

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5321 STKSDA18

CDGSDF W77D

0032353
0032353,

0032353

0032353
0032353

01
02
03 # OPTION A
04
05 # NOTE--1 MASCH 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
06 # NOTE--1 MASCH 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
07 # NOTE--1 MASCH 03-20 JL16 MAIN STORE PLANE - MOD 1 BITS 18 & 22
08
09

-001- 12 #8 TBL 5322
-001- 13 #8 TEST 53
-001- 14 #8 SEGMENT 1
-001- 15 #8 PR CDGSDF
-001- 16 #

17 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 19 OF MEMORY MODULE 0 --
18 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
19 # D(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
21

0032353

22 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
23 # NOTE--1 MASCH 03-16 JL2 MAIN STORE PLANE - MOD 0 - BITS 19&23
24 # NOTE--1 MASCH 03-19 JL2 MAIN STORE PLANE - MOD 1 - BITS 19&23
25 # NOTE--1 MASMA 03-16 JL2 MAIN STORE PLANE - MOD 2 - BITS 19&23
26 # NOTE--1 MASMA 03-19 JL2 MAIN STORE PLANE - MOD 3 - BITS 19&23
27 # NOTE--1 MASMB 03-16 JL2 MAIN STORE PLANE - MOD 4 - BITS 19&23
28 # NOTE--1 MASMB 03-19 JL2 MAIN STORE PLANE - MOD 5 - BITS 19&23
29 # NOTE--1 MASMC 03-16 JL2 MAIN STORE PLANE - MOD 6 - BITS 19&23
30 # NOTE--1 MASMC 03-19 JL2 MAIN STORE PLANE - MOD 7 - BITS 19&23
31
32

0032353
0032353

0032353

33 # OPTION A

34
35 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
36 # NOTE--1 MASCH 03-16 JL16 MAIN STORE PLANE - MOD 0 BITS 19 & 23
37 # NOTE--1 MASCH 03-19 JL16 MAIN STORE PLANE - MOD 1 BITS 19 & 23
38
39

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 111

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5323 STKSDA20

CDGSDF H77D

```

-001- 01 #& TBL 5323
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 20 OF MEMORY MODULE 0 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(000A) TO MODULE(S)
08 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10
0032353 11 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
12 # NOTE--1 MASCH 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20
13 # NOTE--1 MASCH 03-22 JL2 MAIN STORE PLANE - MOD 1 - BITS 16&20
14 # NOTE--1 MASMA 03-12 JL2 MAIN STORE PLANE - MOD 2 - BITS 16&20
15 # NOTE--1 MASMA 03-22 JL2 MAIN STORE PLANE - MOD 3 - BITS 16&20
16 # NOTE--1 MASMB 03-12 JL2 MAIN STORE PLANE - MOD 4 - BITS 16&20
17 # NOTE--1 MASMB 03-22 JL2 MAIN STORE PLANE - MOD 5 - BITS 16&20
18 # NOTE--1 MASMC 03-12 JL2 MAIN STORE PLANE - MOD 6 - BITS 16&20
19 # NOTE--1 MASMC 03-22 JL2 MAIN STORE PLANE - MOD 7 - BITS 16&20
20
0032353 21
0032353 22 # OPTION A
23
0032353 24 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
25 # NOTE--1 MASCH 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20
26 # NOTE--1 MASCH 03-22 JL16 MAIN STORE PLANE - MOD 1 BITS 16 & 20
27
0032353 28
0032353

-001- 31 #& TBL 5324
-001- 32 #& TEST 53
-001- 33 #& SEGMENT 1
-001- 34 #& PR CDGSDF
-001- 35 #
36 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 21 OF MEMORY MODULE 0 --
37 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(000A) TO MODULE(S)
38 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
39 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
40
0032353 41 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
42 # NOTE--1 MASCH 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21
43 # NOTE--1 MASCH 03-21 JL2 MAIN STORE PLANE - MOD 1 - BITS 17&21
44 # NOTE--1 MASMA 03-13 JL2 MAIN STORE PLANE - MOD 2 - BITS 17&21
45 # NOTE--1 MASMA 03-21 JL2 MAIN STORE PLANE - MOD 3 - BITS 17&21
46 # NOTE--1 MASMB 03-13 JL2 MAIN STORE PLANE - MOD 4 - BITS 17&21
47 # NOTE--1 MASMB 03-21 JL2 MAIN STORE PLANE - MOD 5 - BITS 17&21
48 # NOTE--1 MASMC 03-13 JL2 MAIN STORE PLANE - MOD 6 - BITS 17&21
49 # NOTE--1 MASMC 03-21 JL2 MAIN STORE PLANE - MOD 7 - BITS 17&21

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 112

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5324 STKSDA21

CDGSDF

W77D

0032353
003235301
02
03 # OPTION A

0032353

04
05 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
06 # NOTE--1 MASCH 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21
07 # NOTE--1 MASCH 03-21 JL16 MAIN STORE PLANE - MOD 1 BITS 17 & 210032353
003235308
09-001- 12 #& TBL 5325
-001- 13 #& TEST 53
-001- 14 #& SEGMENT 1
-001- 15 #& PR CDGSDF
-001- 16 #17 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 22 OF MEMORY MODULE 0 --
18 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
19 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)

0032353

21
22 # NOTE--1 MASCH 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
23 # NOTE--1 MASCH 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22
24 # NOTE--1 MASCH 03-20 JL2 MAIN STORE PLANE - MOD 1 - BITS 18&22
25 # NOTE--1 MASHA 03-14 JL2 MAIN STORE PLANE - MOD 2 - BITS 18&22
26 # NOTE--1 MASHA 03-20 JL2 MAIN STORE PLANE - MOD 3 - BITS 18&22
27 # NOTE--1 MASHB 03-14 JL2 MAIN STORE PLANE - MOD 4 - BITS 18&22
28 # NOTE--1 MASHB 03-20 JL2 MAIN STORE PLANE - MOD 5 - BITS 18&22
29 # NOTE--1 MASHC 03-14 JL2 MAIN STORE PLANE - MOD 6 - BITS 18&22
30 # NOTE--1 MASHC 03-20 JL2 MAIN STORE PLANE - MOD 7 - BITS 18&220032353
003235331
32
33 # OPTION A

0032353

34
35 # NOTE--1 MASCH 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
36 # NOTE--1 MASCH 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
37 # NOTE--1 MASCH 03-20 JL16 MAIN STORE PLANE - MOD 1 BITS 18 & 220032353
003235338
39

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 113

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5326 STKSDA23

CDGSDF W770

```

-001- 01 #& TBL 5326
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 23 OF MEMORY MODULE 0 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE A(D00A) TO MODULE(S)
08 # 0(2,4,6), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10 #
0032353 11 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
12 # NOTE--1 MASCH 03-16 JL2   MAIN STORE PLANE - MOD 0 - BITS 19&23
13 # NOTE--1 MASCH 03-19 JL2   MAIN STORE PLANE - MOD 1 - BITS 19&23
14 # NOTE--1 MASMA 03-16 JL2   MAIN STORE PLANE - MOD 2 - BITS 19&23
15 # NOTE--1 MASMA 03-19 JL2   MAIN STORE PLANE - MOD 3 - BITS 19&23
16 # NOTE--1 MASMB 03-16 JL2   MAIN STORE PLANE - MOD 4 - BITS 19&23
17 # NOTE--1 MASMB 03-19 JL2   MAIN STORE PLANE - MOD 5 - BITS 19&23
18 # NOTE--1 MASMC 03-16 JL2   MAIN STORE PLANE - MOD 6 - BITS 19&23
19 # NOTE--1 MASMC 03-19 JL2   MAIN STORE PLANE - MOD 7 - BITS 19&23
20 #
0032353 21 #
0032353 22 # OPTION A
0032353 23 #
24 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
25 # NOTE--1 MASCH 03-16 JL16  MAIN STORE PLANE - MOD 0 BITS 19 & 23
26 # NOTE--1 MASCH 03-19 JL16  MAIN STORE PLANE - MOD 1 BITS 19 & 23
27 #
0032353 28 #
0032353

-001- 31 #& TBL 5327
-001- 32 #& TEST 53
-001- 33 #& SEGMENT 1
-001- 34 #& PR CDGSDF
-001- 35 #
36 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 0 OF MEMORY MODULE 1 --
37 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
38 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
39 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
40 #
0032353 41 #      MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
42 #      MASCH 03-33 JK3   MAIN STORE FANOUT BOARD FOR MODULE 1
43 #      MODTIMO GATES MODULE ENABLE (MENGO) ON FANOUT BOARD
44 #      MASCH 03-31 JL2   MAIN STORE PLANE - MOD 1 - BITS 0 & 8
45 #      MASCH 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER
46 #      COMCB0B IS NOT EXERCISED DURING COMMAND DECODER TEST
47 #      MASCH 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
48 #
0032353 49 #
0032353

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 114

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5327 STKSDB00

CDGSDF W77D

0032353

01 # OPTION A

02

03 #

MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

04 #

MASCM 03-33 JK25 MAIN MEMORY FANOUT BOARD FOR MODULE 1

05 #

MODTIMO GATES MODULE ENABLE (MENBO) ON FANOUT BOARD

06 #

MASCM 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8

07 #

MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL

08

09

0032353

0032353

-001- 12 #& TBL 5328

-001- 13 #& :EST 53

-001- 14 #& SEGMENT 1

-001- 15 #& PR CDGSDF

-001- 16 #

17 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 1 OF MEMORY MODULE 1 --

18 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(000B) TO MODULE(S)

19 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).

20 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.

21

0032353

22 #

MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

23 #

MASCM 03-29 JL2 MAIN STORE PLANE - MOD 1 - BITS 1 & 9

24

25

0032353

0032353

0032353

26 # OPTION A

27

28 #

MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

29 #

MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9

30

31

0032353

0032353

STORE CONTROLLER DATA REGISTER TEST

PR-16935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 115

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5329 STKSDB02

CDGSDF W77D

```

-001- 01 #& TBL 5329
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 2 OF MEMORY MODULE 1 --
07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 # 1,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
10 #
0032353 11 # MASCH 06-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
12 # MASCH 03-28 JL2 MAIN STORE PLANE - MOD 1 - BITS 2 & 10
0032353 13 #
0032353 14 #
15 # OPTION A
0032353 16 #
17 # MASCH 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
18 # MASCH 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
0032353 19 #
0032353 20 #

-001- 23 #& TBL 5330
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 3 OF MEMORY MODULE 1 --
29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
30 # 1,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
32 #
0032353 33 # MASCH 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
34 # MASCH 03-27 JL2 MAIN STORE PLANE - MOD 1 - BITS 3 & 11
0032353 35 #
0032353 36 #
37 # OPTION A
0032353 38 #
39 # MASCH 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
40 # MASCH 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
0032353 41 #
0032353 42 #

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 116

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5331 STKSDBD4

CDGSDF W77D

```

-001- 01 #& TBL 5331
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 4 OF MEMORY MODULE 1 --
07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DOOB) TO MODULE(S)
08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
10 #
11 #      MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
12 #      MASCH 03-26 JL2   MAIN STORE PLANE - MOD 1 - BITS 4 & 12
13 #
14 #
15 # OPTION A
16 #
17 #      MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
18 #      MASCH 03-08 JL16   MAIN STORE PLANE - MOD 0 BITS 4 & 12
19 #
20 #

0032353

0032353
0032353

0032353

0032353
0032353

-001- 23 #& TBL 5332
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 5 OF MEMORY MODULE 1 --
29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DOOB) TO MODULE(S)
30 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
31 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
32 #
33 #      MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
34 #      MASCH 03-25 JL2   MAIN STORE PLANE - MOD 1 - BITS 5 & 13
35 #
36 #
37 # OPTION A
38 #
39 #      MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
40 #      MASCH 03-09 JL16   MAIN STORE PLANE - MOD 0 BITS 5 & 13
41 #
42 #

0032353

0032353
0032353

0032353

0032353
0032353

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 117

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5333 STKSDB06

CDGSDF W77D

```

-001- 01 #& TBL 5333
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 6 OF MEMORY MODULE 1 --
07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353 10 #
11 #      MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
12 #      MASCM 03-24 JL2    MAIN STORE PLANE - MOD 1 - BITS 6 & 14
0032353 13 #
0032353 14 #
15 # OPTION A
0032353 16 #
17 #      MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
18 #      MASCM 03-10 JL16  MAIN STORE PLANE - MOD 0 BITS 6 & 14
0032353 19 #
0032353 20 #

-001- 23 #& TBL 5334
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 7 OF MEMORY MODULE 1 --
29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
30 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
31 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353 32 #
33 #      MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
34 #      MASCM 03-23 JL2    MAIN STORE PLANE - MOD 1 - BITS 7 & 15
0032353 35 #
0032353 36 #
37 # OPTION A
0032353 38 #
39 #      MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
40 #      MASCM 03-11 JL16  MAIN STORE PLANE - MOD 0 BITS 7 & 15
0032353 41 #
0032353 42 #

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 118

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5335 STKSDB08

CDGSDF W77D

-001- 01 #& TBL 5335
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 8 OF MEMORY MODULE 1 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
 08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
 10
 11 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 12 # MASCH 03-31 JL2 MAIN STORE PLANE - MOD 1 - BITS 0 & 8
 13
 14
 15 # OPTION A
 16
 17 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 18 # MASCH 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8
 19
 20

0032353

0032353
0032353

0032353

0032353
0032353

-001- 23 #& TBL 5336
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 9 OF MEMORY MODULE 1 --
 29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
 30 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
 31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
 32
 33 # MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
 34 # MASCH 03-29 JL2 MAIN STORE PLANE - MOD 1 - BITS 1 & 9
 35
 36
 37 # OPTION A
 38
 39 # MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
 40 # MASCH 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
 41
 42

0032353

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0032353

0032353

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 119

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5337 STKSDB10

CDGSDF W77D

```

-001- 01 #& TBL 5337
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 10 OF MEMORY MODULE 1 --
07 #      MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 #      1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353 10
11 #      MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
12 #      MASCM 03-28 JL2    MAIN STORE PLANE - MOD 1 - BITS 2 & 10
0032353 13
0032353 14
15 # OPTION A
0032353 16
17 #      MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
18 #      MASCM 03-06 JL16   MAIN STORE PLANE - MOD 0 BITS 2 & 10
0032353 19
0032353 20

-001- 23 #& TBL 5338
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 #      UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 11 OF MEMORY MODULE 1 --
29 #      MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
30 #      1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
31 #      NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
0032353 32
33 #      MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
34 #      MASCM 03-27 JL2    MAIN STORE PLANE - MOD 1 - BITS 3 & 11
0032353 35
0032353 36
37 # OPTION A
0032353 38
39 #      MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
40 #      MASCM 03-07 JL16   MAIN STORE PLANE - MOD 0 BITS 3 & 11
0032353 41
0032353 42

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 120

TROUBLE LOCATION MANUAL

5339 STKSDB12

23:27:57 2/05/81 ****

CDGSDF W77D

-001- 01 #& TBL 5339
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 12 OF MEMORY MODULE 1 --
 07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DDOB) TO MODULE(S)
 08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
 10 #
 11 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
 12 # MASCH 03-26 JL2 MAIN STORE PLANE - MOD 1 - BITS 4 & 12
 13 #
 14 #
 15 # OPTION A
 16 #
 17 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
 18 # MASCH 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
 19 #
 20 #

0032353
 0032353
 0032353
 0032353
 0032353

-001- 23 #& TBL 5340
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 13 OF MEMORY MODULE 1 --
 29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DDOB) TO MODULE(S)
 30 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
 31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
 32 #
 33 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 34 # MASCH 03-25 JL2 MAIN STORE PLANE - MOD 1 - BITS 5 & 13
 35 #
 36 #
 37 # OPTION A
 38 #
 39 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 40 # MASCH 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
 41 #
 42 #

0032353
 0032353
 0032353
 0032353
 0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 121

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5341 STKSDB14

CDGSDF W77D

```

-001- 01 #& TBL 5341
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 14 OF MEMORY MODULE 1 --
07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
10 #
11 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
12 # MASCH 03-24 JL2 MAIN STORE PLANE - MOD 1 - BITS 6 & 14
13 #
14 #
15 # OPTION A
16 #
17 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
18 # MASCH 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
19 #
20 #

0032353

-001- 23 #& TBL 5342
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 15 OF MEMORY MODULE 1 --
29 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
30 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
32 #
33 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
34 # MASCH 03-23 JL2 MAIN STORE PLANE - MOD 1 - BITS 7 & 15
35 #
36 #
37 # OPTION A
38 #
39 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
40 # MASCH 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
41 #
42 #

0032353

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 122

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5343 STKSDBPR

CDGSDF W77D

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-001- 01 #& TBL 5343
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO PL OR PH BIT OF MEMORY MODULE 1 --
07 # MIGHT BE INPUT/OUTPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 # 1(,3,5,7), OR A STUCK DATA LEAD AT (ANY OF) THE MEMORY MODULE(S).
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK.
10 #
0032353 11 # MASCH 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
12 # MASCH 03-18 JL2 MAIN STORE PLANE - MOD 1 - BITS PL&PH
13 # NOTE--1 MASCH 08-14 FA1067 MAIN STORE CONTROLLER - PARITY TRANSLATOR
14 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353 15 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
0032353 16 #
17 #
0032353 18 # OPTION A
19 #
20 # MASCH 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
21 # MASCH 03-17 JL16 MAIN STORE PLANE - MOD 0 BITS PL & PH
22 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353 23 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
0032353 24 #
25 #

-001- 28 #& TBL 5344
-001- 29 #& TEST 53
-001- 30 #& SEGMENT 1
-001- 31 #& PR CDGSDF
-001- 32 #
33 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 16 OF MEMORY MODULE 1 --
34 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
35 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
36 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
0032353 37 #
38 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
0032353 39 # NOTE--1 MASCH 03-22 JL2 MAIN STORE PLANE - MOD 1 - BITS 16&20
0032353 40 #
41 #
0032353 42 # OPTION A
43 #
44 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
0032353 45 # NOTE--1 MASCH 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20
0032353 46 #
47 #

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 123

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5345 STKSDB17

CDGSDF W770

```

-001- 01 #& TBL 5345
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 17 OF MEMORY MODULE 1 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
08 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10
11 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
12 # NOTE--1 MASCM 03-21 JL2 MAIN STORE PLANE - MOD 1 - BITS 17&21
13
14
15 # OPTION A
16
17 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
18 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21
19
20

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-001- 23 #& TBL 5346
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 18 OF MEMORY MODULE 1 --
29 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
30 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
32
33 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
34 # NOTE--1 MASCM 03-20 JL2 MAIN STORE PLANE - MOD 1 - BITS 18&22
35
36
37 # OPTION A
38
39 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
40 # NOTE--1 MASCM 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
41
42

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 124

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5347 STKSDB19

CDGSDF W77D

```

-001- 01 #& TBL 5347
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 19 OF MEMORY MODULE 1 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
08 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10
11 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
12 # NOTE--1 MASCM 03-19 JL2 MAIN STORE PLANE - MOD 1 - BITS 19&23
13
14
15 # OPTION A
16
17 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
18 # NOTE--1 MASCM 03-16 JL16 MAIN STORE PLANE - MOD 0 BITS 19 & 23
19
20

-001- 23 #& TBL 5348
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 20 OF MEMORY MODULE 1 --
29 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(D00B) TO MODULE(S)
30 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
32
33 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
34 # NOTE--1 MASCM 03-22 JL2 MAIN STORE PLANE - MOD 1 - BITS 16&20
35
36
37 # OPTION A
38
39 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
40 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20
41
42

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0032353

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 125

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5349 STKSDB21

CDGSDF W77D

```

-001- 01 #& TBL 5349
-001- 02 #& TEST 53
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 21 OF MEMORY MODULE 1 --
07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
08 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
10
0032353 11 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
12 # NOTE--1 MASCM 03-21 JL2 MAIN STORE PLANE - MOD 1 - BITS 17&21
13
0032353 14
0032353 15 # OPTION A
16
0032353 17 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
0032353 18 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21
19
20

-001- 23 #& TBL 5350
-001- 24 #& TEST 53
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 22 OF MEMORY MODULE 1 --
29 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DO0B) TO MODULE(S)
30 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
31 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
32
0032353 33 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
34 # NOTE--1 MASCM 03-20 JL2 MAIN STORE PLANE - MOD 1 - BITS 18&22
35
0032353 36
0032353 37 # OPTION A
38
0032353 39 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
0032353 40 # NOTE--1 MASCM 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
41
42

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 126

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5351 STKSDB23

CDGSDF W77D

-001- 01 #& TBL 5351
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # UNSUCCESSFUL WRITE/READ OF DATA INTO BIT 23 OF MEMORY MODULE 1 --
 07 # MIGHT BE OUTPUT/INPUT OF STORE DATA(SD) FF'S, FANOUT GATE B(DOGB) TO MODULE(S)
 08 # 1(3,5,7), OR A STUCK DATA LEAD AT ANY MEMORY MODULE.
 09 # NOTE: DATA WORD IS PATTERN THAT WAS READ BACK (BITS 8-15 ARE SD16-23)
 10 #
 11 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
 12 # NOTE--1 MASCH 03-19 JL2 MAIN STORE PLANE - MOD 1 - BITS 19&23
 13 #
 14 #
 15 # OPTION A
 16 #
 17 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
 18 # NOTE--1 MASCH 03-16 JL16 MAIN STORE PLANE - MOD 0 BITS 19 & 23
 19 #
 20 #

0032353

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0032353

-001- 23 #& TBL 5352
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 0
 30 #
 31 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 32 # MASCH 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER

0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 127

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5353 DSS1SD01

CDGSDF W77D

-001- 01 #& TBL 5353
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 1
 08
 09 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

0032353

-001- 12 #& TBL 5354
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 2
 19
 20 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

0032353

-001- 23 #& TBL 5355
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 3
 30
 31 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 128

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5356 DSS1SD04

CDGSDf

W77D

0032353

-001- 01 #& TBL 5356
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDf
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 4
 08 #
 09 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
 10 # MASCH 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER
 11 # MASCH 08-17 FA1062 MAIN STORE CONTROLLER - CHECK CIRCUIT A

0032353

-001- 14 #& TBL 5357
 -001- 15 #& TEST 53
 -001- 16 #& SEGMENT 2
 -001- 17 #& PR CDGSDf
 -001- 18 #
 19 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 20 # IS STUCK ACTIVE ON DATA BIT 5
 21 #
 22 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 25 #& TBL 5358
 -001- 26 #& TEST 53
 -001- 27 #& SEGMENT 2
 -001- 28 #& PR CDGSDf
 -001- 29 #
 30 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 31 # IS STUCK ACTIVE ON DATA BIT 6
 32 #
 33 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 129

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5359 DSS1SD07

CDGSDF W77D

0032353

-001- 01 #& TBL 5359
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 7
 08 #
 09 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

0032353

-001- 12 #& TBL 5360
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 8
 19 #
 20 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

0032353

-001- 23 #& TBL 5361
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 9
 30 #
 31 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 130

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5362 DSS1SD10

CDGSDF W77D

0032353

-001- 01 #& TBL 5362
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 10
 08
 09 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

0032353

-001- 12 #& TBL 5363
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 11
 19
 20 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

0032353

-001- 23 #& TBL 5364
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 12
 30
 31 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 131

TROUBLE LOCATION MANUAL

5365 DSS1SD13

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5365
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 13
 08 #
 09 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 12 #& TBL 5366
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 14
 19 #
 20 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

0032353

-001- 23 #& TBL 5367
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 15
 30 #
 31 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 132

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5368 DSS1SDPL

CDGSDF W77D

0032353

-001- 01 #& TBL 5368
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT PL
 08 #
 09 # MASCH 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
 10 # MASCH 08-17 FA1062 MAIN STORE CONTROLLER - CHECK CIRCUIT A

0032353

-001- 13 #& TBL 5369
 -001- 14 #& TEST 53
 -001- 15 #& SEGMENT 2
 -001- 16 #& PR CDGSDF
 -001- 17 #
 18 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 19 # IS STUCK ACTIVE ON DATA BIT PH
 20 #
 21 # MASCH 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH

0032353

-001- 24 #& TBL 5370
 -001- 25 #& TEST 53
 -001- 26 #& SEGMENT 2
 -001- 27 #& PR CDGSDF
 -001- 28 #
 29 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 30 # IS STUCK ACTIVE ON DATA BIT 16
 31 #
 32 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 133

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5371 DSS1SD17

CDGSDF W77D

0032353

-001- 01 #& TBL 5371
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 17
 08
 09 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21

0032353

-001- 12 #& TBL 5372
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 18
 19
 20 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22

0032353

-001- 23 #& TBL 5373
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 19
 30
 31 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 134

TROUBLE LOCATION MANUAL

5374 DSS1SD20

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5374
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 20
 08
 09 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

0032353

-001- 12 #& TBL 5375
 -001- 13 #& TEST 53
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 18 # IS STUCK ACTIVE ON DATA BIT 21
 19
 20 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21

0032353

-001- 23 #& TBL 5376
 -001- 24 #& TEST 53
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 29 # IS STUCK ACTIVE ON DATA BIT 22
 30
 31 # NOTE--1 MASCH 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 135

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5377 DSS1SD23

CDGSDF W77D

-001- 01 #& TBL 5377
 -001- 02 #& TEST 53
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE)
 07 # IS STUCK ACTIVE ON DATA BIT 23
 08 #
 09 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23

0032353

-001- 12 #& TBL 5401
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 0, 1 OR 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # TWO SUCCESSIVE I/O TRANSMISSIONS TO THE STORE CONTROLLER UNDER TEST FAILED
 18 # TO COMPLETE PROPERLY.
 19 # DATA = I/O ORDER WHICH WAS UNSUCCESSFULLY TRANSMITTED
 20 # ADDRESS = FAILURE EXIT(FAILTST) IN 'MASIOM' SUBROUTINE
 21 #
 22 # MASCM 08-20 FA1064 MAIN STORE CONTROLLER - I/O ORDER DECODER
 23 # MASCM 08-22 FA1063 MAIN STORE CONTROLLER - I/O ORDER RECEIVER
 24 # MASCM 08-23 FC203 MAIN STORE CONTROLLER CLOCK & I/O INTERFACE
 25 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
 26 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 27 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
 28 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 29 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

0032353.

STORE CONTROLLER DATA REGISTER TEST

FR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 136

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5402 ENS1SD00

CDGSDF W77D

0032353

-001- 01 #& TBL 5402
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 0
 08
 09 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 10 # MASCM 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER
 11 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
 12 # WRITE MODE (WRTMDIO) COMES FROM WRTMDOB

0032353

-001- 15 #& TBL 5403
 -001- 16 #& TEST 54
 -001- 17 #& SEGMENT 1
 -001- 18 #& PR CDGSDF
 -001- 19 #
 20 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 21 # IS STUCK ACTIVE ON DATA BIT 1
 22
 23 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

0032353

-001- 26 #& TBL 5404
 -001- 27 #& TEST 54
 -001- 28 #& SEGMENT 1
 -001- 29 #& PR CDGSDF
 -001- 30 #
 31 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 32 # IS STUCK ACTIVE ON DATA BIT 2
 33
 34 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 35 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 137

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5405 ENS1SD03

CDGSDF W77D

0032353

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-001- 01 #& TBL 5405
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
07 # IS STUCK ACTIVE ON DATA BIT 3
08 #
09 # MASCN 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 &11
10 # MASCN 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 &12
11 # MASCN 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 &13
12 # MASCN 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 &14
13 # MASCN 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 &15
14 # MASCN 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL

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0032353

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-001- 17 #& TBL 5406
-001- 18 #& TEST 54
-001- 19 #& SEGMENT 1
-001- 20 #& PR CDGSDF
-001- 21 #
22 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
23 # IS STUCK ACTIVE ON DATA BIT 4
24 #
25 # MASCN 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 &12
26 # MASCN 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER
27 # MASCN 08-17 FA1062 MAIN STORE CONTROLLER - CHECK CIRCUIT A
28 # MASCN 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
29 # WRITE MODE (WRTMDIO) COMES FROM WRTMDDA

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 138

TROUBLE LOCATION MANUAL

5407 ENS1SD05

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5407
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 5
 08 #
 09 # MASC8 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 12 #& TBL 5408
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 6
 19 #
 20 # MASC8 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

0032353

-001- 23 #& TBL 5409
 -001- 24 #& TEST 54
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 7
 30 #
 31 # MASC8 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

STORE CONTROLLER DATA REGISTER TEST

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF

PR-10935-50

ISSUE 05 PAGE 139

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5410 ENS1SD08

CDGSDF W77D

0032353

-001- 01 #& TBL 5410
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 8
 08
 09 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

0032353

-001- 12 #& TBL 5411
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 9
 19
 20 # MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

0032353

-001- 23 #& TBL 5412
 -001- 24 #& TEST 54
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 10
 30
 31 # MASCH 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 140

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5413 ENS1SD11

CDGSDF W77D

0032353

-001- 01 #& TBL 5413
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 11
 08 #
 09 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

0032353

-001- 12 #& TBL 5414
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 12
 19 #
 20 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12

0032353

-001- 23 #& TBL 5415
 -001- 24 #& TEST 54
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 13
 30 #
 31 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 141

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5416 ENS1SD14

CDGSDF W77D

0032353

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-001- 01 #& TBL 5416
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 1
-001- 04 #& PR CDGSDF
-001- 05 #
06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
07 # IS STUCK ACTIVE ON DATA BIT 14
08 #
09 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

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0032353

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-001- 12 #& TBL 5417
-001- 13 #& TEST 54
-001- 14 #& SEGMENT 1
-001- 15 #& PR CDGSDF
-001- 16 #
17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
18 # IS STUCK ACTIVE ON DATA BIT 15
19 #
20 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

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0032353

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-001- 23 #& TBL 5418
-001- 24 #& TEST 54
-001- 25 #& SEGMENT 1
-001- 26 #& PR CDGSDF
-001- 27 #
28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
29 # IS STUCK ACTIVE ON DATA BIT 15
30 #
31 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS 16&17
32 # MASCM 08-17 FA1062 MAIN STORE CONTROLLER - CHECK CIRCUIT A
33 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
34 # WRITE MODE (WRTMDIO) COMES FROM WRTMDDC
35 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
36 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
37 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
38 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 142

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5419 ENS1SDPH

CDGSDF W77D

0032353 .

-001- 01 #& TBL 5419
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT PH
 08
 09 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH

0032353

-001- 12 #& TBL 5420
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 16
 19
 20 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

0032353

-001- 23 #& TBL 5421
 -001- 24 #& TEST 54
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 17
 30
 31 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 143

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5422 ENS1SD18

CDGSDF W77D

0032353

-001- 01 #& TBL 5422
 -001- 02 #& TEST 54
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 18
 08
 09 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22

0032353

-001- 12 #& TBL 5423
 -001- 13 #& TEST 54
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 19
 19
 20 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23

0032353

-001- 23 #& TBL 5424
 -001- 24 #& TEST 54
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 20
 30
 31 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 144

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5425 ENS1SD21

CDGSDF W77D

0032353

-001- 01 #8 TBL 5425
 -001- 02 #8 TEST 54
 -001- 03 #8 SEGMENT 1
 -001- 04 #8 PR CDGSDF
 -001- 05 #
 06 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 07 # IS STUCK ACTIVE ON DATA BIT 21
 08
 09 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17821

0032353

-001- 12 #8 TBL 5426
 -001- 13 #8 TEST 54
 -001- 14 #8 SEGMENT 1
 -001- 15 #8 PR CDGSDF
 -001- 16 #
 17 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 18 # IS STUCK ACTIVE ON DATA BIT 22
 19
 20 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18822

0032353

-001- 23 #8 TBL 5427
 -001- 24 #8 TEST 54
 -001- 25 #8 SEGMENT 1
 -001- 26 #8 PR CDGSDF
 -001- 27 #
 28 # A STORE DATA FLOP GATING SIGNAL (DATA STROBE, NORMAL READ, REFRESH MODE)
 29 # IS STUCK ACTIVE ON DATA BIT 23
 30
 31 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19823

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 145

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5428 SELSTK00

CDGSDF W77D

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-001- 01 #& TBL 5428
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 0 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # MASCN 03-04 JL2 MAIN STORE PLANE - MOD 0 - BITS 0 & 8
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # MASCN 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
0032353 16 #
0032353 17 #
18 # OPTION A
19 #
20 # MASCN 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8
21 # MASCN 03-02 JK25 MAIN MEMORY FANOUT BOARD FOR MODULE 0
22 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
23 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
24 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
25 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 26 # MASCN 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
0032353 27 #
28 #

-001- 31 #& TBL 5429
-001- 32 #& TEST 54
-001- 33 #& SEGMENT 2
-001- 34 #& PR CDGSDF
-001- 35 #
0032353 36 # THE SELECT ENABLE ON STORE DATA BIT 1 OF NEXT LOWER STORE IS STUCK ACTIVE
37 #
38 # MASCN 03-05 JL2 MAIN STORE PLANE - MOD 0 - BITS 1 & 9
-001- 39 #
-001- 40 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 41 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 42 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 43 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 44 #
0032353 45 # MASCN 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
0032353 46 #
0032353 47 #
48 # OPTION A
49 #

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 146

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5429 SELSTK01

CDGSDF W77D

0032353
0032353

01 # MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
 02 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 03 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 04 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 05 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 06 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
 07
 08

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-001- 11 #& TBL 5430
 -001- 12 #& TEST 54
 -001- 13 #& SEGMENT 2
 -001- 14 #& PR CDGSDF
 -001- 15 #
 16 # THE SELECT ENABLE ON STORE DATA BIT 2 OF NEXT LOWER STORE IS STUCK ACTIVE
 17

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0032353

18 # MASCM 03-06 JL2 MAIN STORE PLANE - MOD 0 - BITS 2 & 10
 -001- 19 #
 -001- 20 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 21 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 22 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 23 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 24 #
 25 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 26

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27
28 # OPTION A0032353
0032353

29
 30 # MASCM 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
 31 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 32 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 33 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 34 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 35 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 36
 37

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 147

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5431 SELSTK03

CDGSDF

W77D

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-001- 01 #& TBL 5431
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # THE SELECT ENABLE ON STORE DATA BIT 3 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # MASCM 03-07 JL2 MAIN STORE PLANE - MOD 0 - BITS 3 & 11
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
15 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
16 #
17 #
18 # OPTION A
19 #
20 # MASCM 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
25 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
26 #
27 #

-001- 30 #& TBL 5432
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
35 # THE SELECT ENABLE ON STORE DATA BIT 4 OF NEXT LOWER STORE IS STUCK ACTIVE
36 #
37 # MASCM 03-08 JL2 MAIN STORE PLANE - MOD 0 - BITS 4 & 12
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
44 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
45 #
46 #
47 # OPTION A
48 #
49 # MASCM 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 148

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5432 SELSTK04

CDGSDF W77D

0032353
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01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # MASCH 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
 06 #
 07 #

0032353

-001- 10 #& TBL 5433
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDF
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA BIT '5 OF NEXT LOWER STORE IS STUCK ACTIVE
 16 #
 17 # MASCH 03-09 JL2 MAIN STORE PLANE - MOD 0 - BITS 5 & 13
 -001- 18 #
 -001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 23 #

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0032353.

24 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 25 #
 26 #

0032353

27 # OPTION A

28 #
 29 # MASCH 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
 30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 34 # MASCH 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 35 #
 36 #

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 149

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5434 SELSTK06

CDGSDF W77D

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-001- 01 #8 TBL 5434
-001- 02 #8 TEST 54
-001- 03 #8 SEGMENT 2
-001- 04 #8 PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 6 OF NEXT LOWER STORE IS STUCK ACTIVE
07
08 # MASCH 03-10 JL2 MAIN STORE PLANE - MOD 0 - BITS 6 & 14
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
0032353 16
17
18 # OPTION A
0032353 19
20 # MASCH 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
21 # MASCH 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
22 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
23 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
24 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
25 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 26 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
0032353 27
28

-001- 31 #8 TBL 5435
-001- 32 #8 TEST 54
-001- 33 #8 SEGMENT 2
-001- 34 #8 PR CDGSDF
-001- 35 #
0032353 36 # THE SELECT ENABLE ON STORE DATA BIT 7 OF NEXT LOWER STORE IS STUCK ACTIVE
37
38 # MASCH 03-11 JL2 MAIN STORE PLANE - MOD 0 - BITS 7 & 15
-001- 39 #
-001- 40 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 41 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 42 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 43 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 44 #
0032353 45 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
0032353 46
47
48 # OPTION A
0032353 49

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 150

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5435 SELSTK07

CDGSDF W77D

0032353
0032353

01 # MASCH 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
 02 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 03 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 04 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 05 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 06 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
 07 #
 08 #

0032353

-001- 11 #& TBL 5436
 -001- 12 #& TEST 54
 -001- 13 #& SEGMENT 2
 -001- 14 #& PR CDGSDF
 -001- 15 #
 16 # THE SELECT ENABLE ON STORE DATA BIT 8 OF NEXT LOWER STORE IS STUCK ACTIVE
 17 #

0032353
0032353

18 # MASCH 03-04 JL2 MAIN STORE PLANE - MOD 0 - BITS 0 & 8
 -001- 19 #
 -001- 20 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 21 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 22 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 23 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 24 #

0032353

25 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 26 #
 27 #
 28 # OPTION A
 29 #

0032353
0032353

30 # MASCH 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8
 31 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 32 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 33 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 34 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 35 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 36 #
 37 #

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 151

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5437 SELSTK09

CDGSDF W77D

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-001- 01 #& TBL 5437
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 9 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # MASCM 03-05 JL2 MAIN STORE PLANE - MOD 0 - BITS 1 & 9
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
0032353 16 #
17 #
0032353 18 # OPTION A
19 #
20 # MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 25 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
0032353 26 #
27 #

-001- 30 #& TBL 5438
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
0032353 35 # THE SELECT ENABLE ON STORE DATA BIT 10 OF NEXT LOWER STORE IS STUCK ACTIVE
36 #
37 # MASCM 03-06 JL2 MAIN STORE PLANE - MOD 0 - BITS 2 & 10
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
0032353 44 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
0032353 45 #
46 #
0032353 47 # OPTION A
48 #
49 # MASCM 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 152

0032353
0032353

01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
 06
 07

0032353

-001- 10 #& TBL 5439
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDF
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA BIT 11 OF NEXT LOWER STORE IS STUCK ACTIVE
 16
 17 # MASCM 03-07 JL2 MAIN STORE PLANE - MOD 0 - BITS 3 & 11
 -001- 18 #
 -001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 23 #

0032353
0032353

24 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
 25
 26

0032353

27 # OPTION A

28
 29 # MASCM 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
 30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 34 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
 35
 36

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5440 SELSTK12

CDGSDF W77D

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-001- 01 #& TBL 5440
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 12 OF NEXT LOWER STORE IS STUCK ACTIVE
07
08 # MASCM 03-08 JL2 MAIN STORE PLANE - MOD 0 - BITS 4 & 12
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
0032353 16
17
18 # OPTION A
0032353 19
20 # MASCM 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 25 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
0032353 26
27

-001- 30 #& TBL 5441
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
0032353 35 # THE SELECT ENABLE ON STORE DATA BIT 13 OF NEXT LOWER STORE IS STUCK ACTIVE
36
37 # MASCM 03-09 JL2 MAIN STORE PLANE - MOD 0 - BITS 5 & 13
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
0032353 44 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
0032353 45
46
47 # OPTION A
0032353 48
49 # MASCM 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 154

TROUBLE LOCATION MANUAL

5441 SELSTK13

23:27:57 2/05/81 ****

CDGSDf W77D

0032353
0032353

01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 06 #
 07 #

0032353

-001- 10 #& TBL 5442
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDf
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA BIT 14 OF NEXT LOWER STORE IS STUCK ACTIVE
 16 #
 17 # MASCM 03-10 JL2 MAIN STORE PLANE - MOD 0 - BITS 6 & 14
 -001- 18 #
 -001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 23 #
 24 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 25 #
 26 #

0032353
0032353

0032353

27 # OPTION A
 28 #
 29 # MASCM 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
 30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 34 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 35 #
 36 #

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 155

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5443 SELSTK15

CDGSDF W77D

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-001- 01 #8 TBL 5443
-001- 02 #8 TEST 54
-001- 03 #8 SEGMENT 2
-001- 04 #8 PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 15 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # MASC0 03-11 JL2 MAIN STORE PLANE - MOD 0 - BITS 7 & 15
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # MASC0 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
0032353 16 #
0032353 17 #
18 # OPTION A
19 #
20 # MASC0 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 25 # MASC0 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
0032353 26 #
27 #

-001- 30 #8 TBL 5444
-001- 31 #8 TEST 54
-001- 32 #8 SEGMENT 2
-001- 33 #8 PR CDGSDF
-001- 34 #
0032353 35 # THE SELECT ENABLE ON STORE DATA PL BIT OF NEXT LOWER STORE IS STUCK ACTIVE
36 #
37 # MASC0 03-17 JL2 MAIN STORE PLANE - MOD 0 - BITS PL&PH
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
0032353 44 # MASC0 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
0032353 45 #
46 #
0032353 47 # OPTION A
48 #
49 # MASC0 03-17 JL16 MAIN STORE PLANE - MOD 0 BITS PL & PH

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 156

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5444 SELSTKPL

CDGSDF W77D

```

0032353 01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
0032353 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
05 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
06
07

-001- 10 #& TBL 5445
-001- 11 #& TEST 54
-001- 12 #& SEGMENT 2
-001- 13 #& PR CDGSDF
-001- 14 #
0032353 15 # THE SELECT ENABLE ON STORE DATA PH BIT. OF NEXT LOWER STORE IS STUCK ACTIVE
16
17 # MASCM 03-17 JL2 MAIN STORE PLANE - MOD 0 - BITS PL&PH
-001- 18 #
-001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 23 #
0032353 24 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
0032353 25
26
0032353 27 # OPTION A
28
29 # MASCM 03-17 JL16 MAIN STORE PLANE - MOD 0 BITS PL & PH
30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 34 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
0032353 35
36

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 157

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5446 SELSTK16

CDGSDF W77D

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-001- 01 #& TBL 5446
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # THE SELECT ENABLE ON STORE DATA BIT 16 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # NOTE--1 MASCM 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
15 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
16 #
17 #
18 # OPTION A
19 #
20 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
25 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
26 #
27 #

-001- 30 #& TBL 5447
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
35 # THE SELECT ENABLE ON STORE DATA BIT 17 OF NEXT LOWER STORE IS STUCK ACTIVE
36 #
37 # NOTE--1 MASCM 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
44 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
45 #
46 #
47 # OPTION A
48 #
49 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 158

5447 SELSTK17

CDGSDF W77D

0032353
0032353

01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
 06
 07

0032353

-001- 10 #& TBL 5448
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDF
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA BIT 18 OF NEXT LOWER STORE IS STUCK ACTIVE
 16
 17 # NOTE--1 MASCM 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22
 -001- 18 #
 -001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 23 #

0032353
0032353

24 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
 25
 26

0032353

27 # OPTION A
 28

0032353
0032353

29 # NOTE--1 MASCM 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
 30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 34 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
 35
 36

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5449 SELSTK19

CDGSDF W77D

```

-001- 01 #& TBL 5449
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 19 OF NEXT LOWER STORE IS STUCK ACTIVE
07 #
08 # NOTE--1 MASCM 03-16 JL2 MAIN STORE PLANE - MOD 0 - BITS 19&23
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353 16 #
0032353 17 #
18 # OPTION A
19 #
20 # NOTE--1 MASCM 03-16 JL16 MAIN STORE PLANE - MOD 0 BITS 19 & 23
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 25 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353 26 #
27 #

-001- 30 #& TBL 5450
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
0032353 35 # THE SELECT ENABLE ON STORE DATA BIT 20 OF NEXT LOWER STORE IS STUCK ACTIVE
36 #
37 # NOTE--1 MASCM 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
0032353 44 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
0032353 45 #
0032353 46 #
47 # OPTION A
48 #
49 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 16 & 20

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 160

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5450 SELSTK20

CDGSDF W77D

0032353
0032353

01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH
 06
 07

0032353

-001- 10 #& TBL 5451
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDF
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA BIT 21 OF NEXT LOWER STORE IS STUCK ACTIVE
 16
 17 # NOTE--1 MASCM 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21
 -001- 18 #
 -001- 19 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 20 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 21 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 22 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 23 #

0032353
0032353

24 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
 25
 26
 27 # OPTION A
 28

0032353

29 # NOTE--1 MASCM 03-12 JL16 MAIN STORE PLANE - MOD 0 BITS 17 & 21
 30 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 31 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 32 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 33 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 34 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
 35
 36

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-10935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 161

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5452 SELSTK22

CDGSDF W77D

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-001- 01 #& TBL 5452
-001- 02 #& TEST 54
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
0032353 06 # THE SELECT ENABLE ON STORE DATA BIT 22 OF NEXT LOWER STORE IS STUCK ACTIVE
07
08 # NOTE--1 MASCM 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22
-001- 09 #
-001- 10 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 11 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 12 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 13 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 14 #
0032353 15 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
0032353 16
0032353 17
0032353 18 # OPTION A
19
20 # NOTE--1 MASCM 03-14 JL16 MAIN STORE PLANE - MOD 0 BITS 18 & 22
21 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
22 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
23 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
24 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
0032353 25 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
0032353 26
27

-001- 30 #& TBL 5453
-001- 31 #& TEST 54
-001- 32 #& SEGMENT 2
-001- 33 #& PR CDGSDF
-001- 34 #
0032353 35 # THE SELECT ENABLE ON STORE DATA BIT 23 OF NEXT LOWER STORE IS STUCK ACTIVE
36
37 # NOTE--1 MASCM 03-16 JL2 MAIN STORE PLANE - MOD 0 - BITS 19&23
-001- 38 #
-001- 39 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
-001- 40 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
-001- 41 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
-001- 42 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
-001- 43 #
0032353 44 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
0032353 45
0032353 46
47 # OPTION A
48
49 # NOTE--1 MASCM 03-16 JL16 MAIN STORE PLANE - MOD 0 BITS 19 & 23

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 162

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5453 SELSTK23

CDGSDF W77D

0032353
0032353

01 # IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER
 02 # OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING
 03 # LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT
 04 # CONTROLLER AND NEXT LOWER-NUMBERED ONE.
 05 # NOTE--1 MASCH 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19823
 06
 07

0032353

-001- 10 #& TBL 5454
 -001- 11 #& TEST 54
 -001- 12 #& SEGMENT 2
 -001- 13 #& PR CDGSDF
 -001- 14 #
 15 # THE SELECT ENABLE ON STORE DATA EP BIT OF NEXT LOWER STOR' IS STUCK ACTIVE
 16
 -001- 17 #
 -001- 18 # >>>> IF THE TTY PRINTOUT POINTS TO A STORE CONTROLLER <<<<
 -001- 19 # >>>> OTHER THAN THE FIRST ONE (MAS 0), THEN THE FOLLOWING <<<<
 -001- 20 # >>>> LIST OF SUSPECT CIRCUIT PACK(S) APPLIES TO BOTH THAT <<<<
 -001- 21 # >>>> CONTROLLER AND NEXT LOWER-NUMBERED ONE. <<<<
 -001- 22 #
 23 # NOTE--1 MASCH 08-14 FA1067 MAIN STORE CONTROLLER - PARITY TRANSLATOR

0032353

-001- 26 #& TBL 5501
 -001- 27 #& TEST 55
 -001- 28 #& SEGMENT 1
 -001- 29 #& PR CDGSDF
 -001- 30 #
 31 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 32 # (TSTSD0A0) OF STORE DATA BIT 00 IS STUCK-AT-ONE (HIGH).
 33
 34 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 35 # MASCH 08-20 FA1064 MAIN STORE CONTROLLER - I/O ORDER DECODER
 36 # MASCH 08-22 FA1063 MAIN STORE CONTROLLER - I/O ORDER RECEIVER
 37 # MASCH 08-23 FC203 MAIN STORE CONTROLLER CLOCK & I/O INTERFACE
 38 # MASCH 08-21 FA1065 MAIN STORE CONTROLLER - COMMAND DECODER

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 163

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5502 NLDBS01

CDGSDF W77D

0032353

-001- 01 #& TBL 5502
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSD0A0) OF STORE DATA BIT 01 IS STUCK-AT-ONE (HIGH).
 08
 09 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

0032353

-001- 12 #& TBL 5503
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSD0A0) OF STORE DATA BIT 02 IS STUCK-AT-ONE (HIGH).
 19
 20 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

0032353

-001- 23 #& TBL 5504
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSD0A0) OF STORE DATA BIT 03 IS STUCK-AT-ONE (HIGH).
 30
 31 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 164

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5505 NLDBS04

CDGSDF W77D

0032353

-001- 01 #& TBL 5505
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDOAO) OF STORE DATA BIT 04 IS STUCK-AT-ONE (HIGH).
 08 #
 09 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12

0032353

-001- 12 #& TBL 5506
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDOAO) OF STORE DATA BIT 05 IS STUCK-AT-ONE (HIGH).
 19 #
 20 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 23 #& TBL 5507
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDOAO) OF STORE DATA BIT 06 IS STUCK-AT-ONE (HIGH).
 30 #
 31 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 165

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5508 NLDBS07

CDGSDF W77D

0032353

-001- 01 #& TBL 5508
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDOAO) OF STORE DATA BIT 07 IS STUCK-AT-ONE (HIGH).
 08
 09 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

0032353

-001- 12 #& TBL 5509
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDOAO) OF STORE DATA BIT 08 IS STUCK-AT-ONE (HIGH).
 19
 20 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

0032353

-001- 23 #& TBL 5510
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDOAO) OF STORE DATA BIT 09 IS STUCK-AT-ONE (HIGH).
 30
 31 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 166

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5511 NLDBS10

CDGSDF W77D

0032353

-001- 01 #& TBL 5511
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDOAO) OF STORE DATA BIT 10 IS STUCK-AT-ONE (HIGH).
 08
 09 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 &10

0032353

-001- 12 #& TBL 5512
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDOAO) OF STORE DATA BIT 11 IS STUCK-AT-ONE (HIGH).
 19
 20 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 &11

0032353

-001- 23 #& TBL 5513
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDOAO) OF STORE DATA BIT 12 IS STUCK-AT-ONE (HIGH).
 30
 31 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 &12

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 167

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5514 NLDBS13

CDGSDF

W77D

0032353

-001- 01 #& TBL 5514
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTS00A0) OF STORE DATA BIT 13 IS STUCK-AT-ONE (HIGH).
 08
 09 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 12 #& TBL 5515
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTS00A0) OF STORE DATA BIT 14 IS STUCK-AT-ONE (HIGH).
 19
 20 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

0032353

-001- 23 #& TBL 5516
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTS00A0) OF STORE DATA BIT 15 IS STUCK-AT-ONE (HIGH).
 30
 31 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 168

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5517 NLDBSPR

CDGSDF W77D

0032353

-001- 01 #& TBL 5517
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDOAO) OF STORE DATA BIT PR IS STUCK-AT-ONE (HIGH).
 08
 09 # MASCH 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH

0032353

-001- 12 #& TBL 5518
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDOAO) OF STORE DATA BIT 16 IS STUCK-AT-ONE (HIGH).
 19
 20 # NOTE--1 MASCH 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

0032353

-001- 23 #& TBL 5519
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDOAO) OF STORE DATA BIT 17 IS STUCK-AT-ONE (HIGH).
 30
 31 # NOTE--1 MASCH 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 169

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5520 NLDBS18

CDGSDF W77D

0032353

-001- 01 #& TBL 5520
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDDAD) OF STORE DATA BIT 18 IS STUCK-AT-ONE (HIGH).
 08
 09 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22

0032353

-001- 12 #& TBL 5521
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDDAD) OF STORE DATA BIT 19 IS STUCK-AT-ONE (HIGH).
 19
 20 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23

0032353

-001- 23 #& TBL 5522
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDDAD) OF STORE DATA BIT 20 IS STUCK-AT-ONE (HIGH).
 30
 31 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 170

TROUBLE LOCATION MANUAL

5523 NLDBS21

23:27:57 2/05/81 ****

CDGSDF W77D

-001- 01 #& TBL 5523
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 1
 -001- 04 #& PR CDGSDF
 -001- 05 #

0032353

06 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 07 # (TSTSDOAO) OF STORE DATA BIT 21 IS STUCK-AT-ONE (HIGH).
 08
 09 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21

-001- 12 #& TBL 5524
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 1
 -001- 15 #& PR CDGSDF
 -001- 16 #

0032353

17 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 18 # (TSTSDOAO) OF STORE DATA BIT 22 IS STUCK-AT-ONE (HIGH).
 19
 20 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22

-001- 23 #& TBL 5525
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 1
 -001- 26 #& PR CDGSDF
 -001- 27 #

0032353

28 # THE NORMAL LOAD SIGNAL (NLDB1) WHICH ENABLES THE FAST SET INPUT
 29 # (TSTSDOAO) OF STORE DATA BIT 23 IS STUCK-AT-ONE (HIGH).
 30
 31 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23

STORE CONTROLLER DATA REGISTER TEST

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF

PR-1C935-50

ISSUE 05 PAGE 171

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5526 NLDSA00

CDGSDF W77D

0032353

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-001- 01 #& TBL 5526
-001- 02 #& TEST 55
-001- 03 #& SEGMENT 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
07 # BIT 00 IS STUCK-AT-ONE (HIGH)
08
09 # MASC8 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

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0032353

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-001- 12 #& TBL 5527
-001- 13 #& TEST 55
-001- 14 #& SEGMENT 2
-001- 15 #& PR CDGSDF
-001- 16 #
17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
18 # BIT 01 IS STUCK-AT-ONE (HIGH)
19
20 # MASC8 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

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0032353

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-001- 23 #& TBL 5528
-001- 24 #& TEST 55
-001- 25 #& SEGMENT 2
-001- 26 #& PR CDGSDF
-001- 27 #
28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
29 # BIT 02 IS STUCK-AT-ONE (HIGH)
30
31 # MASC8 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 172

TROUBLE LOCATION MANUAL

5529 NLDSA03

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5529
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 03 IS STUCK-AT-ONE (HIGH)
 08
 09 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

0032353

-001- 12 #& TBL 5530
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # BIT 04 IS STUCK-AT-ONE (HIGH)
 19
 20 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12

0032353

-001- 23 #& TBL 5531
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 29 # BIT 05 IS STUCK-AT-ONE (HIGH)
 30
 31 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

STORE CONTROLLER DATA REGISTER TEST

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET

PR-1C935-50
CDGSDF ISSUE 05 PAGE 173

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5532 NLDSA06

CDGSDF

W77D

0032353

-001- 01 #& TBL 5532
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 06 IS STUCK-AT-ONE (HIGH)
 08
 09 # MASCH 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

0032353

-001- 12 #& TBL 5533
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # BIT 07 IS STUCK-AT-ONE (HIGH)
 19
 20 # MASCH 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

0032353

-001- 23 #& TBL 5534
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 29 # BIT 08 IS STUCK-AT-ONE (HIGH)
 30
 31 # MASCH 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF

ISSUE 05 PAGE 174

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5535 NLDSA09

CDGSDF W77D

0032353

-001- 01 #& TBL 5535
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 09 IS STUCK-AT-ONE (HIGH)
 08
 09 # MASCH 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9

0032353

-001- 12 #& TBL 5536
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # BIT 10 IS STUCK-AT-ONE (HIGH)
 19
 20 # MASCH 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10

0032353

-001- 23 #& TBL 5537
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 29 # BIT 11 IS STUCK-AT-ONE (HIGH)
 30
 31 # MASCH 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 175

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5538 NLDSA12

CDGSDF W77D

0032353

-001- 01 #& TBL 5538
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 12 IS STUCK-AT-ONE (HIGH)
 08 #
 09 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12

0032353

-001- 12 #& TBL 5539
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # BIT 13 IS STUCK-AT-ONE (HIGH)
 19 #
 20 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13

0032353

-001- 23 #& TBL 5540
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 29 # BIT 14 IS STUCK-AT-ONE (HIGH)
 30 #
 31 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 176

TROUBLE LOCATION MANUAL

5541 NLDSA15

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5541
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 15 IS STUCK-AT-ONE (HIGH)
 08
 09 # MASCM 06-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15

0032353

-001- 12 #& TBL 5542
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # PL BIT IS STUCK-AT-ONE (HIGH)
 19
 20 # MASCM 08-16 FA1060 MAIN STORE CONTROLLER - BITS PL&PH

0032353

-001- 23 #& TBL 5543
 -001- 24 #& TEST 55
 -001- 25 #& SEGMENT 2
 -001- 26 #& PR CDGSDF
 -001- 27 #
 28 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 29 # BIT 16 IS STUCK-AT-ONE (HIGH)
 30
 31 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 177

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5544 NLDSA17

CDGSDF W77D

0032353

-001- 01 #& TBL 5544
 -001- 02 #& TEST 55
 -001- 03 #& SEGMENT 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 07 # BIT 17 IS STUCK-AT-ONE (HIGH)
 08
 09 # MASCM 08-23 FC203 MAIN STORE CONTROLLER CLOCK & I/O INTERFACE

0032353

-001- 12 #& TBL 5545
 -001- 13 #& TEST 55
 -001- 14 #& SEGMENT 2
 -001- 15 #& PR CDGSDF
 -001- 16 #
 17 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 18 # BIT 18 OR 19 IS STUCK-AT-ONE (HIGH)
 19
 20 # MASCM 08-18 FA1071 MAIN STORE CONTROLLER - TIMING GENERATOR
 21 # MASCM 08-17 FA1062 MAIN STORE CONTROLLER - CHECK CIRCUIT A
 22 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL

0032353

-001- 25 #& TBL 5546
 -001- 26 #& TEST 55
 -001- 27 #& SEGMENT 2
 -001- 28 #& PR CDGSDF
 -001- 29 #
 30 # THE NORMAL LOAD ENABLE (NLDB1) ON THE INPUT TO STORE ADDRESS
 31 # PH BIT IS STUCK-AT-ONE (HIGH)
 32
 33 # MASCM 08-19 FA1066 MAIN STORE CONTROLLER - CHECK CIRCUIT B

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 178

TROUBLE LOCATION MANUAL

5601 IOPRTYER

23:27:57 2/05/81 ****

CDGSDF W77D

0032353

-001- 01 #& TBL 5601
 -001- 02 #& TEST 56
 -001- 03 #& SEGMENT 0, 1 OR 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # TWO SUCCESSIVE I/O TRANSMISSIONS TO THE STORE CONTROLLER UNDER TEST FAILED
 07 # TO COMPLETE PROPERLY.
 08 # DATA = I/O ORDER WHICH WAS UNSUCCESSFULLY TRANSMITTED
 09 # ADDRESS = FAILURE EXIT(FAILTST) IN 'MASIOM' SUBROUTINE
 10 #
 11 # MASCM 08-20 FA1064 MAIN STORE CONTROLLER - I/O ORDER DECODER
 12 # MASCM 08-22 FA1063 MAIN STORE CONTROLLER - I/O ORDER RECEIVER
 13 # MASCM 08-23 FC203 MAIN STORE CONTROLLER CLOCK & I/O INTERFACE

0032353

-001- 16 #& TBL 5602
 -001- 17 #& TEST 56
 -001- 18 #& SEGMENT 1 OR 2
 -001- 19 #& PR CDGSDF
 -001- 20 #
 21 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 22 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 00 IN THE STORE
 23 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 24 #
 25 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 26 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
 27 # COMPLEMENT WRITE (COMWRTIO) COMES FROM LSCWAD
 28 # PROC 02-21 FA1029 MAIN STORE CONTROL 0
 29 # COMPLEMENT WRITE SHOULD NOT DEPEND ON 'ID' FLOP
 30 # MASCM 03-04 JL1 MAIN STORE PLANE - MOD 0 - BITS 0&8
 31 #
 32 #
 33 # OPTION A
 34 #
 35 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 36 # MASCM 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8
 37 #
 38 #

0032353
0032353

0032353

0032353
0032353

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 179

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5603 CWSTK01

CDGSDF W77D

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-001- 01 #& TBL 5603
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 1 OR 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 01 IN THE STORE
08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
09 #
0032353 10 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
11 # MASCM 03-05 JL1 MAIN STORE PLANE - MOD 0 - BITS 1&9
0032353 12 #
0032353 13 #
14 # OPTION A
0032353 15 #
16 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
0032353 17 # MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
0032353 18 #
19 #

-001- 22 #& TBL 5604
-001- 23 #& TEST 56
-001- 24 #& SEGMENT 1 OR 2
-001- 25 #& PR CDGSDF
-001- 26 #
27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 02 IN THE STORE
29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
0032353 30 #
31 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
0032353 32 # MASCM 03-06 JL1 MAIN STORE PLANE - MOD 0 - BITS 2&10
0032353 33 #
0032353 34 #
35 # OPTION A
0032353 36 #
37 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
0032353 38 # MASCM 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
0032353 39 #
40 #

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 180

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5605 CWSTK03

CDGSDP W770

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-001- 01 #& TBL 5605
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 1 OR 2
-001- 04 #& PR CDGSDP
-001- 05 #
06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 03 IN THE STORE
08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
09
0032353 10 # MASCH 08-05 FA106D MAIN STORE CONTROLLER - BITS 3 & 11
11 # MASCH 03-07 JL1 MAIN STORE PLANE - MOD 0 - BITS 3&11
12
0032353 13
0032353 14 # OPTION A
15
0032353 16 # MASCH 08-05 FA106D MAIN STORE CONTROLLER - BITS 3 & 11
0032353 17 # MASCH 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
18
19

-001- 22 #& TBL 5606
-001- 23 #& TEST 56
-001- 24 #& SEGMENT 1 OR 2
-001- 25 #& PR CDGSDP
-001- 26 #
27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 04 IN THE STORE
29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
30
0032353 31 # MASCH 08-06 FA106D MAIN STORE CONTROLLER - BITS 4 & 12
32 # MASCH 03-08 JL1 MAIN STORE PLANE - MOD 0 - BITS 4&12
33
0032353 34
0032353 35 # OPTION A
36
0032353 37 # MASCH 08-06 FA106D MAIN STORE CONTROLLER - BITS 4 & 12
0032353 38 # MASCH 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
39
40

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDP ISSUE 05 PAGE 181

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5607 CWSTK05

CDGSDF W77D

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-001- 01 #& TBL 5607
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 1 OR 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 05 IN THE STORE
08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
09 #
10 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
11 # MASCM 08-24 FA1068 MAIN STORE CONTROLLER - BUS CONTROL
12 # COMPLEMENT WRITE (COMWRTIO) COMES FROM LSCWEO
13 # MASCM 03-09 JL1 MAIN STORE PLANE - MOD 0 - BITS 5&13
14 #
15 #
16 # OPTION A
17 #
18 # MASCM 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
19 # MASCM 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
20 #
21 #

-001- 24 #& TBL 5608
-001- 25 #& TEST 56
-001- 26 #& SEGMENT 1 OR 2
-001- 27 #& PR CDGSDF
-001- 28 #
29 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
30 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 06 IN THE STORE
31 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
32 #
33 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
34 # MASCM 03-10 JL1 MAIN STORE PLANE - MOD 0 - BITS 6&14
35 #
36 #
37 # OPTION A
38 #
39 # MASCM 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
40 # MASCM 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
41 #
42 #

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 182

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5609 CWSTK07

CDGSDF W77D

-001- 01 #& TBL 5609
 -001- 02 #& TEST 56
 -001- 03 #& SEGMENT 1 OR 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 07 IN THE STORE
 08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 09
 10 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
 11 # MASCM 03-11 JL1 MAIN STORE PLANE - MOD 0 - BITS 7&15
 12
 13
 14 # OPTION A
 15
 16 # MASCM 08-09 FA1060 MAIN STORE CONTROLLER - BITS 7 & 15
 17 # MASCM 03-11 JL16 MAIN STORE PLANE - MOD 0 BITS 7 & 15
 18
 19

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-001- 22 #& TBL 5610
 -001- 23 #& TEST 56
 -001- 24 #& SEGMENT 1 OR 2
 -001- 25 #& PR CDGSDF
 -001- 26 #
 27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 08 IN THE STORE
 29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 30
 31 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 32 # MASCM 03-04 JL1 MAIN STORE PLANE - MOD 0 - BITS 0&8
 33
 34
 35 # OPTION A
 36
 37 # MASCM 08-02 FA1060 MAIN STORE CONTROLLER - BITS 0 & 8
 38 # MASCM 03-04 JL16 MAIN STORE PLANE - MOD 0 BITS 0 & 8
 39
 40

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 183

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5611 CWSTK09

CDGSDF W77D

```

-001- 01 #& TBL 5611
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 1 OR 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 09 IN THE STORE
08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
09
0032353 10 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
11 # MASCM 03-05 JL1 MAIN STORE PLANE - MOD 0 - BITS 1&9
12
13
0032353 14 # OPTION A
15
0032353 16 # MASCM 08-03 FA1060 MAIN STORE CONTROLLER - BITS 1 & 9
17 # MASCM 03-05 JL16 MAIN STORE PLANE - MOD 0 BITS 1 & 9
18
0032353 19
19

-001- 22 #& TBL 5612
-001- 23 #& TEST 56
-001- 24 #& SEGMENT 1 OR 2
-001- 25 #& PR CDGSDF
-001- 26 #
27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 10 IN THE STORE
29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
30
0032353 31 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
32 # MASCM 03-06 JL1 MAIN STORE PLANE - MOD 0 - BITS 2&10
33
34
0032353 35 # OPTION A
36
0032353 37 # MASCM 08-04 FA1060 MAIN STORE CONTROLLER - BITS 2 & 10
38 # MASCM 03-06 JL16 MAIN STORE PLANE - MOD 0 BITS 2 & 10
39
0032353 40
40

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STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 184

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5613 CWSTK11

CDGSDF W77D

```

-001- 01 #& TBL 5613
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 1 OR 2
-001- 04 #& PR CDGSDF
-001- 05 #
06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 11 IN THE STORE
08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
09 #
0032353 10 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
11 # MASCM 03-07 JL1 MAIN STORE PLANE - MOD 0 - BITS 3&11
12 #
13 #
0032353 14 # OPTION A
0032353 15 #
16 # MASCM 08-05 FA1060 MAIN STORE CONTROLLER - BITS 3 & 11
17 # MASCM 03-07 JL16 MAIN STORE PLANE - MOD 0 BITS 3 & 11
18 #
0032353 19 #
0032353

-001- 22 #& TBL 5614
-001- 23 #& TEST 56
-001- 24 #& SEGMENT 1 OR 2
-001- 25 #& PR CDGSDF
-001- 26 #
27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 12 IN THE STORE
29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
30 #
0032353 31 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
0032353 32 # MASCM 03-08 JL1 MAIN STORE PLANE - MOD 0 - BITS 4&12
0032353 33 #
34 #
0032353 35 # OPTION A
0032353 36 #
37 # MASCM 08-06 FA1060 MAIN STORE CONTROLLER - BITS 4 & 12
0032353 38 # MASCM 03-08 JL16 MAIN STORE PLANE - MOD 0 BITS 4 & 12
0032353 39 #
0032353 40 #

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 185

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5615 CWSTK13

CDGSDF W77D

-001- 01 #& TBL 5615
 -001- 02 #& TEST 56
 -001- 03 #& SEGMENT 1 OR 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 13 IN THE STORE
 08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 09 #
 10 # MASCN 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 11 # MASCN 03-09 JL1 MAIN STORE PLANE - MOD 0 - BITS 5&13
 12 #
 13 #
 14 # OPTION A
 15 #
 16 # MASCN 08-07 FA1060 MAIN STORE CONTROLLER - BITS 5 & 13
 17 # MASCN 03-09 JL16 MAIN STORE PLANE - MOD 0 BITS 5 & 13
 18 #
 19 #

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-001- 22 #& TBL 5616
 -001- 23 #& TEST 56
 -001- 24 #& SEGMENT 1 OR 2
 -001- 25 #& PR CDGSDF
 -001- 26 #
 27 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 28 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 14 IN THE STORE
 29 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 30 #
 31 # MASCN 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 32 # MASCN 03-10 JL1 MAIN STORE PLANE - MOD 0 - BITS 6&14
 33 #
 34 #
 35 # OPTION A
 36 #
 37 # MASCN 08-08 FA1060 MAIN STORE CONTROLLER - BITS 6 & 14
 38 # MASCN 03-10 JL16 MAIN STORE PLANE - MOD 0 BITS 6 & 14
 39 #
 40 #

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 186

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5618 CWSTKPR

CDGSDF W77D

0032353

01

-001- 04 #8 TBL 5619
 -001- 05 #8 TEST 56
 -001- 06 #8 SEGMENT 1 OR 2
 -001- 07 #8 PR CDGSDF
 -001- 08 #
 09 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 10 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 16 IN THE STORE
 11 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 0032353
 12
 13 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
 14 # NOTE--1 MASCM 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20

-001- 17 #8 TBL 5620
 -001- 18 #8 TEST 56
 -001- 19 #8 SEGMENT 1 OR 2
 -001- 20 #8 PR CDGSDF
 -001- 21 #
 22 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 23 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 17 IN THE STORE
 24 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 0032353
 25
 26 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
 27 # NOTE--1 MASCM 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21

-001- 30 #8 TBL 5621
 -001- 31 #8 TEST 56
 -001- 32 #8 SEGMENT 1 OR 2
 -001- 33 #8 PR CDGSDF
 -001- 34 #
 35 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 36 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 18 IN THE STORE
 37 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 0032353
 38
 39 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
 40 # NOTE--1 MASCM 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 188

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5622 CWSTK19

CDGSDF W770

0032353

-001- 01 #& TBL 5622
 -001- 02 #& TEST 56
 -001- 03 #& SEGMENT 1 OR 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 19 IN THE STORE
 08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 09
 10 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
 11 # NOTE--1 MASCM 03-16 JL2 MAIN STORE PLANE - MOD 0 - BITS 19&23

0032353

-001- 14 #& TBL 5623
 -001- 15 #& TEST 56
 -001- 16 #& SEGMENT 1 OR 2
 -001- 17 #& PR CDGSDF
 -001- 18 #
 19 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 20 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 20 IN THE STORE
 21 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 22
 23 # NOTE--1 MASCM 08-10 FA1060 MAIN STORE CONTROLLER - BITS 16&20
 24 # NOTE--1 MASCM 03-12 JL2 MAIN STORE PLANE - MOD 0 - BITS 16&20

0032353

-001- 27 #& TBL 5624
 -001- 28 #& TEST 56
 -001- 29 #& SEGMENT 1 OR 2
 -001- 30 #& PR CDGSDF
 -001- 31 #
 32 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 33 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 21 IN THE STORE
 34 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 35
 36 # NOTE--1 MASCM 08-11 FA1060 MAIN STORE CONTROLLER - BITS 17&21
 37 # NOTE--1 MASCM 03-13 JL2 MAIN STORE PLANE - MOD 0 - BITS 17&21

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 189

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5625 CMSTK22

CDGSDF W77D

0032353

-001- 01 #& TBL 5625
 -001- 02 #& TEST 56
 -001- 03 #& SEGMENT 1 OR 2
 -001- 04 #& PR CDGSDF
 -001- 05 #
 06 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 07 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 22 IN THE STORE
 08 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 09 #
 10 # NOTE--1 MASCM 08-12 FA1060 MAIN STORE CONTROLLER - BITS 18&22
 11 # NOTE--1 MASCM 03-14 JL2 MAIN STORE PLANE - MOD 0 - BITS 18&22

0032353

-001- 14 #& TBL 5626
 -001- 15 #& TEST 56
 -001- 16 #& SEGMENT 1 OR 2
 -001- 17 #& PR CDGSDF
 -001- 18 #
 19 # ONE OF THE TWO SIGNALS (COMWRTO, COMWRT1) WHICH SELECTS WHETHER
 20 # THE ACTUAL CONTENT OR THE COMPLEMENT OF DATA BIT 23 IN THE STORE
 21 # CONTROLLER SHOULD BE SENT TO THE MEMORY PLANES IS FAULTY.
 22 #
 23 # NOTE--1 MASCM 08-13 FA1060 MAIN STORE CONTROLLER - BITS 19&23
 24 # NOTE--1 MASCM 03-16 JL2 MAIN STORE PLANE - MOD 0 - BITS 19&23

0032353

-001- 27 #& TBL 5627
 -001- 28 #& TEST 56
 -001- 29 #& SEGMENT 3
 -001- 30 #& PR CDGSDF
 -001- 31 #
 32 # COMPLEMENT WRITE IN INSTRUCTION MODE WAS UNSUCCESSFUL
 33 #
 34 # PROC 02-21 FA1029 MAIN STORE CONTROL 0

A

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 190

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5628 IDFFLT

CDGSDF WZ7D

0032353

```

-001- 01 #& TBL 5628
-001- 02 #& TEST 56
-001- 03 #& SEGMENT 4
-001- 04 #& PR CDGSDF
-001- 05 #
06 # THE INSTRUCTION/DATA FLOP WAS INCORRECTLY FLIPPED TO THE 'I'
07 # STATE AFTER THE COMPLEMENT WRITE OF A DATA WORD.
08 #
09 # PROC 02-21 FA1029 MAIN STORE CONTROL 0
10 # PROC 02-23 FA1046 DOUBLE STORE READ

```

0032353

```

-001- 13 #& TBL 5629
-001- 14 #& TEST 56
-001- 15 #& SEGMENT 5
-001- 16 #& PR CDGSDF
-001- 17 #
18 # COMPLEMENT CORRECTION MICROCODE IS NOT BEING INVOKED AFTER
19 # SINGLE PARITY ERROR
20 #
21 # PROC 02-23 FA1046 DOUBLE STORE READ
22 # PROC 02-35 FA1016 MICRO CONTROL 1
23 # PROC 02-34 FA1017 MICRO CONTROL 2
24 # PROC 06-35 FA1021 MICRO CONTROL 6
25 # PROC 02-31 FA1023 MICRO CONTROL 8
26 # MASCM 08-18 FA1071 MAIN STORE CONTROLLER - TIMING GENERATOR
-002- 27 PBEGIN PROG=CDGSDF TRNO=780315 SERNOB=52302
28 # CU DIAGNOSTIC TEST 54 SOMETIMES CAUSED THE MAS
29 # POWER CONVERTERS TO SHUT DOWN DUE TO OVERCURRENT.
30 # THIS PATCH CHANGES THE TEST SO THE MAS IS NOT PUT
31 # IN A HIGH POWER STATE.
32 # THIS PATCH REPLACES TC49161
0032353 33 TCEND
-001- 34 NOTE *****

```

//780315/**/**

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 191

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

5629 UCODFALT

CDGSDF W77D

0032353 ***/**/790350/**/**/

-002- 01 PBEGIN PROG=CDGSDF

TRNO=790350 SERNOB=53543

0032353

02
03 # THIS PATCH CORRECTS AN ERROR IN TC 52302.
04

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0032353

05 # THIS PATCH MODIFIES A TEST IN THE MAIN STORE DIAGNOSTIC
06 # TEST 54. THIS TEST WOULD ATP BUT WAS NOT PROPERLY
07 # TESTING THE CIRCUIT.

08
09 TCEND
-001- 10 NOTE *****

0032353

17 END

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
0000014	A	BIT12	17-17			
0000015	A	BIT13	17-18			
0000016	A	BIT14	17-19			
0000017	A	BIT15	17-20			
0000020	A	BIT16	17-21			
0000021	A	BIT17	17-22			
0000022	A	BIT18	17-23			
0000023	A	BIT19	17-24			
0000002	A	BIT2	17-07			
0000003	A	BIT3	17-08			
0000004	A	BIT4	17-09			
0000005	A	BIT5	17-10			
0000006	A	BIT6	17-11			
0000007	A	BIT7	17-12			
0000010	A	BIT8	17-13			
0000011	A	BIT9	17-14			
06 0027705	L	BKCSTDF1	26-39	41-07		
06 0030411	L	BKCSTDF2	43-39	60-07		
06 0031210	L	BKCSTDF3	62-39	74-07		
06 0031600	L	BKCSTDF4	76-39	95-07		
	2	R BLKMSK	8-15			
0000161	A	BRXT		LIBNUM=14	30-21, 34-30, 89-13, 92-42	
0145312	A	BRXMNSX		LIBNUM=14	89-19	
0047214	A	BUSCHK	19-08			
'CHECK BUS I						
SOLUTION USI						
NG SER'						
	T	BUSCHKT				
06 0030762	L	BUSELCK	55-32	55-30		
06 0031153	L	BUSELXT	59-20	57-22		
'SER+0'	T	CHDIF		W=1 S=2 N=0 CL=2		
	10	R CHKNDX	8-04			
'CIOBUF+0'	T	CINIT		W=1 S=0 N=0 CL=2		
06 0030724	L	CKENXT	53-37	52-35		
'CIOBUF+0'	T	CKHLT		W=1 S=2 N=0 CL=2		
06 0027727	L	CKSDALP	29-08	30-10		
06 0030005	L	CKSDAND	30-04	29-38		
06 0027722	L	CKSDAO	29-03	28-44		
06 0030070	L	CKSDBLP	32-29	34-19		
06 0030150	L	CKSDBND	34-13	33-47		
06 0030056	L	CKSDBO	32-14	30-06		
06 0030232	L	CKSDDS	37-27	37-24		
06 0030332	L	CKSDDSXT	40-03	39-14		
06 0030426	L	CKSDNW	45-32	45-27		
06 0030535	L	CKSDNWXT	48-05	47-13		
06 0030540	L	CKSDRM	48-31	48-17		
06 0030221	L	CKSDXT	36-12	32-23, 34-15		
'CIOBUF+0'	T	CKSTP		W=1 S=1 N=0 CL=2		
0040374	A	CKTINIT	19-10			
'INITIALIZE						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 194

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ALL STORE CO NTROLLER CIR CUITS'	T	CKTINITT				
0046074	A	CLKSTOP	19-15	48-47, 49-40		
'DISABLE STO RE CONTROLLE R CLOCK OUTP UT'	T	CLKSTOPT				
0046070	A	CLKSTRT	19-17	46-02, 49-43		
'ENABLE STOR E CONTROLLER CLOCK OUTPU T'	T	CLKSTRTT				
0177771	A	CLOCKON	23-01			
06 0030727	L	CLRMT	53-43	53-41		
06 0030335	L	CLRSC3	40-09	40-07		
0043310	A	CLRSER	19-13			
'CLEAR STORE ERROR REGIS TER'	T	CLRSERT				
'REFSTRT'	T	COHENT				

```

<.<(+|8
!$*);
-~/
,%_>?
:#@'=" ab
cdefghi n<(+
+ jklmnopqr
x)z=-stuvw
xyz+L[>=012
3456789+_]#
-|abcdefghi
)jklmno
pqr \ s
tuvwxyz
0123456789

```

'n T CONLC

LIBNUM=10

z

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 195

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDf	W77D
fe<hplx , vsfgs -al ix >k Jo#b'd" ab cdefghi n<+ + jklmnopqr x)tm~stuvw xyz+L [≥e012 3456789+J,Jf -CABCDEFghi)JKLMNO PQR \ S TUVWXYZ 0123456789						
0115272	L	CONSC	33-09	LIBNUM=10 33-05		
CDGSIO	V	CONTREP	7-25	26-38, 43-38, 62-38, 76-38		
'DGREQLO+0'	T	CONTROLBITS		W=7 S=11 N=0 CL=2		
0000035	A	COUNT	24-11	LIBNUM=15 42-11, 61-11, 75-11, 97-01, 97-03, 97-18, 98-11, 99-01, 99-31, 100-14, 101-01, 101-31, 102-16, 103-01, 103-31, 104-12, 105-01, 105-31, 106-12, 107-01, 107-31, 108-12, 109-04, 110-01, 110-31, 111-12, 112-01, 112-31, 113-12, 114-01, 114-31, 115-12, 116-01, 116-23, 117-01, 117-23, 118-01, 118-23, 119-01, 119-23, 120-01, 120-23, 121-01, 121-23, 122-01, 122-23, 123-01, 123-28, 124-01, 124-23, 125-01, 125-23, 126-01, 126-23, 127-01, 127-23, 128-01, 128-12, 128-23, 129-01, 129-14, 129-25, 130-01, 130-12, 130-23, 131-01, 131-12, 131-23, 132-01, 132-12, 132-23, 133-01, 133-13, 133-24, 134-01, 134-12, 134-23, 135-01, 135-12, 135-23, 136-01, 136-10, 136-12, 137-01, 137-15, 137-26, 138-01, 138-17, 139-01, 139-12, 139-23, 140-01, 140-12, 140-23, 141-01, 141-12, 141-23, 142-01, 142-12, 142-23, 143-01, 143-12, 143-23, 144-01, 144-12, 144-23, 145-01, 145-12, 145-23, 146-01, 146-31, 147-11, 148-01, 148-30, 149-10, 150-01, 150-31, 151-11, 152-01, 152-30, 153-10, 154-01, 154-30, 155-10, 156-01, 156-30, 157-10, 158-01, 158-30, 159-10, 160-01, 160-30, 161-10, 162-01, 162-30, 163-10, 163-24, 163-26, 164-01, 164-12, 164-23, 165-01, 165-12, 165-23, 166-01, 166-12, 166-23, 167-01, 167-12, 167-23, 168-01, 168-12, 168-23, 169-01, 169-12, 169-23, 170-01, 170-12, 170-23, 171-01, 171-12, 171-23, 172-01, 172-12, 172-23, 173-01, 173-12, 173-23, 174-01, 174-12, 174-23, 175-01, 175-12, 175-23, 176-01, 176-12, 176-23, 177-01, 177-12, 177-23, 178-01, 178-12, 178-25, 178-34, 179-01, 179-16, 180-01, 180-22, 181-01, 181-22, 182-01, 182-24, 183-01, 183-22, 184-01, 184-22, 185-01, 185-22, 186-01, 186-22, 187-01, 187-22, 188-04, 188-17, 188-30, 189-01, 189-14, 189-27, 190-01, 190-14, 190-27, 191-01, 191-13		
0111432	L	CPATCH	2-20			
0112562	L	CPATCH1	2-21			
0114107	L	CPATCH2	2-22			
0115073	L	CPATCH3	2-23	32-40		
0121236	L	CPATCH7	2-24			
06 0027662	L	CSTDF1	2-12	24-11		
06 0030366	L	CSTDF2	2-13	42-11		
06 0031165	L	CSTDF3	2-14	61-11		
06 0031555	L	CSTDF4	2-15	75-11		
'CHKWRD+0'	T	CTRLCK		W=1 S=16 N=0 CL=2		
0000002	A	CWSTK	179-16	79-32, 80-49, 83-32, 84-49		
0000022	A	CWSTKPR	187-22	79-47, 80-14, 83-47, 84-14, 187-22		
0000002	A	CWSTK00	179-16			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 196

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES
0000003	A	CWSTK01	180-01	
0000004	A	CWSTK02	180-22	
0000005	A	CWSTK03	181-01	
0000006	A	CWSTK04	181-22	
0000007	A	CWSTK05	182-01	
0000010	A	CWSTK06	182-24	
0000011	A	CWSTK07	183-01	
0000012	A	CWSTK08	183-22	
0000013	A	CWSTK09	184-01	
0000014	A	CWSTK10	184-22	
0000015	A	CWSTK11	185-01	
0000016	A	CWSTK12	185-22	
0000017	A	CWSTK13	186-01	
0000020	A	CWSTK14	186-22	
0000021	A	CWSTK15	187-01	
0000023	A	CWSTK16	188-04	
0000024	A	CWSTK17	188-17	
0000025	A	CWSTK18	188-30	
0000026	A	CWSTK19	189-01	
0000027	A	CWSTK20	189-14	
0000030	A	CWSTK21	189-27	
0000031	A	CWSTK22	190-01	
0000032	A	CWSTK23	190-14	
0000000	A	DATAWD	24-11	42-11, 61-11, 75-11

CDGSDf

W77D

```

<.<( +18
!$*);
- /
X_>?
:NB'="
n<(+
+
=)±m--
+L[>=012
3456789↑↓]≠
-CABCDEF GHI
)JKLMNO
PQR \ S
TUVWXYZ
0123456789

```

```

T DELLC LIBNUM=10
06 0027663 L DF1INIT 26-05
06 0030367 L DF2INIT 43-05
06 0031166 L DF3INIT 62-05

```

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 197

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDf	W77D
06 0031556	L	DF4INIT	76-05			
CTSD	V	DGNLIM	7-12			
CTSD	V	DGREQ	7-37			
'SER+0'	T	DHPER		W=1 S=11 N=0 CL=2		
'SER+0'	T	DLPER		W=1 S=15 N=0 CL=2		
06 0031461	L	DRTST	70-20	70-17		
	O	R DRO		W=1 S=10 N=0 CL=1 LIBNUM=14		70-34
'#	P					
ROC		02-23				
FA1046		DOUB				
LE		STORE REA				
D'	T	DSR		LIBNUM=12		
0000064	A	DSS1SD	127-23	38-31, 39-41		
0000105	A	DSS1SDPH	133-13	39-06, 133-13		
0000104	A	DSS1SDPL	133-01	38-43, 133-01		
0000064	A	DSS1SD00	127-23			
0000065	A	DSS1SD01	128-01			
0000066	A	DSS1SD02	128-12			
0000067	A	DSS1SD03	128-23			
0000070	A	DSS1SD04	129-01			
0000071	A	DSS1SD05	129-14			
0000072	A	DSS1SD06	129-25			
0000073	A	DSS1SD07	130-01			
0000074	A	DSS1SD08	130-12			
0000075	A	DSS1SD09	130-23			
0000076	A	DSS1SD10	131-01			
0000077	A	DSS1SD11	131-12			
0000100	A	DSS1SD12	131-23			
0000101	A	DSS1SD13	132-01			
0000102	A	DSS1SD14	132-12			
0000103	A	DSS1SD15	132-23			
0000106	A	DSS1SD16	133-24			
0000107	A	DSS1SD17	134-01			
0000110	A	DSS1SD18	134-12			
0000111	A	DSS1SD19	134-23			
0000112	A	DSS1SD20	135-01			
0000113	A	DSS1SD21	135-12			
0000114	A	DSS1SD22	135-23			
0000115	A	DSS1SD23	136-01			
0000002	A	ENS1SD	137-01	46-26, 47-49, 51-19, 53-32		
0000023	A	ENS1SDPH	143-01	47-05, 52-27, 143-01		
0000022	A	ENS1SDPL	142-23	46-39, 51-47, 142-23		
0000002	A	ENS1SD00	137-01			
0000003	A	ENS1SD01	137-15			
0000004	A	ENS1SD02	137-26			
0000005	A	ENS1SD03	138-01			
0000006	A	ENS1SD04	138-17			
0000007	A	ENS1SD05	139-01			
0000010	A	ENS1SD06	139-12			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 198

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
0000011	A	ENS1SD07	139-23			
0000012	A	ENS1SD08	140-01			
0000013	A	ENS1SD09	140-12			
0000014	A	ENS1SD10	140-23			
0000015	A	ENS1SD11	141-01			
0000016	A	ENS1SD12	141-12			
0000017	A	ENS1SD13	141-23			
0000020	A	ENS1SD14	142-01			
0000021	A	ENS1SD15	142-12			
0000024	A	ENS1SD16	143-12			
0000025	A	ENS1SD17	143-23			
0000026	A	ENS1SD18	144-01			
0000027	A	ENS1SD19	144-12			
0000030	A	ENS1SD20	144-23			
0000031	A	ENS1SD21	145-01			
0000032	A	ENS1SD22	145-12			
0000033	A	ENS1SD23	145-23			
06 0031074	L	EPSELTST	58-15	58-13		
0146074	A	ERCOLL	19-19			
'COLLECT ERRORS IN STORE ERROR REG'						
	T	ERCOLLT				
15	R	ERRI		W=1 S=5 N=0 CL=1 LIBNUM=14	93-09	
10	R	ERSAVE	8-21			
0	J	ESS2	1-10			
1	J	ESS2E	1-09			
CSYSUB	V	EXCOFLPG	7-28	93-24		
0145454	A	EXERLD	19-21	71-09		
'ENABLE EXTERNAL LOAD OF STORE ERROR REG'						
	T	EXERLDT				
0145454	A	EXLDB15	19-24			
'ENABLE EXTERNAL LOAD OF STORE ERROR REG W/ ERROR COLLECTION'						
	T	EXLDB15T				
CDGNOF	V	FAILOFF	3-41			
06 0032332	L	FAILXTH	94-05	93-46		
06 0032333	L	FAILXTL	94-07	93-48		
0010000	A	FAK01D	22-36			
0005440	A	FASTREF	19-26			
'PERFORM FAST REFRESH ON STORE'						
	T	FASTREFT				
0045454	A	FASTRNC	19-28			
'FORCE FAST						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 199

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
REFRESH W/O CLOCK'	T	FASTRNCT				
0146074	A	FBIT09	19-32			
0146064	A	FBIT09R	19-35			
'COLLECT ERR ORS IN SER W / REFRESH ON'	T	FBIT09RT				
'COLLECT ERR ORS IN STORE ERROR REGIS TER'	T	FBIT09T				
0146076	A	FBRMB15	19-37			
'FANOUT BOAR D REFRESH W/ ERROR COLLE CT'	T	FBRMB15T				
'CIOBUF+0'	T	FBT09		W=1 S=17 N=0 CL=2		
0020000	A	FBUSRDN	22-38			
0046314	A	FCHKBT	19-39			
0046304	A	FCHKBTR	19-41			
'ACTIVATE SH IFTER CHECK BIT W/ REFRE SH ENABLED'	T	FCHKBTRT				
'ACTIVATE MA IN SHIFTER C HECK BIT'	T	FCHKBTT				
0040574	A	FCHKOS	19-43			
'FORCE ZEROS TO MODULE S ELECT XOR CH ECK & WRITE PROTECT SELE CT XOR CHECK CIRCUITS'	T	FCHKOST				
0041274	A	FCHK1S	19-46			
'FORCE ONES TO MODULE S ELECT XOR CH ECK & WRITE PROTECT SELE CT XOR CHECK CIRCUITS'	T	FCHK1ST				
0140574	A	FCKOS15	19-49			
'FORCE ZEROS TO CHECK CK TS W/ ERROR COLLECT'	T	FCKOS15T				
0141274	A	FCK1S15	20-02			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 200

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'FORCE ONES TO CHECK 'CK TS W/ ERROR COLLECT'	T	FCK1S15T				
0043074	A	FDISIDLE	20-04			
'FORCE DISAB LE OF IDLE F LOP'	T	FDISIDLET				
0000002	A	FFOBRM	22-44			
0145454	A	FFRMB15	19-30			
'FORCE FAST REFRESH & BI T 15'	T	FFRMB15T				
0020016	A	FGLDNCFR	23-02			
1	J	FIELD	1-09			
CDGSIO	V	FINAL	7-24	41-06, 60-06, 74-06, 95-06		
0046156	A	FLSHD	20-06			
0046154	A	FLSHONC	20-09			
'PRESENT ZER O TO INPUT O F MAIN SHIFT ER'	T	FLSHONCT				
'START ZERO INTO MAIN SH IFTER'	T	FLSHOT				
0044474	A	FNOTIM	20-16			
0044470	A	FNOTIMC	20-19	38-09, 49-46, 64-49		
'BLOCK TIMIN G TO FANOUT BOARDS(W/CLO CK)'	T	FNOTIMCT				
'INHIBIT TIM ING SIGNALS TO FANOUT BO ARDS'	T	FNOTIMT				
0045434	A	FNOWRT	20-12			
0045430	A	FNOWRTC	20-14	45-38		
'FORCE WRITI NG OF ONES W / CLOCK RUNN ING'	T	FNOWRTCT				
'FORCE WRITI NG OF ALL-ON ES DATA'	T	FNOWRTT				
10	R	FNBCNT	8-05	32-21, 32-22		
0041550	A	FOBMT	20-21			
'FANOUT BOAR D MAINTENANC						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 201

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
E TEST'	T	FOBMTT				
CDGSFA	V	FOBNDX	7-17			
CDGSFA	V	FOBNDX1	5-01	32-16		
'PAGSCR+26'	T	FOBNUM		W=20 S=0 N=32 CL=2		
'SER+0'	T	FOBPARER		W=1 S=10 N=0 CL=2		
'SER+0'	T	FOBTERA		W=1 S=7 N=0 CL=2		
'SER+0'	T	FOBTERB		W=1 S=0 N=0 CL=2		
0000004	A	FOUR	17-25			
0041474	A	FRFSELO	20-23			
'CANCEL SELE CT FROM FOB0 DURING REFR						
ESH'	T	FRFSELOT				
0042334	A	FRFSEL1	20-25			
'CANCEL SELE CT FROM FOB1 DURING REFR						
ESH'	T	FRFSEL1T				
0042534	A	FRFSEL2	20-27			
'CANCEL SELE CT FROM FOB2 DURING REFR						
ESH'	T	FRFSEL2T				
0043234	A	FRFSEL3	20-29			
'CANCEL SELE CT FROM FOB3 DURING REFR						
ESH'	T	FRFSEL3T				
0043434	A	FRFSEL4	20-31			
'CANCEL SELE CT FROM FOB4 DURING REFR						
ESH'	T	FRFSEL4T				
0044354	A	FRFSEL5	20-33			
'CANCEL SELE CT FROM FOB5 DURING REFR						
ESH'	T	FRFSEL5T				
0044554	A	FRFSEL6	20-35			
'CANCEL SELE CT FROM FOB6 DURING REFR						
ESH'	T	FRFSEL6T				
0045254	A	FRFSEL7	20-37			
'CANCEL SELE CT FROM FOB7 DURING REFR						
ESH'	T	FRFSEL7T				
0044334	A	FRREQA	20-39			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 202

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'SET REFRESH REQUEST ON TIMING BOARD (FA1071)'		T FRREQAT				
0040674	A	FRREQB	20-42			
0040664	A	FRREQBR	20-45			
'SET REFRESH REQUEST ON FA1062 W/ RE FRESH ENABLE D'		T FRREQBRT				
'SET REFRESH REQ COPY ON CHECK BOARD A (FA1062)'		T FRREQBT				
0100000	A	FSBITO9	22-41			
'CIOBUF+0'		T FSEL		W=1 S=16 N=0 CL=2		
0046074	A	FSELA	20-47			
'FORCE STORE SELECT (ISOL ATE) SIGNAL ON FA1071'		T FSELAT				
0004714	A	FSELB	21-02			
0004710	A	FSELBC	21-05			
'FORCE DUPLI CATED STORE SELECT (SELB) W/ CLOCK'		T FSELBCT				
'FORCE DUPLI CATED STORE SELECT (SELB) ACTIVE'		T FSELBT				
0046516	A	FSTFBRM	21-07			
'FORCE CONTR OLLER & FANO UT BOARDS TO REFRESH'		T FSTFBRMT				
0042354	A	FSTNM	21-09			
0052354	A	FSTNMFR	21-11			
'FORCE NORMA L MODE IN CO NTROLLER & R EFRESH TO FA NOUT BOARDS'		T FSTNMFR				
0042344	A	FSTNMR	21-13			
'FORCE MODE TO NORMAL W/ REFRESH ENA BLED'		T FSTNMRT				

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 203

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'FORCE MODE FLOP TO NORM AL STATE'	T	FSTNMT				
0046514	A	FSTRM	21-15	49-37		
'FORCE MODE FLOP TO REFR ESH STATE'	T	FSTRMT				
0046234	A	FTINWP	21-17			
'FORCE IN-WR ITE-PROTECT INHIBIT'	T	FTINWPT				
'CHKWRD+0'	T	FXORG		W=1 S=17 N=0 CL=2		
0000004	A	HALTCLK	22-46			
1	R	HIPATBAK	8-13			
		HIPATBAKXT	8-14	IS A SYNONYM OF R1XT		
0000034	A	IDFFLT	191-01	90-21,191-01		
06 0032057	L	IDMDCBK	86-26	86-24		
'ILLEGAL CON DITIONAL IN THE IF MACRO'	T	IFERROR1				
'DATA > 0 OR DATA < 0 IS MEANINGLESS USE DATA = = 0'	T	IFERROR2				
'TWO CONSTAN TS ARE BEING COMPARED IN IF MACRO'	T	IFERROR3				
'AN ITEM WAS COMPARED TO SOMETHING O THER THAN A CONSTANT'	T	IFERROR4				
'DESTINATION OF AN = STA TEMENT IS A CONSTANT'	T	IFERROR5				
'ATTEMPTING TO SET ITEM = TO SOMETHI NG OTHER THA N A CONSTANT'	T	IFERROR6				
'ITEM MAY ON LY USE THE = AND = COND ITIONALS'	T	IFERROR7				
CSYSUB	V	INIT_OCC	7-29	91-38		
0000001	A	INITBIT	22-42			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF. ISSUE 05 PAGE 204

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
CSYSUB	V	INITOST	7-30			
0000033	A	INSTCOMP	190-27	87-28,190-27		
CSYSUB	V	INSYNC	7-31			
CDGNM	X	INTRPNO3	3-38	26-45, 30-30, 31-34, 34-38, 35-41, 36-18, 38-19, 43-45, 46-15, 47-37, 50-28, 54-18, 56-20, 59-03, 62-45, 65-20, 69-11, 70-18, 71-28, 76-45, 79-20, 83-20, 87-16, 94-12		
'PAGSCR+25'	T	IOADDR		W=20 S=0 N=31 CL=2		
'I&'	T	IOBRK				
0006220	A	IOGO	21-19			
'ENABLE ON-LINE I/O ACCESS TO OFF-LINE STORE'	T	IOGOT				
0000001	A	IOPRTYER	97-03	136-12,179-01		
0125252	A	IOTSTA	21-21			
'I/O TEST PARTERN #1'	T	IOTSTAT				
0052525	A	IOTST5	21-23			
'I/O TEST PARTERN #2'	T	IOTST5T				
0000066	A	IS_RXT		LIBNUM=14 93-13		
'CHKWRD+0'	T	ISOBTS		W=7 S=0 N=0 CL=2		
0067214	A	ISOCHK	21-25			
'ISOLATE FROM FAR STORES & VERIFY VIA SER'	T	ISOCHKT				
0027214	A	ISOCHKU	21-29			
'APPLY ONES TO STORE DATA BUS(W/O ISOLATE)'	T	ISOCHKUT				
0040000	A	ISOLATE	22-40	69-39		
0043454	A	KILL12	21-31			
'CLEAR SHIFT REGISTER BIT IN POSITION 12'	T	KILL12T				
'CIOBUF+0'	T	LDFG		W=1 S=15 N=0 CL=2		
0025730	A	LDPARX		LIBNUM=14 30-23, 34-32, 92-44		
0020000	A	LOADCHK	22-39			
'PAGSCR'	T	LONAM_BASADDR				
'SER'	T	LONAM_CHDIF				
'CIOBUF'	T	LONAM_CINIT				
'CIOBUF'	T	LONAM_CKHLT				
'CIOBUF'	T	LONAM_CKSTP				
'DGREQLO'	T	LONAM_CONTROLBITS				
'CHKWRD'	T	LONAM_CTRLCK				
'SER'	T	LONAM_DHPER				

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 205

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W770
'SER'	T	LONAM_DLPER				
'CIOBUF'	T	LONAM_FBT09				
'PAGSCR'	T	LONAM_FOBNUM				
'SER'	T	LONAM_FOBPAR				
		ER				
'SER'	T	LONAM_FOBTER				
		A				
'SER'	T	LONAM_FOBTER				
		B				
'CIOBUF'	T	LONAM_FSEL				
'CHKWRD'	T	LONAM_FXORG				
'PAGSCR'	T	LONAM_IOADDR				
'CHKWRD'	T	LONAM_ISOBTS				
'CIOBUF'	T	LONAM_LDFG				
'PAGSCR'	T	LONAM_LOOPSU				
		B				
'SER'	T	LONAM_MODER				
'SER'	T	LONAM_MPXER				
'MASCIOSC'	T	LONAM_MSSCO				
'MASCIOSC'	T	LONAM_MSSC1				
'MASCIOSC'	T	LONAM_MSSC2				
'MASCIOSC'	T	LONAM_MSSC3				
'STRSTAT'	T	LONAM_PASBT				
'STRSTAT'	T	LONAM_PHBAK				
'STRSTAT'	T	LONAM_PLBAK				
'SER'	T	LONAM_PWER				
'CIOBUF'	T	LONAM_REFST				
'SER'	T	LONAM_RSELER				
'RSLCHK'	T	LONAM_RSELD6				
'RSLCHK'	T	LONAM_RSEL7				
'CHKWRD'	T	LONAM_RSVD0				
'RSLCHK'	T	LONAM_RSVD1				
'RSLCHK'	T	LONAM_SAPHLP				
'RSLCHK'	T	LONAM_SA17LP				
'SER'	T	LONAM_SCMER				
'CHKWRD'	T	LONAM_SDPCK				
'SER'	T	LONAM_SHER				
'STRSTAT'	T	LONAM_SNGBTE				
		R				
'DGREQLO'	T	LONAM_SPARE				
'CIOBUF'	T	LONAM_STORDE				
		R				
'RSLCHK'	T	LONAM_STRSLP				
'SER'	T	LONAM_TBTR				
'PAGSCR'	T	LONAM_TMSAV				
'DGREQLO'	T	LONAM_TSTNUM				
'PAGSCR'	T	LONAM_UPADDR				
'CHKWRD'	T	LONAM_WDISOL				
'MASCIOSC'	T	LONAM_OSSCO				

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 206

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'MASCIOSC'	T	LONAM_OSSC1				
'MASCIOSC'	T	LONAM_OSSC2				
'MASCIOSC'	T	LONAM_OSSC3				
'STRSTAT'	T	LONAM_DWDSTR				
		0				
'CIOBUF'	T	LONAM_O10TH				
'CHKWRD'	T	LONAM_2EPCK				
CDGNM	V	LOOPIND	7-09	26-34, 26-35, 26-37, 26-37, 26-39, 26-39, 43-34, 43-35, 43-37, 43-37, 43-39, 43-39, 62-34, 62-35, 62-37, 62-37, 62-39, 62-39, 76-34, 76-35, 76-37, 76-37, 76-39, 76-39		
'PAGSCR+24'	T	LOOPSUB		W=20 S=0 N=30 CL=2		
000000	A	LPINDX	30-32	30-35, 31-01, 31-04, 31-13, 31-16, 31-36, 31-39, 34-40, 34-43, 35-08, 35-11, 35-20, 35-23, 35-43, 35-46, 38-21, 38-24, 38-33, 38-36, 38-45, 38-48, 39-31, 39-34, 46-17, 46-20, 46-29, 46-32, 46-41, 46-44, 47-39, 47-42, 51-01, 51-04, 51-29, 51-32, 52-09, 52-12, 53-14, 53-17, 56-22, 56-25, 56-37, 56-40, 57-04, 57-07, 57-43, 57-46, 59-05, 59-08, 65-22, 65-25, 65-37, 65-40, 66-04, 66-07, 66-39, 66-42, 69-13, 69-16, 70-31, 70-34, 71-31, 71-34, 72-01, 72-04, 72-28, 72-31, 72-43, 72-46, 79-22, 79-25, 79-37, 79-40, 80-04, 80-07, 80-39, 80-42, 83-22, 83-25, 83-37, 83-40, 84-04, 84-07, 84-39, 84-42, 87-18, 87-21, 90-11, 90-14, 94-14, 94-17		
	4	R LSTPAT	8-17			
TTYTBL	X	MXAS	26-28	26-28, 43-28, 62-28, 76-28		
'#	M					
ASCM 08-24						
FA1068	MAIN					
STORE CONTR						
OLLER - BUS						
CONTROL'	T	MASCBSCL		LIBNUM=12		
'#	M					
ASCM 08-02						
FA1060	MAIN					
STORE CONTR						
OLLER - BITS						
D & 8'	T	MASCBSO		LIBNUM=12		
'#	M					
ASCM 08-03						
FA1060	MAIN					
STORE CONTR						
OLLER - BITS						
1 & 9'	T	MASCBS1		LIBNUM=12		
'# NOTE--1	M					
ASCM 08-11						
FA1060	MAIN					
STORE CONTR						
OLLER - BITS						
17&21'	T	MASCBS10		LIBNUM=12		
'# NOTE--1	M					
ASCM 08-12						
FA1060	MAIN					
STORE CONTR						
OLLER - BITS						
18&22'	T	MASCBS11		LIBNUM=12		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 207

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'# NOTE--1	M					
ASCM 08-13						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
19823'	T	MASCBS12		LIBNUM=12		
'#	M					
ASCM 08-04						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
2 810'	T	MASCBS2		LIBNUM=12		
'#	M					
ASCM 08-05						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
3 811'	T	MASCBS3		LIBNUM=12		
'#	M					
ASCM 08-06						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
4 812'	T	MASCBS4		LIBNUM=12		
'#	M					
ASCM 08-07						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
5 813'	T	MASCBS5		LIBNUM=12		
'#	M					
ASCM 08-08						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
6 814'	T	MASCBS6		LIBNUM=12		
'#	M					
ASCM 08-09						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
7 815'	T	MASCBS7		LIBNUM=12		
'#	M					
ASCM 08-16						
FA1060 MAIN						
STORE CONTR						
OLLER - BITS						
PL&PH'	T	MASCBS8		LIBNUM=12		
'# NOTE--1	M					

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 208

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ASCM 08-10 FA1060 MAIN STORE CONTR OLLER - BITS 16&20'						
'#	M	T MASCBS9		LIBNUM=12		
ASCM 08-17 FA1062 MAIN STORE CONTR OLLER - CHEC K CIRCUIT A'						
'#	M	T MASCCHKA		LIBNUM=12		
ASCM 08-19 FA1066 MAIN STORE CONTR OLLER - CHEC K CIRCUIT B'						
'#	M	T MASCCHKB		LIBNUM=12		
ASCM 08-23 FC203 MAIN STORE CONTR OLLER CLOCK & I/O INTERF ACE'						
'#	M	T MASCCLK		LIBNUM=12		
ASCM 08-21 FA1065 MAIN STORE CONTR OLLER - COMM AND DECODER'						
0046071	A	MASCINT	21-33	LIBNUM=12 26-41, 29-11, 32-32, 37-30, 43-41, 45-35, 48-34, 55-35, 58-18, 62-41, 64-46, 68-32, 69-36, 76-41, 78-33, 82-33, 86-29		
'INITIALIZE STORE CONTR OLLER'						
		T MASCINTT				
MASACS	V	MASCIOSC	7-04			
0046076	A	MASCLK	21-37			
0046074	A	MASCLKN	21-40			
'FAKE SECOND HALF OF STO RE CLOCK CYC LE'						
		T MASCLKNT				
'FAKE FIRST HALF OF STOR E CLOCK CYCL E'						
'#	M	T MASCLKT				
ASCM 08-22 FA1063 MAIN						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 209

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
STORE CONTR OLLER - I/O ORDER RECEIV ER'						
'#	M					
ASCN 08-20 FA1064 MAIN STORE CONTR OLLER - I/O ORDER DECODE R'		T MASCMTCA		LIBNUM=12		
'#	M					
ASCN 08-20 FA1064 MAIN STORE CONTR OLLER - I/O ORDER DECODE R'		T MASCMTCB		LIBNUM=12		
'#	M					
ASCN 08-14 FA1067 MAIN STORE CONTR OLLER - PARI TY TRANSLATO R'		T MASCPCAR		LIBNUM=12		
'#	M					
ASCN 08-18 FA1071 MAIN STORE CONTR OLLER - TIM ING GENERATO R'		T MASCTIM V MASERCNT	7-35	LIBNUM=12		
'#	M					
ASCN 03-02 JK25 MAIN MEMORY FANO UT BOARD FOR MODULE 0'		T MASFOBA0		LIBNUM=12		
'#	M					
ASCN 03-33 JK25 MAIN MEMORY FANO UT BOARD FOR MODULE 1'		T MASFOBA1		LIBNUM=12		
'#	M					
ASCN 03-02 JK3 MAIN STORE FANO T BOARD FOR MODULE 0'		T MASFOB0		LIBNUM=12		
'#	M					
ASCN 03-33 JK3 MAIN STORE FANO T BOARD FOR						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 210

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
MODULE 1'	T	MASFOB1		LIBNUM=12		
CDGSIO	X	MASIOB	7-19	26-40, 29-10, 32-31, 37-29, 38-08, 43-40, 45-34, 45-37, 46-01, 48-33, 48-46, 49-36, 49-39, 49-42, 49-45, 55-34, 58-17, 62-40, 64-45, 64-48, 68-31, 68-34, 69-35, 69-38, 71-08, 71-11, 76-40, 78-32, 82-32, 86-28, 91-40		
MASACS	V	MASONOFF	7-39			
'#	M					
ASCM 03-04						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 0 & 8'	T	MASPLA000		LIBNUM=12		
'#	M					
ASCM 03-05						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 1 & 9'	T	MASPLA001		LIBNUM=12		
'#	M					
ASCM 03-06						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 2 & 10'	T	MASPLA002		LIBNUM=12		
'#	M					
ASCM 03-07						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 3 & 11'	T	MASPLA003		LIBNUM=12		
'#	M					
ASCM 03-08						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 4 & 12'	T	MASPLA004		LIBNUM=12		
'#	M					
ASCM 03-09						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 5 & 13'	T	MASPLA005		LIBNUM=12		
'#	M					
ASCM 03-10						
JL16	MAIN					
STORE PLANE						
- MOD 0 BIT						
S 6 & 14'	T	MASPLA006		LIBNUM=12		
'#	M					
ASCM 03-11						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 211

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	M77D
JL16 STORE PLANE - MOD 0 BIT S 7 & 15'	T	MASPLA007		LIBNUM=12		
'# NOTE--1 M ASCM 03-12 JL16 STORE PLANE - MOD 0 BIT S 16 & 20'	T	MASPLA008		LIBNUM=12		
'# NOTE--1 M ASCM 03-12 JL16 STORE PLANE - MOD 0 BIT S 17 & 21'	T	MASPLA009		LIBNUM=12		
'# NOTE--1 M ASCM 03-14 JL16 STORE PLANE - MOD 0 BIT S 18 & 22'	T	MASPLA010		LIBNUM=12		
'# NOTE--1 M ASCM 03-16 JL16 STORE PLANE - MOD 0 BIT S 19 & 23'	T	MASPLA011		LIBNUM=12		
'# M ASCM 03-17 JL16 STORE PLANE - MOD 0 BIT S PL & PH'	T	MASPLA012		LIBNUM=12		
'# M ASCM 03-31 JL16 STORE PLANE - MOD 1 BIT S 0 & 8'	T	MASPLA100		LIBNUM=12		
'# M ASCM 03-29 JL16 STORE PLANE - MOD 1 BIT S 1 & 9'	T	MASPLA101		LIBNUM=12		
'# M ASCM 03-28 JL16 STORE PLANE - MOD 1 BIT						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 212

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
STORE PLANE - MOD 1 BIT						
S 2 & 10'	T	MASPLA102		LIBNUM=12		
'# M						
ASCM 03-27						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 3 & 11'	T	MASPLA103		LIBNUM=12		
'# M						
ASCM 03-26						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 4 & 12'	T	MASPLA104		LIBNUM=12		
'# M						
ASCM 03-25						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 5 & 13'	T	MASPLA105		LIBNUM=12		
'# M						
ASCM 03-24						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 6 & 14'	T	MASPLA106		LIBNUM=12		
'# M						
ASCM 03-23						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 7 & 15'	T	MASPLA107		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-22						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 16 & 20'	T	MASPLA108		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-21						
JL16 MAIN						
STORE PLANE - MOD 1 BIT						
S 17 & 21'	T	MASPLA109		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-20						
JL16 MAIN						
STORE PLANE						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 213

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
- MOD 1 BIT S 18 & 22'	T	MASPLA110		LIBNUM=12		
'# NOTE--1 M ASCM 03-19 JL16 MAIN STORE PLANE						
- MOD 1 BIT S 19 & 23'	T	MASPLA111		LIBNUM=12		
'# NOTE--1 M ASCM 03-18 JL16 MAIN STORE PLANE						
- MOD 1 BIT S PL & PH'	T	MASPLA112		LIBNUM=12		
'# M ASCM 03-04 JL2 MAIN STORE PLANE						
- MOD 0 - B ITS 0 & 8'	T	MASPLO00		LIBNUM=12		
'# M ASCM 03-05 JL2 MAIN STORE PLANE						
- MOD 0 - B ITS 1 & 9'	T	MASPLO01		LIBNUM=12		
'# M ASCM 03-06 JL2 MAIN STORE PLANE						
- MOD 0 - B ITS 2 & 10'	T	MASPLO02		LIBNUM=12		
'# M ASCM 03-07 JL2 MAIN STORE PLANE						
- MOD 0 - B ITS 3 & 11'	T	MASPLO03		LIBNUM=12		
'# M ASCM 03-08 JL2 MAIN STORE PLANE						
- MOD 0 - B ITS 4 & 12'	T	MASPLO04		LIBNUM=12		
'# M ASCM 03-09 JL2 MAIN STORE PLANE						
- MOD 0 - B						

STORE CONTROLLER DATA REGISTER TEST

PR-10935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 214

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ITS 5 & 13'	T	MASPL005		LIBNUM=12		
'#	M					
ASCM 03-10						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 6 & 14'	T	MASPL006		LIBNUM=12		
'#	M					
ASCM 03-11						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 7 & 15'	T	MASPL007		LIBNUM=12		
'#	M					
ASCM 03-17						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS PL&PH'	T	MASPL008		LIBNUM=12		
'# NOTE--1	M					
ASCM 03-12						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 16&20'	T	MASPL009		LIBNUM=12		
'# NOTE--1	M					
ASCM 03-13						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 17&21'	T	MASPL010		LIBNUM=12		
'# NOTE--1	M					
ASCM 03-14						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 18&22'	T	MASPL011		LIBNUM=12		
'# NOTE--1	M					
ASCM 03-16						
JL2	MAIN					
STORE PLANE						
- MOD 0 - B						
ITS 19&23'	T	MASPL012		LIBNUM=12		
'#	M					
ASCM 03-31						
JL2	MAIN					
STORE PLANE						
- MOD 1 - B						
ITS 0 & 8'	T	MASPL100		LIBNUM=12		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 215

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES
'#	M	ASCM 03-29		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 1 & 9'	T	MASPL101		LIBNUM=12
'#	M	ASCM 03-28		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 2 & 10'	T	MASPL102		LIBNUM=12
'#	M	ASCM 03-27		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 3 & 11'	T	MASPL103		LIBNUM=12
'#	M	ASCM 03-26		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 4 & 12'	T	MASPL104		LIBNUM=12
'#	M	ASCM 03-25		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 5 & 13'	T	MASPL105		LIBNUM=12
'#	M	ASCM 03-24		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 6 & 14'	T	MASPL106		LIBNUM=12
'#	M	ASCM 03-23		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS 7 & 15'	T	MASPL107		LIBNUM=12
'#	M	ASCM 03-18		
JL2	MAIN	STORE PLANE		
		- MOD 1 - B		
ITS PL&PH'	T	MASPL108		LIBNUM=12
'#	M	NOTE--1		

CDGSDF W77D

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 216

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ASCM 03-22						
JL2		MAIN				
STORE PLANE						
- MOD 1 - B						
ITS 16820'	T	MASPL109		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-21						
JL2		MAIN				
STORE PLANE						
- MOD 1 - B						
ITS 17821'	T	MASPL110		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-20						
JL2		MAIN				
STORE PLANE						
- MOD 1 - B						
ITS 18822'	T	MASPL111		LIBNUM=12		
'# NOTE--1 M						
ASCM 03-19						
JL2		MAIN				
STORE PLANE						
- MOD 1 - B						
ITS 19823'	T	MASPL112		LIBNUM=12		
'# M						
ASMA 03-04						
JL2		MAIN				
STORE PLANE						
- MOD 2 - B						
ITS 0 & 8'	T	MASPL200		LIBNUM=12		
'# M						
ASMA 03-05						
JL2		MAIN				
STORE PLANE						
- MOD 2 - B						
ITS 1 & 9'	T	MASPL201		LIBNUM=12		
'# M						
ASMA 03-06						
JL2		MAIN				
STORE PLANE						
- MOD 2 - B						
ITS 2 & 10'	T	MASPL202		LIBNUM=12		
'# M						
ASMA 03-07						
JL2		MAIN				
STORE PLANE						
- MOD 2 - B						
ITS 3 & 11'	T	MASPL203		LIBNUM=12		
'# M						
ASMA 03-08						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 217

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 4 & 12'	T	MASPL204		LIBNUM=12		
'#	M					
ASMA 03-09						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 5 & 13'	T	MASPL205		LIBNUM=12		
'#	M					
ASMA 03-10						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 6 & 14'	T	MASPL206		LIBNUM=12		
'#	M					
ASMA 03-11						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 7 & 15'	T	MASPL207		LIBNUM=12		
'#	M					
ASMA 03-17						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS PL&PH'	T	MASPL208		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-12						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 16&20'	T	MASPL209		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-13						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 17&21'	T	MASPL210		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-14						
JL2		MAIN				
		STORE PLANE				
		- MOD 2 - B				
ITS 18&22'	T	MASPL211		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-16						
JL2		MAIN				

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 218

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
STORE PLANE - MOD 2 - B						
ITS 19&23'	T	MASPL212		LIBNUM=12		
'#	M					
ASMA 03-31 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 0 & 8'	T	MASPL300		LIBNUM=12		
'#	M					
ASMA 03-29 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 1 & 9'	T	MASPL301		LIBNUM=12		
'#	M					
ASMA 03-28 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 2 & 10'	T	MASPL302		LIBNUM=12		
'#	M					
ASMA 03-27 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 3 & 11'	T	MASPL303		LIBNUM=12		
'#	M					
ASMA 03-26 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 4 & 12'	T	MASPL304		LIBNUM=12		
'#	M					
ASMA 03-25 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 5 & 13'	T	MASPL305		LIBNUM=12		
'#	M					
ASMA 03-24 JL2 MAIN STORE PLANE - MOD 3 - B						
ITS 6 & 14'	T	MASPL306		LIBNUM=12		
'#	M					
ASMA 03-23 JL2 MAIN STORE PLANE						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 219

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
- MOD 3 - B						
ITS 7 & 15'	T	MASPL307		LIBNUM=12		
'#	M					
ASMA 03-18						
JL2	MAIN					
STORE PLANE						
- MOD 3 - B						
ITS PL&PH'	T	MASPL308		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-22						
JL2	MAIN					
STORE PLANE						
- MOD 3 - B						
ITS 16&20'	T	MASPL309		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-21						
JL2	MAIN					
STORE PLANE						
- MOD 3 - B						
ITS 17&21'	T	MASPL310		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-20						
JL2	MAIN					
STORE PLANE						
- MOD 3 - B						
ITS 18&22'	T	MASPL311		LIBNUM=12		
'# NOTE--1	M					
ASMA 03-19						
JL2	MAIN					
STORE PLANE						
- MOD 3 - B						
ITS 19&23'	T	MASPL312		LIBNUM=12		
'#	M					
ASMB 03-04						
JL2	MAIN					
STORE PLANE						
- MOD 4 - B						
ITS 0 & 8'	T	MASPL400		LIBNUM=12		
'#	M					
ASMB 03-05						
JL2	MAIN					
STORE PLANE						
- MOD 4 - B						
ITS 1 & 9'	T	MASPL401		LIBNUM=12		
'#	M					
ASMB 03-06						
JL2	MAIN					
STORE PLANE						
- MOD 4 - B						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 220

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES
ITS 2 810'	T	MASPL402		LIBNUM=12
'#	M			
ASMB 03-07				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 3 811'	T	MASPL403		LIBNUM=12
'#	M			
ASMB 03-08				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 4 812'	T	MASPL404		LIBNUM=12
'#	M			
ASMB 03-09				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 5 813'	T	MASPL405		LIBNUM=12
'#	M			
ASMB 03-10				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 6 814'	T	MASPL406		LIBNUM=12
'#	M			
ASMB 03-11				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 7 815'	T	MASPL407		LIBNUM=12
'#	M			
ASMB 03-17				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS PL&PH'	T	MASPL408		LIBNUM=12
'# NOTE--1	M			
ASMB 03-12				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 16820'	T	MASPL409		LIBNUM=12
'# NOTE--1	M			
ASMB 03-13				
JL2	MAIN			
STORE PLANE				
- MOD 4 - B				
ITS 17821'	T	MASPL410		LIBNUM=12

STORE CONTROLLER DATA REGISTER TEST

PR-10935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 221

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
'# NOTE--1 M						
ASMB 03-14						
JL2 MAIN						
STORE PLANE						
- MOD 4 - B						
ITS 18&22'	T	MASPL411		LIBNUM=12		
'# NOTE--1 M						
ASMB 03-16						
JL2 MAIN						
STORE PLANE						
- MOD 4 - B						
ITS 19&23'	T	MASPL412		LIBNUM=12		
'# M						
ASMB 03-31						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 0 & 8'	T	MASPL500		LIBNUM=12		
'# M						
ASMB 03-29						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 1 & 9'	T	MASPL501		LIBNUM=12		
'# M						
ASMB 03-28						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 2 & 10'	T	MASPL502		LIBNUM=12		
'# M						
ASMB 03-27						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 3 & 11'	T	MASPL503		LIBNUM=12		
'# M						
ASMB 03-26						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 4 & 12'	T	MASPL504		LIBNUM=12		
'# M						
ASMB 03-25						
JL2 MAIN						
STORE PLANE						
- MOD 5 - B						
ITS 5 & 13'	T	MASPL505		LIBNUM=12		
'# M						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 222

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ASMB 03-24						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 6 814'	T	MASPL506		LIBNUM=12		
'#	M					
ASMB 03-23						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 7 815'	T	MASPL507		LIBNUM=12		
'#	M					
ASMB 03-18						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS PL&PH'	T	MASPL508		LIBNUM=12		
'# NOTE--1	M					
ASMB 03-22						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 16&20'	T	MASPL509		LIBNUM=12		
'# NOTE--1	M					
ASMB 03-21						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 17&21'	T	MASPL510		LIBNUM=12		
'# NOTE--1	M					
ASMB 03-20						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 18&22'	T	MASPL511		LIBNUM=12		
'# NOTE--1	M					
ASMB 03-19						
JL2		MAIN				
STORE PLANE						
- MOD 5 - B						
ITS 19&23'	T	MASPL512		LIBNUM=12		
'#	M					
ASMC 03-04						
JL2		MAIN				
STORE PLANE						
- MOD 6 - B						
ITS 0 & 8'	T	MASPL600		LIBNUM=12		
'#	M					
ASMC 03-05						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 223

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 1 & 9'	T	MASPL601		LIBNUM=12		
'#	M					
ASMC 03-06						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 2 & 10'	T	MASPL602		LIBNUM=12		
'#	M					
ASMC 03-07						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 3 & 11'	T	MASPL603		LIBNUM=12		
'#	M					
ASMC 03-08						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 4 & 12'	T	MASPL604		LIBNUM=12		
'#	M					
ASMC 03-09						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 5 & 13'	T	MASPL605		LIBNUM=12		
'#	M					
ASMC 03-10						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 6 & 14'	T	MASPL606		LIBNUM=12		
'#	M					
ASMC 03-11						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS 7 & 15'	T	MASPL607		LIBNUM=12		
'#	M					
ASMC 03-17						
JL2		MAIN				
		STORE PLANE				
		- MOD 6 - B				
ITS PL&PH'	T	MASPL608		LIBNUM=12		
'# NOTE--1	M					
ASMC 03-12						
JL2		MAIN				

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 224

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W770
STORE PLANE - MOD 6 -> B						
ITS 16820'	T	MASPL609		LIBNUM=12		
'# NOTE--1 M						
ASMC 03-13						
JL2 MAIN						
STORE PLANE - MOD 6 - B						
ITS 17821'	T	MASPL610		LIBNUM=12		
'# NOTE--1 M						
ASMC 03-14						
JL2 MAIN						
STORE PLANE - MOD 6 - B						
ITS 18822'	T	MASPL611		LIBNUM=12		
'# NOTE--1 M						
ASMC 03-16						
JL2 MAIN						
STORE PLANE - MOD 6 - B						
ITS 19823'	T	MASPL612		LIBNUM=12		
'# M						
ASMC 03-31						
JL2 MAIN						
STORE PLANE - MOD 7 - B						
ITS 0 & 8'	T	MASPL700		LIBNUM=12		
'# M						
ASMC 03-29						
JL2 MAIN						
STORE PLANE - MOD 7 - B						
ITS 1 & 9'	T	MASPL701		LIBNUM=12		
'# M						
ASMC 03-28						
JL2 MAIN						
STORE PLANE - MOD 7 - B						
ITS 2 & 10'	T	MASPL702		LIBNUM=12		
'# M						
ASMC 03-27						
JL2 MAIN						
STORE PLANE - MOD 7 - B						
ITS 3 & 11'	T	MASPL703		LIBNUM=12		
'# M						
ASMC 03-26						
JL2 MAIN						
STORE PLANE						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 225

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
- MOD 7 - B						
ITS 4 812'	T	MASPL704		LIBNUM=12		
'#	M					
ASMC 03-25						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 5 813'	T	MASPL705		LIBNUM=12		
'#	M					
ASMC 03-24						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 6 814'	T	MASPL706		LIBNUM=12		
'#	M					
ASMC 03-23						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 7 815'	T	MASPL707		LIBNUM=12		
'#	M					
ASMC 03-18						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS PL8PH'	T	MASPL708		LIBNUM=12		
'# NOTE--1	M					
ASMC 03-22						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 16820'	T	MASPL709		LIBNUM=12		
'# NOTE--1	M					
ASMC 03-21						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 17821'	T	MASPL710		LIBNUM=12		
'# NOTE--1	M					
ASMC 03-20						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						
ITS 18822'	T	MASPL711		LIBNUM=12		
'# NOTE--1	M					
ASMC 03-19						
JL2	MAIN					
STORE PLANE						
- MOD 7 - B						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 226

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
ITS 19823'	T	MASPL712		LIBNUM=12		
TDATA,	V	MATYPE	5-02	33-02		
3 R	MCHB			LIBNUM=14	29-22, 29-26, 29-28, 29-48, 30-17, 31-30, 33-32, 33-36, 33-38, 34-08, 34-26,	
0000231 A	MCHBXF			35-37, 37-45, 40-19, 47-31, 47-33, 49-14, 54-04, 71-24, 92-49, 93-42, 94-08		
0000231 A	MCHBXT			LIBNUM=14	26-20, 38-02, 40-25, 43-20, 49-20, 54-10, 62-20, 76-20, 88-37, 88-43, 89-13,	
0 R	MCHTR			89-44, 92-01, 92-12		
0026472 A	MCHTRXMCBXX			LIBNUM=14	29-39, 33-48, 37-36, 39-20, 40-10, 47-19, 49-05, 50-34, 52-41, 53-44, 57-28,	
0000164 A	MCSXF			66-28, 70-25, 72-22, 80-28, 84-28, 90-05, 93-33		
'#	P			LIBNUM=14	29-40, 33-49, 37-37, 37-47, 38-03, 40-11, 40-21, 40-26, 49-06, 49-16, 49-21,	
ROC 02-35				53-45, 54-06, 54-11, 91-46, 92-02, 92-08, 92-13, 92-24, 93-34		
FA1016	MICR			LIBNUM=14	92-46	
O CONTROL 1'	T	MC1		LIBNUM=14	70-25	
'#	P					
ROC 02-34						
FA1017	MICR					
O CONTROL 2'	T	MC2		LIBNUM=12		
'#	P					
ROC 06-35						
FA1021	MICR					
O CONTROL 6'	T	MC6		LIBNUM=12		
'#	P					
ROC 02-31						
FA1023	MICR					
O CONTROL 8'	T	MC8		LIBNUM=12		
CDGSIO	V	MDSELMSK	7-20			
14 P	MEMADDR		8-25			
14 R	MEMADDR0		8-23			
15 R	MEMADDR1		8-24			
CDGSBS	V	MEMOP	7-14	29-16, 29-21, 33-26, 33-31, 38-15, 45-48, 46-08, 48-44, 49-30, 50-03, 55-47, 56-09, 56-16,		
				58-30, 58-41, 58-48, 64-41, 65-10, 65-16, 68-48, 69-07, 70-03, 70-14, 78-43, 78-49, 79-10,		
				79-16, 82-43, 82-49, 83-10, 83-16, 86-39, 87-06		
CDGSBS	V	MEMOPI	7-15	86-45, 87-12		

t.-(18
 !s*)
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 :#a'=" ab
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 + jklmnopqr"

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 227

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
<pre> #?@-~stuvw xyz+L[>#012 3456789+~]# -CABCDEFGHI JJKLMNO PQR \ S TUVWXYZ 0123456789 </pre>						
	T	MICODETR		LIBNUM=14		
06 0032213	L	MICROCK	91-35	91-33		
06 0032346	L	MICROXT	94-34	93-49		
	7	R MNRNDX	8-20			
'SER+0'	T	MODER	W=1 S=5 N=0	CL=2		
CDGSIO	V	MODSIZE	7-21	32-34		
0000000	A	MOD0	17-27			
0000001	A	MOD1	17-28			
0000002	A	MOD2	17-29			
0000003	A	MOD3	17-30			
0000004	A	MOD4	17-31			
0000005	A	MOD5	17-32			
0000006	A	MOD6	17-33			
0000007	A	MOD7	17-34			
'SER+0'	T	MPXER	W=1 S=1 N=0	CL=2		
0000215	A	MSXF	LIBNUM=14	37-36, 40-10, 49-05, 53-44		
0000215	A	MSXT	LIBNUM=14	26-20, 38-02, 40-25, 43-20, 49-20, 54-10, 62-20, 76-20		
0006071	A	MSCITNS	21-35			
'INITIALIZE						
STORE CONTR						
LLER W/O FOR						
CE SELECT'						
	T	MSCITNST				
06 0030517	L	MASKBTS	47-30	47-28		
'MASCIOSC+0'	T	MSSC0	W=20 S=0 N=0	CL=2		
'MASCIOSC+2'	T	MSSC1	W=20 S=0 N=2	CL=2		
'MASCIOSC+4'	T	MSSC2	W=20 S=0 N=4	CL=2		
'MASCIOSC+6'	T	MSSC3	W=20 S=0 N=6	CL=2		
'#	P					
ROC 02-21						
FA1029 MAIN						
STORE CONTR						
OL 0'	T	MSTCONTO		LIBNUM=12		
0000321	A	MSTOP		LIBNUM=14	93-30	
0106263	A	MTRCD	18-24			
0106203	A	MTCWR	18-26			
CDGNM	V	NABT	3-31			
CDGNM	V	NADD OFF	3-22			
CDGNM	V	NADMIC	3-23			
CDGNM	V	NBEGIN	3-28			
CDGNM	V	NBGN	3-39	24-14, 42-14, 61-14, 75-14		
CDGNM	V	NCLRMS	3-33			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 228

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
CDGNM	V	NCOMPARE	3-26	30-35, 31-04, 31-16, 31-39, 34-43, 35-11, 35-23, 35-46, 38-24, 38-36, 38-48, 39-34, 46-20, 46-32, 46-44, 47-42, 51-04, 51-32, 52-12, 53-17, 56-25, 56-40, 57-07, 57-46, 59-08, 65-25, 65-40, 66-07, 66-42, 69-16, 70-34, 71-34, 72-04, 72-31, 72-46, 79-25, 79-40, 80-07, 80-42, 83-25, 83-40, 84-07, 84-42, 87-21, 90-14, 94-17		
CDGNM	V	NDISABA	3-07			
CDGNM	V	NDISABB	3-08			
CDGNM	V	NEXECUTE	3-25			
CDGNM	V	NFAILTST	3-16	31-10, 31-22, 35-17, 35-29, 38-42, 39-05, 46-38, 47-04, 51-46, 52-26, 56-46, 57-13, 59-14, 65-46, 66-13, 70-40, 71-40, 72-10, 72-37, 73-04, 79-46, 80-13, 83-46, 84-13, 87-27, 90-20, 94-23		
CDGNM	V	NFLBITS	3-37	30-41, 31-45, 35-04, 36-04, 38-30, 39-40, 46-26, 47-48, 51-18, 53-31, 56-31, 58-04, 65-31, 66-48, 69-22, 79-31, 80-48, 83-31, 84-48		
CDGNM	V	NFRZ	3-27			
0000001	A	NLDBS	163-26	65-32, 66-49		
0000021	A	NLDBSPR	169-01	65-47, 66-14, 169-01		
0000001	A	NLDBS00	163-26			
0000002	A	NLDBS01	164-01			
0000003	A	NLDBS02	164-12			
0000004	A	NLDBS03	164-23			
0000005	A	NLDBS04	165-01			
0000006	A	NLDBS05	165-12			
0000007	A	NLDBS06	165-23			
0000010	A	NLDBS07	166-01			
0000011	A	NLDBS08	166-12			
0000012	A	NLDBS09	166-23			
0000013	A	NLDBS10	167-01			
0000014	A	NLDBS11	167-12			
0000015	A	NLDBS12	167-23			
0000016	A	NLDBS13	168-01			
0000017	A	NLDBS14	168-12			
0000020	A	NLDBS15	168-23			
0000022	A	NLDBS16	169-12			
0000023	A	NLDBS17	169-23			
0000024	A	NLDBS18	170-01			
0000025	A	NLDBS19	170-12			
0000026	A	NLDBS20	170-23			
0000027	A	NLDBS21	171-01			
0000030	A	NLDBS22	171-12			
0000031	A	NLDBS23	171-23			
CDGNM	V	NLDMIR	3-24			
0000032	A	NLDSA	172-01	69-23		
0000056	A	NLDSAPH	178-25	71-41, 178-25		
0000052	A	NLDSAPL	177-12	72-38, 177-12		
0000032	A	NLDSA00	172-01			
0000033	A	NLDSA01	172-12			
0000034	A	NLDSA02	172-23			
0000035	A	NLDSA03	173-01			
0000036	A	NLDSA04	173-12			
0000037	A	NLDSA05	173-23			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 229

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
0000040	A	NLDSA06	174-01			
0000041	A	NLDSA07	174-12			
0000042	A	NLDSA08	174-23			
0000043	A	NLDSA09	175-01			
0000044	A	NLDSA10	175-12			
0000045	A	NLDSA11	175-23			
0000046	A	NLDSA12	176-01			
0000047	A	NLDSA13	176-12			
0000050	A	NLDSA14	176-23			
0000051	A	NLDSA15	177-01			
0000053	A	NLDSA16	177-23	73-05, 177-23		
0000054	A	NLDSA17	178-01	72-11, 178-01		
0000055	A	NLDSA89	178-12	70-41, 178-12		
CDGNM	V	NLP_END	3-29			
CDGNM	V	NMASK	3-18	39-28, 53-03, 57-36, 66-36, 80-36, 84-36		
CDGNM	V	NMICRO	3-02	26-19, 39-19, 43-19, 47-18, 50-33, 52-40, 57-27, 62-19, 66-27, 70-24, 72-21, 76-19, 80-27, 84-27, 88-36, 88-42, 88-48, 89-12, 89-18, 89-24, 89-31, 89-43, 90-01		
0106260	A	NMLRD	18-28			
0106200	A	NMLWR	18-30	29-12, 33-10		
CDGNM	V	NNO3CD	3-34	26-24, 28-43, 37-23, 40-06, 43-24, 45-26, 47-27, 48-16, 53-40, 55-29, 58-12, 59-23, 62-24, 64-28, 68-26, 69-30, 71-03, 73-14, 76-24, 78-27, 82-27, 86-23, 91-32		
0000360	A	NOPXF		LIBNUM=14		
06 0032243	L	NOPLD	92-21	92-23		
06 0032256	L	NOPST	92-36	92-31		
CDGNM	V	NOSTPMES	7-10			
CDGNM	V	NPASSTST	3-19			
CDGNM	V	NPROS	3-14			
CDGNM	V	NRDMICRO	3-32			
CDGNM	V	NRESCLK	3-12			
CDGNM	V	NRETRN	3-03	39-24, 47-23, 50-38, 52-45, 57-32, 66-32, 70-29, 72-26, 80-32, 84-32, 90-09		
CDGNM	V	NRTNBLM	3-45	26-08, 43-08, 62-08, 76-08		
0006074	A	NSELA	20-49			
'RESET STORE						
SELECT (ISOL						
ATE) SIGNAL						
ON FA1071'	T	NSELAT				
CDGNM	V	NSEND	3-20	89-06, 89-37		
CDGNM	V	NSKIP	3-30			
CDGNM	V	NSTOP	3-06			
CDGNM	V	NSTPCLK	3-10			
CDGNM	V	NSTRT	3-05			
CDGNM	V	NSTRTCLK	3-09			
CDGNM	V	NTESTSEG	3-15	28-08, 37-08, 45-08, 55-08, 64-08, 68-08, 78-08, 82-08, 86-08, 88-08, 91-08		
CDGNM	V	NTGCLK	3-11			
CDGNM	V	NTMTEST	3-35			
CDGNM	V	NTTY	3-17	57-41		
0000000	A	NUM	23-05	41-14, 60-14, 74-14		
CDGNM	V	NWDTST	3-46	39-13, 47-12, 52-34, 57-21, 66-21, 80-21, 84-21		
5 R		NXTPAT	8-18			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 230

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W770
CDGNN	V	NZERO_ER	3-13			
CDGNN	V	NZERPT	3-36			
CDGNN	V	N2RETRN	3-04			
CDGNN	V	N2SEND	3-21	26-13, 43-13, 62-13, 76-13, 88-30		
0000200	A	OFLNCHNK	17-37			
0007760	A	OFLNDONE	17-38			
0000001	A	ONE	17-35			
0177777	A	ONES	17-36	29-04, 29-05, 32-25, 32-26, 48-37, 48-38, 56-02, 56-03, 58-34, 58-35, 65-03, 65-04, 82-36, 82-37, 83-03, 83-04		

```

<.(AON
  X$*);
-S/
X_>?
:#@'L" ab
cdefghi n$(*
+ jklmnopqr~
*)z=-stuvv
xyz+Lr[3e012
3456789+j_j]f
-[IBCLEFGHI
  >JKLMNO
PQR \ S
LLLWLYZ
0123456789

```

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W770
0001303	A	ORG_000001	26-02	43-02, 62-02, 76-02		
0000731	A	ORG_000147	48-07	48-11		
0000541	A	ORG_000167	32-46			
0000530	A	ORG_000174	32-11			
0000537	A	ORG_000215	32-36			
0000760	A	ORG_000227	49-32			
0001004	A	ORG_000303	50-41	50-45		
0001016	A	ORG_000310	51-22	51-26		
0001030	A	ORG_000314	52-02	52-06		
0001046	A	ORG_000331	53-07	53-11		
0001274	A	ORG_000361	74-02			
0000644	A	ORG_000475	41-02			
0001136	A	ORG_000570	60-02			
0001512	A	ORG_000571	95-02			
0000226	A	PAXF	7-36	26-29, 29-13, 29-19, 32-33, 38-12, 43-29, 45-44, 46-05, 48-40, 49-27, 49-49, 55-36, 58-19, 62-29, 64-37, 65-06, 65-13, 68-37, 68-49, 69-41, 70-04, 71-13, 76-29, 78-39, 78-46, 79-06,		
CTSD	V	PAGSCR				

W=1 S=0 N=0 CL=1 LIBNUM=14
LIBNUM=14 93-33

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 231

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
0000000	A	PARITY	26-10	79-13, 82-39, 82-46, 83-06, 83-13, 86-35, 86-42, 87-02, 87-09, 91-45, 93-01, 93-43		
10	R	PARSLT	8-22	43-10, 62-10, 76-10, 88-27, 89-03, 89-34		
3	R	PASBT	16-08	W=1 S=6 N=0 CL=1		
CDGNOF	V	PASSOFF	3-40			
CDGNM	V	PASSTST	7-11	41-09, 60-09, 74-09, 95-09		
16	P	PATADDR	8-26			
0	R	PATBAK	8-12			
6	R	PATNDX	8-19			
0000025	A	PH	16-04	LIBNUM=14 31-16, 35-23, 38-48, 46-44, 52-12, 57-07, 59-08, 66-07, 72-46, 80-07, 84-07		
3	R	PHBAK		W=1 S=1 N=0 CL=1		
0000024	A	PL	16-03	LIBNUM=14 31-04, 35-11, 38-36, 46-32, 51-32, 56-40, 65-40, 72-31, 79-40, 83-40		
3	R	PLBAK		W=1 S=0 N=0 CL=1		
'CDGSDF'	T	PNAME				
0000020	A	PRHOLD	17-45	30-09, 34-18, 45-46, 55-45, 58-28, 86-37		
0000005	A	PRHOL1	17-46	82-41, 83-08		
0000012	A	PRH1LO	17-47	78-41, 79-08		
0000017	A	PRH1L1	17-48	29-06, 32-27, 48-42, 69-06		
CBLM	V	PRTMASER	7-06			
0000002	A	PRWOH1LO	17-49			
0000007	A	PRWOH1L1	18-01			
0000010	A	PRW1HOLD	18-02			
0000015	A	PRW1HOL1	18-03			
'SER+0'	T	PWER		W=1 S=12 N=0 CL=2		
11	R	RANGE	8-07			
14	P	RA0		LIBNUM=3 8-25, 26-44, 29-13, 29-14, 29-19, 29-20, 30-29, 31-33, 34-37, 35-40, 36-17, 38-12, 38-13, 38-18, 43-44, 45-44, 45-45, 46-05, 46-06, 46-14, 47-36, 48-40, 48-41, 49-27, 49-28, 49-49, 50-01, 50-27, 54-17, 56-19, 59-02, 62-44, 64-37, 64-38, 65-06, 65-07, 65-13, 65-14, 65-19, 69-10, 70-17, 71-27, 76-44, 78-39, 78-40, 78-46, 78-47, 79-06, 79-07, 79-13, 79-14, 79-19, 82-39, 82-40, 82-46, 82-47, 83-06, 83-07, 83-13, 83-14, 83-19, 86-35, 86-36, 86-42, 86-43, 87-02, 87-03, 87-09, 87-10, 87-15, 91-45, 92-07, 93-01, 93-02, 93-43, 93-44, 94-11		
16	P	RA1		LIBNUM=3 8-26, 93-04		
0047410	A	RDAD	21-42			
0047414	A	RDADNC	21-45	68-35, 69-39		
'GATE STORE ADDRESS BITS TO STORE DATA BUS'	T	RDADNCT				
'GATE STORE ADDRESS BUS(SAO-16,PL) TO STORE DATA BUS(SDO-15,PL,PH) W/ CLOCK'	T	RDADT				
'CIOBUF+0'	T	REFST		W=1 S=3 N=0 CL=2		
0006070	A	REFSTOP	21-47			
'INHIBIT REF						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 232

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
RESHING OF STORE'	T	REFSTOPT				
0006060	A	REFSTRT	21-49	91-41		
'RESUME REGULAR REFRESHING OF STORE'	T	REFSTRTT				
0046060	A	RFWISOL	22-02			
'RESUME REGULAR REFRESH W/ STORE ISOLATED'	T	RFWISOLT				
CSYSUB	V	RGCHKADR	7-32			
'BASE ADDRESS OF STORE/MOD UNDER TEST'	T	RMK_BASADDR				
'ERROR INDICATED BY STORE COMMAND CHECKER'	T	RMK_CHDIF				
'MAIN STORE CONTROLLER INITIALIZATION'	T	RMK_CINIT				
'CONTROLLER CLOCK HALT'	T	RMK_CKHLT				
'CONTROLLER CLOCK STEP'	T	RMK_CKSTP				
'	T	RMK_CONTROLBITS				
'RESULT OF STORE CONTROL LEADS ISOLATION'	T	RMK_CTRLCK				
'ERROR IN PARITY OVER HIGH DATA BITS'	T	RMK_DHPER				
'ERROR IN PARITY OVER LOW DATA BITS'	T	RMK_DLPER				
'FORCE BIT 15 ACTIVE IN MAIN SHIFTER'	T	RMK_FBT09				
'NUMBER OF MEMORY MODULES (OR FANOUT BOARDS) IN						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET · SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 233

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES
STORE CURRENTLY UNDER TEST	T	RMK_FOBNUM		
' PARITY ERROR DETECTED IN ADDRESS READING FROM BOARD(S)	T	RMK_FOBPARER		
' STORE TIMING ERROR FROM SELECTED BOARD	T	RMK_FOBTERA		
' STORE TIMING ERROR FROM UNSELECTED BOARD	T	RMK_FOBTERB		
' STORE SELECT FORCE ON TIMING BOARD A	T	RMK_FSEL		
' I/O ORDER CHECKER EXCLUSIVE-OR RESULT	T	RMK_FXORG		
' I/O SUBCHANNEL ADDRESS FOR CURRENT STORE CONTROLLER	T	RMK_IOADDR		
' SDO-6, SDB-14 ISOLATE TEST RESULTS	T	RMK_ISOBTS		
' ORDER CHECKER EXCLUSIVE-OR LOAD	T	RMK_LDFG		
' ADDRESS OF SUBSTITUTE FOR 'LOOP' IN N TTY TROUBLE MESSAGE	T	RMK_LOOPSUB		
' ALL-SEEMS-WELL SIGNAL NOT RECEIVED FROM MODULE (FOB) ACCESS DURING NORMAL CYCLE	T	RMK_MODER		
' MULTIPLEXOR ERROR DETECTED				

CDGSDF

W77D

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF

ISSUE 05 PAGE 234

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDf	W77D
CTED BY CHEC K BOARD'	T	RMK_MPXER				
' ADDRESS O F MY STORE S	T	RMK_MSSCO				
' ADDRESS OF MY STORE SU	T	RMK_MSSC1				
BCHANNEL 1' ' ADDRESS OF MY STORE SU	T	RMK_MSSC2				
BCHANNEL 2' ' ADDRESS OF MY STORE SU	T	RMK_MSSC3				
BCHANNEL 3' ' REPEAT PAS S THRU TEST INDICATOR'	T	RMK_PASBT				
' PARITY HIG H VALUE READ BACK'	T	RMK_PHBAK				
' PARITY LOW VALUE READ BACK'	T	RMK_PLBAK				
' ERROR IN P ARITY FOR WI DE STORE'	T	RMK_PWER				
' STORE REFR ESH STOP'	T	RMK_REFST				
' ALL MEMORY MODULES(FOB S) NOT SELEC TED DURING R EFRESH'	T	RMK_RSELER				
' REFRESH SE LECTS FROM F ANOUT BOARDS 0-6'	T	RMK_RSELO6				
' REFRESH SE LECT FROM FA NOUT BOARD 7'	T	RMK_RSEL7				
' SD7,SD15 I SOLATE TEST RESULT - ONL Y GATED INTO BY LOAD I/O DECODER CHE CK (LOADCHK) OPTION'	T	RMK_RSVD0				
' RESERVED -						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 235

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
FOBTERA IS SET AFTER IN IT'	T	RMK_RSVD1				
' STORE ADDR ESS PARITY H IGH LOOP RES ULT'	T	RMK_SAPHLP				
' STORE ADDR ESS BIT 17 L OOP RESULT'	T	RMK_SA17LP				
' PREMATURE STORE COMPLE TE(SCM)'	T	RMK_SCHER				
' RESULT OF STORE DATA P ARITY ISOLAT ION'	T	RMK_SDPCK				
' ERROR IN M AIN SHIFTER OPERATION'	T	RMK_SHER				
' SINGLE BIT ERROR FLAG'	T	RMK_SNGBTER				
' '	T	RMK_SPARE				
' 4/8 MAINTENANCE STATE CODE'	T	RMK_STORDER				
' STORE SELECT ADDRESS BITS LOOP RES ULT'	T	RMK_STRSLP				
' ERROR DETECTED ON TIMING BOARD'	T	RMK_TBER				
' SAVE TIMING CONSTANT FOR TIMEOUT SUBROUTINE'	T	RMK_TIMSAV				
' DIAGNOSTIC TEST NUMBER REQUESTED'	T	RMK_TSTNUM				
' ADDRESS OF NEXT BLOCK OF MEMORY TO BE UPDATED AFTER MEMORY PATTERN TEST'	T	RMK_UPADDR				
' RESULTS OF WIDE DATA(S						

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 236

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
D16-23) ISOL						
'ATE'	T	RMK_WDISOL				
' ADDRESS OF OTHER STORE SUBCHANNEL						
0'	T	RMK_OSSCO				
' ADDRESS OF OTHER STORE SUBCHANNEL						
1'	T	RMK_OSSC1				
' ADDRESS OF OTHER STORE SUBCHANNEL						
2'	T	RMK_OSSC2				
' ADDRESS OF OTHER STORE SUBCHANNEL						
3'	T	RMK_OSSC3				
' WIDE STORE INDICATOR B						
IT'	T	RMK_DWDSTRO				
' 0-10 BIT TIMING SHIFTER FAKE'						
' RESULT OF EXTENDED(3 BIT) PARITY ISOLATE'	T	RMK_2EPCK				
0010177	A	RSELBTS	15-22	15-23		
0167600	A	RSELBTSN	15-23			
'SER+0'	T	RSELER		W=1 S=6 N=0 CL=2		
'RSLCHK+0'	T	RSELO6		W=7 S=0 N=0 CL=2	15-22	
'RSLCHK+0'	T	RSEL7		W=1 S=14 N=0 CL=2		15-22
06 0030014	L	RSFLTA	30-16	29-24, 29-32		
06 0030157	L	RSFLTB	34-25	33-34, 33-42		
'CHKWRD+0'	T	RSVD0		W=1 S=7 N=0 CL=2		
'RSLCHK+0'	T	RSVD1		W=1 S=7 N=0 CL=2		
06 0030043	L	RSWDF A	31-29	30-02		
06 0030206	L	RSWDF B	35-36	34-11		
0047214	A	RTNADHI	22-04			
'LOAD SA17 & SAPH INTO SER'	T	RTNADHIT				
'DGSDF'	T	RTNAME				
0000261	A	RTNMCHB				
O R RO						
			LIBNUM=14	29-45, 34-05, 37-42, 40-16, 49-11, 54-01, 93-39		
			LIBNUM=3	8-12, 26-44, 29-25, 29-27, 29-29, 29-31, 29-36, 29-37, 29-39, 29-40, 29-45, 30-29, 31-33, 32-46, 32-48, 33-03, 33-35, 33-37, 33-39, 33-41, 33-45, 33-46, 33-48, 33-49, 34-05, 34-37, 35-40, 36-17, 37-36, 37-37, 37-42, 38-02, 38-03, 38-18, 40-10, 40-11, 40-16, 40-25, 40-26, 43-44, 46-14, 47-36, 49-05, 49-06, 49-11, 49-20, 49-21, 50-27, 53-44, 53-45,		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 237

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
				54-01, 54-10, 54-11, 54-17, 56-19, 59-02, 62-44, 65-19, 69-10, 70-17, 71-19, 71-20, 71-22, 71-27, 76-44, 79-19, 83-19, 87-15, 91-45, 91-46, 92-01, 92-02, 92-07, 92-08, 92-12, 92-13, 92-23, 92-24, 92-25, 92-26, 92-28, 92-30, 92-33, 92-49, 93-07, 93-16, 93-17, 93-30, 93-33, 93-34, 93-39, 93-43, 93-45, 94-11		
0000123	A	ROXF		LIBNUM=14 30-21, 34-30, 92-42		
1	R	R1		LIBNUM=3 8-13, 33-02, 33-03, 71-13, 71-14, 71-15, 71-18, 92-37, 92-38, 93-44, 93-47, 94-06		
0000125	A	R1XT		LIBNUM=14 8-14		
13	R	R11		LIBNUM=3 71-22, 71-24		
14	R	R12		LIBNUM=3 8-23, 32-33, 32-46, 33-07, 33-19, 33-29, 55-36, 55-43, 56-05, 56-13, 58-19, 58-26, 58-37, 58-45, 68-37, 68-44, 68-49, 69-04, 69-41, 69-48, 70-04, 70-05, 70-11		
0000107	A	R12XT		LIBNUM=14 92-01		
15	R	R13		LIBNUM=3 8-24, 32-34, 33-24, 33-30, 55-37, 55-44, 55-48, 56-06, 56-10, 56-14, 58-20, 58-27, 58-31, 58-38, 58-42, 58-46, 68-38, 68-45, 69-01, 69-05, 69-42, 69-49, 70-06, 70-12		
0000113	A	R13XT		LIBNUM=14 92-12		
16	R	R14		LIBNUM=3 29-04, 29-23, 30-05, 30-07, 32-25, 33-33, 34-14, 34-16, 45-41, 48-37, 55-40, 56-02, 58-23, 58-34, 64-34, 65-03, 68-41, 69-45, 78-36, 79-03, 82-36, 83-03, 86-32, 86-48, 93-01		
17	R	R15		LIBNUM=3 29-05, 29-49, 30-01, 30-08, 32-26, 34-09, 34-10, 34-17, 45-42, 48-38, 55-41, 56-03, 58-24, 58-35, 64-35, 65-04, 68-42, 69-46, 78-37, 79-04, 82-37, 83-04, 86-33, 86-49, 93-02		
2	R	R2		LIBNUM=3 8-15, 29-13, 29-19, 33-19, 33-29, 38-12, 45-44, 46-05, 48-40, 49-27, 49-49, 55-43, 56-05, 56-13, 58-26, 58-37, 58-45, 64-37, 65-06, 65-13, 68-44, 69-04, 69-48, 70-11, 78-39, 78-46, 79-06, 79-13, 82-39, 82-46, 83-06, 83-13, 86-35, 86-42, 87-02, 87-09, 93-45		
3	R	R3		LIBNUM=3 8-16, 29-14, 29-20, 29-23, 29-49, 30-01, 33-24, 33-30, 33-33, 34-09, 34-10, 37-46, 38-13, 40-20, 45-45, 46-06, 47-32, 48-41, 49-15, 49-28, 50-01, 54-05, 55-44, 56-06, 56-14, 58-27, 58-38, 58-46, 64-38, 65-07, 65-14, 68-45, 69-05, 69-49, 70-12, 78-40, 78-47, 79-07, 79-14, 82-40, 82-47, 83-07, 83-14, 86-36, 86-43, 87-03, 87-10, 93-47, 94-06		
4	R	R4		LIBNUM=3 8-17, 26-28, 26-29, 43-28, 43-29, 62-28, 62-29, 76-28, 76-29		
5	R	R5		LIBNUM=3 8-18		
6	R	R6		LIBNUM=3 8-19, 29-06, 29-30, 29-31, 30-09, 32-27, 33-40, 33-41, 34-18, 38-14, 45-46, 46-07, 48-42, 49-29, 50-02, 55-45, 56-07, 56-15, 58-28, 58-39, 58-47, 64-39, 65-08, 65-15, 68-46, 69-06, 70-01, 70-13, 78-41, 78-48, 79-08, 79-15, 82-41, 82-48, 83-08, 83-15, 86-37, 86-44, 87-04, 87-11		
7	R	R7		LIBNUM=3 8-20, 29-12, 29-18, 33-10, 33-28, 38-11, 45-40, 46-04, 48-36, 49-26, 49-48, 55-39, 56-01, 56-12, 58-22, 58-33, 58-44, 64-33, 65-02, 65-12, 68-40, 69-03, 69-44, 70-10, 78-35, 78-45, 79-02, 79-12, 82-35, 82-45, 83-02, 83-12, 86-31, 86-41, 86-47, 87-08		
10	R	R8		LIBNUM=3 8-21, 8-?		
CTVTAB	V	SXMCH		LIBNUM=10 29-47, 34-07, 37-44, 40-18, 49-13, 54-03, 93-32, 93-41		
14	P	SXMCHX_XCSYS				
		UB		LIBNUM=10		
06	0031520	L	SANLDCKH	71-30 71-27		
06	0031337	L	SANLDCKL	68-29 68-27		
06	0031414	L	SANLDCKM	69-33 69-31		
06	0031473	L	SANLDCNT	71-06 71-04		
06	0031546	L	SANLDXT	73-17 73-15		
'RSLCHK+0'	T	SAPHLP		W=1 S=10 N=0 CL=2 71-20, 71-34		
0000225	A	SARXT		LIBNUM=14 88-37		
0000303	A	SARDXT		LIBNUM=14 89-25		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 238

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDf	W77D
'RSLCHK+0'	T	SA17LP		W=1 S=11 N=0 CL=2		72-04
06 0032255	L	SBNOP	92-32	92-29		
'SER+0'	T	SCMER		W=1 S=3 N=0 CL=2		
06 0031615	L	SCWCKO	78-30	78-28		
06 0031732	L	SCWCKOXT	81-05	80-22		
06 0031736	L	SCWCK1	82-30	82-28		
06 0032053	L	SCWCK1XT	85-05	84-22		
06 0031225	L	SDNLDCK	64-31	64-29		
06 0031333	L	SDNLDXT	67-05	66-22		
'CHKWRD+0'	T	SDPCK		W=1 S=14 N=0 CL=2		
0015471	A	SDRX		LIBNUM=14		88-49
0000056	A	SDRXF		LIBNUM=1A		50-34, 72-22
0000056	A	SDRXT		LIBNUM=14		88-43
0000055	A	SDR1XF		LIBNUM=14		29-39, 33-48, 39-20, 47-19, 52-41, 57-28, 66-28, 80-28, 84-28
0000034	A	SELSTK	146-01	56-32, 58-05		
0000066	A	SELSTKEP	163-10	59-15, 163-10		
0000055	A	SELSTKPH	157-10	57-14, 157-10		
0000054	A	SELSTKPL	156-30	56-47, 156-30		
0000034	A	SELSTK00	146-01			
0000035	A	SELSTK01	146-31			
0000036	A	SELSTK02	147-11			
0000037	A	SELSTK03	148-01			
0000040	A	SELSTK04	148-30			
0000041	A	SELSTK05	149-10			
0000042	A	SELSTK06	150-01			
0000043	A	SELSTK07	150-31			
0000044	A	SELSTK08	151-11			
0000045	A	SELSTK09	152-01			
0000046	A	SELSTK10	152-30			
0000047	A	SELSTK11	153-10			
0000050	A	SELSTK12	154-01			
0000051	A	SELSTK13	154-30			
0000052	A	SELSTK14	155-10			
0000053	A	SELSTK15	156-01			
0000056	A	SELSTK16	158-01			
0000057	A	SELSTK17	158-30			
0000060	A	SELSTK18	159-10			
0000061	A	SELSTK19	160-01			
0000062	A	SELSTK20	160-30			
0000063	A	SELSTK21	161-10			
0000064	A	SELSTK22	162-01			
0000065	A	SELSTK23	162-30			
0001524	A	SERNO_047336	96-02			
0001530	A	SERNO_049161	96-16			
0001534	A	SERNO_050373	96-28			
0002115	A	SERNO_052302	191-28			
0002121	A	SERNO_053543	192-02			
0047114	A	SERREAD	22-09	71-12		

'READ BACK S

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 239

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDf	W77D
TORE ERROR R						
EGISTER(SER)'T SERREADT						
'SER+0'	T	SHER		W=1 S=4 N=0 CL=2		
0000036	A	SIRXF		LIBNUM=14 90-05		
0000036	A	SIRXT		LIBNUM=14 89-44		
CSYSUB	V	SMDMCHB	37-49	38-01, 40-23, 40-24, 49-18, 49-19, 54-08, 54-09, 91-48, 91-49, 92-10, 92-11		
CDGNM	V	SMDMCHBF	3-42			
CSYSUB	V	SMDMIRL	29-42	29-43, 34-02, 34-03, 37-39, 37-40, 38-05, 38-06, 40-13, 40-14, 40-28, 40-29, 49-08, 49-09, 49-23, 49-24, 53-17, 53-48, 54-13, 54-14, 92-04, 92-05, 92-15, 92-16, 93-36, 93-37		
CDGNM	V	SMDMIRLF	3-43			
CSYSUB	V	SMCH	29-46	34-06, 37-43, 40-17, 49-12, 54-02, 93-31, 93-40		
CDGNM	V	SMCHF	3-44			
	3	R SNGBTER	16-07	W=1 S=5 N=0 CL=1		
'DGREQLO+0'	T	SPARE		W=2 S=7 N=0 CL=2		
0044714	A	SPAREA	22-07			
'SPARE I/O 0						
RDER'						
0106274	A	SPRRD	18-32			
0106214	A	SPRWR	18-34			
'MXAS'						
	17	R SS_R		LIBNUM=14 93-17		
	15	R SS_S		LIBNUM=14 92-38		
0000065	A	STDF1		LIBNUM=15 23-05		
0000066	A	STDF2		LIBNUM=15 41-14		
0000067	A	STDF3		LIBNUM=15 60-14		
0000070	A	STDF4		LIBNUM=15 74-14		
0000002	A	STEPCLK	22-45			
0000002	A	STKSDA	97-18	30-42, 31-46		
0000022	A	STKSDAPR	108-12	31-11, 31-23, 108-12		
0000002	A	STKSDA00	97-18			
0000003	A	STKSDA01	98-11			
0000004	A	STKSDA02	99-01			
0000005	A	STKSDA03	99-31			
0000006	A	STKSDA04	100-14			
0000007	A	STKSDA05	101-01			
0000010	A	STKSDA06	101-31			
0000011	A	STKSDA07	102-16			
0000012	A	STKSDA08	103-01			
0000013	A	STKSDA09	103-31			
0000014	A	STKSDA10	104-12			
0000015	A	STKSDA11	105-01			
0000016	A	STKSDA12	105-31			
0000017	A	STKSDA13	106-12			
0000020	A	STKSDA14	107-01			
0000021	A	STKSDA15	107-31			
0000023	A	STKSDA16	109-04			
0000024	A	STKSDA17	110-01			
0000025	A	STKSDA18	110-31			
0000026	A	STKSDA19	111-12			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDf ISSUE 05 PAGE 240

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES
0000027	A	STKSDA20	112-01	
0000030	A	STKSDA21	112-31	
0000031	A	STKSDA22	113-12	
0000032	A	STKSDA23	114-01	
0000033	A	STKSDB	114-31	35-05, 36-05
0000053	A	STKSDBPR	123-01	35-18, 35-30, 123-01
0000033	A	STKSDB00	114-31	
0000034	A	STKSDB01	115-12	
0000035	A	STKSDB02	116-01	
0000036	A	STKSDB03	116-23	
0000037	A	STKSDB04	117-01	
0000040	A	STKSDB05	117-23	
0000041	A	STKSDB06	118-01	
0000042	A	STKSDB07	118-23	
0000043	A	STKSDB08	119-01	
0000044	A	STKSDB09	119-23	
0000045	A	STKSDB10	120-01	
0000046	A	STKSDB11	120-23	
0000047	A	STKSDB12	121-01	
0000050	A	STKSDB13	121-23	
0000051	A	STKSDB14	122-01	
0000052	A	STKSDB15	122-23	
0000054	A	STKSDB16	123-28	
0000055	A	STKSDB17	124-01	
0000056	A	STKSDB18	124-23	
0000057	A	STKSDB19	125-01	
0000060	A	STKSDB20	125-23	
0000061	A	STKSDB21	126-01	
0000062	A	STKSDB22	126-23	
0000063	A	STKSDB23	127-01	
0000010	A	STOPREF	22-47	
'CIOBUF+0'	T	STORDER		W=10 S=4 N=0 CL=2
TDATA	V	STRLIM	7-41	29-36, 33-45
CDGSIO	V	STRSIZE	7-22	
'RSLCHK+0'	T	STRSLP		W=1 S=12 N=0 CL=2
3	R	STRSTAT	8-16	16-03, 16-04, 16-06, 16-07, 16-08
16	R	STR1S		W=1 S=17 N=0 CL=1 LIBNUM=14
0041634	A	STSBY	22-11	37-46, 40-20, 49-15, 54-05
0041630	A	STSBYCL	22-13	
'SET STORE B				
USY(SBY) W/				
CLOCK'	T	STSBYCLT		
'SET STORE B				
USY(SBY) SIG				
NAL'	T	STSBYT		
CDGSIO	V	STSELSK	7-23	
0000000	A	SUCCESS	30-32	30-35, 31-01, 31-04, 31-13, 31-16, 31-36, 31-39, 34-40, 34-43, 35-08, 35-11, 35-20, 35-23, 35-43, 35-46, 38-21, 38-24, 38-33, 38-36, 38-45, 38-48, 39-31, 39-34, 46-17, 46-20, 46-29, 46-32, 46-41, 46-44, 47-39, 47-42, 51-01, 51-04, 51-29, 51-32, 52-09, 52-12, 53-14, 53-17,

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 241

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
				56-22, 56-25, 56-37, 56-40, 57-04, 57-07, 57-43, 57-46, 59-05, 59-08, 65-22, 65-25, 65-37, 65-40, 66-04, 66-07, 66-39, 66-42, 69-13, 69-16, 70-31, 70-34, 71-31, 71-34, 72-01, 72-04, 72-28, 72-31, 72-43, 72-46, 79-22, 79-25, 79-37, 79-40, 80-04, 80-07, 80-39, 80-42, 83-22, 83-25, 83-37, 83-40, 84-04, 84-07, 84-39, 84-42, 87-18, 87-21, 90-11, 90-14, 94-14, 94-17		
0000000	A	SV REG				
'SER+0'	T	TBER		W=1 S=13 N=0 CL=2		
0000070	A	TESTNO	23-05	41-14, 60-14, 74-14, 97-01, 136-10, 163-24, 178-34		
0116774	L	TFAPCH2	2-26			
0000003	A	THREE	18-05			
'PAGSCR+27'	T	TIMSAV		W=20 S=0 N=33 CL=2		
0000065	A	TLMFRST	5-01	23-05, 97-01		
0600000	L	TPATCH	2-28			
0001534	A	TR_012592	96-28			
0001524	A	TR_012707	96-02			
0001530	A	TR_013267	96-16			
0000541	A	TR_12582	32-11	32-36, 32-46		
0001512	A	TR_12707	26-02	41-02, 43-02, 60-02, 62-02, 74-02, 76-02, 95-02		
0000727	A	TR_13267	48-07			
0002115	A	TR_780315	48-11	49-32, 50-41, 51-22, 52-02, 53-07, 191-28		
0002121	A	TR_790350	50-45	51-26, 52-06, 53-11, 192-02		
06 0032346	L	TRORG	26-02	32-11, 32-36, 41-02, 43-02, 48-07, 48-11, 49-32, 50-41, 50-45, 51-22, 51-26, 52-02, 52-06, 53-07, 53-11, 60-02, 62-02, 74-02, 76-02, 95-02		
'DGREQLO+0'	T	TSTNUM		W=7 S=0 N=0 CL=2		
0	U	TTYTBL				
0000002	A	TWO	18-06			
'5'	T	TXTSEG				
0000035	A	UCODFALT	191-13	94-24, 191-13		
'PAGSCR+28'	T	UPADDR		W=4 S=0 N=34 CL=2		
CSYSUB	V	UPD_OTS	7-33			
0007114	A	USERRED	22-15			
'READ STORE						
ERROR REG (W						
/O ISOLATE)'	T	USERREDT				
CBLM	V	WAIT	7-07			
'CHKWRD+0'	T	WDISOL		W=4 S=10 N=0 CL=2		
TDATA	V	WDSTR	7-42	29-37, 33-46		
0010000	A	WPBLKSZE	18-07			
0046061	A	WPCLR	22-17			
'STORE INITI						
ALIZE, INCL.						
WRITE PROTE						
CT'	T	WPCLRT				
TDATA	V	WPTBL	7-43			
0000322	A	YTXF		LIBNUM=14 93-13		
0015472	A	ZDRX		LIBNUM=14 89-32		
0025750	A	ZERX		LIBNUM=14 93-11		
0000000	A	ZERO	18-08			
0000000	A	ZEROS	18-09	45-41, 45-42, 55-40, 55-41, 58-23, 64-34, 64-35, 68-41, 68-42, 78-36, 78-37, 79-03, 79-04, 86-32, 86-33		

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 242

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

VALUE	T	NAME	DEF/REF	ATTRIBUTES AND REFERENCES	CDGSDF	W77D
0026750	A	ZMINTX		LIBNUM=14 30-25, 34-34, 92-48, 93-15		
'MASCIOSC+1'	T	OSSC0		W=20 S=0 N=1 CL=2		
'MASCIOSC+3'	T	OSSC1		W=20 S=0 N=3 CL=2		
'MASCIOSC+5'	T	OSSC2		W=20 S=0 N=5 CL=2		
'MASCIOSC+7'	T	OSSC3		W=20 S=0 N=7 CL=2		
0000000	A	O TEST	18-10			
3	R	OWDSTRO	16-06	W=1 S=4 N=0 CL=1		
0052354	A	O1ONOR	22-19			
'O-10 TIMING (O10TIM) THEN REFRESH REQ WEST CLEAR'	T	O1ONORT				
0054334	A	O1OREF	22-22			
'O-10 TIMING (O10TIM) THEN REFRESH REQ WEST SET'	T	O1OREFT				
0056074	A	O10TIM	22-25			
'FORCE O-10 TIMING SIGNA L ACTIVE'	T	O10TIMT				
'CIOBUF+0'	T	O10TM		W=1 S=14 N=0 CL=2		
0000001	A	1TEST	18-11			
'CHKWRD+0'	T	2EPCK		W=1 S=15 N=0 CL=2		
1	J	3ACC	1-09			

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 243

TROUBLE LOCATION MANUAL

23:27:57 2/05/81 ****

COUNT	FLAG	PAGE-LINE OF FLAG
2	E	92-49, 93-04

CDGSDF W77D

2 FLAG(S) FOUND IN THIS ASSEMBLY

STORE CONTROLLER DATA REGISTER TEST

PR-1C935-50

23:27:57 2/05/81 TRADE SECRET - SEE TRADE SECRET RESTRICTIVE NOTICE ON COVER SHEET CDGSDF ISSUE 05 PAGE 244