

SHEET INDEX

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CAD. 1,2,3,4,5,6,7	5

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER
	0.75	-48	2 CIRCUITS
		GRD	2 CIRCUITS
BATTERY SYMBOL		VOLTAGE RANGE	
	-48		42.75-52.5

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DESIGN REQ	J18001
EQUIPMENT DRAWING	J18001AM

OPTION INDEX

APP OR WRG	LOCATION
Z	2AB, 2BB
Y	2AB, 2CB
X	2B0, 2B1
W	2A0, 2C0

APPARATUS INDEX

DESIG	LOCATION		
	FS	APP FIG.	EQPT
RELAYS			
A(0,1)	206	1	
B(0,1)	207	1	
C(0,1)	208	1	

LEAD INDEX

DESIG	LOCATION	
	FS	CAD.
SIGNAL DISTRIBUTOR CKT		
20	2F6	SD4
201	2F6	SD4
SD2	2F6	SD4
SD4	2F6	SD3
SD5	2F6	SD0
SD6	2F6	SD0

102.

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY
TRUNK CIRCUIT	1		1 PER 2 CKTS
IN IDLE STATE TOLL TRUNK PRESENTS BAT. ON	TIP		2
	RING		Y
IN IDLE STATE LOCAL OFFICE TRUNK REQUIRES BATTERY ON.	TIP		X
	RING		W

EQUIPMENT NOTES:

201. THERE SHALL BE 2 CIRCUITS PER PLUG IN UNIT.
202. ON A BAY OF PLUG-IN UNITS, THERE SHALL BE FOUR SEPARATE LOCAL CABLES AS FOLLOWS:
 A CABLE - FOR ALL TIP AND RING LEADS
 B CABLE - FOR ALL SCANNER LEADS
 C CABLE - FOR ALL SIGNAL DISTR LEADS
 D CABLE - FOR ALL BATTERY AND GRD LEADS
 THE "A" CABLE SHALL BE LOCATED ON THE OPPOSITE SIDE OF THE BAY FROM CABLES B, C, AND D.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
 RESISTANCE VALUES ARE IN OHMS,
 CAPACITANCE VALUES ARE IN MICROFARADS,
 VALUES PRECEDED BY THE SYMBOL + (PLUS)
 OR - (MINUS) ARE IN VOLTS.
302. CIRCUIT STATES:

*COIN ANI or operator identified
 0+ or 1+ call (no overtime)
 inband signalling
 04645404640*

WORKING LIMITS

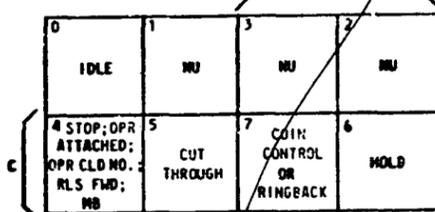
	TO LOCAL OFF	TO TOLL OFF
MAX EXT CKT LP RES (SUPR)	4000	9200
MIN INS RES	30,000	30,000
BAT LIMITS	42.75 - 52.25	-

103.

NETWORK NO.	NETWORK VALUES	
	RESISTANCE IN OHMS	CAPACITANCE IN UF
R	200	542.5
T	200	542.5

104.

CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	ALM	TD



TYPICAL CALL SEQUENCES:

- NON COIN: 0464540
 COIN WITHOUT OVERTIME: 046454640
 COIN WITH OVERTIME: 04645754640

NOTE: AS OF THE DATE OF ISSUE OF THIS DOCUMENT, ALL OTHER DOCUMENTS REFERRED TO HEREIN MAY NOT BE AVAILABLE FOR DISTRIBUTION; THEY WILL HOWEVER, BE AVAILABLE BY ABOUT APRIL 1, 1966.

SD-18003-01

TRAFFIC SERVICE SYSTEMS
 TRAFFIC SERVICE POSITION SYSTEM NO. 1
 INCOMING TRUNK CIRCUIT
 LOOP SIGNALING - HF PULSING
 REVERSE BATTERY SUPERVISION

(INC TRK 2W LP-DP)

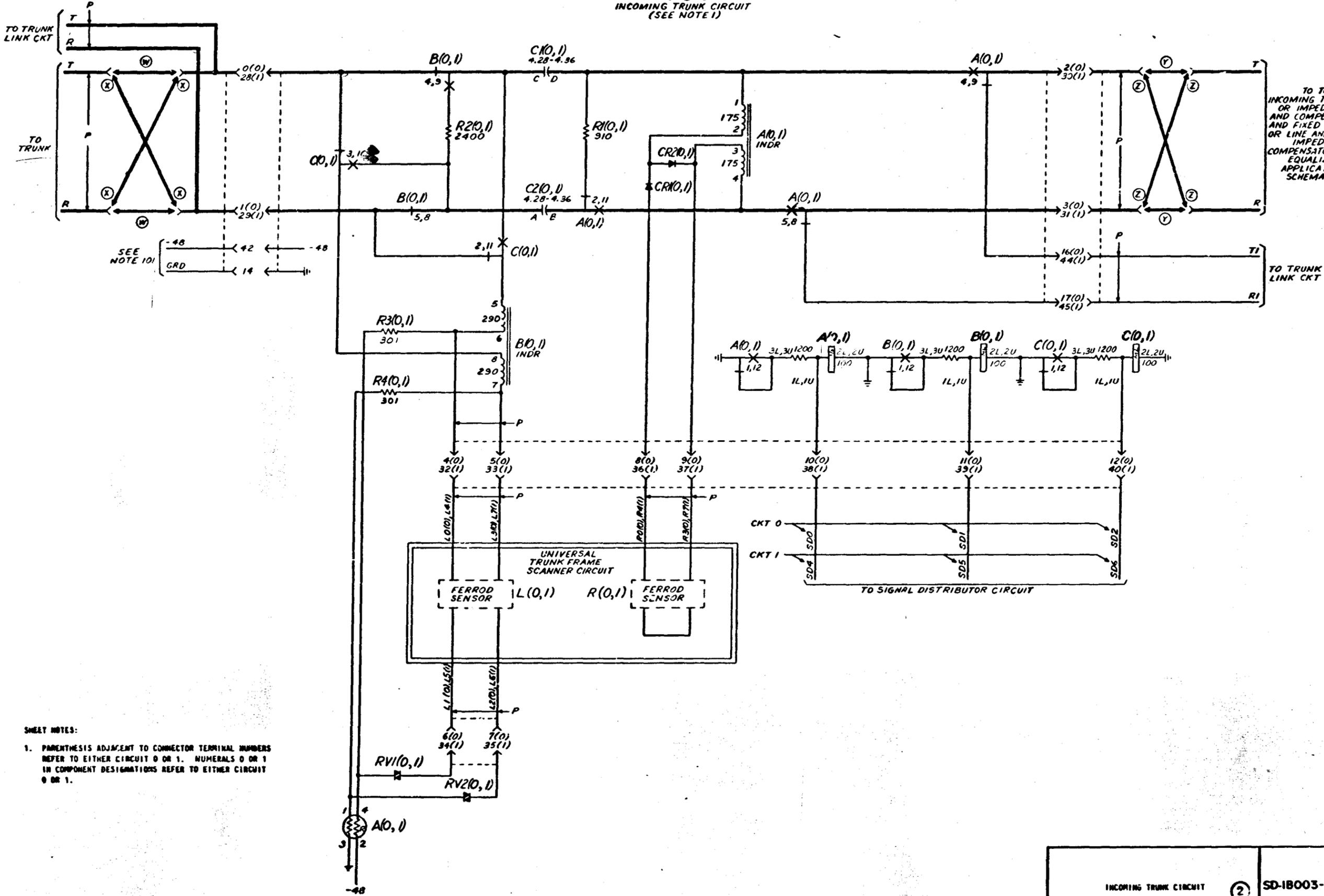
BELL TELEPHONE LABORATORIES INCORPORATED 6S

AT&T CO STANDARD

SD-18003-01-1
 5 SHEETS

FS I
INCOMING TRUNK CIRCUIT
(SEE NOTE 1)

DRAWING
ISSUE
1 E-2
65



TO TOLL
INCOMING TRUNK CKT
OR IMPEDANCE
AND COMPENSATOR
AND FIXED PAD CKT
OR LINE AND BALANCE
IMPEDANCE
COMPENSATOR AND
EQUALIZER
APPLICATION
SCHEMATIC

SEE
NOTE 101
-48
42
-48
GRD
14

SHEET NOTES:
1. PARENTHESIS ADJACENT TO CONNECTOR TERMINAL NUMBERS REFER TO EITHER CIRCUIT 0 OR 1. NUMERALS 0 OR 1 IN COMPONENT DESIGNATIONS REFER TO EITHER CIRCUIT 0 OR 1.

SD-1B003-01-2

APP FIG. 1

REPLY		A(0)		A(1)		B(0)		B(1)		C(0)		C(1)		DESIG
CODE	AM1	AM1		CODE										
OPT. NO.	CONT. ARR.	LOC.												
12	EMB	2D5	EMB	2D5	EMB	2D6	EMB	2D7	EMB	2D7				12
11	EMB	2C4	EMB		EMB		EMB	2C3	EMB					11
10	EMB		EMB		EMB		EMB	2B2	EMB					10
9	EMB	2A7	EMB	2A3	EMB	2B2	EMB	2C3	EMB					9
8	EMB	2B5	EMB	2B2	EMB	2C3	EMB		EMB					8
7	EMB	2B5	EMB	2B2	EMB	2C3	EMB		EMB					7
6	EMB	2A7	EMB	2A3	EMB	2B2	EMB		EMB					6
5	EMB		EMB		EMB		EMB	2B2	EMB					5
4	EMB	2C4	EMB		EMB		EMB	2C3	EMB					4
3	EMB	2D5	EMB	2D6	EMB	2D7	EMB	2D8	EMB					3
2	EMB	2D6	EMB	2D7	EMB	2D8	EMB		EMB					2
1	EMB	2D6	EMB	2D7	EMB	2D8	EMB		EMB					1
0	EMB	2D6	EMB	2D7	EMB	2D8	EMB		EMB					0

CAPACITOR

DESIG	LOC	CODE
[1] C1(0)	2A3	570C
[1] C2(0)	2B3	570C
[1] C1(1)	2A3	570C
[1] C2(1)	2B3	570C

DIODE

DESIG	LOC	CODE
[2] CR1(0.1)	2B4	446F
[2] CR2(0.1)	2B4	446F

INDUCTOR

DESIG	LOC	CODE
[1] A(0)	2B5	1633B
[1] B(0)	2B3	1633B
[1] A(1)	2B5	1633B
[1] B(1)	2B3	1633B

LAMP

DESIG	LOC	CODE
[2] A(C.1)	2H2	11B.RES

RESISTOR

DESIG	LOC	CODE
[2] R1(0.1)	2B4	KS-16645 L1,910
[2] R2(0.1)	2B3	KS-13492 L1,2400
[2] R3(0.1)	2D2	KS-14603 L3C,301
[2] R4(0.1)	2D2	KS-14603 L3C,301

VARIATOR

DESIG	LOC	CODE
[2] RV1(0.1)	2H2	100D
[2] RV2(0.1)	2H3	100D

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INCORPORATING TRUNK CIRCUIT	2	SD-1B003-01-3
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