

SHEET INDEX

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FS 7 SUPPLEMENTARY SIGNAL DETECTOR																										
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CONTENTS	SHEET NO.	ISSUE NO.																									
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	
CAD 1-3	G1	1	2	3	4	5	5	7	7	7	10	11															
CAD 4	G2	1	2	3	4	5	5	7	7	7	10	11															
CAD 5	G3	1	2	3	4	5	5	7	7	7	10	11															
CAD 6-14	G4	1	2	3	4	5	5	7	7	7	10	11															
CAD 15-19	G5	-	-	-	4	5	5	7	7	7	10	11															
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CAD 21, 22	G7	-	-	-	-	5	5	7	7	7	10	11															
CAD 23	G8	-	-	-	-	5	5	7	7	7	10	11															
CAD 24,25	G9	-	-	-	-	5	5	7	7	7	10	11															
	G10	-	-	-	-	-	-	7	7	7	10	11															
	G11	-	-	-	-	-	-	7	7	7	10	11															
	G12	-	-	-	-	-	-	7	7	7	10	11															
	G13	-	-	-	-	-	-	7	7	7	10	11															
B01 SINGLE SIGNAL CODE ONE DIGIT OR TWO DIGIT	H1	1	2	2	4	5	5	7	7	7	11																
B02 SINGLE CODE ONE DIGIT OR TWO DIGIT	H2	1	2	2	4	5	5	7	7	7	11																
B03 2 DIGIT, OR; 3 DIGIT, SIGNAL CODE UP TO 5 CODES PER LOCATION	H3	1	2	2	4	5	5	7	7	7	11																
B04 2 DIGIT, OR; 3 DIGIT, SIGNAL CODE COMMON TTR FOR TWO ORDER CKTS J993400	H4	-	-	-	-	5	6	7	7	7	11																
B05 2 DIGIT, OR; 3 DIGIT, SIGNAL CODE COMMON TTR FOR TWO ORDER CKTS DIALED CUT-THRU J99340E.	H5	-	-	-	-	5	6	7	7	7	11																
CPS 1 4 WAY - 4 WIRE BRIDGE CKT	J1	1	1	1	4	4	4	4	4	4	10	10															
CPS 2 DUAL AMPLIFIER CKT	J2	1	1	1	4	4	4	4	4	4	10	10															
CPS 3 SIGNAL RECEIVER	J3A	1	2	2	2	2	2	7	7	7	10	10															
	J3B	1	1	1	4	4	4	7	7	7	10	10															
CPS 4 SUPPLEMENTARY SIGNAL DETECTOR	J4	1	2	2	4	4	4	7	7	7	10	10															
CPS 5 PART OF DECODER	J5	1	2	2	4	4	4	4	4	4	10	10															
CPS 6 PART OF DECODER	J6A	1	2	2	2	2	2	2	2	2	10	10															
	J6B	1	2	2	4	4	4	4	4	4	10	10															
CPS 7 PART OF DECODER	J7A	1	2	2	2	2	2	2	2	2	10	10															
	J7B	1	2	2	4	4	4	4	4	4	10	10															
CPS 8 TO 11 TALK BATTERY FEED CIRCUIT	J8	1	1	3	4	5	5	7	7	7	10	10															
CPS 12 PRE-EMPT CIRCUIT	J9	1	1	1	4	4	4	7	7	7	10	11															
CPS 13 CALL-RECALL-ALERT CIRCUIT	J10	1	2	2	4	4	4	7	7	7	7																
CPS 14 LINE CUT CIRCUIT	J11	1	2	2	2	2	2	7	7	7	10	10															
CPS 15 CALL-RECALL-ALERT CKT WITH TIMED LOCKUP	J12	-	-	-	-	-	-	7	7	9	9																
CPS 16 SUPPLEMENTARY SIGNAL DETECTOR AND INHIBIT	J13A																										
	J13B																										

SHEET INDEX NOTES

- WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
- THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
- THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
- SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
- THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

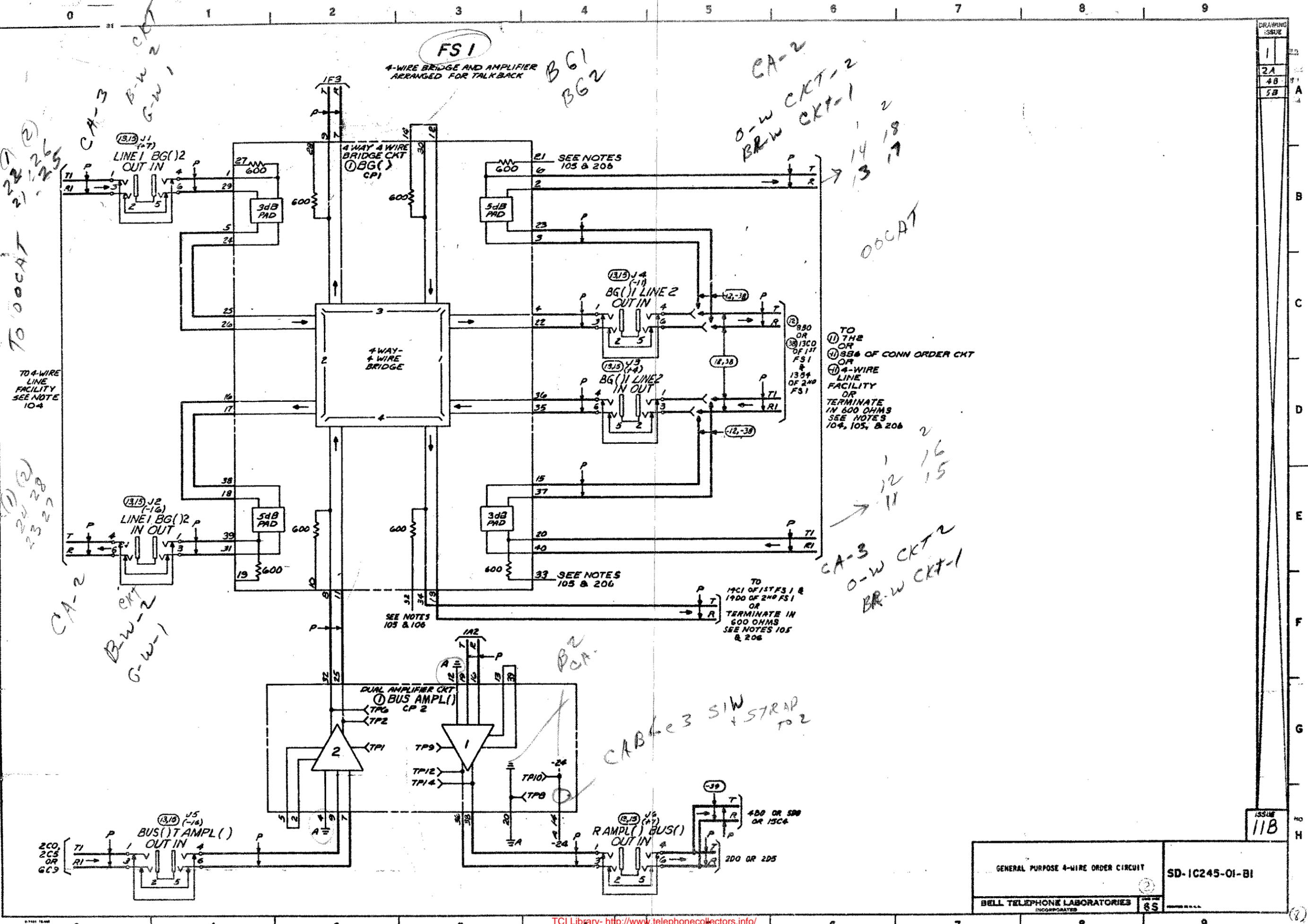
SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	J99340A J99340B J99340C J99340D J99340E ED-1C385-()
EQUIPMENT DESIGN REQ	J99340 (801-026-155)
MAINTENANCE REQUIREMENTS	

1
2A
4B
5B

ISSUE
11B

FS 1
4-WIRE BRIDGE AND AMPLIFIER
ARRANGED FOR TALKBACK

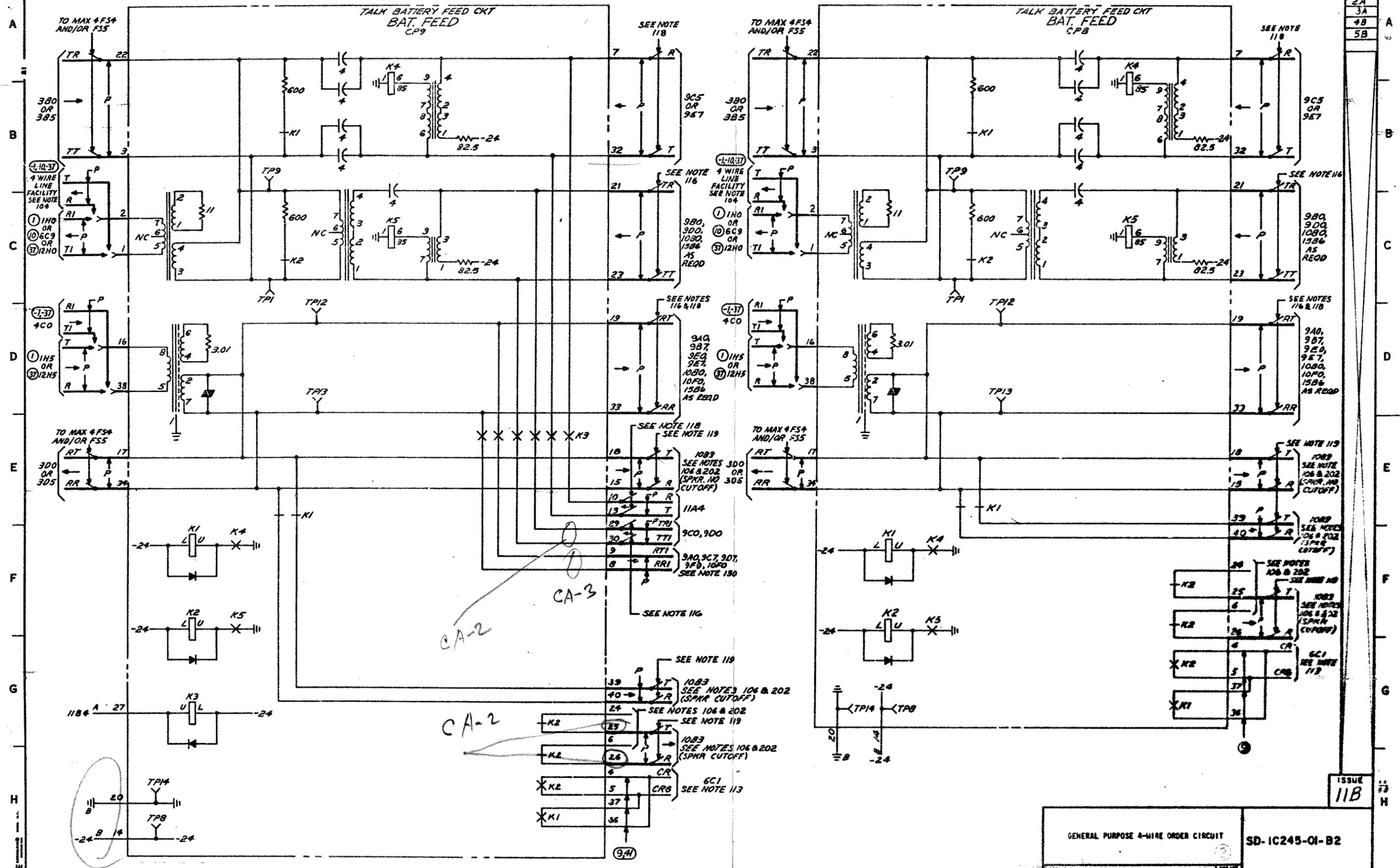


SD-1C245-01-B1

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-1C245-01-B1
BELL TELEPHONE LABORATORIES INCORPORATED	85

② FS2
TALK BATTERY FEED WITH OR WITHOUT PICKUP

③ FS3 (MFR DISC)
TALK BATTERY FEED WITHOUT PICKUP



DRAWING
ISSUE
1
2A
3A
4B
5B

ISSUE 11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT
SD-1C245-01-B2
BELL TELEPHONE LABORATORIES INCORPORATED
65

SD-1C245-01-B2

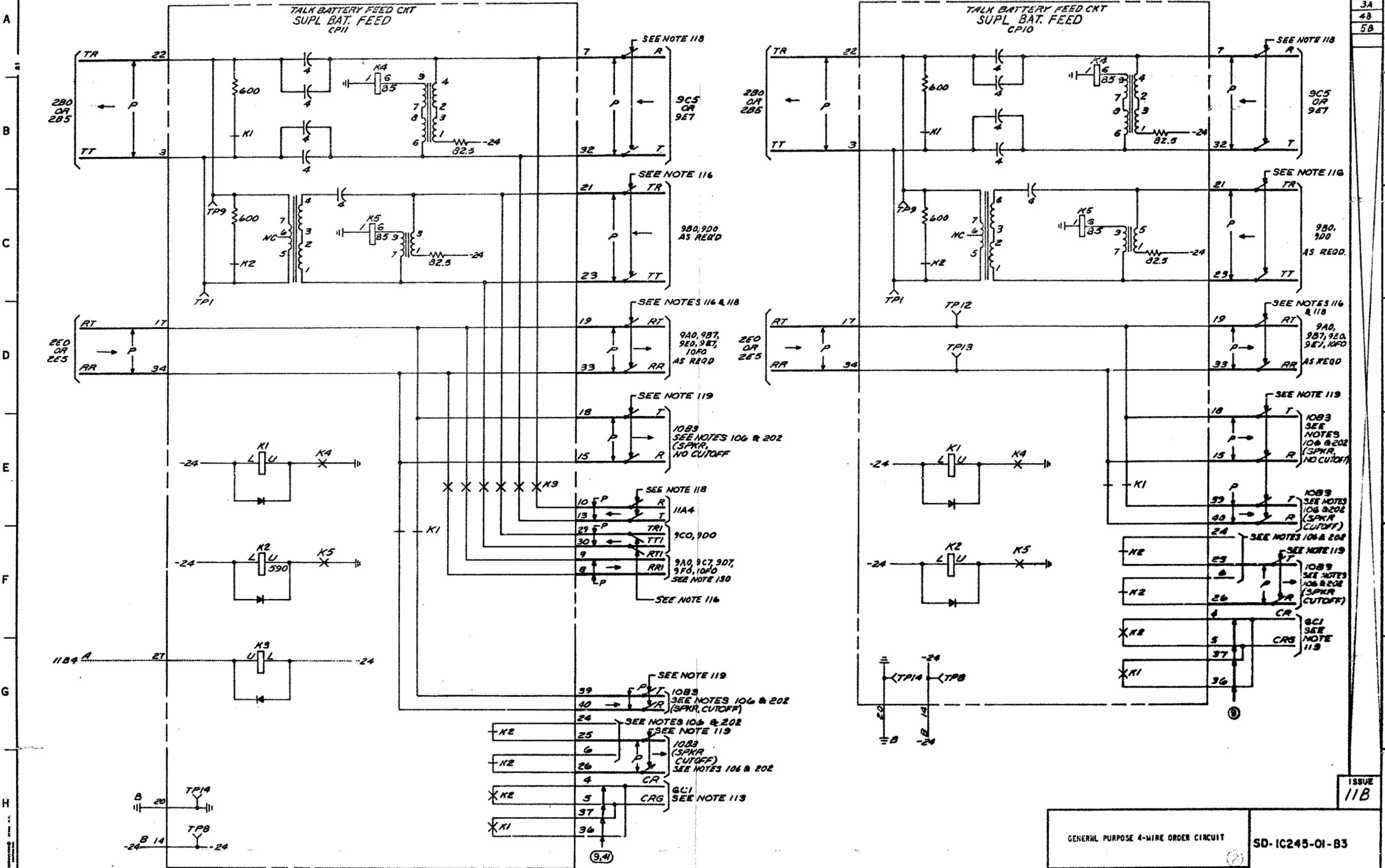
FS 4

SUPPLEMENTARY TALK BATTERY FEED WITH OR WITHOUT PICKUP

FS 5 (MFR DISC)

SUPPLEMENTARY TALK BATTERY FEED WITHOUT PICKUP

DRAWING	1
ISSUE	1
NO.	2A
NO.	3A
NO.	4B
NO.	5B



SD-IC245-01-B3

ISSUE 11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

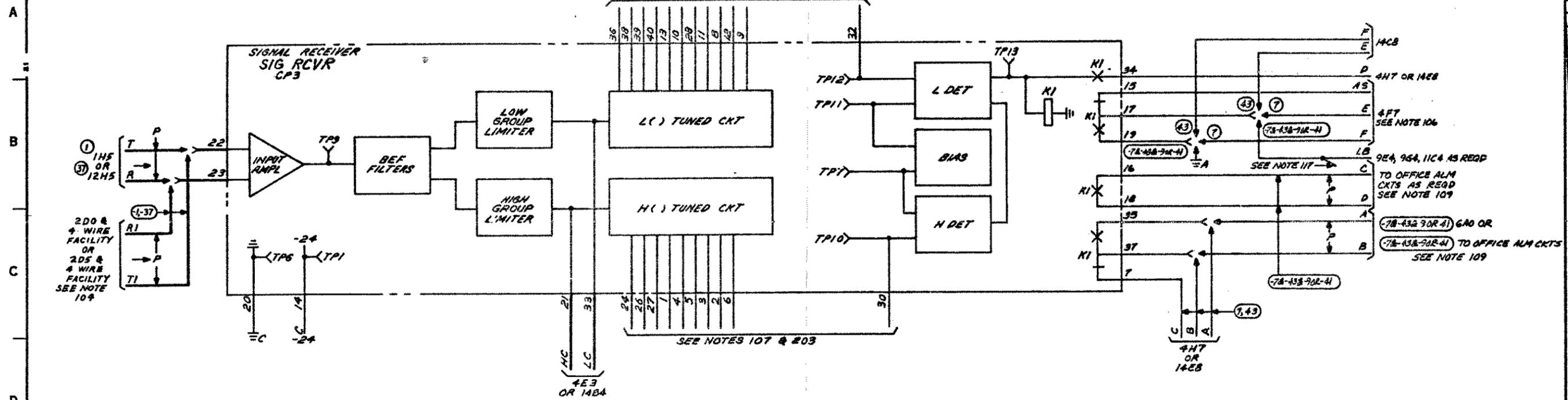
BELL TELEPHONE LABORATORIES INCORPORATED

SD-IC245-01-B3

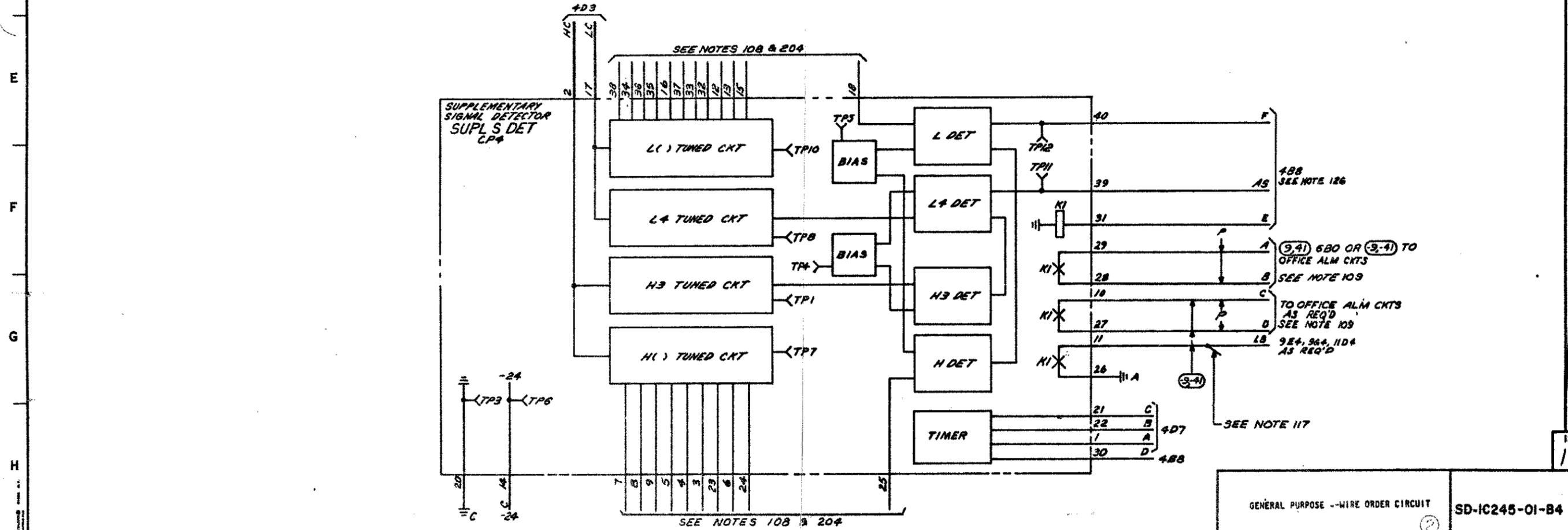
65

DRAWING	1
ISSUE	2A
	3A
	5B

FS 6
SIGNAL RECEIVER



FS 7
SUPPLEMENTARY SIGNAL DETECTOR



ISSUE
11B

GENERAL PURPOSE -- WIRE ORDER CIRCUIT

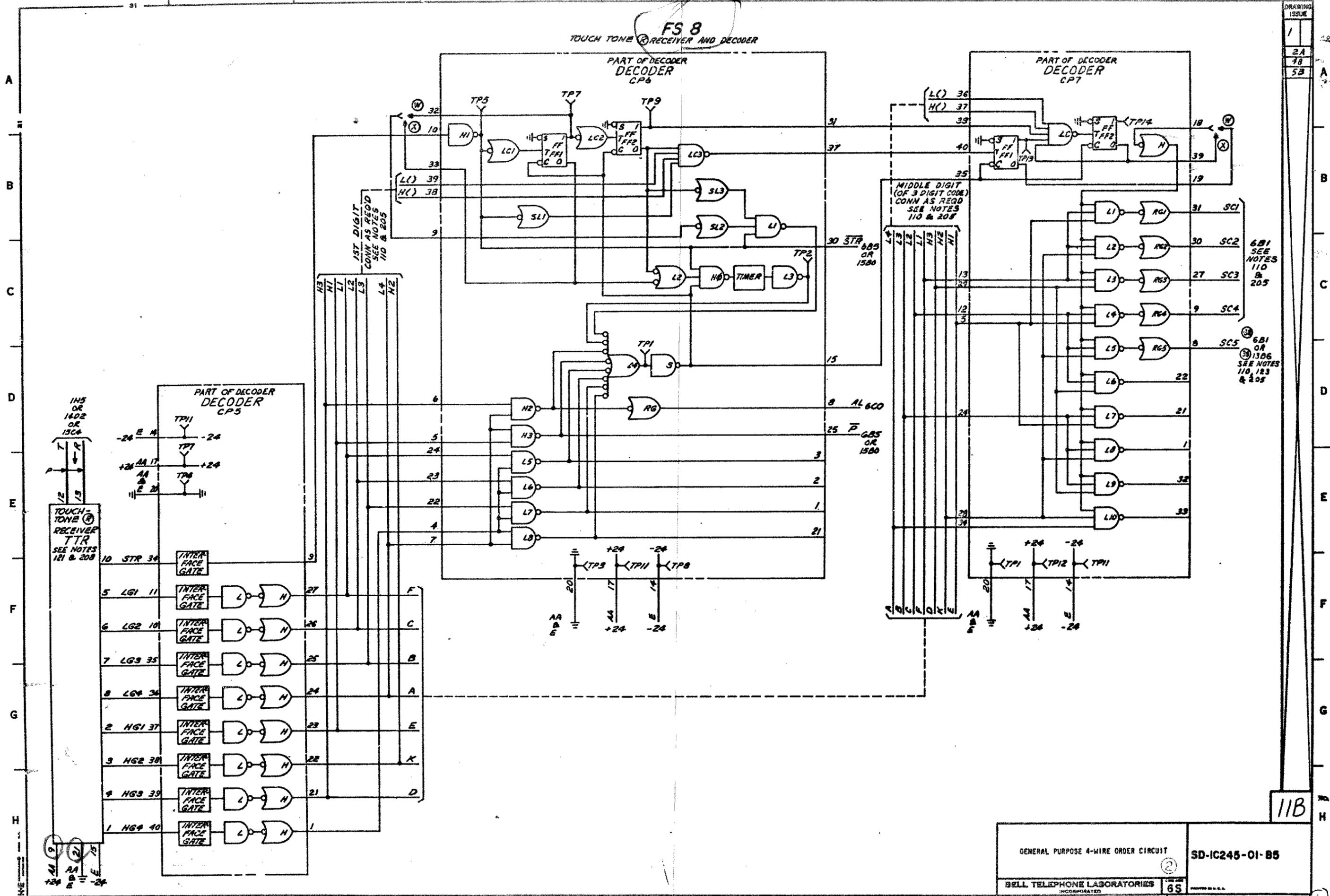
SD-IC245-01-B4

BELL TELEPHONE LABORATORIES
INCORPORATED

6S

SD-IC245-01-B4

FS 8
TOUCH-TONE RECEIVER AND DECODER



DRAWING	1
ISSUE	2A
	7B
	5B

MIDDLE DIGIT
(OF 3 DIGIT CODE)
CONN AS REQD
SEE NOTES
110 & 205

6B1
SEE NOTES
110
&
205

6B1
OR
13B6
SEE NOTES
110, 183
&
205

1H5
OR
14D2
OR
13C4

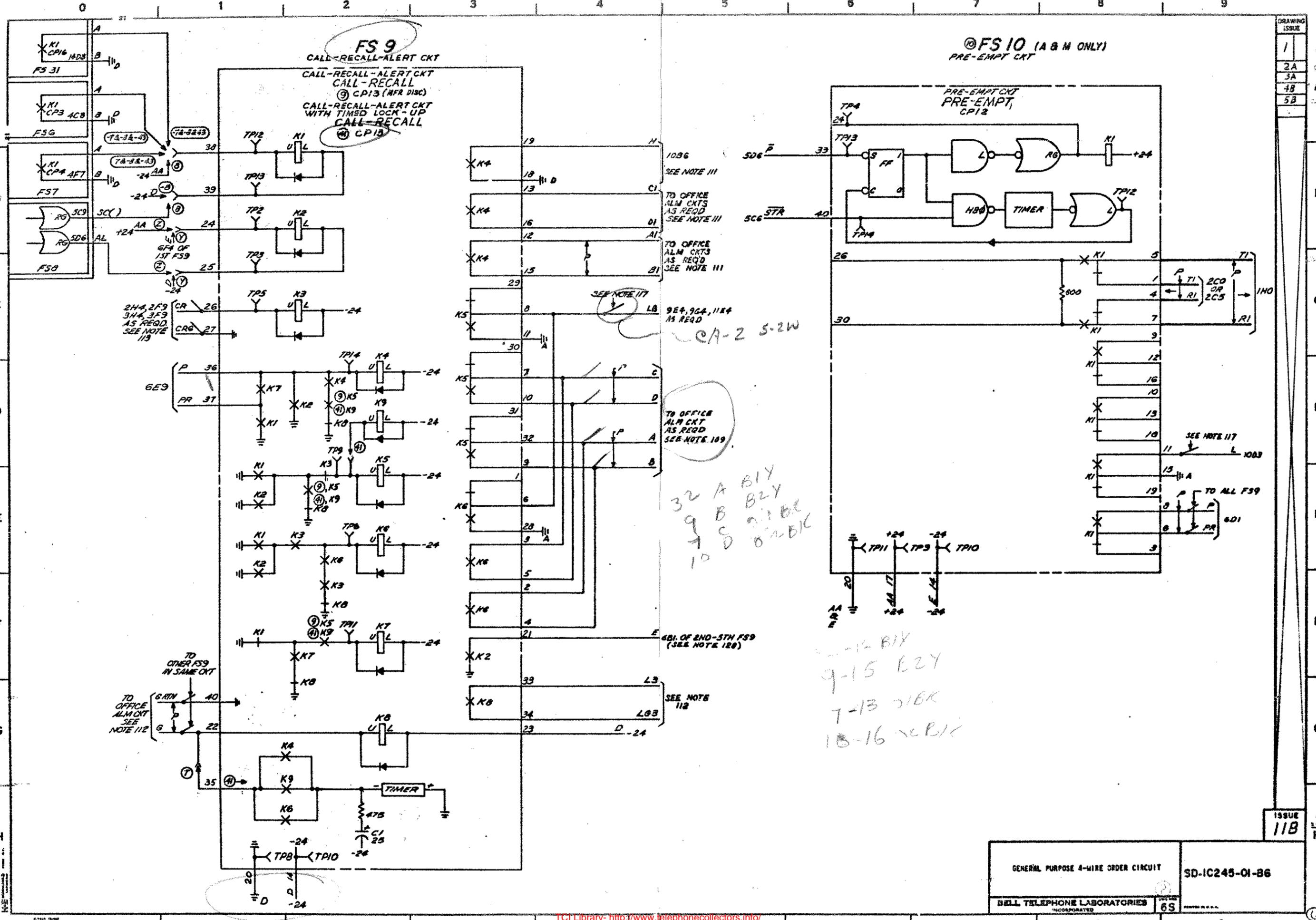
PART OF DECODER
DECODER
CP5

INTER-FACE
GATE

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT
SD-IC245-01-85
BELL TELEPHONE LABORATORIES
INCORPORATED
6S

SD-IC245-01-85

SD-IC245-01-86



DRAWING	1
ISSUE	1
2A	
3A	
4B	
5B	

ISSUE 118

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

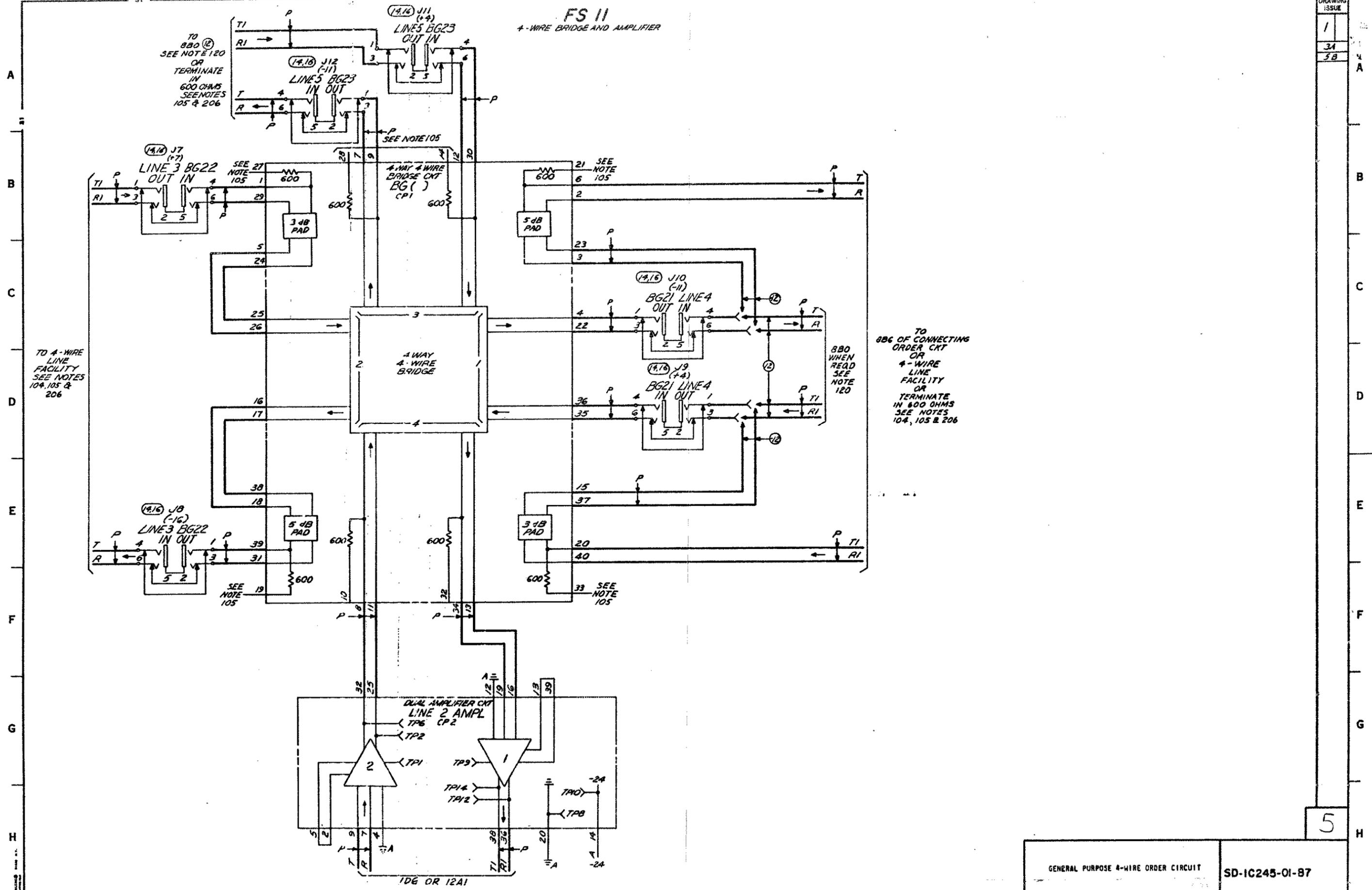
BELL TELEPHONE LABORATORIES INCORPORATED

SD-IC245-01-86

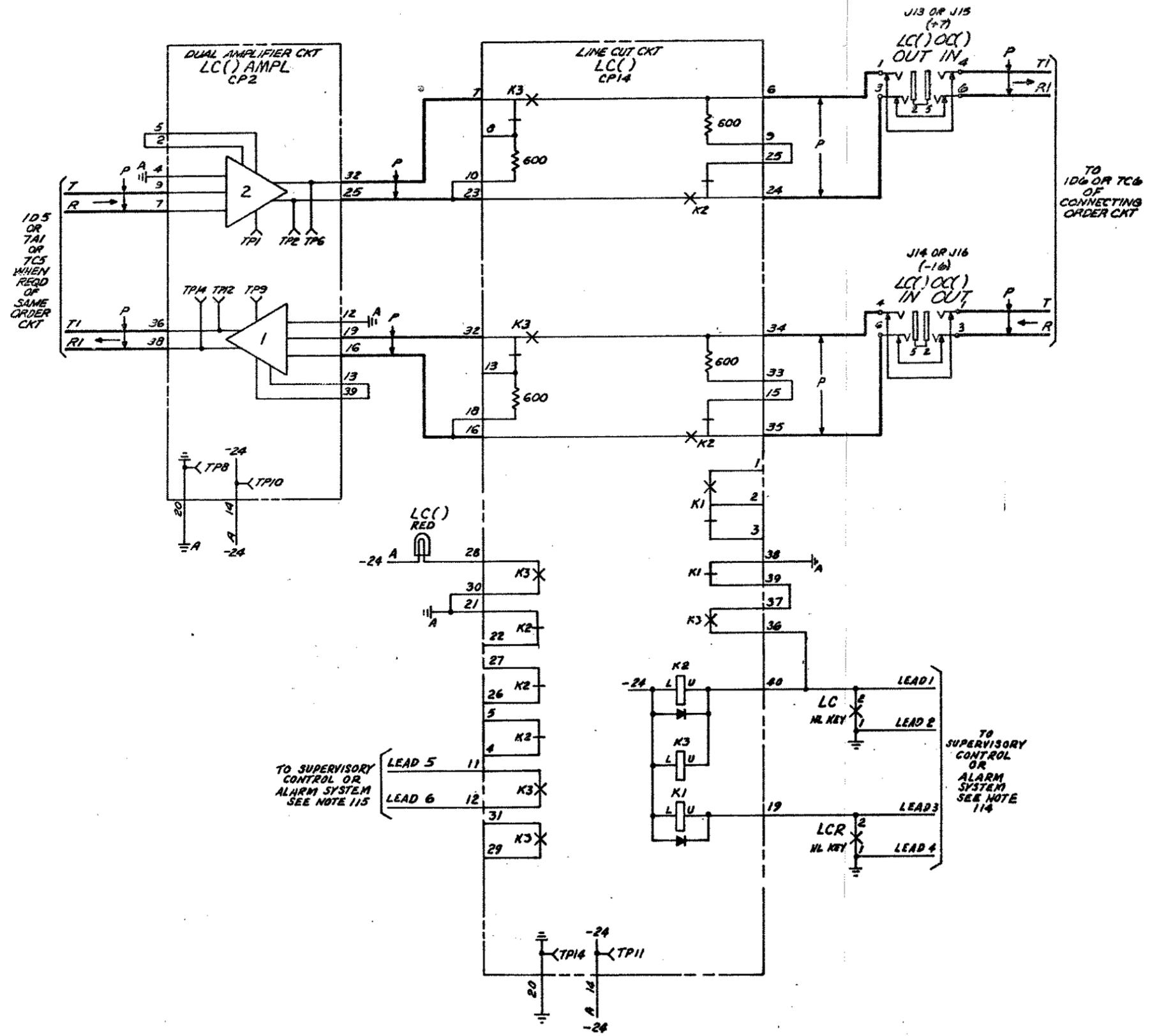
65

DRAWING
ISSUE
1
34
5B

FS II
4-WIRE BRIDGE AND AMPLIFIER



FS 12
4-WIRE LINE CONNECT CKT



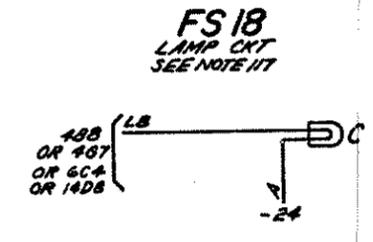
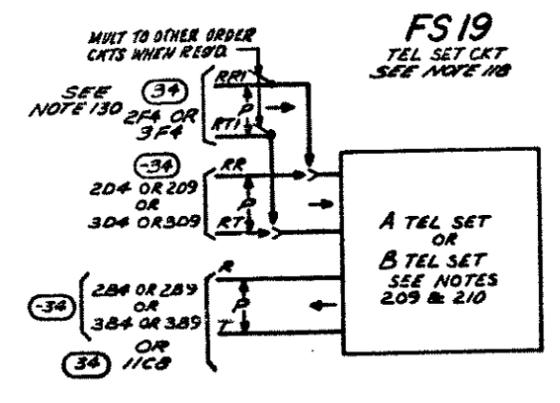
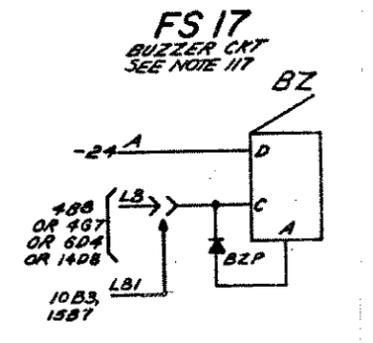
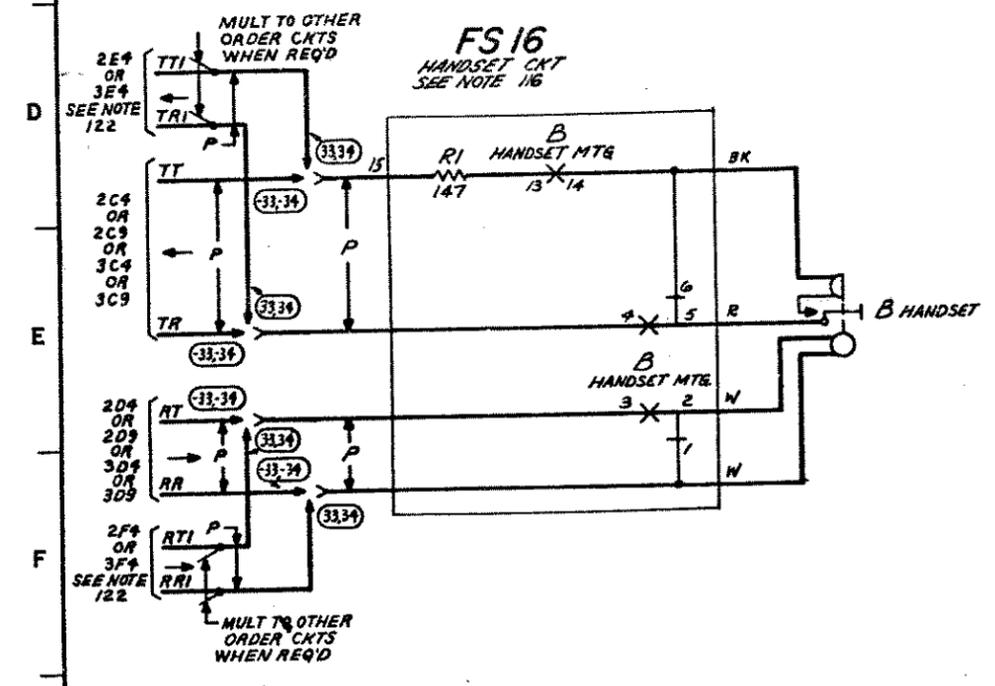
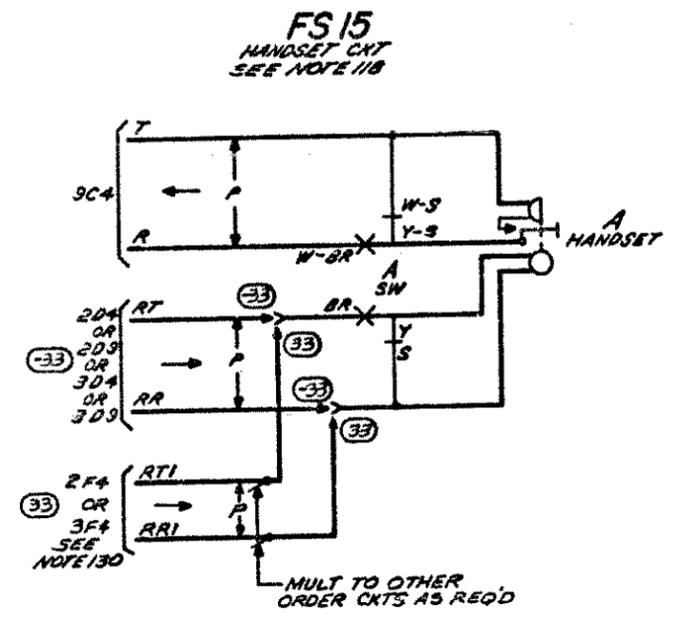
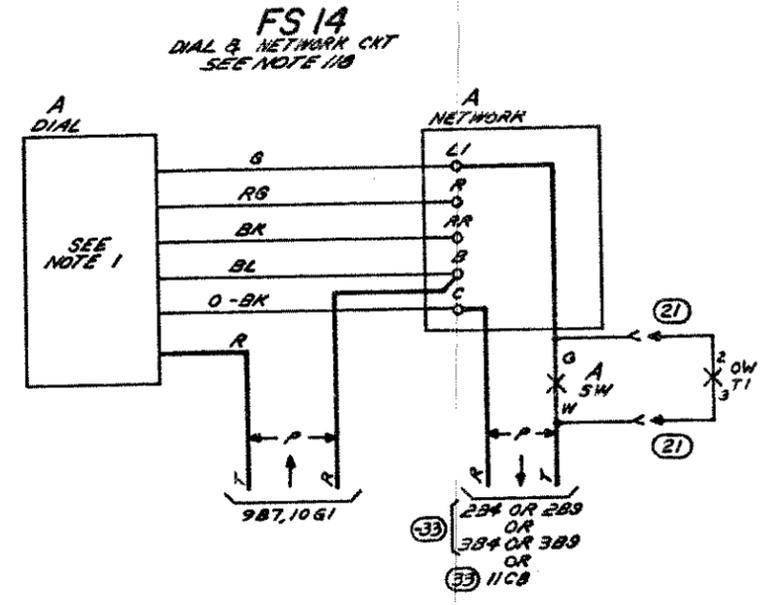
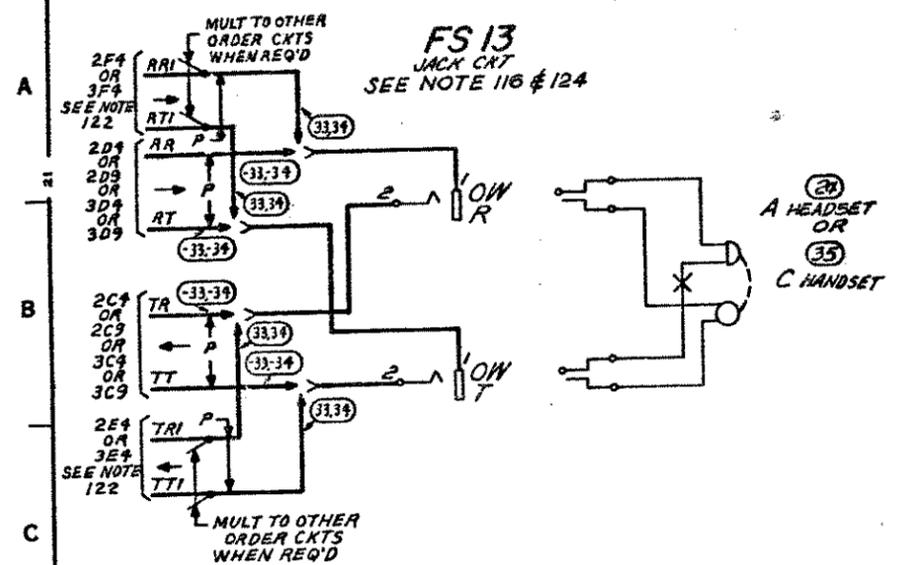
DRAWING	ISSUE
1	34
	58

ISSUE
108

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-IC245 -01-88
BELL TELEPHONE LABORATORIES INCORPORATED	65

SD-IC245-01-88

DRAWING
ISSUE
1
2A
3A
4B
5B



NOTES:
1. INSULATE AND STORE UNUSED LEADS.

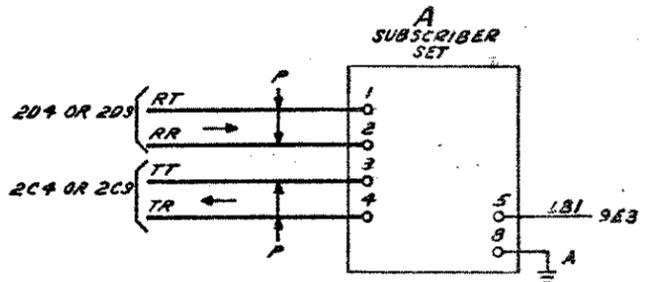
SD-IC245-01-89

ISSUE
11B

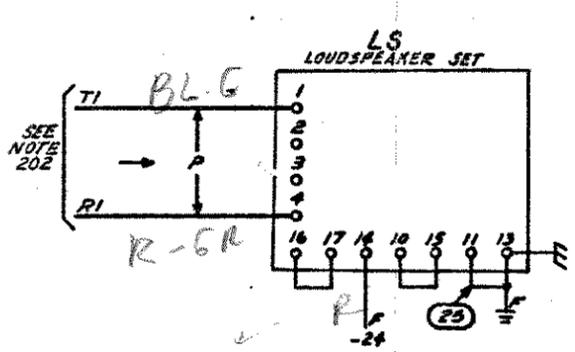
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-IC245-01-89
BELL TELEPHONE LABORATORIES INCORPORATED	65

DRAWING	1
ISSUE	11B
REV	2A
REV	4B
REV	5B

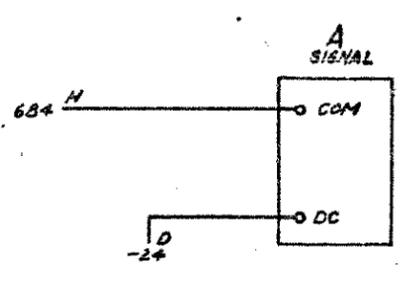
FS 20 (MFR DISC.)
OUTDOOR HANDSET CKT
SEE NOTE 1 & 116



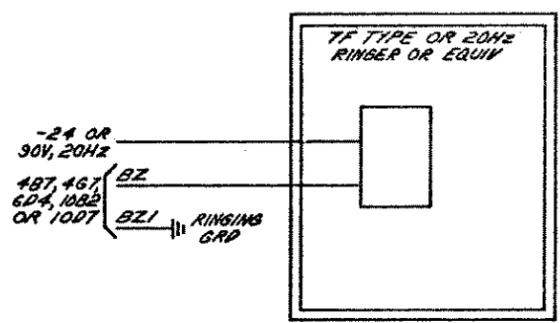
FS 21
LOUDSPEAKER CKT
SEE NOTE 2, 106, 119 & 129



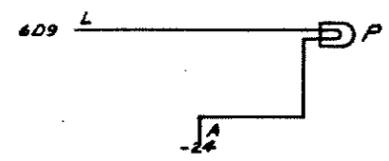
FS 22
HORN CKT
SEE NOTE 3



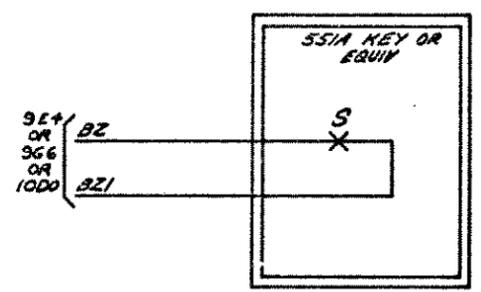
FS 23 (MFR DISC.)
BUZZER OR BELL
SEE NOTE 118



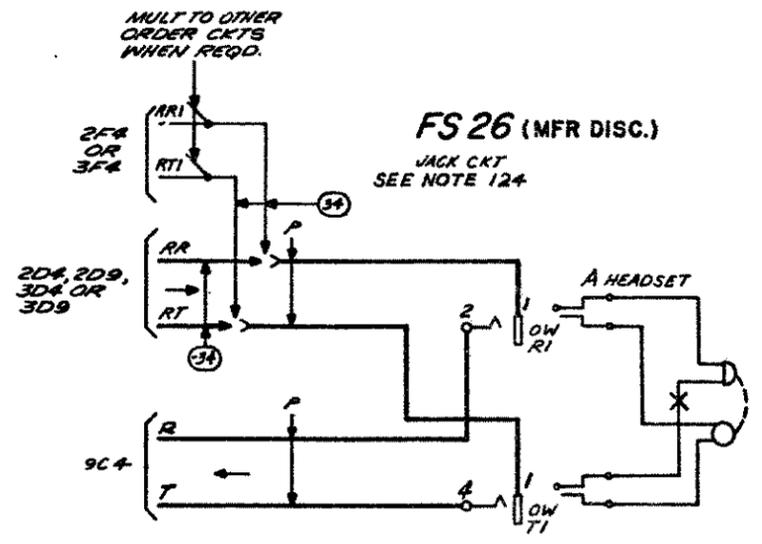
FS 24
PREEMPT LAMP CKT



FS 25 (MFR DISC.)
KEY



FS 26 (MFR DISC.)
JACK CKT
SEE NOTE 124



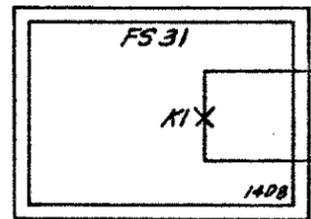
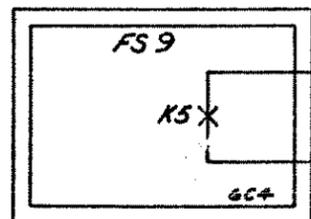
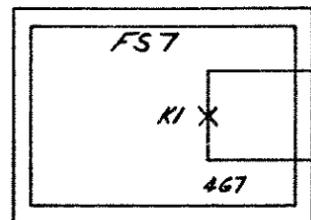
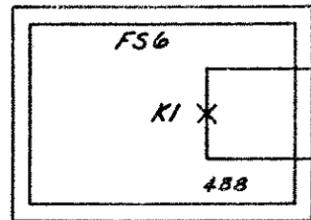
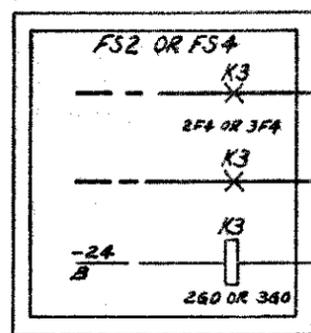
- NOTES:**
1. SEE BSP SECTION 502-201-102.
 2. SEE BSP SECTION 443-220-100
 3. SEE BSP SECTION 443-110-400.

ISSUE
11B

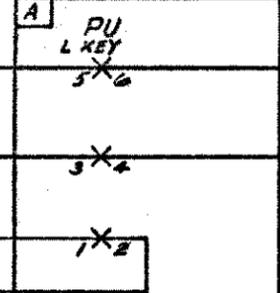
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-IC245-01-B10
BELL TELEPHONE LABORATORIES INCORPORATED	

SD-IC245-01-B10

FS 27
LINE PICKUP KEYS
SEE NOTE 1



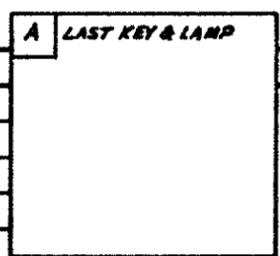
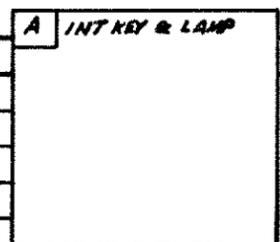
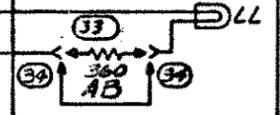
1ST KEY & LAMP



(7B-43A-90C-4)

(9,4)

TO OTHER
ORDER CIRCUITS



NOTE:
1. FOR ADDITIONAL PICKUP KEY POSITIONS
SEE CIRCUIT NOTE 125.

DRAWING
ISSUE
48

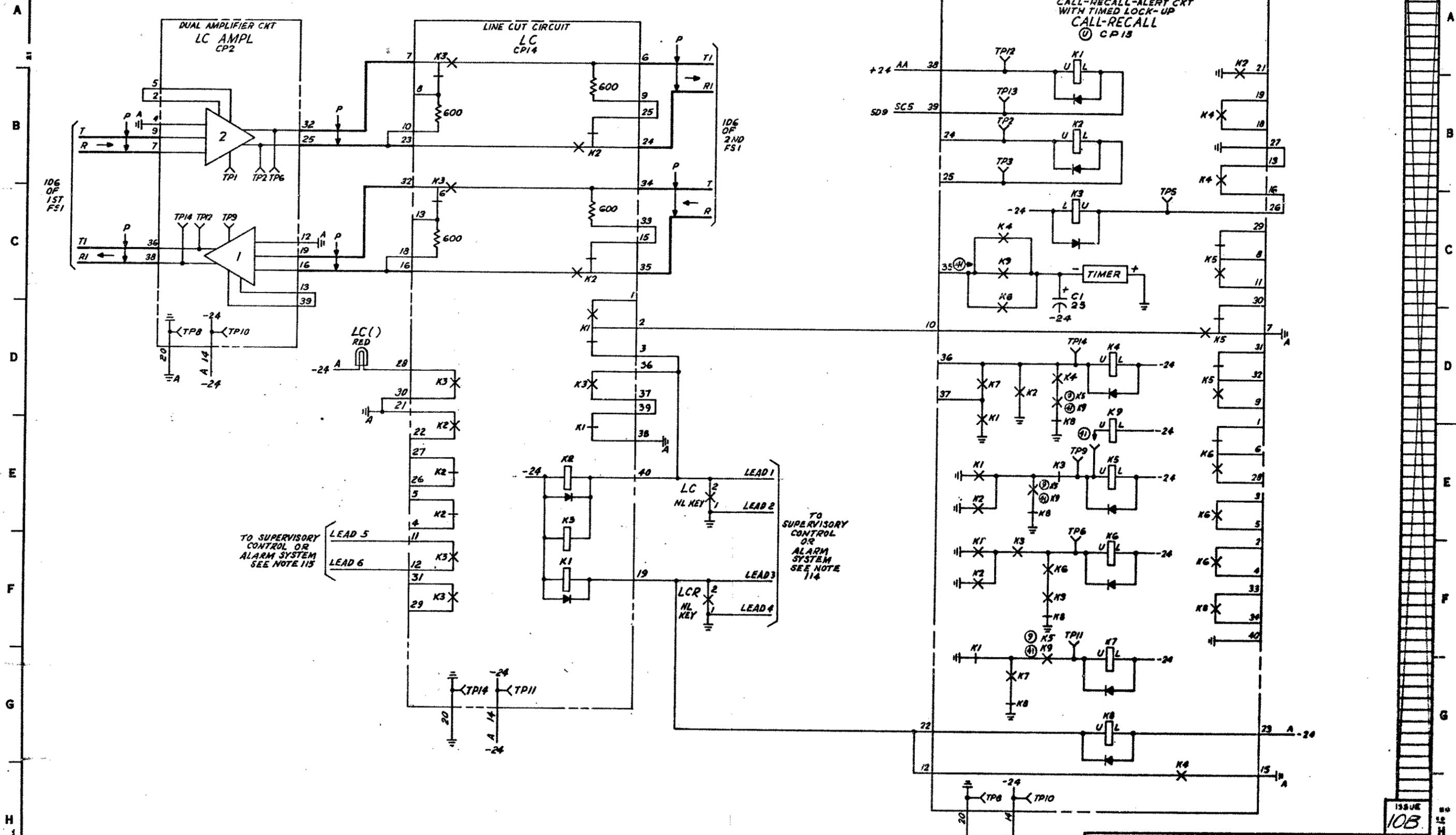
ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	2	SD-1C245-01-B11
BELL TELEPHONE LABORATORIES INCORPORATED	6S	MADE IN U.S.A.

SD-1C245-01-B11

FS 29
4-WIRE LINE CONNECT CKT
ARRANGED FOR DIALED CUT-THROUGH

**CALL-RECALL-ALERT CKT
CALL-RECALL**
Ⓢ CP 13 (MFR DISC)
CALL-RECALL-ALERT CKT
WITH TIMED LOCK-UP
CALL-RECALL
Ⓢ CP 13



TO SUPERVISORY CONTROL OR ALARM SYSTEM SEE NOTE 113

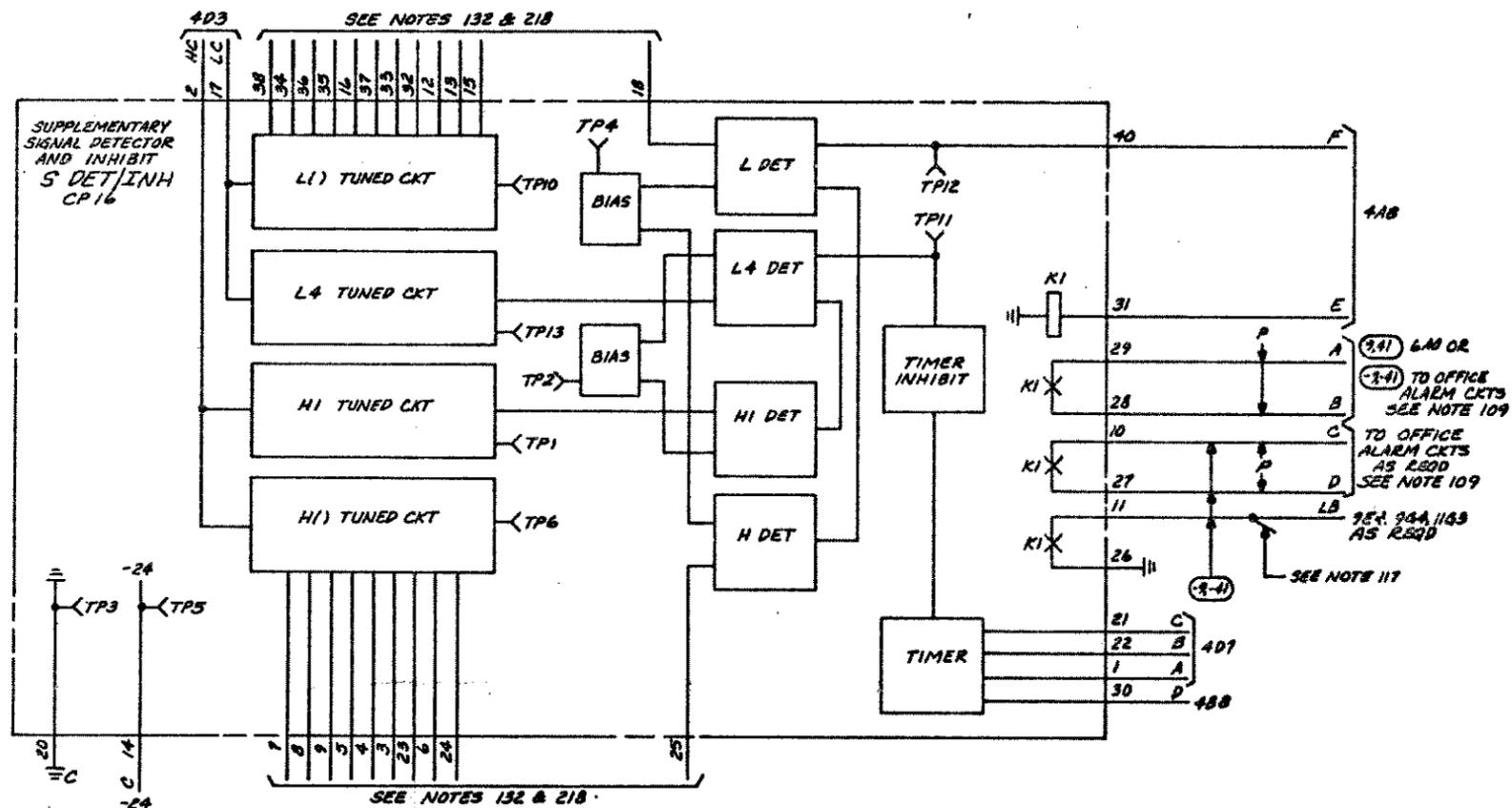
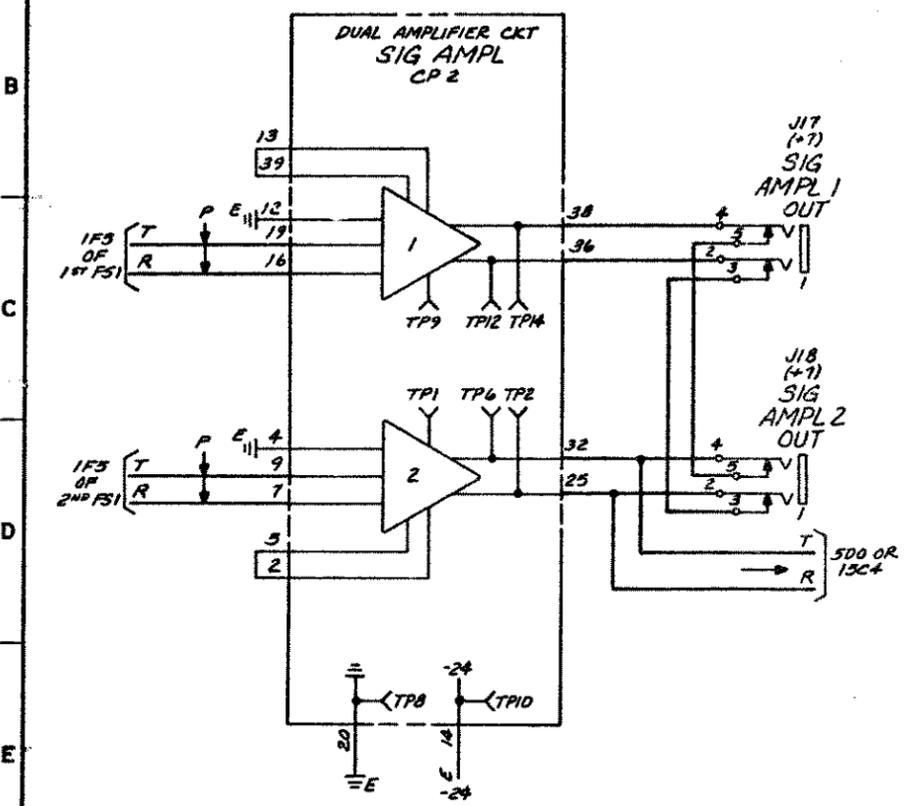
TO SUPERVISORY CONTROL OR ALARM SYSTEM SEE NOTE 114

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	②	SD-IC245-01-813
BELL TELEPHONE LABORATORIES	65	

SD-IC245-01-813

FS 30
SIGNALING AMPLIFIER CKT

FS 31
SUPPLEMENTARY SIGNAL DETECTOR
WITH INHIBIT CKT

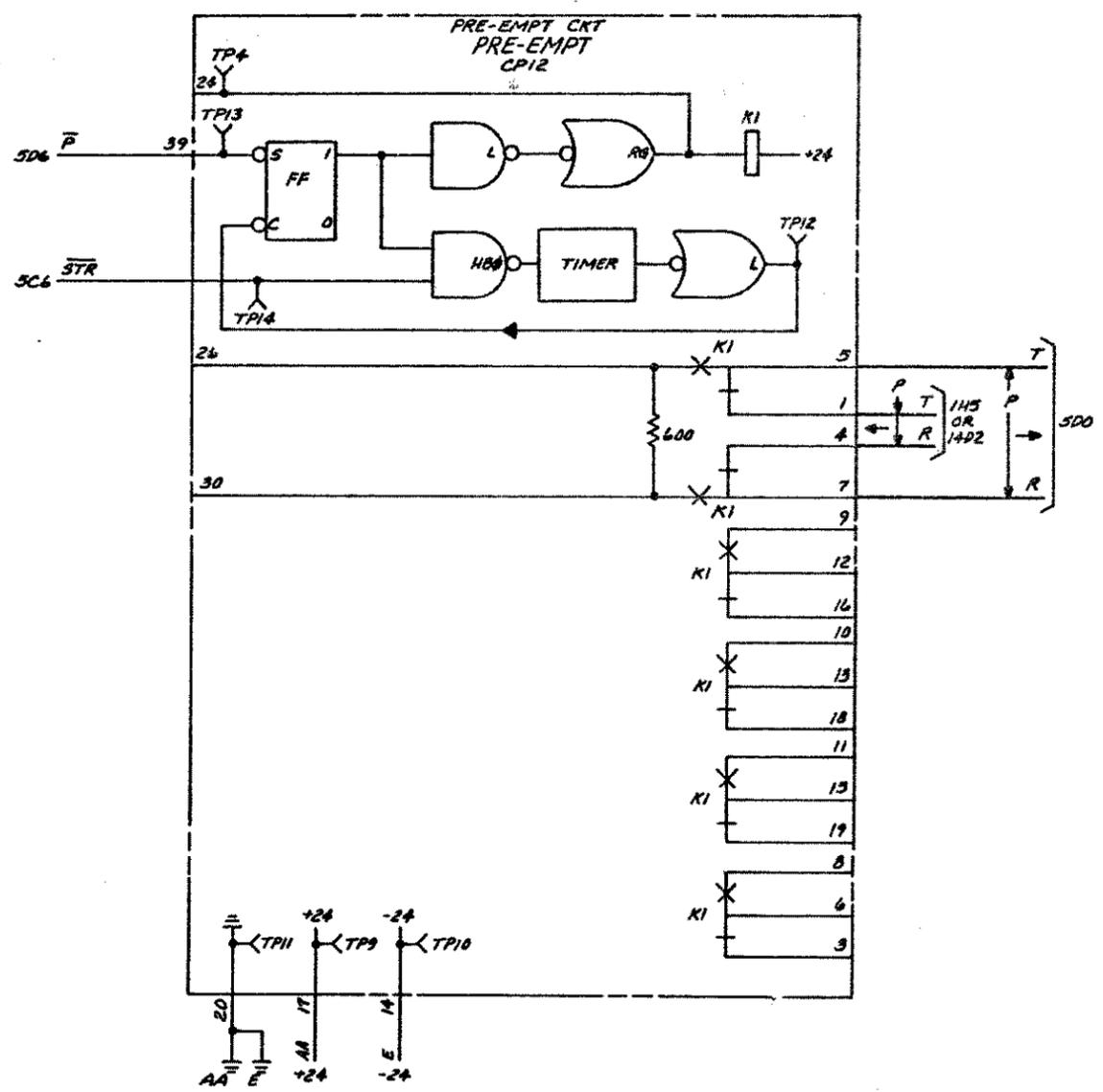


1118

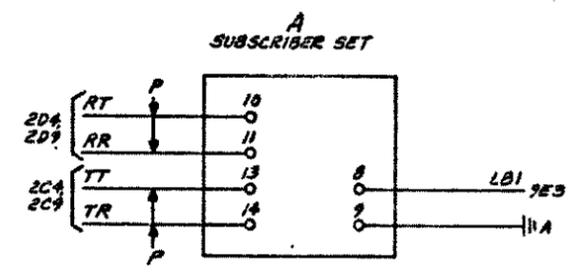
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT		SD-1C245-01-B14	
BELL TELEPHONE LABORATORIES INCORPORATED		6S	PRINTED IN U.S.A.

SD-1C245-01-B14

FS 32
TOUCH-TONE RECEIVER INHIBIT CRT



FS 33
OUTDOOR HANDSET CRT
SEE NOTES 1 & 116



NOTES:
1. SEE BSP SECTION 502-501-117.

SD-1C245-01-815

111B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT		SD-1C245-01-815	
BELL TELEPHONE LABORATORIES INCORPORATED		65	PRINTED IN U.S.A.

APP FIG. 1

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
BG() (CP1)	182	SEE NOTE 201
BUS AMPL(CP2)	163	SEE NOTE 201

APP FIG. 2

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
BAT FEED(CP9)	2A2	SEE NOTE 201
		ED-1C311-G2 (MD)
		ED-1C311-G5

APP FIG. 3 (MFR DISC.)

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
BAT FEED(CP8)	2A7	SEE NOTE 201
		ED-1C311-G1

APP FIG. 4

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
SUPL BAT FEED(CP11)	3A2	SEE NOTE 201
		ED-1C311-G4 (MD)
		ED-1C311-G6

APP FIG. 5 (MFR DISC.)

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
SUPL BAT FEED(CP10)	3A7	SEE NOTE 201
		ED-1C311-G3

APP FIG. 6

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
SIG RCVR(CP3)	4A1	SEE NOTE 201
		ED-1C306-G1 (MD)
		ED-1C306-G2

APP FIG. 7

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
SUPL S DET(CP4)	4E2	SEE NOTE 201

APP FIG. 8

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
DECODER(CP5)	5E1	SEE NOTE 201
DECODER(CP6)	5A4	SEE NOTE 201
DECODER(CP7)	5A8	SEE NOTE 201

APP FIG. 9 (MFR DISC.)

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
CALL-RECALL(CP13)	6A2	SEE NOTE 201
		ED-1C313

APP FIG. 10 (A & M ONLY)

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
PRE-EMPT(CP12)	6A7	SEE NOTE 201
		ED-1C312-G1 (MD)
		ED-1C312-G2

APP FIG. 11

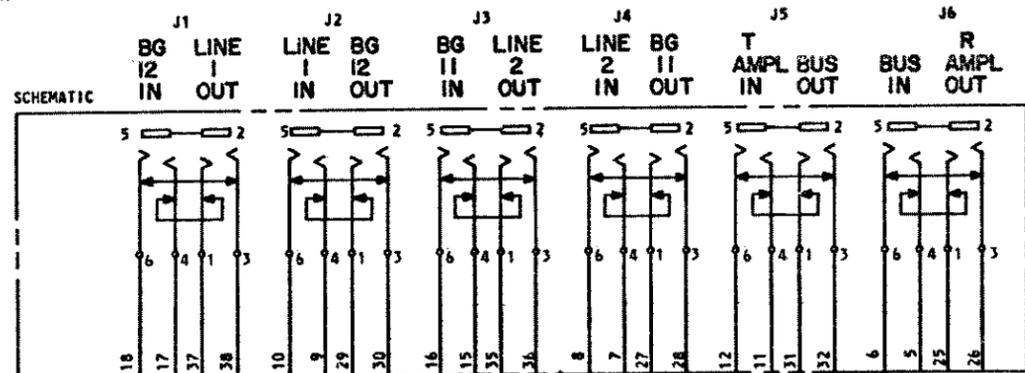
CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
LINE 2 AMPL(CP2)	7B2	SEE NOTE 201
		ED-1C304
		ED-1C305

APP FIG. 12

CIRCUIT PACK		
DESIG	FS LOC	EQPT LOC
LC() AMPL(CP2)	8A1	SEE NOTE 201
LC() (CP14)	8A3	SEE NOTE 201
		ED-1C305-G1 (MD)
		ED-1C314-G2

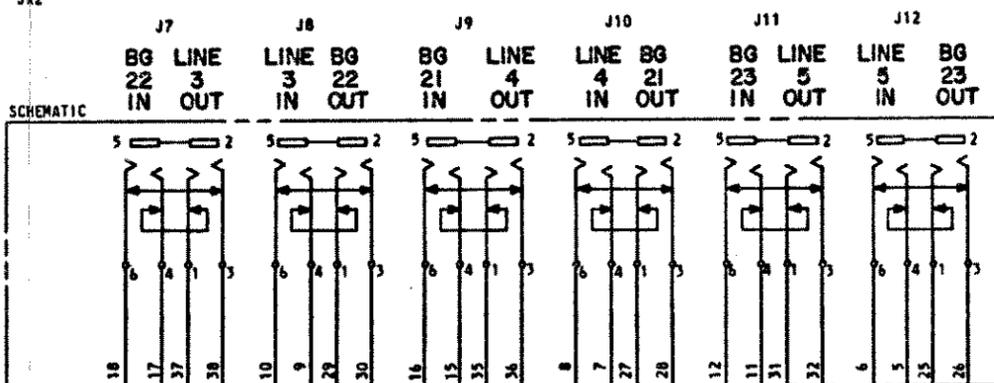
APP FIG. 13

JACK UNIT (PLUG-IN)
JK1



APP FIG. 14

JACK UNIT (PLUG-IN)
JK2



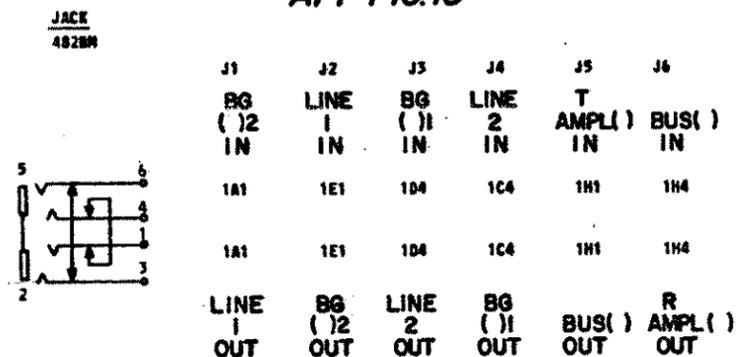
JACK UNIT
ED-1C315-()-01 E/M

JACK	DESIG	LOC	CODE
J1	1A1	1A1	4828M
J2	1E1	1E1	4828M
J3	1D4	1D4	4828M
J4	1C4	1C4	4828M
J5	1H1	1H1	4828M
J6	1H4	1H4	4828M

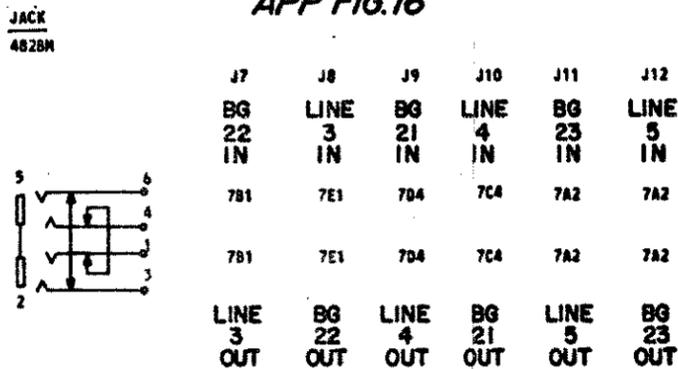
JACK UNIT
ED-1C315-()-62 E/M

JACK	DESIG	LOC	CODE
J7	7B1	7B1	4828M
J8	7E1	7E1	4828M
J9	7D4	7D4	4828M
J10	7C4	7C4	4828M
J11	7A2	7A2	4828M
J12	7A2	7A2	4828M

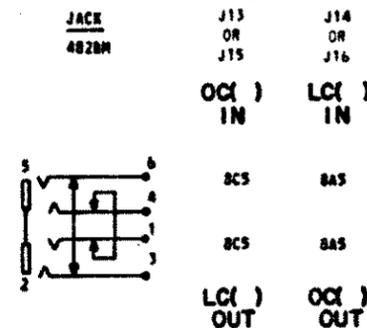
APP FIG. 15



APP FIG. 16

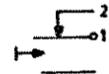


APP FIG. 17



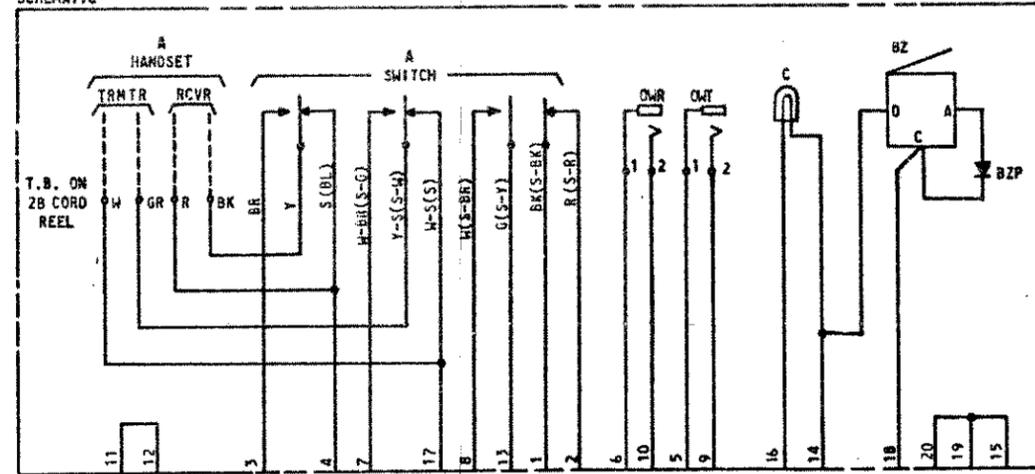
APP FIG. 18

KEY	LC ()	LCR ()	LAMP	LOC	CODE
547A			DESIG LC ()	8E9 13E2	A1 (RD) A1 (RD)
	8E2 13D4	8D2 13E4			



HANDSET UNIT (PLUG-IN)
HANDSET

SCHEMATIC



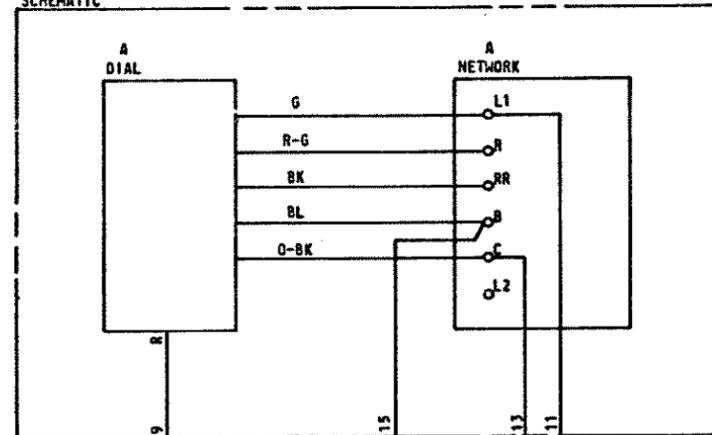
HANDSET UNIT
ED-6G190-()-G2 E/M

APP	DESIG	CONT	LOC	CODE	
SET, HAND	A		988	65GR-61	
SWITCH (HANDSET)	A	BR	988	235F	
		Y			
		S			
		W-BR	988		
		Y-S			
		W-S			
		W	985		
JACK	OWR	1	9A1	223A	
		2			
		OWT	1	9B1	223A
			2		
BUZZER	BZ		90A	7F-49	
DIODE	BZP		7E4	446F	
LAMP	C		9F4	A1	

APP FIG. 20

DIAL UNIT (PLUG-IN)
TT DIAL

SCHEMATIC



DIAL UNIT
ED-6G191-()-G2 E/M

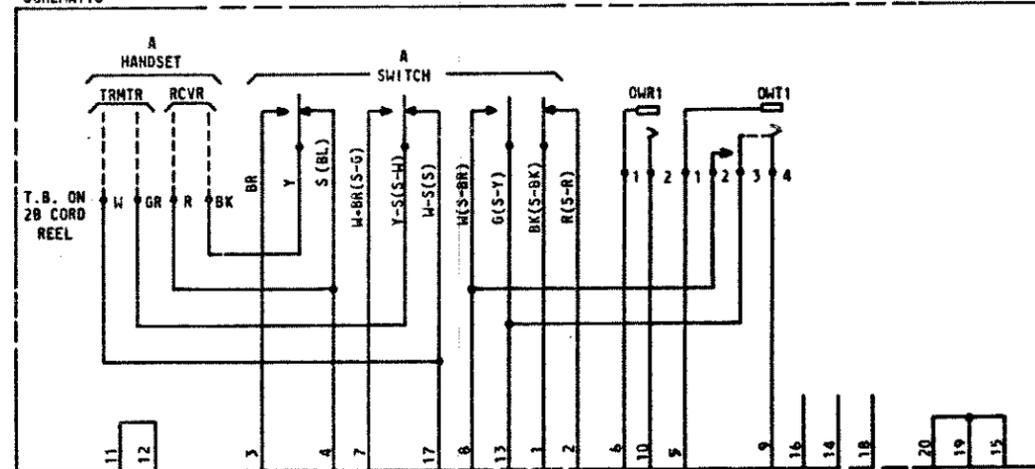
DIAL	LOC	CODE
DESIG A	9A3	35C3A

NETWORK	LOC	CODE
DESIG A	9A5	425E

APP FIG. 21 (MFR DISC.)

HANDSET UNIT (PLUG-IN)
HANDSET

SCHEMATIC



HANDSET UNIT
ED-6G190-()-G3 E/M

APP	DESIG	CONT	LOC	CODE	
SET, HAND	A		988	65GR-61	
SWITCH (HANDSET)	A	BR	988	235F	
		Y			
		S			
		W-BR	988		
		Y-S			
		W-S			
		W	985		
JACK	OWR1	1	10F2	223A	
		2			
		OWT1	1,4	10G2	213A
			2,3	9C6	

SD-1C245-01-C2

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

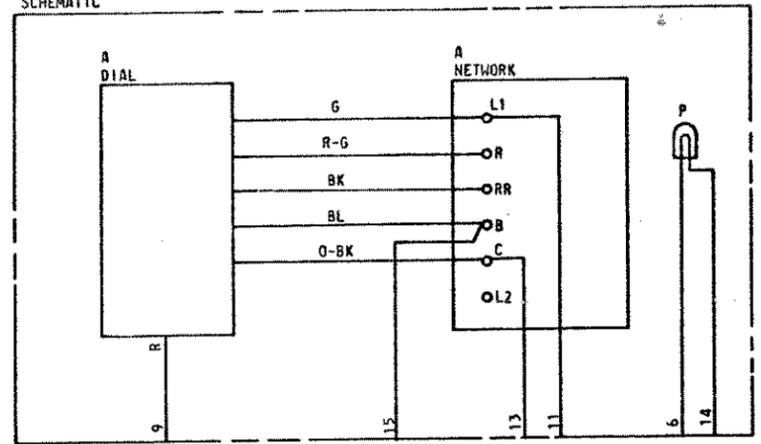
SD-1C245-01-C2

BELL TELEPHONE LABORATORIES
INCORPORATED

65

APP FIG. 22

DIAL UNIT (PLUG-IN)
TY DIAL
SCHEMATIC

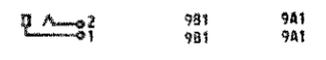


DIAL UNIT
ED-6G191-()-G3E/H

DIAL		
DESIG	LOC	CODE
A	9A3	35C3A
NETWORK		
DESIG	LOC	CODE
A	9A5	425E
LAMP		
DESIG	LOC	CODE
P	10D4	A1

APP FIG. 23

JACK
223A



OWT	OWR
9B1	9A1
9B1	9A1

APP FIG. 24

SET, HEAD TELEPHONE

DESIG	LOC	CODE
A	9B2 OR 10G3	52PR

APP FIG. 25 (MFR Disc.)

SET, LOUDSPEAKER

DESIG	LOC	CODE
LS	10B4	106A*

* LOUDSPEAKER SET IS PROVIDED WITH 2Y LAMP WHICH SHALL BE DISCARDED AND REPLACED WITH A1 LAMP.

APP FIG. 26

SIGNAL, HORN

DESIG	LOC	CODE
A	10B7	KS-16301, L8 BACKBOX KS-16301, L15 RELAY AND L2(115V DC) HORN(MFR DISC) OR L5(115V AC) HORN OR L6(48V AC) HORN(MFR DISC) (SEE NOTE 217)

APP FIG. 27 (MFR DISC.)

SET, SUBSCRIBER

DESIG	LOC	CODE
A	10B2	695A

APP FIG. 28

SET, TELEPHONE

DESIG	LOC	CODE
A	9D8	2504B-() SEE NOTE 209

CONNECTOR, JACK

DESIG	LOC	CODE
A	9D8	KS-16690, L1

APP FIG. 29

SET, TELEPHONE

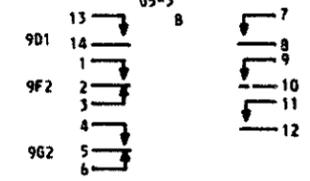
DESIG	LOC	CODE
B	9D8	2554B-() SEE NOTE 210

APP FIG. 30

SET, HAND

DESIG	LOC	CODE
B	9E2	65GR-()

HANDSET MTG
G5-3



RESISTOR

DESIG	LOC	CODE
R1	9E1	KS-14603, L3A 147Ω

APP FIG. 31

BUZZER

DESIG	LOC	CODE
BZ	9D4	7F-49

DIODE

DESIG	LOC	CODE
BZP	9D4	446F

APP FIG. 32

LAMP

DESIG	LOC	CODE
C	9G4	A1

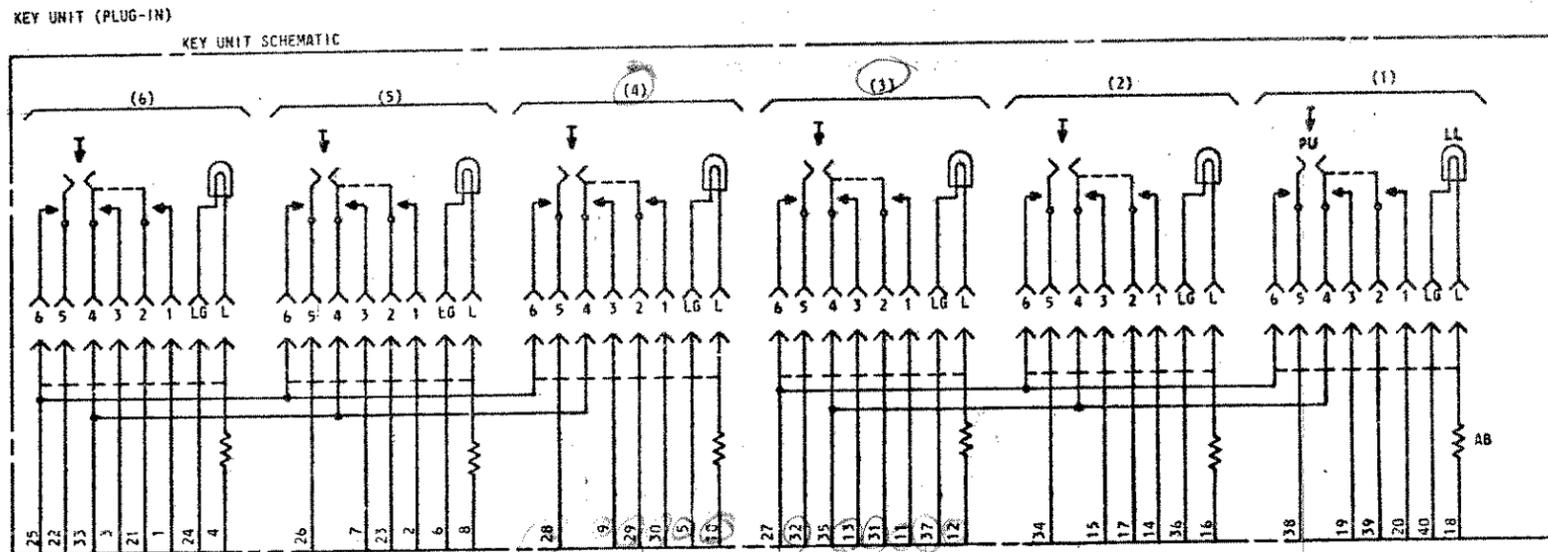
DRAWING
ISSUE
49
53

ISSUE
11B

SD-IC245-01-C3

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	2	SD-IC245-01-C3
BELL TELEPHONE LABORATORIES INCORPORATED	6S	PRINTED IN U.S.A.

APP FIG. 33



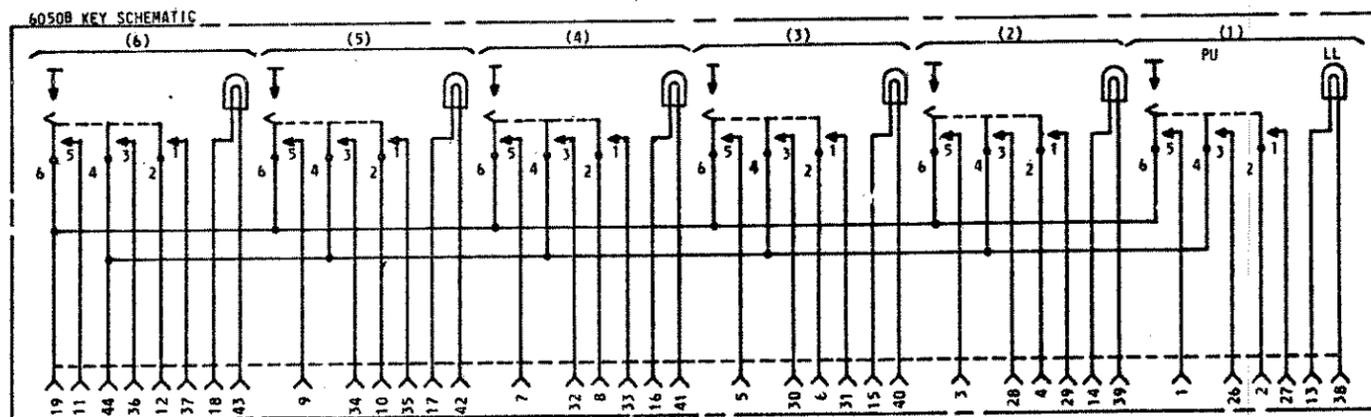
KEY UNIT (PLUG IN) ED-66193-()5E/M

53502 KEY*				PLUG		RESISTOR	
DESIG	CONT	LOC	LAMP	DESIG	LOC	CODE	
(1)**			DESIG	LOC			
(1)**						508A	
(2)						508B	
(3)					6JAB	KS-20289, L6C	
(4)	PU	1-2				348	
(5)		3-4	1186	LL		1106	
(6)		5-6				508E	
						508F	

* PROVIDED WITH [6] 53A LAMPS

** NUMBERS IN PARENTHESIS ARE FOR REFERENCE ONLY AND ASSOCIATE KEY SEGMENT WITH PLUGS AND RESISTORS.

APP FIG. 34
(SEE NOTE 214 FOR CABLES REQ'D)



KEY E/M LAMP*

6050B-() KEY				
DESIG	CONT	LOC	52A LAMP	
(1)**			DESIG	LOC
(1)**				
(2)	1-2			
(3)	3-4			
(4)	PU	1186	LL	1106
(5)	5-6			
(6)				

* KEY IS PROVIDED WITH 51A LAMPS WHICH ARE TO BE DISCARDED AND REPLACED BY 52A LAMPS.

** NUMBERS IN PARENTHESIS ARE FOR REFERENCE ONLY.

APP FIG. 35

SEE NOTE 215

SET, HAND

DESIG	LOC	CODE
c	982	G3CR-()

SD-10245-01-C4

DRAWING ISSUE 48

A

B

C

D

E

F

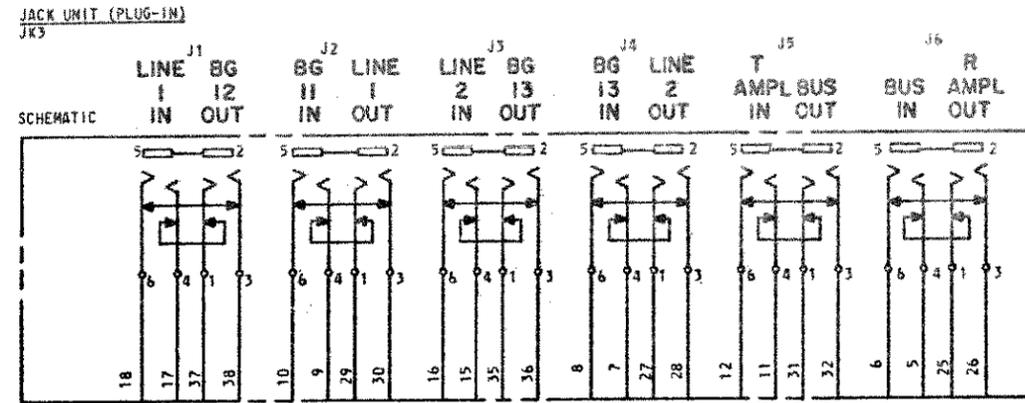
G

H

ISSUE 11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	2	SD-10245-01-C4
BELL TELEPHONE LABORATORIES INCORPORATED	65	

APP FIG. 36



JACK UNIT
ED-1C315-()-G3E/W

JACK	DESIG	LOC	CODE
J1	12F1	4828M	
J2	12F4	4828M	
J3	12B2	4828M	
J4	12A3	4828M	
J5	12H1	4828M	
J6	12H4	4828M	

APP FIG. 37

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
BG() (CP1)	12C2	SEE NOTE 201	ED-1C304
BUS AMPL() (CP2)	12G3	NOTE 201	ED-1C305

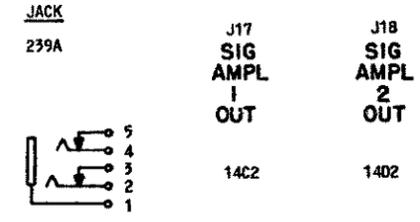
APP FIG. 38

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
LC AMPL (CP2)	13A1	SCE	ED-1C305
LC (CP14)	13A3	NOTE	ED-1C314
CALL RECALL (CP13) (MD)	13A7	201	ED-1C313
(U) CP15	13A7		ED-1C767

APP FIG. 39

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
SIG AMPL (CP2)	14B1	SEE NOTE 201	ED-1C305

APP FIG. 40



APP FIG. 41

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
CALL-RECALL (CP15)	6A2	NOTE 201	ED-1C767

APP FIG. 42

SET, LOUDSPEAKER	FS LOC	EQPT LOC	CODE
LS	10B4	SEE NOTE 129	1060

APP FIG. 43

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
S DET/INH (CP16)	14C3	SEE NOTE 201	ED-2C439-() GR1

APP FIG. 44

CIRCUIT PACK	FS LOC	EQPT LOC	CODE
PRE-EMPT (CP12)	15A2	SEE NOTE 201	ED-1C312-() GR2

APP FIG. 45

SET, SUBSCRIBER	FS LOC	EQPT LOC	CODE
A	15B7		526A

CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER
A	1-1/3	-24 SIG	APP FIG. 11, 37, 38, 39, AND [2] APP FIG. 1, 12, 18, AND [5] APP FIG. 19, 22, 31, 32
B	1-1/3	-24 SIG	APP FIG. 2 OR 3 AND [4] APP FIG. 4 OR 5
C	1-1/3	-24 SIG	APP FIG. 6, 7
D	1-1/3	-24 SIG	[5] APP FIG. 26 [5] APP FIG. 9 OR 41
E	1-1/3	-24 SIG	APP FIG. 8, 10
F	1-1/3	-24 SIG	[5] APP FIG. 25
AA	1-1/3	+24 SIG	APP FIG. 8, 10

BATTERY SYMBOL		VOLTAGE RANGE	
-24	+24	21-27	21-27

CURRENT DRAINS AMPERES	LIST 1 AVERAGE DRAIN PER BUSY HOUR		LIST 2 CURRENT DRAIN FOR DISTRIBUTION EQPT	
	-24	+24	-24	+24
SINGLE CODE AUX STATION J99340A	0.7	-	1.0	-
SINGLE CODE MAIN STATION J99340B	1.0	-	1.5	-
MULTIPLE CODE MAIN OFFICE J99240C	1.3	0.6	2.8	0.6
MULTIPLE CODE MAIN OFFICE J99340B	1.9	0.5	4.3	0.5
MULTIPLE CODE MAIN OFFICE J 99340E	1.6	0.5	3.3	0.5
APP FIG 19, 20	0.1	-	0.1	-
APP FIG 19, 22	0.15	-	0.15	-
APP FIG 42	0.2	-	0.2	-
APP FIG 31	0.05	-	0.05	-
APP FIG 32	0.05	-	0.05	-
SINGLE CODE MAIN STATION J99340F	1.0	-	1.5	-
MULTIPLE CODE MAIN OFFICE J99340G	1.3	0.6	2.6	0.6

CIRCUIT NOTES: (CONT)

FEATURE OR OPTION	PROVIDE		QUANTITY
	APP FIG.	APP OR WRG	
MAIN STATION SINGLE CODE (SEE B02)			
BRIDGE AND AMPLIFIER WITH TALKBACK	1		
BRIDGE AND AMPLIFIER WITH LOOPBACK	37		1 PER CKT
TALK BATTERY FEED	2		
SUPPLEMENTARY TALK BATTERY FEED	4		AS REQD MAX 3 PER CKT
SIGNAL RECEIVER AND DETECTOR	SINGLE DIGIT CODE	6	1 PER CKT
	TWO DIGIT CODE	7	1 PER APP FIG. 6
	TWO CODE DIGIT WITH INHIBIT CKT	43	
LINE TEST AND MICE JKS WITH TALKBACK	13		
LINE TEST AND MICE JKS WITH LOOPBACK	36		
CALL-RECALL-ALERT	41		
BRIDGE AND AMPLIFIER FOR ADDITIONAL LINES AND/OR LINE CONNECT	11		1 PER CKT
TEST AND MAINTENANCE JACKS FOR ADDITIONAL LINES	14		

MULTIPLE CODE SIGNALING UP TO 5 CODES PER LOCATION (SEE B03)			
BRIDGE AND AMPLIFIER WITH TALKBACK	1		
TALK BATTERY FEED	2		1 PER CKT
SUPPLEMENTARY TALK BATTERY FEED	4		AS REQD SEE B03
TOUCH-TONE [®] RECEIVER AND DECODER 2 DIGIT	8	W	
TOUCH-TONE [®] RECEIVER AND DECODER 3 DIGIT	8	X	1 PER CKT
TOUCH-TONE [®] RECEIVER INHIBIT CKT	44		
CALL-RECALL-ALERT	FIRST APP FIG. 41	41	2 PER STATION CODE, SEE B03
	ADDITIONAL APP FIG. 41		Y
	TIMER		T
LINE TEST AND MAINTENANCE JACKS	15		
BRIDGE AND AMPLIFIER FOR ADDITIONAL LINES AND/OR LINE CONNECT	11		1 PER CKT
TEST AND MAINTENANCE JACKS FOR ADDITIONAL LINES	16		
LINE CONNECT	RELAY AND AMPLIFIER	12	AS REQD MAX 2 PER CKT
	KEYS AND LAMPS	18	1 PER APP FIG. 12
TEST AND MAINTENANCE JACKS FOR LINE CONNECT	17		AS REQ MAX 2 PER CKT

OPERATORS POSITION FOR BAY MOUNTING ASSEMBLY SEE NOTE 213	WITHOUT PICKUP	PLUG-IN HAND SET UNIT		QUANTITY
		PLUG-IN DIAL UNIT	WITHOUT PREEMPT	
		PLUG-IN DIAL UNIT	20	
		WITH PREEMPT	22	
		LOUDSPEAKER	25	
	WITH PICKUP	PLUG-IN HAND SET UNIT	19	MAX 1 PER POSITION MAX 1 POSITION PER APP FIG 2 OR 4
		PLUG-IN DIAL UNIT	20	
		PLUG-IN KEY UNIT	33	
MISC MOUNTED OPERATOR'S POSITION	DESK TELEPHONE SET	WITHOUT PICK-UP	28	MAX 2 PER APP FIG. 2 OR 4
		WITH PICK-UP	29, 34	MAX 1 PER APP FIG. 2 OR 4 & 34
	WALL TELEPHONE SET	WITHOUT PICK-UP	29	MAX 2 PER APP FIG. 2 OR 4
		WITH PICK UP	29, 34	MAX 1 PER APP FIG. 2, OP 4 & 34

CIRCUIT NOTES: (CONT)

FEATURE OR OPTION	PROVIDE		QUANTITY
	APP FIG.	APP OR WRG	
MISC MOUNTED TEL JACK AND HAND SET APPEARANCES	TEL JACKS	23	AS REQ SEE NOTE 211
	HEAD SET	24	
	OUTDOOR HAND SET	45	AS REQ
	HAND SET WITH HAND SET MTG	30	
	HAND SET	35	
MISC MOUNTED SIGNALS	HORN SIGNAL	26	MAX 1 PER APP FIG. 9
	BUZZER	31	AS REQ.
MISC MOUNTED LOUDSPEAKER	LAMP	32	AS REQ SEE NOTE 212
		42	AS REQ, MAX 2 PER APP FIG 2, 3 4 OR 5

MULTIPLE CODE SIGNALING FOR TWO ORDER CKTS UP TO 5 CODES PER LOCATION (SEE B04)			
BRIDGE AND AMPLIFIER WITH TALKBACK	1		2 PER CKT
TALK BATTERY FEED	2		1 PER APP FIG. 1
SUPPLEMENTARY TALK BATTERY FEED	4		AS REQD MAX 4 PER APP FIG. 1 SEE NOTE 219
SIGNALING AMPLIFIER	TOUCH-TONE [®] RECEIVER AND DECODER 2 DIGIT	39	
	TOUCH-TONE [®] RECEIVER AND DECODER 3 DIGIT	8	W X
TOUCH-TONE [®] RECEIVER INHIBIT CKT	44		1 PER CKT
CALL-RECALL-ALERT	FIRST APP FIG. 41	41	Z 1 PER STATION CODE, MAX 5 PER CKT
	ADDITIONAL APP FIG. 41		Y
	TIMER		T
LINE TEST AND MAINTENANCE JACKS	15		1 PER APP FIG. 1
TEST AND MAINTENANCE JACKS FOR SIGNALING AMPLIFIER	40		1 PER CKT.

MULTIPLE CODE SIGNALING FOR TWO ORDER CKTS AND DIALED CUT THROUGH FOR UP TO 5 CODES PER LOCATION (SEE B05)			
BRIDGE AND AMPLIFIER WITH TALKBACK	1		2 PER CKT
TALK BATTERY FEED	2		1 PER APP FIG. 1
SUPPLEMENTARY TALK BATTERY FEED	4		AS REQD MAX 2 PER APP FIG. 1
SIGNALING AMPLIFIER	TOUCH-TONE [®] RECEIVER AND DECODER 2 DIGIT	39	
	TOUCH-TONE [®] RECEIVER AND DECODER 3 DIGIT	8	W X
TOUCH-TONE [®] RECEIVER INHIBIT CKT	44		1 PER CKT
CALL-RECALL-ALERT	FIRST APP FIG. 41	41	Z 1 PER STATION CODE, MAX 4 PER CKT
	ADDITIONAL APP FIG. 41		Y
LINE CONNECT CKT	38		
LINE CUT KEY AND LAMP CKT	18		1 PER CKT
TEST AND MAINTENANCE JACKS FOR SIGNALING AMPL	40		
LINE TEST AND MAINTENANCE JACKS	15		1 PER APP FIG. 1

CIRCUIT NOTES: (CONT)

RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES	CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
					STD	A&M	MD
78				127	2	3	
					4	5	
					41	9	
					1		
					42	25	
108				131		21	
118					49	27	

- 104. TRANSMISSION LEVELS ARE -16 dB TRANSMITTING AND +7 dB RECEIVING. IF THE CONNECTING CIRCUIT REQUIRES GAIN, LOSS AND/OR EQUALIZATION THE INTERCONNECTION SHOULD BE MADE VIA A 44VA TYPE TELEPHONE REPEATER OR EQUIVALENT. (SEE B5P SECTIONS 332-104-100 AND AB24.100.01).
- 105. WHEN ALL PORTS OF THE 4-WAY 4-WIRE BRIDGE ARE NOT USED THE UNUSED PORTS MUST BE TERMINATED IN 600 OHMS. THIS MAY BE ACCOMPLISHED IN ACCORDANCE WITH NOTE 206, OR PORTS MAY BE TERMINATED BY CONNECTING T, R AND T1, R1 LEADS TO MULTI-STATION LINE CIRCUIT FIGURE 21, OR EQUIVALENT.
- 106. LOUDSPEAKER CONNECTIONS MAY BE MADE TO ANY TALK BATTERY FEED CIRCUIT AND MAY BE WIRED TO PROVIDE (a) NO SPEAKER CUT-OFF, (b) SPEAKER CUT-OFF BY ASSOCIATED OFF-HOOK TELEPHONE SET AT THE SIGNALING POSITION, OR (c) SPEAKER CUT-OFF BY ASSOCIATED OFF-HOOK TELEPHONE SET OR ASSOCIATED ACTIVE JACK AT ANY POSITION. FOR REQUIRED WIRING CONNECTIONS SEE NOTE 202. IT IS RECOMMENDED THAT A LOUDSPEAKER LOCATED IN THE VICINITY OF ANY TELEPHONE INSTRUMENT BE PROVIDED WITH SPEAKER CUT-OFF BY THAT INSTRUMENT TO PREVENT ACOUSTIC FEEDBACK. CONNECTIONS MAY BE MADE TO REMOTELY LOCATED JACKS TO FACILITATE PORTABLE SPEAKERS.
- 107. THE SIGNAL RECEIVER CIRCUIT MUST BE STRAPPED IN ACCORDANCE WITH NOTE 203 FOR THE SINGLE DIGIT STATION CODE OR THE FIRST DIGIT OF A TWO-DIGIT STATION CODE.
- 108. THE SUPPLEMENTARY SIGNAL DETECTOR MUST BE STRAPPED IN ACCORDANCE WITH NOTE 204 FOR THE SECOND DIGIT OF A TWO-DIGIT STATION CODE. FOR A TWO DIGIT CODE, THE FIRST AND SECOND DIGITS MUST BE DIFFERENT NUMBERS, i.e. CODES 00, 11, 22, ... 99 ARE NOT ALLOWED
- 109. CONNECT AS REQUIRED TO GIVE PROPER AUDIBLE AND VISUAL SIGNALS PROVIDED FOR SERVICE ALARMS: ALARM LEADS ARE DESIGNATED AS FOLLOWS:

ALARM CIRCUITS	LEADS			
	A	B	C	D
	SA	SAW	SAV	SAVR
TB-3 AND TH-3 RADIO OFFICE AUDIBLE AND VISUAL ALARM CKT	A	AWTB	-	-
TB-3 AND TH-3 RADIO DISTRIBUTION FUSE AND INDIVIDUAL ALARMS FOR COMMON OFFICE AND BLDG EQPT	-	-	F	FOTB
ANNUNCIATOR CKT	A	-	AC	-
AUDIBLE ALARM AND PILOT LAMP CKT	A	-	AV	-
AUDIBLE AND VISUAL ALARM CKT	A	-	SAV	-

* CONNECT TO LOCAL CKT GRD. IN THESE CASES PAIRING OF LEADS IS NOT REQUIRED.

GENERAL PURPOSE 4-WIRE ORDER CKT

SD-IC245-01-01

BELL TELEPHONE LABORATORIES

65

INCORPORATED

ISSUE 11B

SD-IC245-01-01

CIRCUIT NOTES: (CONT)

110. THE DECODER MUST BE STRAPPED IN ACCORDANCE WITH NOTE 205. FOR 3 DIGIT OFFICE CODE, OPTION 15 IS REQUIRED. FOR 2 DIGIT OFFICE CODE, OPTION 15 IS REQUIRED AND STRAPPING FOR THE MIDDLE DIGIT IS NOT REQUIRED. FROM ONE TO FIVE SEPARATE LAST DIGITS MAY BE USED BY PROVIDING A CONNECTION BETWEEN CP13 (CALL-RECALL-ALERT CKT) AND THE ASSOCIATED STATION CODE (SC) OUTPUT OF CP7.

111. RELAY (K4) OF FS 9 OPERATES UPON RECEPTION OF THE INCOMING ALERT SIGNAL. THE OUTPUT LEAD H MAY BE CONNECTED TO MISC. HORN SIGNAL. LEADS A1, B1, C1, D1 MAY BE CONNECTED AS REQUIRED TO GIVE AUDIBLE AND VISUAL SIGNALS PROVIDED FOR SERVICE ALARMS. ALARM LEADS ARE DESIGNATED AS FOLLOWS:

ALARM CIRCUITS	LEADS			
	A1	B1	C1	D1
	SERVICE ALARM DESIGNATIONS			
	SAT	SART	SAVI	SAVRT
TD-3 AND TH-3 RADIO OFFICE AUDIBLE AND VISUAL ALARM CKT	A	ARTN	-	-
TD-3 RADIO DISTRIBUTION FUSE AND INDIVIDUAL ALARMS FOR COMMON OFFICE AND BLDG EQPT	-	-	F	FRTN
ANNUNCIATOR CKT	A	*	AC	*
AUDIBLE ALARM AND PILOT LAMP CKT	A	*	SV	*
AUDIBLE AND VISUAL ALARM CKT	A	*	SV	*

* CONNECT TO LOCAL CKT GRD. IN THESE CASES PAIRING OF LEADS IS NOT REQUIRED.

112. RELAY (K8) OPERATED PREVENTS LOCK-UP OF SERVICE ALARM OUTPUTS ON INCOMING SIGNALING. THIS IS PROVIDED BY A LOCKING CONTACT CLOSURE IN CONNECTING CIRCUIT AND SHOULD BE USED ONLY IN UNATTENDED OR PARTIALLY ATTENDED OFFICES. THE CONTROL SHOULD APPEAR AT ONLY ONE LOCATION WITHIN THE OFFICE. LEADS L3 AND L63 FROM RELAY (K8) CONTACT 6 MAY BE CONNECTED TO PROVIDE A VISUAL INDICATION AS REQUIRED. ALARM CIRCUIT CONNECTING LEADS ARE DESIGNATED AS FOLLOWS:

ALARM CIRCUITS	LEADS	
	G	GRTN
TD-2, TD-3 AND TH-3 RADIO DISTRIBUTION FUSE AND INDIVIDUAL ALARMS FOR COMMON OFFICE AND BLDG EQPT	G	GRTN

113. IN OFFICES PROVIDED WITH MORE THAN ONE STATION CODE ONE FS9 IS PROVIDED WITH EACH SEPARATE CODE. THE CR AND CRG LEADS SHOULD BE CONNECTED TO THE TALK BATTERY FEED CIRCUIT(S) USED WITH THE TELEPHONE INSTRUMENT(S) ASSOCIATED WITH THE SAME CODE.

114. CONNECT AS REQUIRED TO PROVIDE A 300 ms GROUND FOR REMOTE OPERATION OF LINE CONNECT (LEADS 1 AND 2) OR LINE CONNECT RELEASE (LEADS 3 AND 4).

CONTROL CIRCUITS	LEADS			
	1	2	3	4
E TYPE STATUS REPORTING AND CONTROL SYSTEM REMOTE SWITCH CKT	RS	RSR	RS	RSR

CIRCUIT NOTES: (CONT)

119. CONNECT AS REQUIRED TO PROVIDE A STATUS INDICATION OF STATE OF LINE CONNECT CIRCUIT.

CONTROL CIRCUITS	LEADS	
	5	6
E TYPE STATUS REPORTING AND CONTROL SYSTEM STATUS INPUT CKT	SI	SIR

116. MULTIPLE CONNECTION OF JACKS FOR USE OF HEADSETS AND/OR HANDSETS MAY BE MADE TO TERMINALS 21, 23 OR 29, 30 (OF CP9 OR CP11) TO PROVIDE CONVENIENT ACCESS APPEARANCES. THEY SHOULD BE WIRED TO INCLUDE NO MORE THAN 50-OHM LOOP RESISTANCE BETWEEN THE TALK BATTERY FEED AND THE JACK APPEARANCE. DIRECT CONNECTIONS OF HEADSETS AND/OR HANDSETS MAY ALSO BE MADE TO THESE TERMINALS. NOTE, HOWEVER, THAT SIMULTANEOUS USE OF MORE THAN THREE HEADSETS AND/OR HANDSETS PER CP9 OR CP11 TALK BATTERY FEED (I.E. THREE MULTIPLES AT TERMINALS 21, 23 OR 29, 30 OR COMBINATION THEREOF) WILL RESULT IN SEVERE DEGRADATION OF THE TRANSMIT POWER.

117. MULTIPLE CONNECTION OF LAMPS AND BUZZERS TO FS 6, FS 7, FS9 OR FS 31 SHOULD BE LIMITED TO A MAXIMUM TOTAL CURRENT DRAIN OF 500 MA.

118. ONE BATTERY FEED (CP9 OR CP11) CAN SUPPLY TWO APPEARANCES OF A TEL SET OR HANDSET WITH TT DIAL AND NETWORK. THREE APPEARANCES PER BATTERY FEED ARE TOLERABLE WHEN SPECIFICALLY REQUIRED. NOTE THE APPEARANCES MAY BE CONNECTED AT TERMINALS 7, 32 (WITHOUT PICKUP), OR 10, 13 (WITH PICKUP) OR A COMBINATION THEREOF.

119. LOUDSPEAKERS SHALL BE LIMITED TO A MAXIMUM OF 2 PER TALK BATTERY FEED.

120. UNDER APP FIG. OPTION 11 AND 12 THE FIRST LINE CONNECT CIRCUIT (FS12) SHOULD BE CONNECTED TO PORT 3 OF THE 4-WAY 4-WIRE BRIDGE (FS11). IF REQUIRED, A SECOND LINE CONNECT CIRCUIT (FS12) MAY BE CONNECTED TO PORT 1 OF THE 4-WAY 4-WIRE BRIDGE (FS11). IF THE SECOND LINE CONNECT CIRCUIT IS NOT REQUIRED, PORT 1 OF THE 4-WAY 4-WIRE BRIDGE (FS11) SHOULD BE WIRED IN ACCORDANCE WITH APP FIG. OPTION -12.

121. THE TOUCH-TONE CALLING RECEIVER IS A J99289B,L2 UNIT MODIFIED TO OPERATE WITH -24 AND +24 VOLTS INSTEAD OF -48 VOLTS. REFER TO NOTE 208 FOR MODIFICATION.

122. WHEN A SINGLE JACK CIRCUIT APPEARANCE IS REQUIRED FOR MORE THAN ONE ORDER CIRCUIT IN CONJUNCTION WITH PICKUP KEYS, THE TRANSMIT AND RECEIVE PAIRS FROM MAKE CONTACTS OF THE K3 RELAY IN ASSOCIATED BATTERY FEEDS SHALL BE MULTIPLED TOGETHER. THIS ALSO APPLIES WHEN A SINGLE HANDSET APPEARANCE PER FS16 IS REQUIRED.

123. UNDER APP FIG OPTION 38 THE SC5 LEAD IS CONNECTED TO CP13 (CKT5) TO PROVIDE CUT-THRU WHEN A 5 IS DIALED AS THE LAST DIGIT OF AN OFFICE CODE. THE NUMBER OF STATION CODES AVAILABLE IS THEREFORE LIMITED TO A MAXIMUM OF FOUR BY PROVIDING A CONNECTION BETWEEN CP13 (CKT1-4) AND THE ASSOCIATED STATION CODE (SC1-4) OUTPUT OF CP7.

124. WHEN HEAD SET IS NOT IN USE IT MUST BE UNPLUGGED TO ALLOW CALL RELAY TO RELEASE.

125. IF REQUIRED, A 6051B KEY UNIT WITH 12 PICKUP KEYS, MISC. MOUNTED, MAY OPTIONALLY BE USED TO ACCOMMODATE UP TO A MAX. OF 12 ORDER WIRES. IF EVEN A GREATER NUMBER OF ORDER WIRES ARE TO BE ACCOMMODATED, REFERENCE SHOULD BE MADE TO BSP 512-240-101 AND BSP 512-240-100. THE FORMER DESCRIBES 400 SERIES TYPE KEY MOUNTINGS IN MODULES OF 6, 12 AND 18 LINE SIZES, WHILE THE LATTER DESCRIBES 400 SERIES TYPE KEY MOUNTINGS IN MODULES OF 30, 60, 90 AND 120 LINE SIZES.

126. IF ALL-STATIONS ALARM IS NOT REQ'D REMOVE LEAD "AS" BETWEEN CP3 & CP4.

127. STARTING WITH ISSUE 7B TALK BATTERY FEED WITHOUT PICKUP APP. FIG. 3 & 5 HAVE BEEN RATED MD. THIS FEATURE IS IN TALK BATTERY FEED APP FIGS. 2 & 4.

128. LEAD E MAY BE LEFT UNCONNECTED IF ALL STATIONS ALERT IS NOT REQUIRED FOR 2ND-5TH FS9.

CIRCUIT NOTES: (CONT)

129. TWO WIRING MODIFICATIONS ARE REQUIRED FOR 106D REPLACEMENT LOUDSPEAKER:

- FOR 24 VOLT DC SUPPLY CONNECT SPADE TIPPED LEAD FROM PILOT LAMP SOCKET TO SCREW TERMINAL 10.
- GROUNDING SCREEN PREVIOUSLY LOCATED ON THE AMPLIFIER RETAINING BRACKET IN THE 106A IS NOW LOCATED ON THE CHASSIS NEXT TO THE LAMP SOCKET IN THE 106D.

130. WHEN A PARTICULAR TEL SET OR NETWORK & HANDSET IS ASSOCIATED WITH MORE THAN ONE ORDER CIRCUIT IN CONJUNCTION WITH PICK UP KEYS, A SEPERATE BATTERY FEED MUST BE PROVIDED IN EACH ORDER CIRCUIT. RECEIVE PAIRS FROM MAKE CONTACTS OF THE K3 RELAYS IN ASSOCIATED BATTERY FEEDS SHALL BE MULTIPLIED TOGETHER. TRANSMIT PAIRS ARE CONNECTED AS SHOWN ON FS27.

131. AS OF ISSUE 10B APP FIG 21 IS RATED MFR DISC. AND IS REPLACED BY APP FIG 19.

132. THE SUPPLEMENTARY SIGNAL DETECTOR WITH INHIBIT CIRCUIT MUST BE STRAPPED IN ACCORDANCE WITH NOTE 218 FOR THE SECOND DIGIT OF A TWO-DIGIT STATION CODE. FOR A TWO DIGIT CODE, THE FIRST AND SECOND DIGITS MUST BE DIFFERENT NUMBERS, I.E. CODES 00, 11, 22, ...99 ARE NOT ALLOWED.

DRAWING ISSUE
1
2A
3A
5B

ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT
SD-1C245-01-D2
BELL TELEPHONE LABORATORIES
INCORPORATED
65
MADE IN U.S.A.

SD-1C245-01-D2

EQUIPMENT NOTES:

201. THE CIRCUIT PACK EQUIPMENT LOCATIONS ARE VARIED BY THE EQUIPMENT CODE AND ARE CONTROLLED BY THE EQUIPMENT DRAWINGS.
202. THE FOLLOWING TABLE PROVIDES INFORMATION FOR CONNECTIONS TO LOUDSPEAKER FOR CONDITIONS SPECIFIED.

CONDITION	CONNECTIONS AT TALK BATTERY FEED CONNECTOR CP8, CP9, CP10 OR CP11		
	"T" LEAD	"R" LEAD	STRAP
1 NO SPEAKER CUTOFF	18	15	-
2 CUTOFF BY TEL. SET	39	40	-
3 CUTOFF BY JACKS OR HANDSET	25	26	18-24, 6-15
4 CUTOFF BY EITHER 2 OR 3	25	26	24-39, 6-40

203. THE SIGNAL RECEIVER MUST BE STRAPPED FOR THE OFFICE CODE. THE STRAPS ARE MADE BETWEEN TERMINALS ON THE CP3 CONNECTOR AS SHOWN IN TABLE BELOW.

DIGIT	STRAP ON THE CP3 CONNECTOR	
1	5-24-30, 2-4	28-32-36, 8-11
2	6-27-30, 1-2	
3	5-26-30, 3-4	10-32-38, 13-28, 8-11
4	5-24-30, 2-4	
5	6-27-30, 1-2	28-32-39, 9-11
6	5-26-30, 3-4	
7	5-24-30, 2-4	13-32-40, 11-12, 1-2, 6-27-30, 10-28
8	6-27-30, 1-2	
9	5-26-30, 3-4	
0		

204. THE SUPPLEMENTARY SIGNAL DETECTOR MUST BE STRAPPED FOR THE SECOND DIGIT OF AN OFFICE CODE. THE STRAPS ARE MADE BETWEEN TERMINALS ON THE CP4 CONNECTOR AS SHOWN IN TABLE BELOW. THE SECOND DIGIT OF THE TWO DIGIT STATION CODE MUST NOT BE THE SAME NUMBER AS THE FIRST DIGIT.

DIGIT	STRAP ON THE CP4 CONNECTOR	
1	3-7-25, 24-6	12-18-36, 32-35
2	4-8-25, 5-6	
3	3-9-25, 24-23	13-18-34, 36-38, 32-35
4	3-7-25, 24-6	
5	4-8-25, 5-6	15-18-36, 33-35
6	3-9-25, 24-23	
7	3-7-25, 24-6	16-18-38, 34-36, 35-37, 4-8-25, 5-6
8	4-8-25, 5-6	
9	3-9-25, 24-23	
0		

EQUIPMENT NOTES: (CONT)

205. FOR THREE DIGIT OFFICE CODE, (X) OPTION: THE DECODER MUST BE STRAPPED FOR THE FIRST AND MIDDLE DIGIT OF AN OFFICE CODE AND THE LAST DIGIT FOR A POSITION OR STATION WITHIN AN OFFICE. THE STRAPS FOR THE FIRST DIGIT ARE MADE BETWEEN TERMINALS ON THE CP5 AND CP6 CONNECTORS AND FOR THE MIDDLE DIGIT BETWEEN TERMINALS ON THE CP7 CONNECTOR. THE LAST DIGIT REQUIRES A SINGLE CONNECTION BETWEEN THE CP13 OR CP15 CONNECTOR ASSOCIATED WITH THE STATION CALL-RECALL-ALERT UNIT AND A TERMINAL ON THE CP7 CONNECTOR AS SHOWN IN TABLE BELOW. FOR TWO DIGIT OFFICE CODE, (X) OPTION: STRAPPING FOR THE MIDDLE DIGIT IS NOT REQUIRED. STRAPPING FOR THE FIRST AND LAST DIGITS IS THE SAME AS ABOVE.

DIGIT	1ST DIGIT		MIDDLE DIGIT	LAST DIGIT	
	FROM CP5 CONN.	TO CP6 CONN.		FROM CP7 CONN.	TO CP13 OR CP15 CONN.
1	27	39	5-37	31	1
	23	38			
2	27	39	28-37	30	2
	22	38			
3	27	39	29-37	27	3
	21	38			
4	26	39	5-37	9	4
	23	38			
5	26	39	28-37	8	5
	22	38			
6	26	39	29-37		
	21	38			
7	25	39	5-37		
	23	38			
8	25	39	28-37	24-36	
	22	38			
9	25	39	29-37		
	21	38			
0	24	39	28-37	34-36	
	22	38			

206. THE FOLLOWING TABLE PROVIDES STRAPPING INFORMATION FOR 600 OHM TERMINATIONS AT THE PORTS OF THE 4-WAY 4-WIRE BRIDGE.

PORT	STRAP ON THE CP1 CONNECTOR	
	IN	OUT
1		33-40
	1	2-21
2		27-29
	2	19-31
3		12-14
	3	7-28
4		10-11
	4	13-32

207. PAIRING INFORMATION APPLIES TO LEADS 23 INCHES AND LONGER.
208. THE RECEIVER IS TO BE SPECIFIED AND ORDERED AS A H310-116 TOUCH TONE CALLING RECEIVER. THE H310-116 TOUCH TONE RECEIVER IS A MODIFIED VERSION OF THE J99289B, L2 TOUCH TONE RECEIVER SHOWN IN DETAIL ON SD-98148-01. THE BASIC MODIFICATION IS TO CHANGE THE RECEIVER SUPPLY VOLTAGE FROM -48V AND GROUND TO -24V AND +24V, RESPECTIVELY. CHANGES INCLUDE THE FOLLOWING: ALL GROUND POINTS ARE RETURNED TO +24V; ALL -48V POINTS ARE RETURNED TO -24V; ALL -22V POINTS ARE RETURNED TO CHASSIS GROUND. ALL VOLTAGE MEASUREMENTS REMAIN THE SAME AS SPECIFIED FOR THE J99289B, L2

EQUIPMENT NOTES: (CONT)

209. RECEIVER WHEN REFERENCED TO THE GRD TEST POINT ON CP5 (B19) INSTEAD OF FRAME GROUND. TO PROVIDE THE ABOVE MENTIONED CHANGES, THE FOLLOWING WIRING MODIFICATIONS ARE INCLUDED.
- THE UNIT CONNECTOR (F) TERMINALS 9, 15, AND 21 ARE ASSIGNED TO +24, -24 AND GROUND RESPECTIVELY.
 - THE LEAD TO DIODE CR1 OF FS1 ON SD-98148-01 IS REMOVED. (DIODE CR1 IS NO LONGER NEEDED.)
 - TERMINAL 2 OF B1, B2, B9, B19 AND TERMINAL 9 OF (F) CONNECTOR ARE REMOVED FROM CHASSIS GROUND.
 - TERMINAL 5 OF B9, B19 AND TERMINAL 21 OF (F) CONNECTOR ARE CONNECTED TO CHASSIS GROUND.
210. THE 2504B TELEPHONE SET IS FACTORY WIRED FOR 2-WIRE SERVICE. THE FOLLOWING TABLE SHOWS THE CHANGE NECESSARY TO CONVERT THE SET TO 4-WIRE SERVICE. REFER TO BSP 529-225-102 FOR CONNECTIONS. SEE CAD 12 FOR EXTERNAL CONNECTIONS.

LEADS	REMOVE FROM NETWORK	CONNECT TO T81
DIAL (W)	GN	17
LINE SWITCH (BK)	R	18
MOUNTING CORD (BR-Y)		17
MOUNTING CORD (Y-BR)		18

LEADS	REMOVE FROM NETWORK	CONNECT TO NETWORK
DIAL (W)	GN	G
RECEIVER (W)	R	L2
LINE SWITCH (BK)	R	L2
LINE SWITCH (BL)	L2	L1

211. WHEN MULTIPLE APPEARANCES OF TELEPHONE JACKS ARE REQUIRED THEY SHALL BE MOUNTED ON A MISCELLANEOUS BASIS. IF AN EXISTING JACK MOUNTING WITH CONVENIENT SPARE POSITIONS IS NOT AVAILABLE IT IS RECOMMENDED THAT A 255A, OR 215 TYPE JACK MOUNTING OR JACK MOUNTINGS PER ED-60613-() BE USED.
212. WHEN LAMP APPEARANCES ARE REQUIRED THEY SHALL BE MOUNTED ON A MISCELLANEOUS BASIS. IF AN EXISTING JACK MOUNTING WITH A CONVENIENT SPARE POSITION IS NOT AVAILABLE IT IS RECOMMENDED THAT A 1B TYPE INDICATOR OR JACK MOUNTINGS REFERRED TO IN NOTE 211 BE USED.
213. TYPICAL EQUIPMENT ARRANGEMENTS FOR AN OPERATOR'S ORDER CIRCUIT POSITION IN A BAY FRAMEWORK MOUNTING ARE SHOWN ON ED-1C385-().
214. REFER TO BSP 512-210-103 FOR CABLE REQUIRED, INSTALLATION AND CONNECTIONS FOR 6050 B KEY.
215. WHEN HANDSET HANGER IS REQUIRED EITHER A 9B OR 10-61 HANDSET HANGER IS RECOMMENDED.
216. THE FOLLOWING TABLE SHOWS THE WIRING CHANGES NECESSARY TO CONVERT A 2554B TELEPHONE SET WITH J-K CONTACTS OMITTED FROM 2-WIRE TO 4-WIRE SERVICE. OTHERWISE SEE NOTE 210.

LEADS	REMOVE FROM		CONNECT TO	
	NETWORK	TERM STRIP	NETWORK	TERM STRIP
DIAL (W)	GN		G	
RECEIVER (W)	R		L2	
LINE SWITCH (BK)	R		L2	
LINE SWITCH (Y)		2	L2	
LINE SWITCH (BR)	C		L1	
ADD NEW LEAD			C	2

EQUIPMENT NOTES: (CONT)

217. THE XS-16301, L5 115V AC HORN HAS A SOUND LEVEL PRESSURE OF 105dB SPL AT 4 FEET. THIS HIGH LEVEL OUTPUT WAS SELECTED FOR NOISY ENVIRONMENTS IN VERY LARGE OFFICES AND WILL DEFINITELY BE TOO LOUD FOR MANY APPLICATIONS. FOR THOSE SITUATIONS, AN ALARM WITH APPROPRIATE LOUDNESS WILL HAVE TO BE PROVIDED ON A LOCAL BASIS.
218. THE SUPPLEMENTARY SIGNAL DETECTOR WITH INHIBIT CIRCUIT MUST BE STRAPPED FOR THE SECOND DIGIT OF AN OFFICE CODE. THE STRAPS ARE MADE BETWEEN TERMINALS ON THE CP16 CONNECTOR AS SHOWN IN TABLE BELOW. THE SECOND DIGIT OF THE TWO DIGIT STATION CODE MUST NOT BE THE SAME NUMBER AS THE FIRST DIGIT.

DIGIT	STRAP ON THE CP16 CONNECTOR	
1	3-7-25, 24-6	12-18-36, 32-35
2	4-8-25, 5-6	
3	3-9-25, 24-23	13-18-34, 36-38, 32-35
4	3-7-25, 24-6	
5	4-8-25, 5-6	15-18-34, 33-35
6	3-9-25, 24-23	
7	3-7-25, 24-6	16-18-38, 34-36, 35-37, 4-8-25, 5-6
8	4-8-25, 5-6	
9	3-9-25, 24-23	
0		

219. WHEN FS 32 IS USED, A MAXIMUM OF THREE FS 4'S (SUPPLEMENTARY TALK BATTERY FEED) MAY BE EQUIPPED IN CKT 2.
220. WHEN FS32 IS USED, A MAXIMUM OF TWO FS4'S (SUPPLEMENTARY TALK BATTERY FEED) MAY BE EQUIPPED.

DRAWING
ISSUE
24
58

ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT SD-1C245-01-D3

BELL TELEPHONE LABORATORIES INCORPORATED 6S

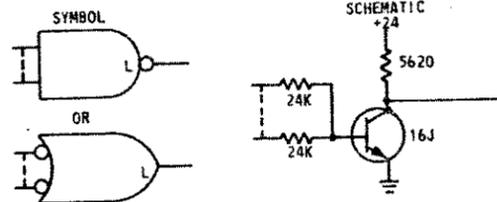
INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS;
CAPACITANCE VALUES ARE IN MICROFARADS;
VALUES PRECEDED BY THE SYMBOL + (PLUS) OR
- (MINUS) ARE IN VOLTS.

302. FOR ALL GATES; ANY "0" INPUT PRODUCES A "1" OUTPUT. ALL "1" INPUTS PRODUCE A "0" OUTPUT. A "0" IS DEFINED AS A VOLTAGE LEVEL GREATER THAN +3 VOLTS. A "1" IS DEFINED AS A VOLTAGE LEVEL LESS THAN +0.3 VOLTS (NOMINALLY GROUND).

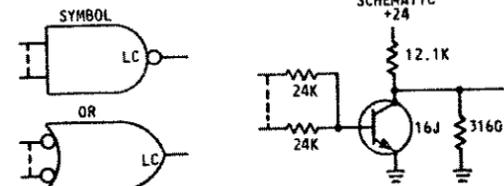
LOW FANOUT GATE

USED WHERE FANOUT REQUIREMENTS ARE LOW.



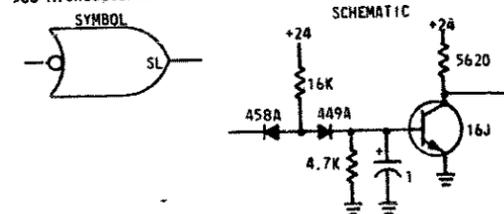
LOW FANOUT CLAMPED GATE

USED WHERE THE "0" OUTPUT MUST BE CLAMPED AT NO MORE THAN +6 VOLTS.



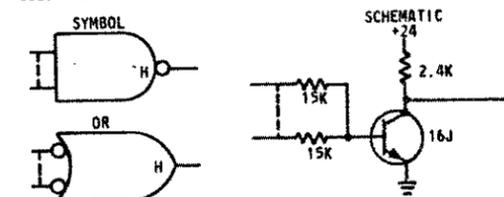
LOW FANOUT SLOW GATE

A "1" TO "0" TRANSITION AT THE INPUT PRODUCES A "1" TO "0" TRANSITION AT THE OUTPUT DELAYED BY APPROXIMATELY 300 MICROSECONDS.



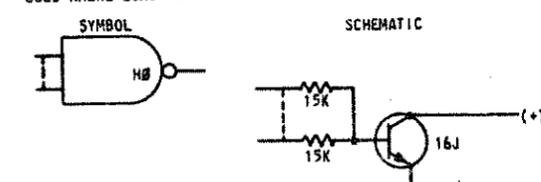
HIGH FANOUT GATE

USED WHERE FANOUT REQUIREMENTS ARE HIGH.



HIGH FANOUT OPEN GATE

USED WHERE LOAD IS FURNISHED BY A TIMER

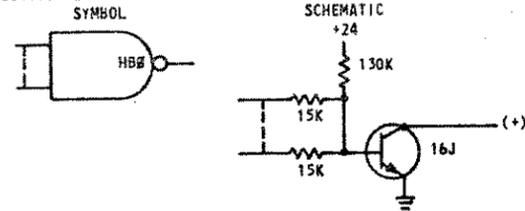


INFORMATION NOTES: (CONT)

302. (CONT)

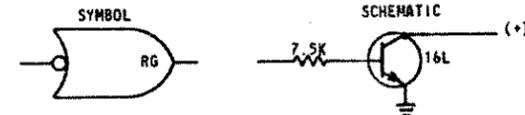
HIGH FANOUT OPEN BIASED GATE

USED WHERE LOAD IS FURNISHED BY A TIMER AND ADDITIONAL NOISE MARGIN IS REQUIRED.



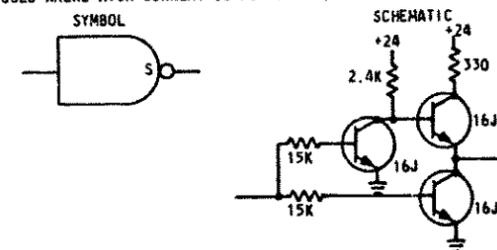
RELAY GROUND GATE

FURNISHES GROUND FOR DRIVING A RELAY MAXIMUM CURRENT CAPABILITY = 75 ma.



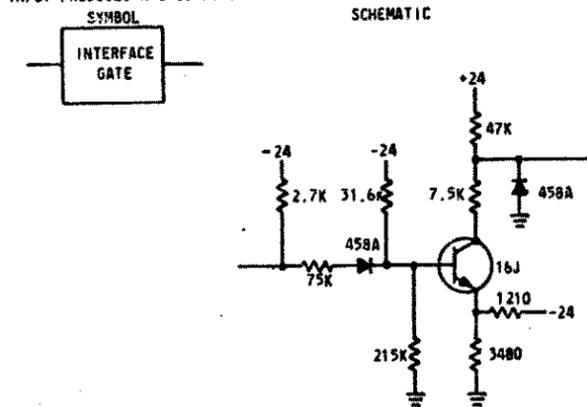
SUPER GATE

USED WHERE HIGH CURRENT OUTPUT IS REQUIRED.



INTERFACE GATE

USED AS INTERFACE BETWEEN TOUCH TONE RECEIVER AND TRL GATES. GROUND AT INPUT PRODUCES A "1" OUTPUT. OPEN AT INPUT PRODUCES A "0" OUTPUT.

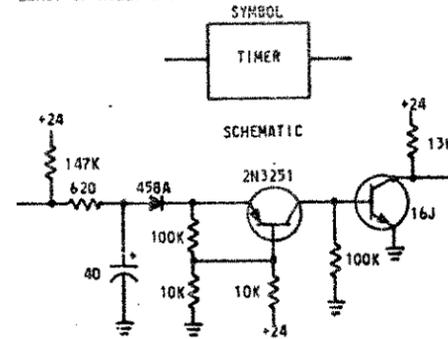


INFORMATION NOTES: (CONT)

302. (CONT)

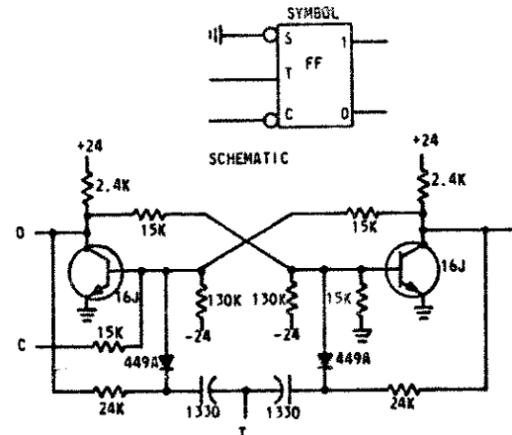
TIMER

A "1" INPUT PRODUCES A "0" OUTPUT. AN OPEN INPUT STARTS THE TIMER AND PRODUCES A "1" OUTPUT IN APPROXIMATELY 5 SECONDS. TIMING MAY BE RESTARTED DURING THE 5 SECOND PERIOD BY A "1" ON THE INPUT FOR AT LEAST 25 MILLISECONDS.



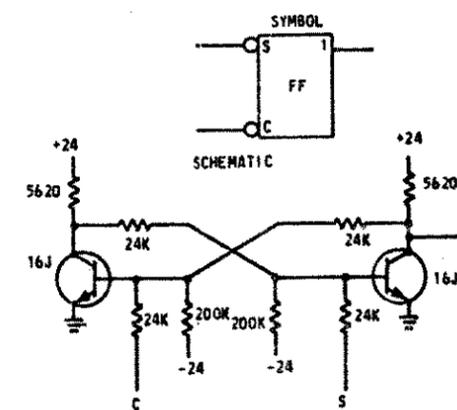
TOGGLE FLIP FLOP

A "0" ON THE "C" INPUT PRODUCES A "1" ON THE "0" OUTPUT AND A "0" ON THE "1" OUTPUT. A "0" TO "1" TRANSITION ON THE "1" INPUT WITH A "1" ON THE "C" INPUT CAUSES BOTH OUTPUTS TO CHANGE STATE.



FLIP FLOP

A "0" ON THE "C" INPUT WITH A "1" ON THE "S" INPUT PRODUCES A "0" ON THE "1" OUTPUT. A "0" ON THE "S" INPUT WITH A "1" ON THE "C" INPUT PRODUCES A "1" ON THE "1" OUTPUT. A "1" ON BOTH INPUTS CAUSES NO CHANGE. A "0" ON BOTH INPUTS IS NOT ALLOWED.

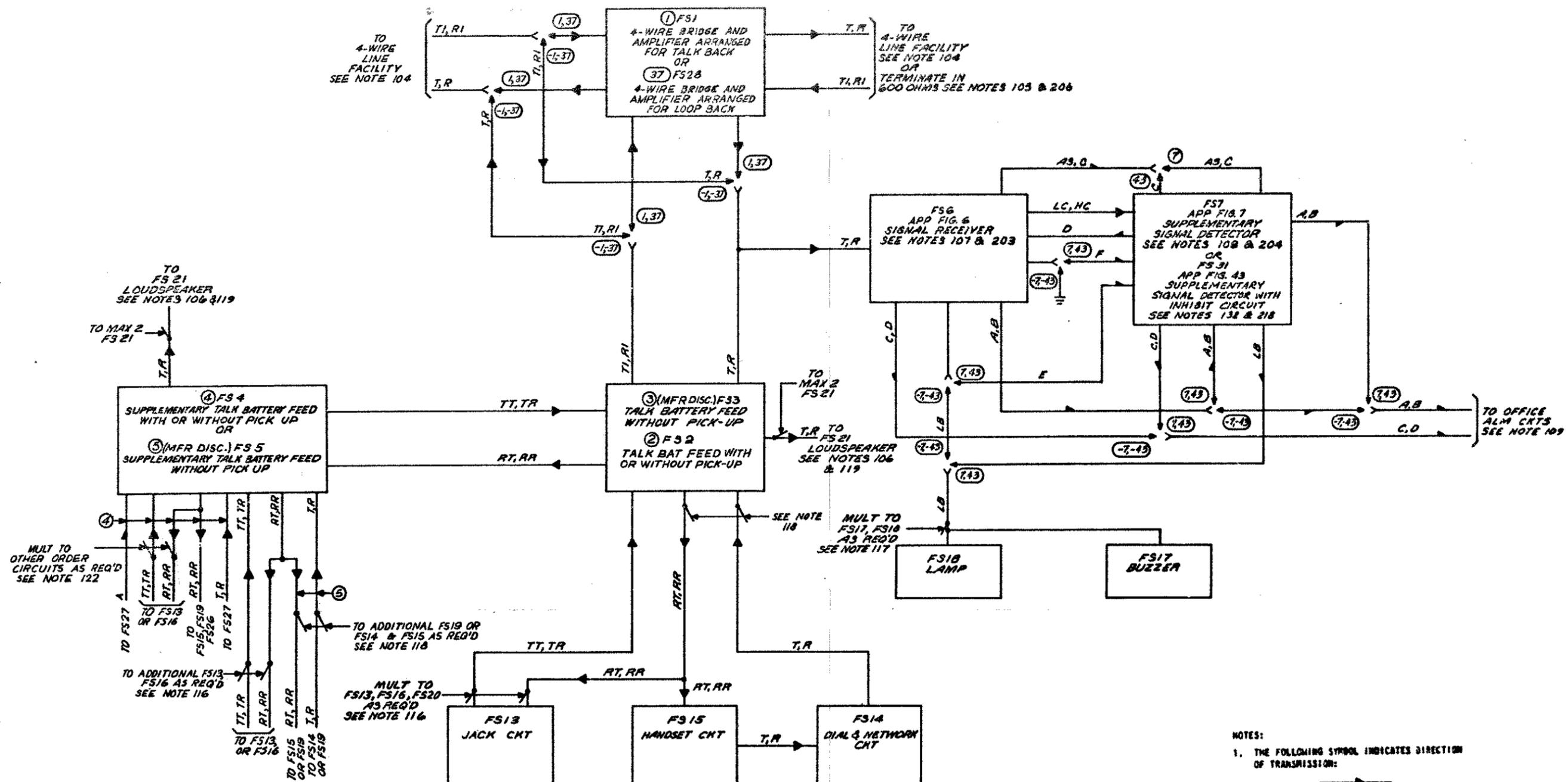


SD-1G245-01-D4

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT ② SD-1G245-01-D4
BELL TELEPHONE LABORATORIES INCORPORATED 6S

DRAWING	1
ISSUE	118
NO.	118

BD 1
 AUX STATION - SINGLE SIGNAL CODE
 ONE DIGIT (6.27) OR TWO DIGIT (6.27)
 J93340 A



- NOTES:
1. THE FOLLOWING SYMBOL INDICATES DIRECTION OF TRANSMISSION:
 2. THE FOLLOWING SYMBOL INDICATES DIRECTION OF CONTROL FUNCTION:

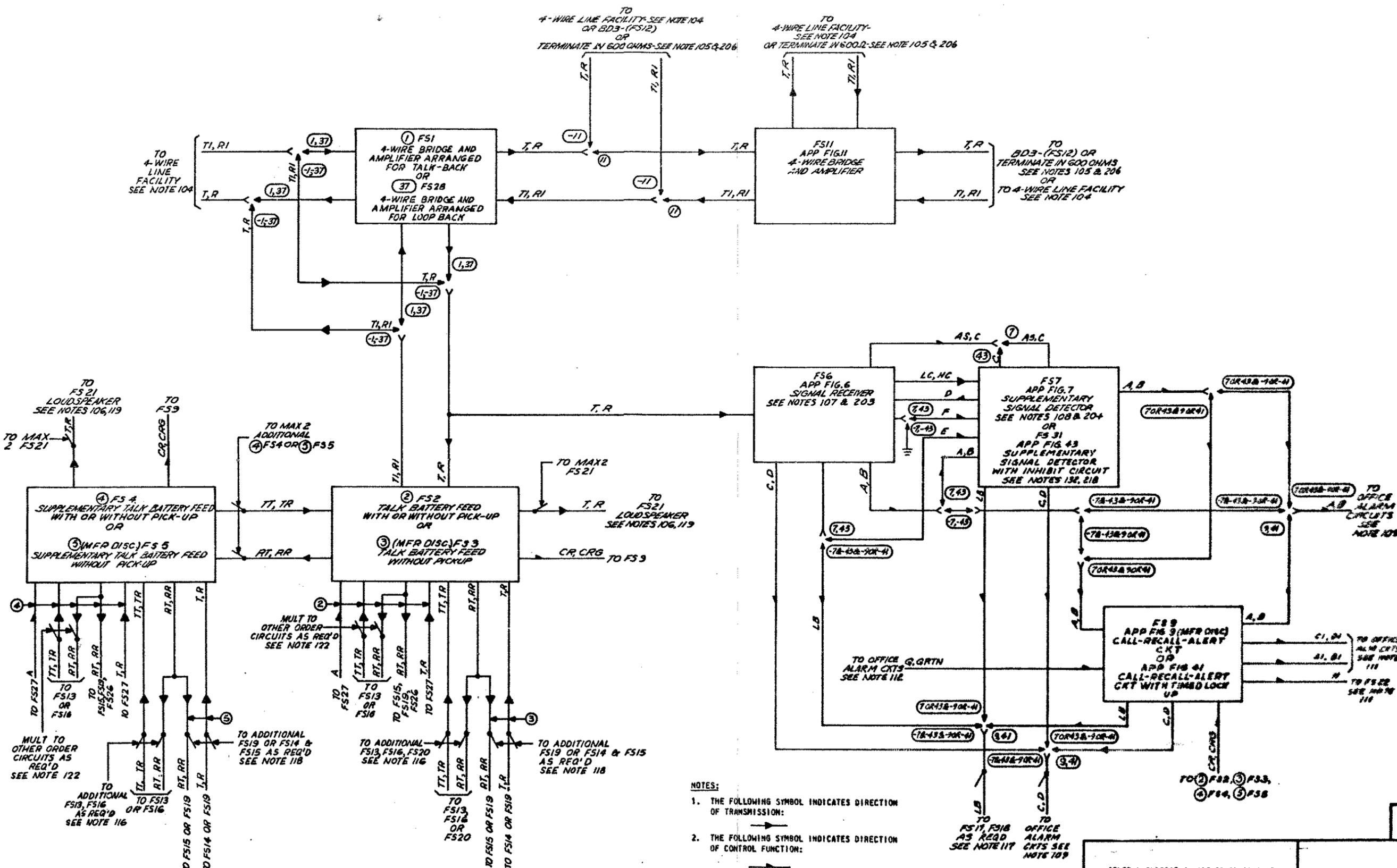
SD-IC245-01-H1

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-IC245-01-H1
BELL TELEPHONE LABORATORIES INCORPORATED	

BD2

MAIN STATION - SINGLE CODE
ONE DIGIT (637) OR TWO DIGIT (637)
J99340B OR J99340F

DRAWING	1
ISSUE	2A
	4B
	5B



- NOTES:
1. THE FOLLOWING SYMBOL INDICATES DIRECTION OF TRANSMISSION:
 2. THE FOLLOWING SYMBOL INDICATES DIRECTION OF CONTROL FUNCTION:

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-H2

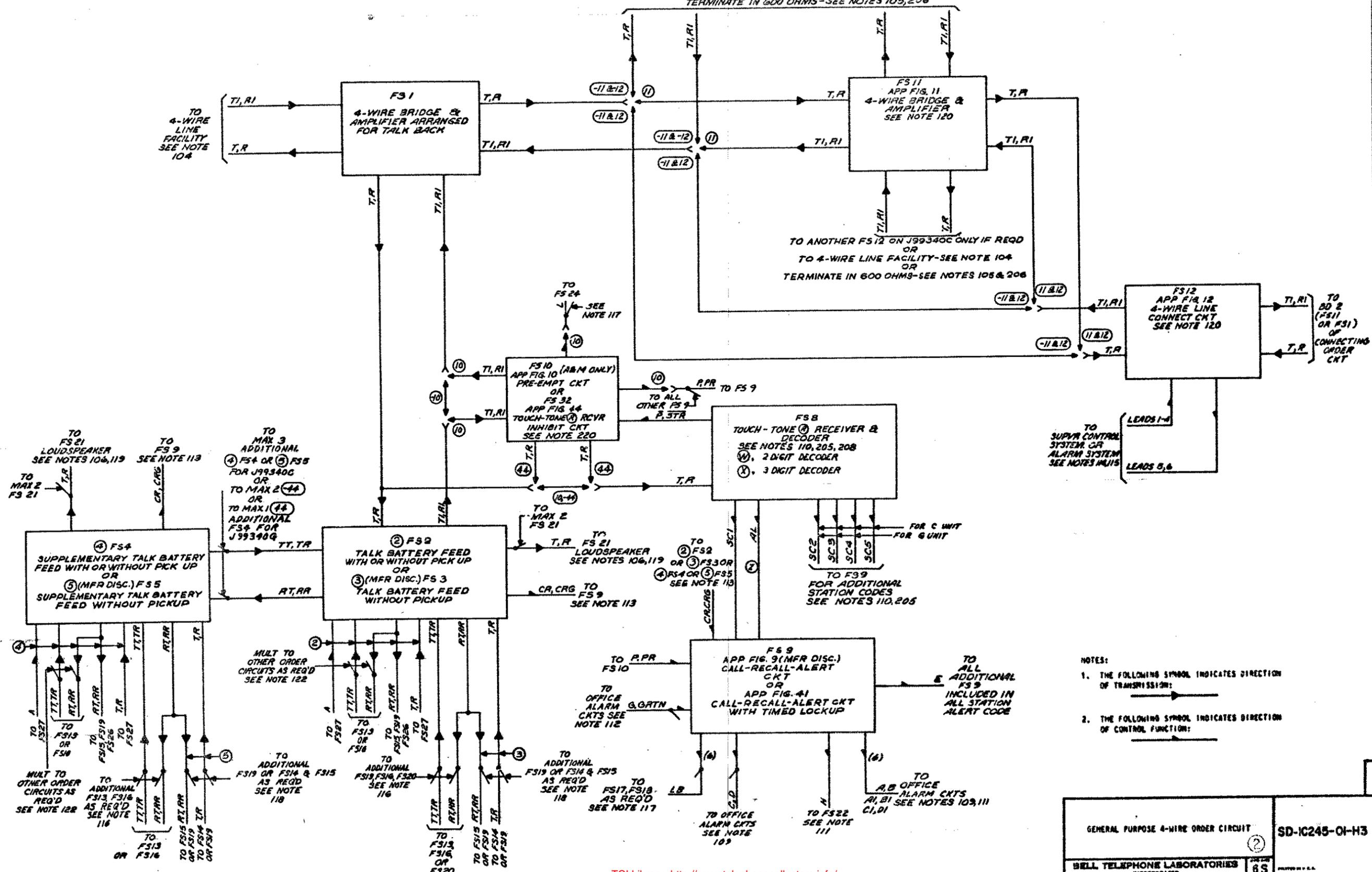
ISSUE 11B

SD-1C245-01-H2

BD 3

(W), 2 DIGIT, OR (X), 3 DIGIT SIGNAL CODE UP TO 5 CODES PER LOCATION FOR J99340C AND UP TO 4 CODES PER LOCATION FOR J99340G SEE NOTE 220

TO 4-WIRE LINE FACILITY-SEE NOTE 104 OR TERMINATE IN 600 OHMS-SEE NOTES 105,206



- NOTES: 1. THE FOLLOWING SYMBOL INDICATES DIRECTION OF TRANSMISSION: [arrow symbol] 2. THE FOLLOWING SYMBOL INDICATES DIRECTION OF CONTROL FUNCTION: [arrow symbol]

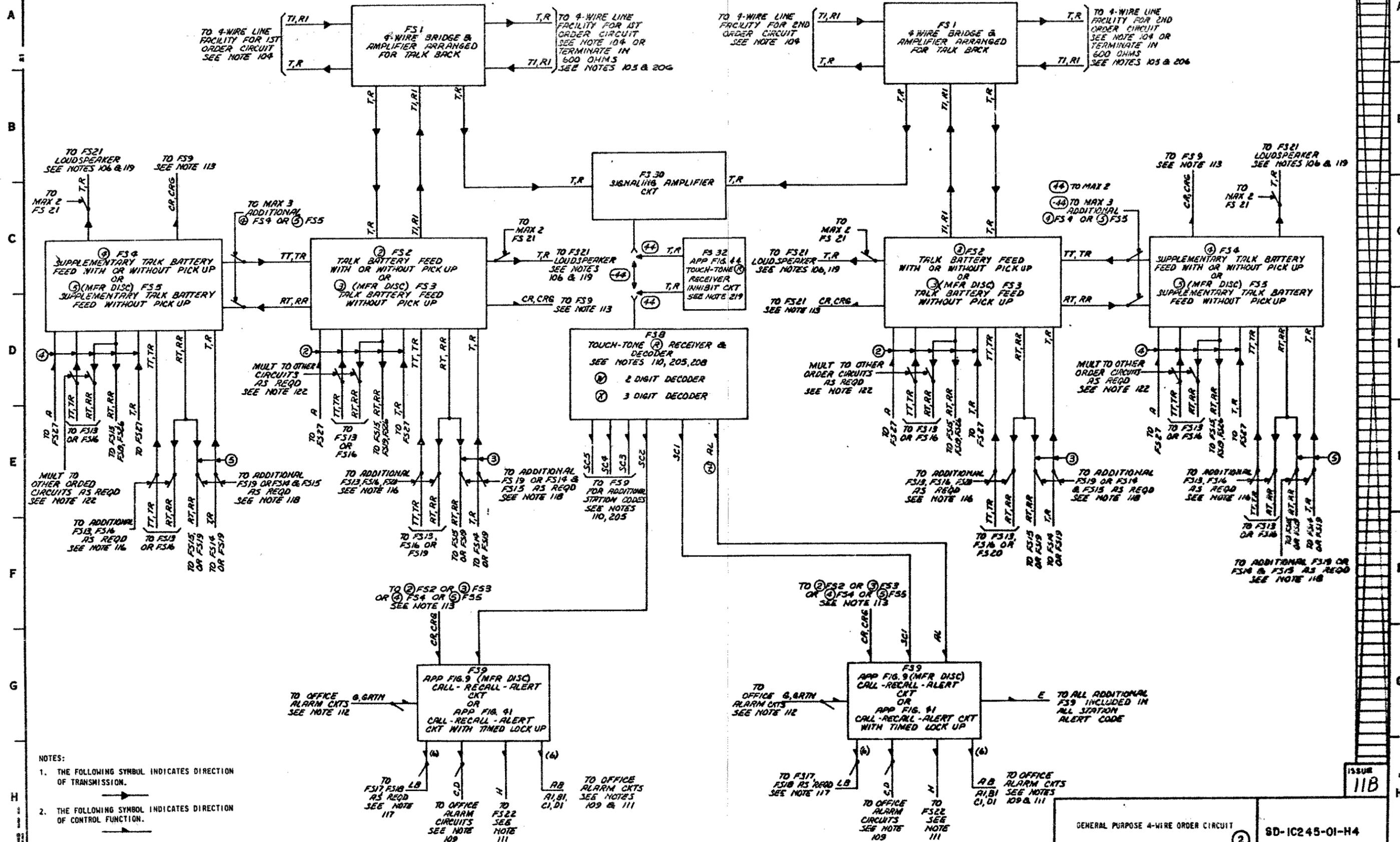
Table with 2 columns: DRAWING ISSUE, 1, 2A, 4B, 5B

ISSUE 11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT (2) SD-IC245-01-H3 BELL TELEPHONE LABORATORIES INCORPORATED 65

SD-IC245-01-H3

BD 4
 (2) 2 DIGIT OR (3) 3 DIGIT
 SIGNAL CODE
 COMMON TTR FOR TWO ORDER CIRCUITS
 J99340D
 SEE NOTE 219



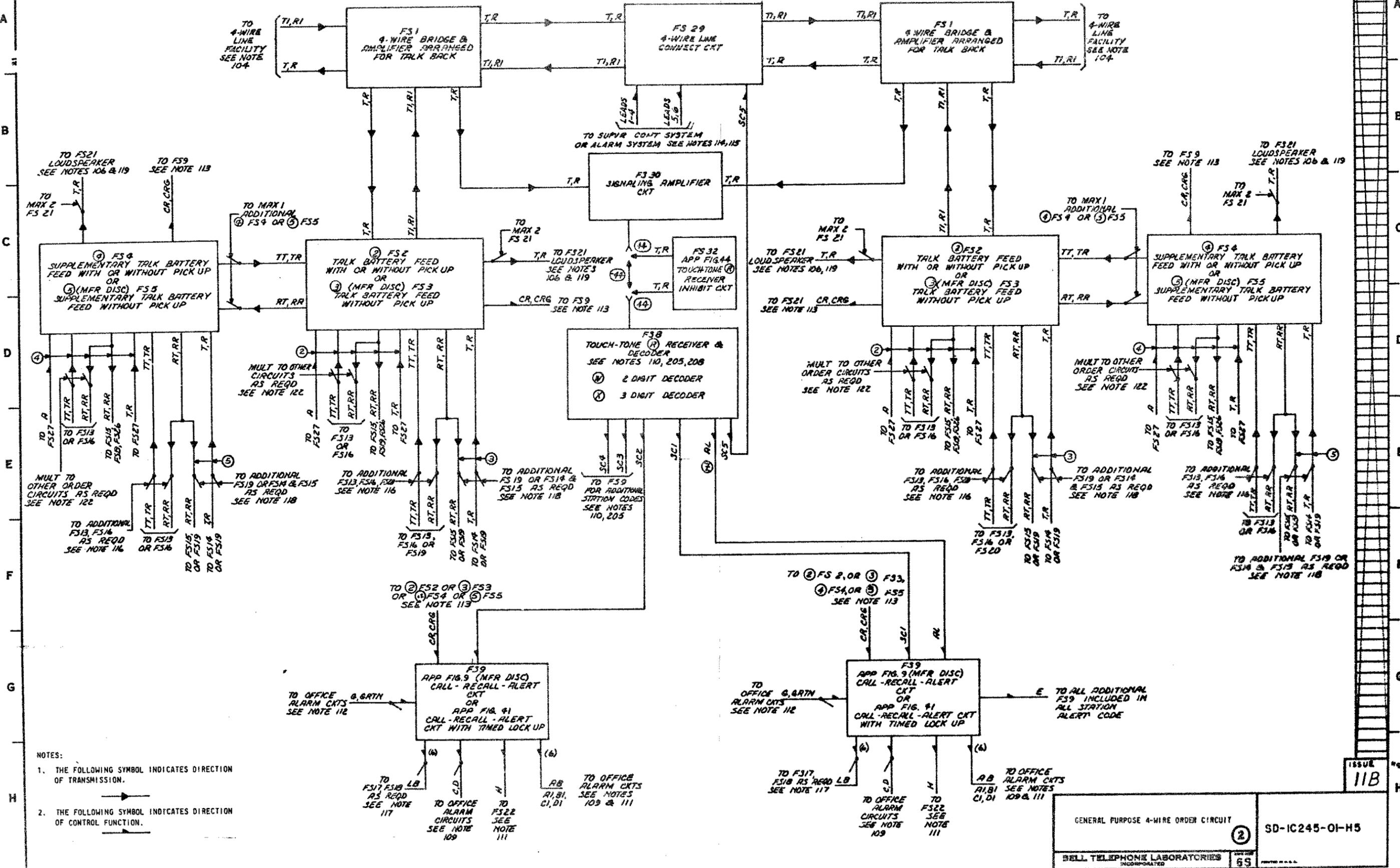
- NOTES:
1. THE FOLLOWING SYMBOL INDICATES DIRECTION OF TRANSMISSION.
 2. THE FOLLOWING SYMBOL INDICATES DIRECTION OF CONTROL FUNCTION.

DRAWING ISSUE

ISSUE 11B

SD-1C245-01-H4

BD 5
 ②, 2 DIGIT, OR ③, 3 DIGIT
 SIGNAL CODE
 COMMON TTR FOR TIME ORDER CIRCUITS
 DIALED CUT-THRU
 J993406



- NOTES:
1. THE FOLLOWING SYMBOL INDICATES DIRECTION OF TRANSMISSION.
 2. THE FOLLOWING SYMBOL INDICATES DIRECTION OF CONTROL FUNCTION.

SD-IC245-01-H5

CPS I
4 WAY 4-WIRE BRIDGE CIRCUIT

COMPONENT LIST

RESISTOR

DESIG	CODE	KS-20616, L1A, 796
R1-R24	258A	600
R25-R32		106
R33		3520
R34, R35		106
R36		182
R37		2150
R38, R39		182
R40		2150
R41		106
R42, R43		3520
R44		106
R45		182
R46, R47		2150
R48	258A	182

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C304
CONNECTOR ON FRAME	908C

SYMBOL
SHOWN IN FS 1, FS 11

INPUT/OUTPUT INFORMATION

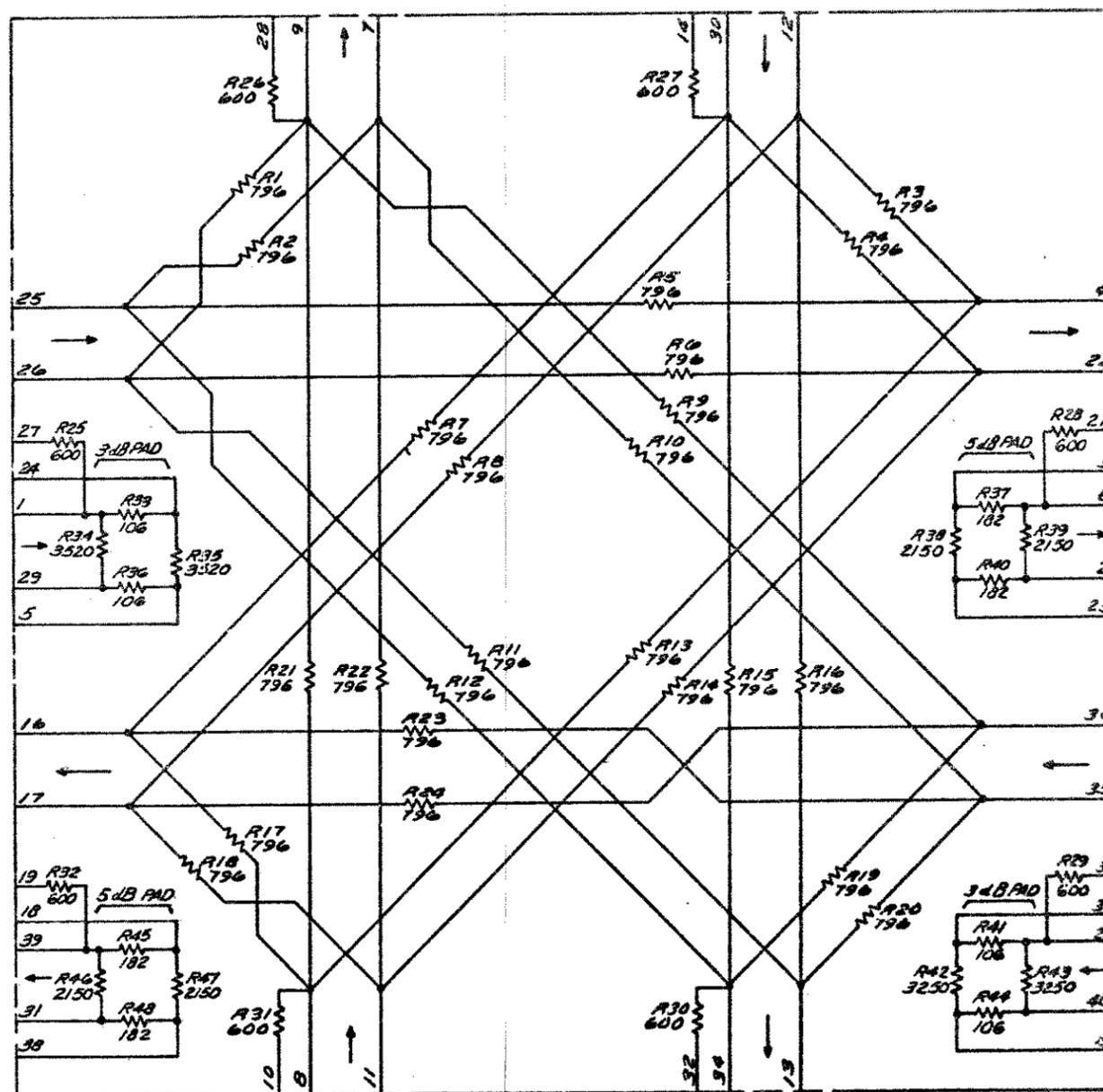
FUNCTION: PROVIDES THE LINE DROP AND THROUGH CONNECTION FOR MULTIPLE OFFICE 4 WIRE 600 OHM VOICE ORDER CIRCUITS.

IMPEDANCE: APPROXIMATELY 600 OHMS AT ANY PORT WHEN ALL OTHER PORTS ARE TERMINATED IN 600 OHMS.

LOSS: ANY INPUT TO ANY OTHER NUMBERED OUTPUT - 15 dB.
ANY INPUT TO THE SIMILARLY NUMBERED OUTPUT AND ALL OTHER INPUTS >70dB.
ANY OUTPUTS TO THE SIMILARLY NUMBERED INPUT AND ALL OTHER OUTPUTS >70 dB.

CIRCUIT DESCRIPTION:

RESISTORS (R1) - (R24) FORM A FOUR PORT BALANCED NETWORK EACH HAVING A 600 OHM BALANCED INPUT AND OUTPUT. (R26), (R27), (R30), AND (R31) ARE 600 OHM TERMINATING RESISTORS WHICH MAY BE USED WHEN THE INPUTS AND OUTPUTS OF PORTS 3 AND 4 ARE NOT USED. ASSOCIATED WITH PORTS 1 AND 2 ARE BALANCED RESISTIVE PADS. 3 dB FOR INPUTS AND 5 dB FOR OUTPUTS. (R41) - (R44) FORM A 3 dB PAD AND (R37) - (R40) FORM A 5 dB PAD FOR PORT 1. (R33) - (R36) FORM A 3 dB PAD AND (R45)-(R48) FORM A 5dB PAD FOR PORT 2. THESE PADS PERMIT CONNECTING TO 4 WIRE FACILITIES HAVING STANDARD LEVELS OF +7 dB RECEIVING AND -16 dB TRANSMITTING. (R25), (R28), (R29), AND (R32) ARE 600 OHM RESISTORS ASSOCIATED WITH THE PADS TO PROVIDE TERMINATIONS WHEN PORTS 1 AND 2 ARE NOT USED.



PORT 1

PORT 2

PORT 3

PORT 4

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS.

4 WAY 4-WIRE BRIDGE CKT
CPS I

10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-IC245-01-J1

BELL TELEPHONE LABORATORIES
INCORPORATED

63

SD-IC245-01-J1

CPS 2 DUAL AMPLIFIER CIRCUIT

DRAWING ISSUE
48

COMPONENT LIST

CAPACITOR	
DESIG	CODE
[2] C1.0, C1.1	602A
[2] C2.0, C2.1	535H
[2] C3.0, C3.1	542C
[2] C4.0, C4.1	KS-16390, L3 602B
[2] C5.0, C5.1	KS-19953, L3 .022
[2] C6.0, C6.1	KS-16390, L12
[2] C7.0, C7.1	KS-16390, L12
[2] C8.0, C8.1	KS-16390, L12
[2] C9.0, C9.1	600A

DIODE	
DESIG	CODE
[2] CR1.0, CR1.1	459AF
[2] CR2.0, CR2.1	459AF
[2] CR3.0, CR3.1	446Y
[2] CR4.0, CR4.1	446Y

POTENTIOMETER	
DESIG	CODE
[2] R2.0, R2.1	KS-14786, L3A, 1K KS-20231, L1A, 1K

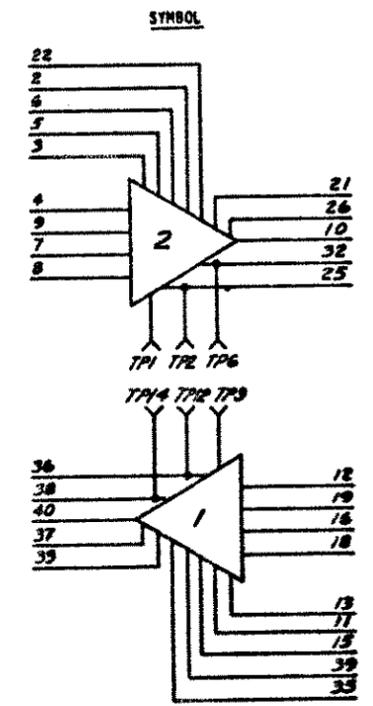
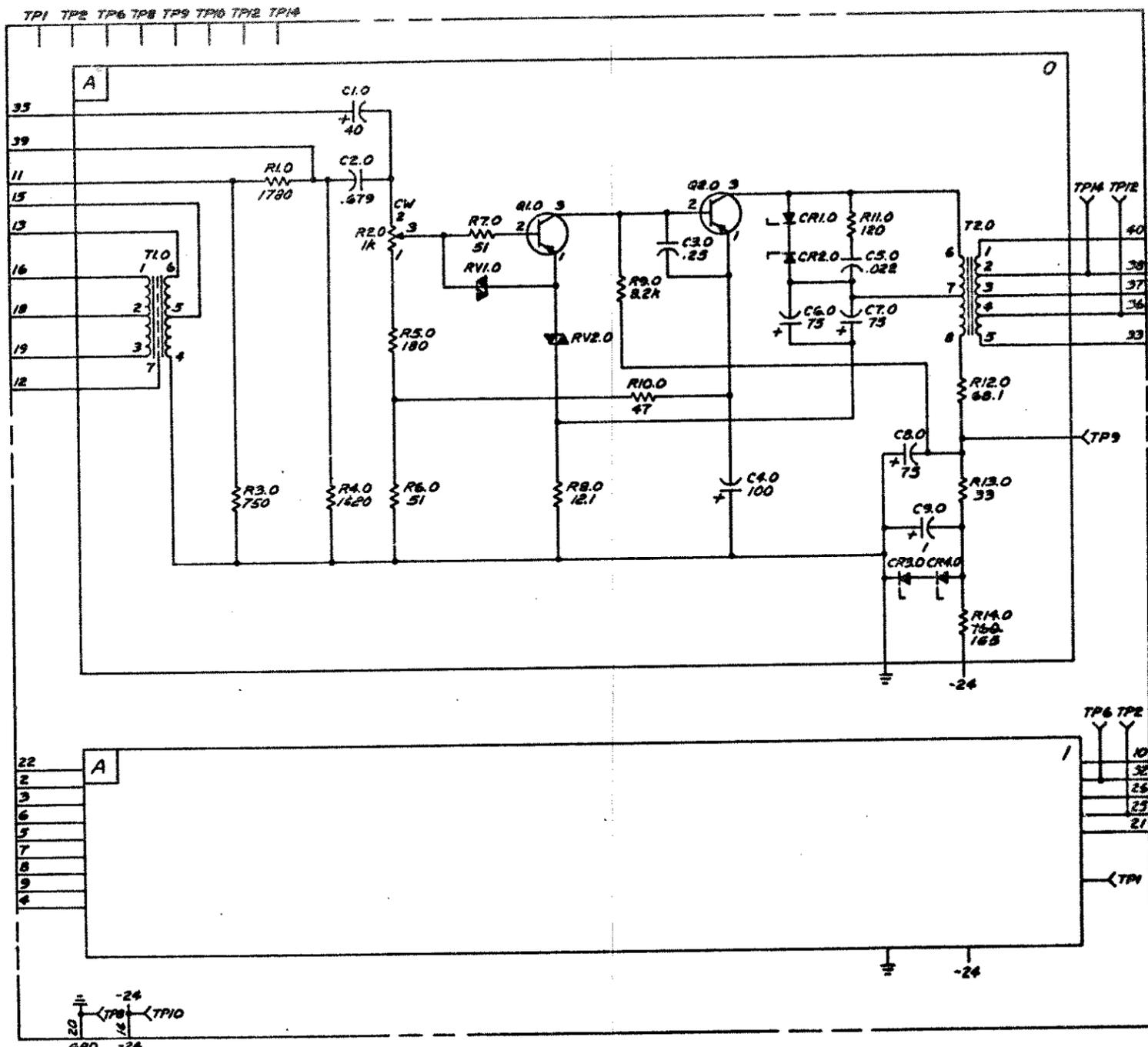
RESISTOR	
DESIG	CODE
[2] R1.0, R1.1	258A, KS-20610, L1A, 1780
[2] R3.0, R3.1	258A, KS-20610, L1A, 750
[2] R4.0, R4.1	258A, KS-20610, L1A, 1620
[2] R5.0, R5.1	KS-13490, L1, 180
[2] R6.0, R6.1	KS-13490, L1, 51
[2] R7.0, R7.1	KS-13490, L1, 51
[2] R8.0, R8.1	258A, KS-20610, L1A, 12.1
[2] R9.0, R9.1	KS-13490, L1, 8.2K
[2] R10.0, R10.1	KS-13490, L1, 47
[2] R11.0, R11.1	KS-13490, L1, 120
[2] R12.0, R12.1	221A, 68.1
[2] R13.0, R13.1	KS-13490, L1, 33
[2] R14.0, R14.1	KS-13491, L1, 160 KS-20289, L6C, 165

TRANSFORMER	
DESIG	CODE
[2] T1.0, T1.1	2532AD
[2] T2.0, T2.1	2568B

TRANSISTOR	
DESIG	CODE
[2] Q1.0, Q1.1	12P
[2] Q2.0, Q2.1	45A

VARISTOR	
DESIG	CODE
[2] RV1.0, RV1.1	100D
[2] RV2.0, RV2.1	100D

MANUFACTURING REFERENCES	
CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C305
CONNECTOR ON FRAME	908C



- NOTES:**
- UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS;
CAPACITANCE VALUES ARE IN MICROFARADS;
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.
 - GROUND RETURN

INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES TWO IDENTICAL VOICE FREQUENCY AMPLIFIERS OPERATING FROM A COMMON -24 VOLT SUPPLY. PIN CONNECTIONS FOR VARIOUS INPUT AND OUTPUT IMPEDANCES ARE SHOWN IN TABLE A.

SIGNAL LEVELS: MAX OUTPUT LEVEL +18 dBm
GAIN RANGE - 12 TO 27 dB
GAIN RANGE WITH 10 dB PAD (STRAP TERMS 1-13 INPUT OR 11-15 FOR 1200Ω INPUT) - 2 TO 17 dB

FREQUENCY RESPONSE: 200 Hz - 20 KHz; 60 Hz - 20 KHz WITH TERMINALS 35 AND 39 STRAPPED (AMP 1), 2 AND 22 (AMP 2)

Z	AMPLIFIER 1		AMPLIFIER 2	
	INPUT TERMS	STRAP	INPUT TERMS	STRAP
1200	16-19 (CT-18)	(15, 39)	33-40 (CT-37) (CT-8)	(2, 6)
600	16-19 (CT-18)	(15, 39)	36-38 (CT-37) (CT-8)	(2, 5)
300	18-19 (15, 39)	33-37	8-9 (2, 6)	21-26
150	18-19 (15, 39)	36-37	8-9 (2, 5)	25-26

CIRCUIT DESCRIPTION

EACH AMPLIFIER IS COMPOSED OF AN INPUT CIRCUIT, OUTPUT CIRCUIT, AND A FEEDBACK SYSTEM. THE INPUT CIRCUIT CONSISTS OF A TERMINATED TRANSFORMER (T1), A CONTINUOUS GAIN CONTROL POTENTIOMETER (R2) OF APPROXIMATELY 15 dB RANGE AS PART OF A VOLTAGE DIVIDER CIRCUIT (C1), (R2), (R5), AND (R6), AND A 10 dB PAD (R1) AND (R3) TO REDUCE OVERALL GAIN WHEN REQUIRED. (R4) PROVIDES A TERMINATION FOR THE PAD ON (T1). CAPACITOR (C1) MAY BE STRAPPED IN PARALLEL WITH (C2) TO REDUCE THE LOW END CUT-OFF FREQUENCY. THE OUTPUT CIRCUIT CONSISTS OF A MULTIPLE WINDING TRANSFORMER (T2) IN WHICH THE PRIMARY WINDING IS TAPPED FOR A 10:1 IMPEDANCE DIVISION TO FURNISH FEEDBACK. THE FEEDBACK SYSTEM CONSISTS OF BOTH AC AND DC LOOPS. A PORTION OF THE OUTPUT SIGNAL FROM (T2) IS FED BACK THROUGH CAPACITORS (C6) AND (C7) AND ACROSS (R8) TO THE EMITTER OF TRANSISTOR (Q1). VARISTOR (RV2) PROVIDES TEMPERATURE

CIRCUIT DESCRIPTION (CONT)

COMPENSATION FOR (Q1). THE COLLECTOR CURRENT OF (Q2) IS STABILIZED BY EMITTER RESISTORS (R6) AND (R10). A PORTION OF DC VOLTAGE DEVELOPED ACROSS (R6) AND (R10) IS FED BACK THROUGH (R5) TO THE BASE OF (Q1) TO STABILIZE ITS COLLECTOR CURRENT. CAPACITOR (C4) IS AN EMITTER BYPASS FOR (Q2). (R11), (C3), AND (C5) SERVE TO CONTROL THE FEEDBACK LOOP CUT-OFF CHARACTERISTICS. (R14), (CR3), AND (CR4) PROVIDE REGULATION TO PERMIT OPERATION OVER A RANGE OF BATTERY SUPPLY VOLTAGE FROM -19 TO -29 VOLTS. (R13), (C8), AND (C9) SERVE AS BATTERY DECOUPLING. (RV1), (CR1), AND (CR2) PROVIDE LIGHTNING AND SURGE PROTECTION.

DUAL AMPLIFIER CKT
CPS 2

ISSUE
105

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

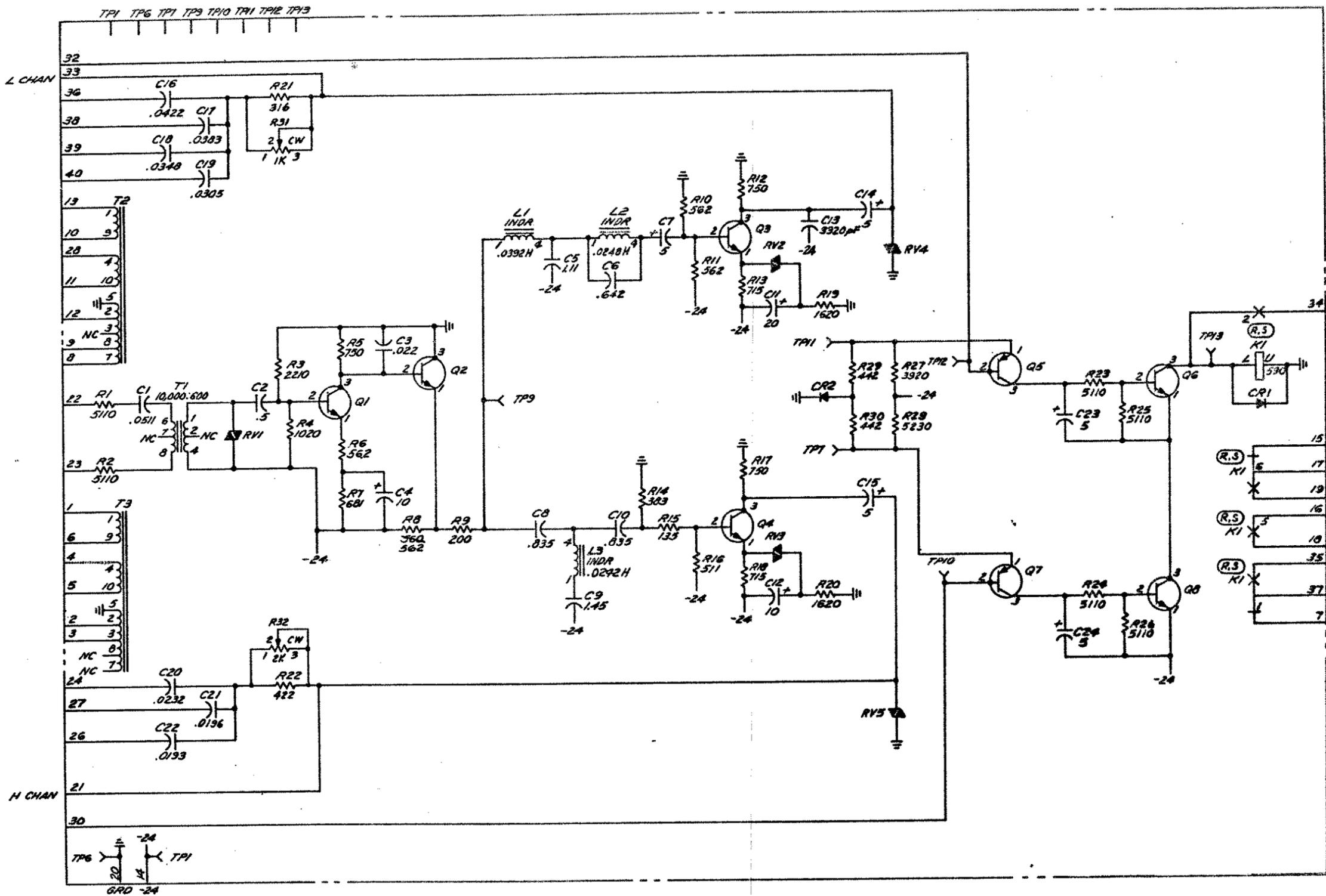
SD-1C245-01-J2

BELL TELEPHONE LABORATORIES
INCORPORATED

SD-1C245-01-J2

PART OF CPS 3
SIGNAL RECEIVER

DRAWING ISSUE
1
2A
A
B
C
D
E
F
G
H
ISSUE
10B
12
H



SD-1C245-01-J3A

SIGNAL RECEIVER
PART OF CPS 3

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT
BELL TELEPHONE LABORATORIES
INCORPORATED

SD-1C245-01-J3A
6S

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION				TEST SET		DIRECT CURRENT FLOW REQ					REMARKS	
DESIG	CODE	OPT	FIG.	BSP FIG.	CONT PRES	ARM. TRVL	BLOCK OR INSULATE		TEST CLIP DATA		PREP	NOTE	TEST WDG	TEST FOR	TEST PA	TEST MA	TEST HA		READJ
							CONN BAT.	CONN GRD											
RELAY																			
K1	BF6	R			3				L(K1)	U(K1)		1		0			25.3	24.0	
K1	MB1	S			200				L(K1)	U(K1)		1		0			25.5	24.0	

PART OF CPS 3
SIGNAL RECEIVER

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	(A) ED-1C306(1)G1 (S) ED-1C306(1)G2
CONNECTOR ON FRAME	908C

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST

COMPONENT LIST

RELAY

DESIG	K1	K1	
CODE	BF6	MB1	
OPTION	R	S	
CONT ARR	LOC	CONT ARR	LOC
6	BM D9	ERM D9	
5	BM E9	ERM E9	
4	BM	ERM	
3	BM	ERM	
2	BM C8	ERM C8	
1	BM E9	ERM E9	
COIL	C9	C9	

COMPONENT LIST (CONT)

RESISTOR

DESIG	CODE
R1, R2	257A KS-20616, L1A, 5510
R3	257A KS-20616, L1A, 2210
R4	257A KS-20616, L1A, 1020
R5	257A KS-20616, L1A, 750
R6	257A KS-20616, L1A, 56.2
R7	257A KS-20616, L1A, 681
R8	KS-15491 L1-560, KS-20289, L6C, 562
R9	237A, 200
R10, R11	221A, 562
R12	257A, KS-20616, L1A, 750
R13	221A, 715
R14	221A, 383
R15	257A, KS-20616, L1A, 135
R16	257A, KS-20616, L1A, 511
R17	257A, KS-20616, L1A, 750
R18	221A, 715
R19, R20	257A, KS-20616, L1A, 1620
R21	257A, KS-20616, L1A, 316
R22	257A, KS-20616, L1A, 422
R23, R24	257A, KS-20616, L1A, 5110
R25, R26	257A, KS-20616, L1A, 5110
R27	221A, 3920
R28	221A, 5230
R29, R30	257A, KS-20616, L1A, 442

CAPACITOR

DESIG	CODE
C1	542W
C2	542A
C3	KS-19066 L1, KS-20977, L4, .022
C4	601B
C5	535GT
C6	535GR
C7	601A
C8	535GS
C9	535CA
C10	535GS
C11	602C
C12	601B
C13	KS-16591 L1, 3320pF
C14	601A
C15	601A
C16	577D, .0422
C17	577D, .0383
C18	577D, .0348
C19	577D, .0305
C20	577D, .0232
C21	577D, .0196
C22	577D, .0193
C23, C24	601A

TRANSFORMER

DESIG	CODE
T1	2564W
T2, T3	2597A

TRANSISTOR

DESIG	CODE
Q1-Q4	46F-66F
Q5	120
Q6	46F-66F
Q7	120
Q8	46F-66F

VARIATOR

DESIG	CODE
RV1-RV5	1000

DIODE

DESIG	CODE
CR1	400J
CR2	446B

INDUCTOR

DESIG	CODE
L1	1592C, .0392H
L2	1552C, .0248H
L3	1592C, .0242H

POTENTIOMETER

DESIG	CODE
R31	KS-20231, L2, KS-20231, L2A, 1K
R32	KS-20231, L2, KS-20231, L2A, 2K

INPUT/OUTPUT INFORMATION

FUNCTION: RESPONDS TO A PAIR OF TONES WHICH CONSTITUTE A VALID TOUCH-TONE (TT) DIGIT AND GIVES A RELAY CONTACT CLOSURE OUTPUT UPON RECEPTION OF THE PARTICULAR DIGIT FOR WHICH THE RECEIVER IS STRAPPED.

INPUT: TOUCH-TONE DIGIT SIGNAL OF -1 dBm NOMINAL ON PINS 22 AND 23. INPUT IMPEDANCE APPROXIMATELY 10K OHMS FOR BRIDGING ONTO A 600 OHM BALANCED LINE.

CIRCUIT DESCRIPTION

THE BALANCED INPUT CIRCUIT CONSISTS OF RESISTORS (R1) AND (R2), CAPACITOR (C1), AND TRANSFORMER (T1). IT PROVIDES A HIGH INPUT IMPEDANCE FOR BRIDGING ONTO A LOW IMPEDANCE BALANCED CIRCUIT. (C1) BLOCKS DC FROM THE PRIMARY WINDING OF (T1). TRANSISTORS (Q1) AND (Q2), AND ASSOCIATED BIASING COMPONENTS FORM AN INPUT AMPLIFIER TO PROVIDE THE NECESSARY GAIN AS WELL AS IMPEDANCE TRANSFORMATION TO DRIVE THE CUT-APART FILTER AND LIMITERS. THE CUT APART FILTER SEPARATES THE INCOMING SIGNAL INTO TWO CHANNELS. (L1), (L2), (C5), AND (C6) FORM A LOW CHANNEL FILTER WHICH REJECTS FREQUENCIES ABOVE 1175 Hz. (C8), (C9), (C19) AND (L3) FORM A HIGH CHANNEL FILTER WHICH REJECTS FREQUENCIES BELOW 965 Hz. (Q3) AND (Q4) ARE LIMITERS FOR LOW AND HIGH CHANNELS RESPECTIVELY. EACH LIMITER PRODUCES A SQUARE WAVE OF FIXED AMPLITUDE. THE LIMITERS PROVIDE PROTECTION AGAINST FALSE OPERATION OF THE RECEIVER CAUSED BY SPEECH OR NOISE SIMULATION OF TOUCH-TONE FREQUENCIES. ONLY A SINGLE FREQUENCY INPUT WILL ALLOW THE LIMITER OUTPUT FUNDAMENTAL TO HAVE THE REQUIRED ENERGY TO BE ABOVE THRESHOLD. EACH LIMITER CONNECTS TO A SERIES RESONANT CIRCUIT WHICH IN TURN CONNECTS TO A THRESHOLD DETECTOR. THE FREQUENCY OF THE SERIES RESONANT CIRCUIT IS DETERMINED BY STRAPS AS INDICATED IN NOTE 203. THE DETECTOR OPERATES WHEN THE TUNED CIRCUIT IS PRESENTED WITH A SIGNAL AT OR NEAR ITS RESONANT FREQUENCY. THE OPERATING THRESHOLD IS SET AT APPROXIMATELY 2 dB BELOW THE PEAK-TUNED CIRCUIT RESPONSE AS DETERMINED BY THE BIAS CIRCUIT (CR2, R27-R30) AND THE BANDWIDTH ADJUSTING POTENTIOMETER (RV1) OR (RV2). (Q5), LOW TONE DETECTOR, OR (Q7), HIGH TONE DETECTOR, IS TURNED ON BY THE NEGATIVE PEAKS OF AN AC SIGNAL APPEARING ON THE BASE. A CHARGE IS BUILT UP ON (C23) OR (C24) AND FORWARD BIASES THE OUTPUT TRANSISTORS (Q6) OR (Q8). IF BOTH (Q6) AND (Q8) ARE ON SIMULTANEOUSLY, INDICATING A VALID TOUCH-TONE DIGIT FOR WHICH THE RECEIVER IS STRAPPED, A PATH TO -24 VOLTS IS PROVIDED WHICH OPERATES RELAY (K1) (K1) REMAINS OPERATED AS LONG AS BOTH (Q6) AND (Q8) ARE ON OR UNTIL RELEASED BY AN EXTERNAL CIRCUIT IF THE SELF LOCKING PATH THROUGH PIN 34 IS USED.

NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS, CAPACITANCE VALUES ARE IN MICROFARADS, VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
- GROUND RETURN.

RECORD OF CHANGES

DWG ISS	PREV FURN	STD	MFR DISC	SEE NOTE
7B	R	S	R	

SIGNAL RECEIVER
PART OF CPS 3

ISSUE
10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

BELL TELEPHONE LABORATORIES INCORPORATED

SD-1C245-OI-J38

6S

SD-1C245-OI-J38

CIRCUIT REQUIREMENTS

CPS 4
SUPPLEMENTARY SIGNAL DETECTOR

APPARATUS				MECH REQ			CIRCUIT PREPARATION			TEST SET PREP	SEE TEST NOTE	DIRECT CURRENT FLOW REQ			REMARKS
DESIG	CODE	OPT	FIG	BSP FIG	COMT PRES	ARM TRVL	BLOCK OR INSULATE	TEST CLIP DATA				TEST HDG	TEST FOR	TEST READJ	
RELAY								CONN BAT.	CONN GRD						
K1	BF6	R		3				U(K1)	L(K1)	B/G	1		25.5	24.0	
K1	MB1	S		200				U(K1)	L(K1)	B/G	1		25.5	24.00	

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST

COMPONENT LIST

RELAY

DESIG	K1	K1	
CODE	BF6	MB1	
OPTION	R	S	
CONT ARR	LOC	CONT ARR	LOC
6	BM	EBM	EBM
5	BM	H5	EBM
4	BM	H5	EBM
3	BM		
2	BM	A5	A5
1	BM	G5	G5
COIL	H3	H3	

CAPACITOR

DESIG	CODE
C1	5770, .0422
C2	5770, .0383
C3	5770, .0348
C4	5770, .0305
C5	5770, .0305
C6	5770, .0193
C7	5770, .0232
C8	5770, .0196
C9	5770, .0193
[4] C10.0-C10.3	601A
C11	602A
C12	600A 1.0

DIODE

DESIG	CODE
CR1	400J
CR2	446D
CR3, CR4	446B

POTENTIOMETER

DESIG	CODE
R12	KS-20231, L2, KS-20231, L2A, 1K
R13	
R14	
R15	KS-20231, L2, KS-20231, L2A, 2K

RESISTOR

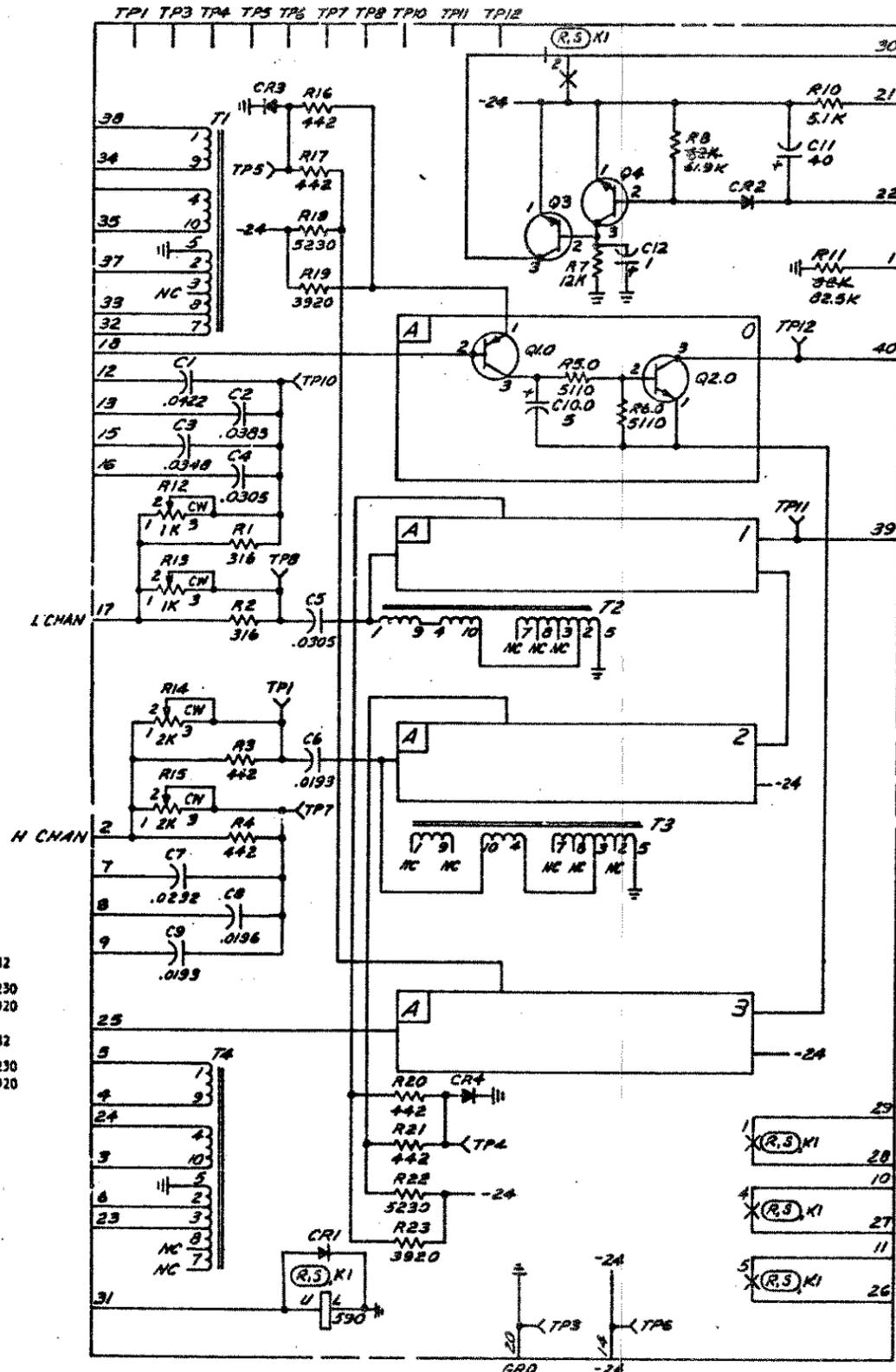
DESIG	CODE	RESISTOR (CONT)	CODE
R1	237A, KS-20616, L1A, 316	R16	237A, 442
R2	237A, KS-20616, L1A, 316	R17	221A, 5230
R3	237A, KS-20616, L1A, 442	R18	221A, 3920
R4	237A, KS-20616, L1A, 442	R19	
[4] R5.0-R5.3	237A, KS-20616, L1A, 5110	R20	237A, 442
[4] R6.0-R6.3	237A, KS-20616, L1A, 5110	R21	
R7	KS-13490, L1, 12K	R22	221A, 3230
R8	KS-16645, L1, 62K	R23	221A, 3920
R10	KS-16645, L1, KS-20616, L1A, 5.1K		
R11	KS-16645, L1, KS-20616, L1A, 82.5K		

TRANSFORMER

DESIG	CODE
T1-T4	2597A

TRANSISTOR

DESIG	CODE
[4] Q1.0-Q1.3	12D
[4] Q2.0-Q2.3	16F 66F
Q3	16L 66L
Q4	16J 66J



MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	(R) ED-10307(1)01 (S) ED-10307(1)02
CONNECTOR ON FRAME	908C

SYMBOL
SHOWN IN FS 7

INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES TWO DIGIT DETECTORS WHICH RESPOND TO VALID TOUCH-TONE DIGITS WHEN USED IN CONJUNCTION WITH CP3. A TIMER CIRCUIT PROVIDES AN INTERDIGITAL TIME-OUT PERIOD OF APPROXIMATELY 2.5 SECONDS.

INPUT: SQUARE WAVE SIGNAL OF APPROXIMATELY 1.25 VOLTS PEAK TO PEAK.

CIRCUIT DESCRIPTION

TWO HIGH CHANNEL AND TWO LOW CHANNEL TUNED CIRCUITS ARE PROVIDED WHICH ARE DRIVEN FROM THE HIGH AND LOW LIMITERS, RESPECTIVELY, OF CP3. ONE HIGH CHANNEL (T3) AND (C6) AND ONE LOW CHANNEL (T2) AND (C5) ARE CONNECTED TO RESPOND TO TONES L4 (941 Hz) AND H3 (1477 Hz). THE OTHER TWO TUNED CIRCUITS MUST BE STRAPPED IN ACCORDANCE WITH NOTE 204. OPERATION OF THE DETECTORS IS IDENTICAL WITH THAT OF CP3. THE TIMER CIRCUIT CONSISTS OF TRANSISTORS (Q3) AND (Q4), REFERENCE DIODE (CR2), AND ASSOCIATED RESISTORS AND CAPACITORS. IT PROVIDES AN INTERDIGITAL TIME-OUT PERIOD OF APPROXIMATELY 2.5 SECONDS WHEN USED WITH CP3. WITH RELAY (K1) RELEASED AND PINS 21 AND 22 CONNECTED EXTERNALLY, CAPACITOR (C11) IS DISCHARGED, TRANSISTOR (Q4) IS OFF AND (Q3) IS ON. THIS APPLIES -24 VOLTS TO PIN 30. WITH THE CONNECTION BETWEEN PINS 21 AND 22 REMOVED AND PINS 22 AND 1 CONNECTED EXTERNALLY, CAPACITOR (C11) CHARGES THROUGH RESISTOR (R11). IN APPROXIMATELY 2.5 SECONDS THE REFERENCE VOLTAGE OF DIODE (CR2) IS OVERCOME. THIS FORWARD BIASES (Q4) WHICH TURNS OFF (Q3) REMOVING VOLTAGE FROM PIN 30. RECONNECTING PINS 21 AND 22 DISCHARGES (C11) THROUGH (R10) PROVIDING A TIMER RECOVERY TIME OF APPROXIMATELY 0.2 SECONDS. WITH RELAY (K1) OPERATED CONTACT 2 CONNECTS -24 VOLTS TO PIN 30 DIRECTLY.

NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; VALUES PRECEDED BY THE SYMBOL + (PLUS) ARE IN VOLTS; ARE - (MINUS) ARE IN VOLTS.
- ⊥ GROUND RETURN.

RECORD OF CHANGES					
ENG	PREV	STD	NR	DISC	SEE
ISS	FURN		S	R	NOTE
78	R	S	R		

SUPPLEMENTARY SIGNAL DETECTOR

CPS 4

ISSUE
103

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-IC245-01-J4

BELL TELEPHONE LABORATORIES
INCORPORATED

65

SD-IC245-01-J4

CPS 5
PART OF DECODER

7M 7T 7T0

COMPONENT LIST

DESIG	CODE
[9] CR1.0-CR1.8	458B
[9] CR2.0-CR2.8	458A

RESISTOR

DESIG	CODE
[9] R1.0-R1.8	KS-13490, L1, 2.7k
[9] R2.0-R2.8	KS-16648, L1, KS-20616, L1A, 75k
[9] R3.0-R3.8	237A, KS-20616, L1A, 31.6k
[9] R4.0-R4.8	237A, KS-20616, L1A, 215k
[9] R5.0-R5.8	KS-16649, L1, 47k, KS-20616, L1A, 47.5k
[9] R6.0-R6.8	KS-16648, L1, KS-20616, L1A, 7.5k
[9] R7.0-R7.8	237A, KS-20616, L1A, 1210
[9] R8.0-R8.8	238A, KS-20616, L1A, 3480
[8] R9.9-R9.16	KS-16649, L1, 24k, KS-20616, L1A, 23.7k
[8] R10.9-R10.16	238A, KS-20616, L1A, 5620
[8] R11.9-R11.16	KS-16648, L1, KS-20616, L1A, 15k
[8] R12.9-R12.16	KS-13490, L1, 2.4k

TRANSISTOR

DESIG	CODE
[9] Q1.0-Q1.8	76A 66J
[8] Q2.9-Q2.16	76A 66J
[8] Q3.9-Q3.16	76A 66J

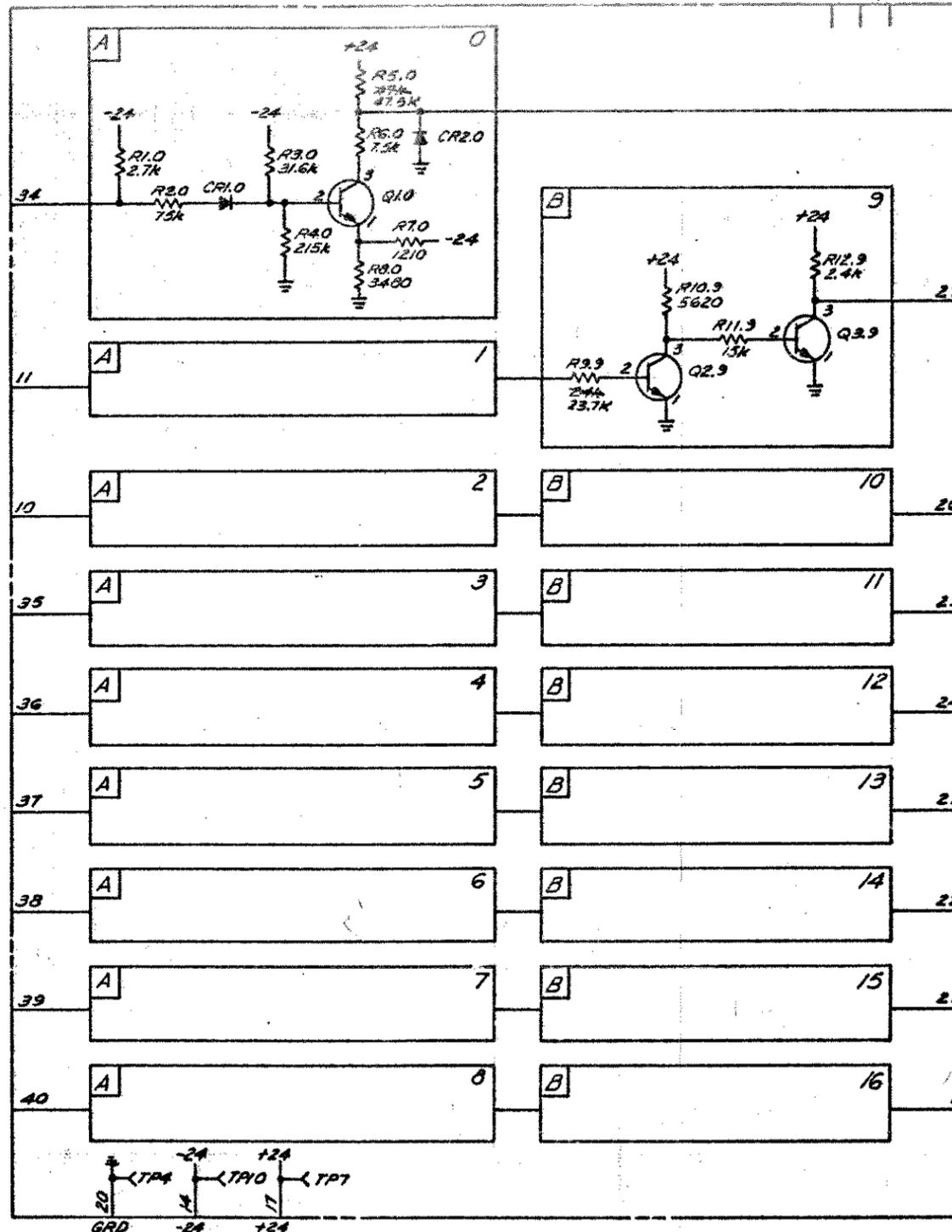
INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES NINE SEPARATE INPUTS WHICH SUPPLY -24 VOLTS THROUGH 2700 OHM RESISTORS TO THE INPUT CONNECTING CIRCUIT. PROVIDES NINE SEPARATE OUTPUTS CAPABLE OF DRIVING CONNECTING TRL CIRCUITS.

LEVELS: OUTPUTS ARE AT APPROXIMATELY GROUND POTENTIAL IF THE ASSOCIATED INPUT IS GROUNDED, AND GREATER THAN +6 VOLTS IF THE ASSOCIATED INPUT IS OPEN.

CIRCUIT DESCRIPTION:

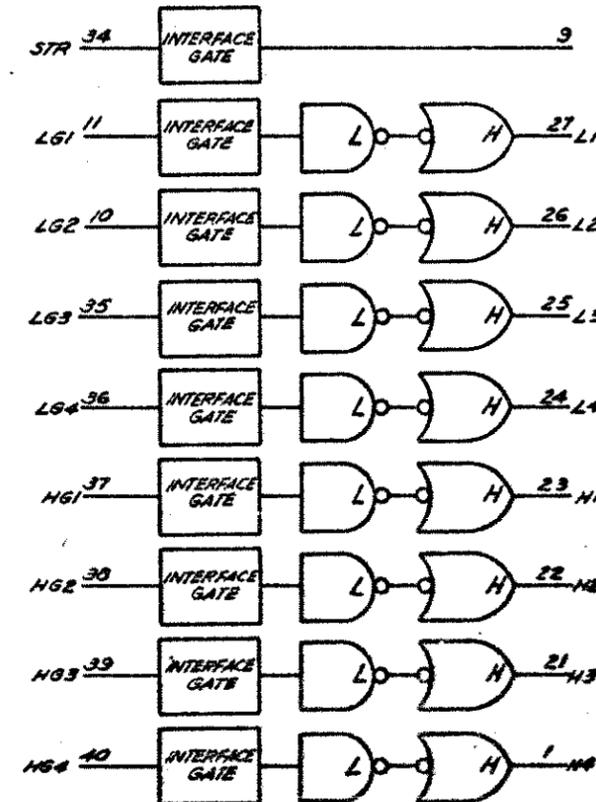
TRANSISTORS (Q1.0) TO (Q1.8) AND ASSOCIATED COMPONENTS FORM NINE IDENTICAL INTERFACE GATES WHOSE FUNCTION IS TO PROVIDE THE PROPER LOGIC LEVEL CONVERSION BETWEEN THE INPUT CONNECTING CIRCUITS AND THE TRL GATES WHICH FOLLOW. (Q2.9) TO (Q2.16) AND ASSOCIATED RESISTORS FORM EIGHT IDENTICAL L GATES WHICH IN TURN DRIVE EIGHT IDENTICAL H GATES COMPOSED OF (Q3.9) TO (Q3.16) AND ASSOCIATED RESISTORS. THE H GATES ARE NEEDED TO SATISFY THE CURRENT REQUIREMENTS OF THE CONNECTING OUTPUT CIRCUITS. OPERATION OF THE INDIVIDUAL GATE CIRCUITS IS EXPLAINED IN NOTE 302.



MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C303
CONNECTOR ON FRAME	908C

SYMBOL



NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
- ⊥ GROUND RETURN

PART OF DECODER

CPS 5

ISSUE 10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-IC245-01-J5

BELL TELEPHONE LABORATORIES INCORPORATED

6S

SD-IC245-01-J5

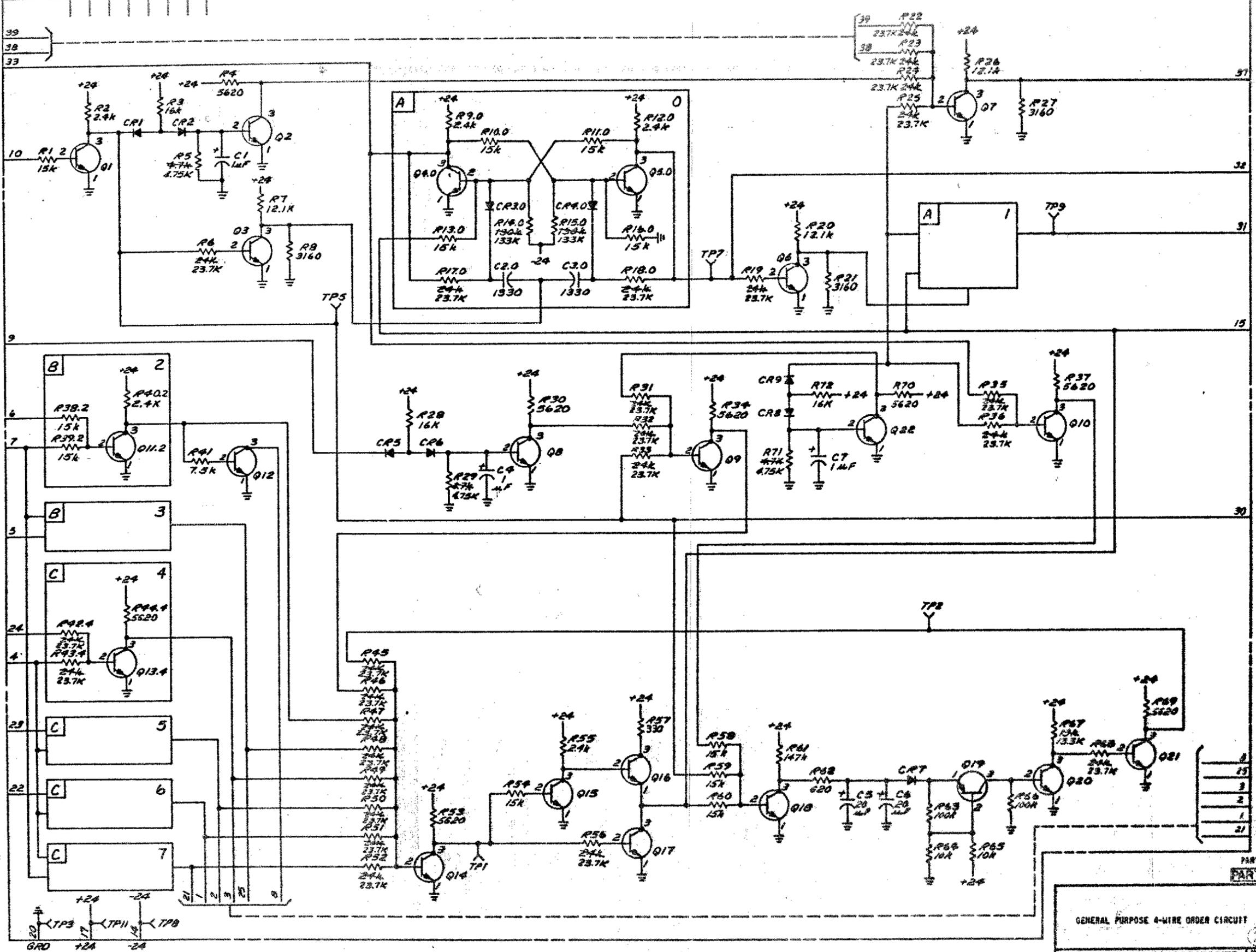
PART OF CPS 6
PART OF DECODER

DRAWING
ISSUE
1
2A

TP1 TP2 TP3 TP5 TP7 TP8 TP9 TP11

A
B
C
D
E
F
G
H

A
B
C
D
E
F
G
H



SD-IC245-01-J6A

PART OF DECODER
PART OF CPS 6

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

BELL TELEPHONE LABORATORIES
INCORPORATED

SD-IC245-01-J6A

ISSUE
10B

65

PART OF CPS6
PART OF DECODER

COMPONENT LIST

CAPACITOR

DESIG	CODE
C1	600A
[2] C2.0-C2.1	794C, KS-20977, L4, 1330
C3.0-C3.1	794C, KS-20977, L4, 1330
C4	600A
C5, C6	602C
C7	600A
DIODE	
DESIG	
CODE	
CR1	458A
CR2	449A
[2] CR3.0-CR3.1	449A
[2] CR4.0-CR4.1	449A
CR5	458A
CR6	449A
CR7	458A
CR8	449A
CR9	458A

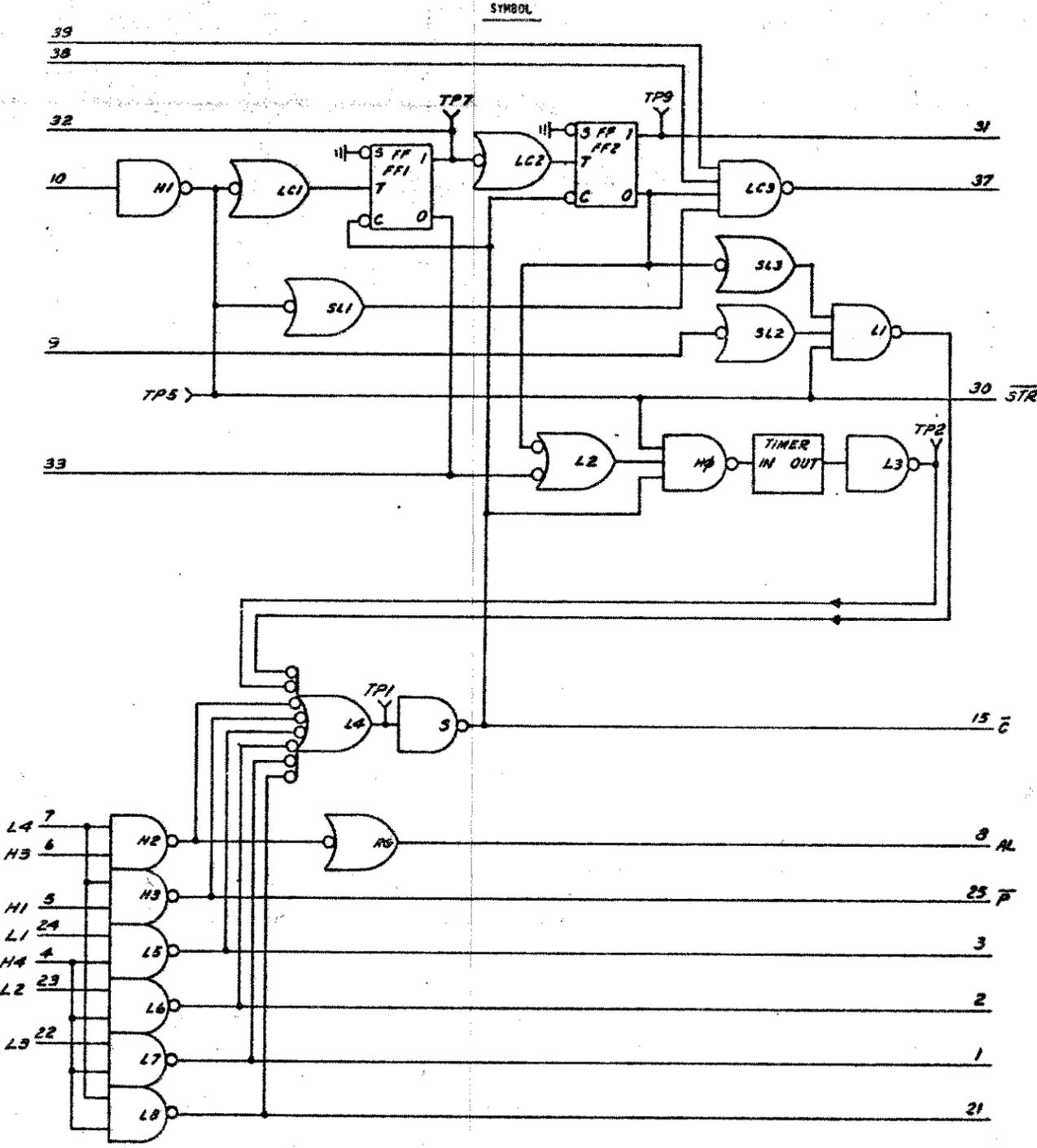
RESISTOR

DESIG	CODE
R1	KS-16645, L1, KS-20616, L1A, 15K
R2	KS-13490, L1, 2.4K
R3	KS-16645, L1, KS-20616, L1A, 16K
R4	258A, KS-20616, L1A, 5620
R5	KS-16645, L1, 4.7K, KS-20616, L1A, 4.75K
R6	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R7	257A, KS-20616, L1A, 12.1K
R8	257A, KS-20616, L1A, 3160
[2] R9.0, R9.1	KS-13490, L1, 2.4K
[2] R10.0, R10.1	KS-16645, L1, KS-20616, L1A, 15K
[2] R11.0, R11.1	KS-16645, L1, KS-20616, L1A, 15K
[2] R12.0, R12.1	KS-13490, L1, 2.4K
[2] R13.0, R13.1	KS-16645, L1, KS-20616, L1A, 15K
[2] R14.0, R14.1	KS-16645, L1, 130K, KS-20616, L1A, 133K
[2] R15.0, R15.1	KS-16645, L1, 130K, KS-20616, L1A, 133K
[2] R16.0, R16.1	KS-16645, L1, KS-20616, L1A, 15K
[2] R17.0, R17.1	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
[2] R18.0, R18.1	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R19	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R20	257A, KS-20616, L1A, 12.1K
R21	257A, KS-20616, L1A, 3160
R22-R25	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R26	257A, KS-20616, L1A, 12.1K
R27	257A, KS-20616, L1A, 3160
R28	KS-16645, L1, KS-20616, L1A, 16K
R29	KS-16645, L1, 4.7K, KS-20616, L1A, 4.75K
R30	258A, KS-20616, L1A, 5620
R31-R33	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R34	258A, KS-20616, L1A, 5620
R35, R36	KS-16645, 24K, KS-20616, L1A, 23.7K
R37	258A, KS-20616, L1A, 5620
[2] R38.2-R38.3	KS-16645, L1, KS-20616, L1A, 15K
[2] R39.2, R39.3	KS-16645, L1, KS-20616, L1A, 19K
[2] R40.2-R40.3	KS-13490, L1, 2.4K
R41	KS-16645, L1, KS-20616, L1A, 7.5K
[4] R42.4-R42.7	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
[4] R43.4-R43.7	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
[4] R44.4-R44.7	258A, KS-20616, L1A, 5620
R45-R52	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R53	258A, KS-20616, L1A, 5620
R54	KS-16645, L1, KS-20616, L1A, 15K
R55	KS-13490, L1, 2.4K
R56	KS-16645, L1, 24K, KS-20616, L1A, 23.7K
R57	KS-13490, L1, 330
R58-R60	KS-16645, L1, KS-20616, L1A, 15K
R61	257A, KS-20616, L1A, 147K
R62	KS-16645, L1, KS-20616, L1A, 620
R63	KS-16645, L1, KS-20616, L1A, 100K
R64, R65	258A, KS-20616, L1A, 10K
R66	KS-16645, L1, KS-20616, L1A, 100K
R67	KS-16645, L1, 130K, KS-20616, L1A, 13.3K
R68	KS-16645, L1, KS-20616, L1A, 24K
R69	258A, KS-20616, L1A, 5620
R70	258A, KS-20616, L1A, 5620
R71	KS-16645, L1, 4.7K, KS-20616, L1A, 4.75K
R72	KS-16645, L1, KS-20616, L1A, 16K

COMPONENT LIST (CONT)

TRANSISTOR

DESIG	CODE
Q1-Q3	74 66J
[2] Q4.0-Q4.1	74 66J
[2] Q5.0-Q5.1	74 66J
Q6-Q10	74 66J
[2] Q11.2-Q11.3	74 66J
Q12	74 66L
[4] Q13.4-Q13.7	74 66J
Q14-Q18	74 66J
Q19	2N3251
Q20, Q21	74 66J
Q22	74 66J



MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C309
CONNECTOR ON FRAME	908C

INPUT/OUTPUT INFORMATION-

FUNCTION: WHEN USED AS PART OF THE DECODER IT PROVIDES THE COUNTING REQUIRED TO RECOGNIZE A PARTICULAR SET OF FIVE TOUCH-TONE 2-DIGIT OR 3-DIGIT CODES AS WELL AS SPECIAL SINGLE DIGIT CODES. INTER-DIGITAL TIMING IS PROVIDED WHICH REQUIRES THE SEPARATE DIGITS IN A VALID CODE BE SPACED NO MORE THAN 5 SECONDS APART.

LEVELS: LOGIC LEVELS REQUIRED FOR OPERATION OF THE VARIOUS TTL CIRCUITS ARE GIVEN IN NOTE 302.

CIRCUIT DESCRIPTION

A STEERING PULSE OF APPROXIMATELY 50 MSEC DURATION APPEARING ON PIN 10 IS FED THROUGH GATES (H1) AND (LC1) TO TOGGLE FLIP-FLOP (FF1). THE "1" OUTPUT OF FF1 IS FED THROUGH (LC2) AND APPEARS AT THE TOGGLE INPUT OF (FF2). SINCE ONLY A NEGATIVE GOING TRANSITION WILL TOGGLE A FLIP-FLOP, (FF2) WILL CHANGE STATE AT ONE HALF THE (FF1) RATE. BOTH FLIP-FLOPS ARE CLEARED SIMULTANEOUSLY EITHER BY A TIMER, WHICH ALLOWS A PERIOD OF NO MORE THAN FIVE SECONDS BETWEEN STEERING PULSES, OR BY RECOGNIZING THE COUNT FOR WHICH THE UNIT IS STRAPPED. A STRAP BETWEEN PINS 9 AND 32 PERMITS A 2 DIGIT COUNT WHILE A STRAP BETWEEN PINS 9 AND 33 PERMITS A 3 DIGIT COUNT. TIMING IS INITIATED WHEN ALL THREE INPUTS TO THE M2 GATE ARE IN THE "1" STATE WHICH TURNS OFF THE M2 GATE. THIS CONDITION IS PRESENT WHEN THE ZERO OUTPUT OF EITHER FLIP-FLOP HAS REACHED THE "0" STATE CAUSING THE (L2) GATE OUTPUT TO BECOME "1" AND THE (H1) GATE OUTPUT HAS RETURNED TO THE "1" STATE. AFTER APPROXIMATELY FIVE SECONDS THE TIMER OUTPUT TURNS OFF GATE (L3); TURNS ON GATE (L4); TURNS OFF THE S GATE, CLEARING (FF1) AND (FF2). IN ORDER TO CLEAR THE FLIP-FLOPS AFTER THE PROPER COUNT, ALL THREE INPUTS TO GATE (L1) MUST BE IN THE "1" STATE TO TURN IT OFF. TWO OF THE INPUTS ARE FROM SLOW GATES (SL2) AND (SL3) WHICH HAVE, WHEN COUNTING THREE DIGITS, THE ZERO OUTPUTS OF (FF1) AND (FF2) RESPECTIVELY AS THEIR INPUTS. THE THIRD INPUT TO GATE (L1) IS THE OUTPUT OF GATE (H1). WHEN COUNTING TWO DIGITS THE "1" OUTPUT OF (FF1) IS CONNECTED TO (SL2). THE TURNING OFF OF L1 CLEARS BOTH FLIP-FLOPS IN THE SAME MANNER AS GATE (L3). THE OUTPUT OF THE (S) GATE IS ALSO FED TO THE (M2) GATE INPUT WHICH CLEARS THE TIMER. THE FIRST DIGIT OF AN OFFICE CODE PRODUCES A POSITIVE PULSE AT PIN 37. THIS REQUIRES THAT ALL FOUR INPUT LEADS TO GATE (LC3) BE IN THE "1" STATE DURING THE DIGIT PERIOD. PINS 38 AND 39 ARE DIGIT INPUTS WHICH OCCUR COINCIDENT WITH THE STEERING PULSE ON PIN 10. THE OUTPUT OF GATE (M5) TURNS ON SLOW GATE (SL1) WHOSE OUTPUT IS IN THE "1" STATE DURING THIS PERIOD. THE ZERO OUTPUT OF (FF2) IS THE FOURTH INPUT TO (LC3) AND, SINCE FF2 IS IN THE CLEAR CONDITION, IS ALREADY IN THE "1" STATE. THE INPUTS TO GATES (M2), (M3), AND (L3) TO (L8) ARE WIRED TO PRODUCE A "0" STATE OUTPUT FOR ANY OF SIX SPECIAL SINGLE DIGIT CODES. ALL OUTPUTS ARE FED TO GATE (L4) SO THAT THE TURNING OFF OF ANY OF THESE GATES WILL CLEAR FLIP-FLOPS (FF1) AND (FF2). AN M2 GATE IS SWITCHED BY THE OUTPUT OF GATE (M2) WHICH CAN OPERATE AN EXTERNAL RELAY. OPERATION OF THE INDIVIDUAL TTL CIRCUITS IS DESCRIBED IN NOTE 302.

NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; CAPACITANCE VALUES ARE IN PICOFARADS; VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS
- GROUND RETURN

PART OF DECODER
PART OF CPS 6

ISSUE
10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-IC245-01-J6B

BELL TELEPHONE LABORATORIES
INCORPORATED

65

SD-IC245-01-J6B

PART OF CPS7
PART OF DECODER

COMPONENT LIST

CAPACITOR

DESIG	CODE
[2] C1.0, C1.1	5946, KS-20977, L4, 1330
[2] C2.0, C2.1	5946, KS-20977, L4, 1330

DIODE

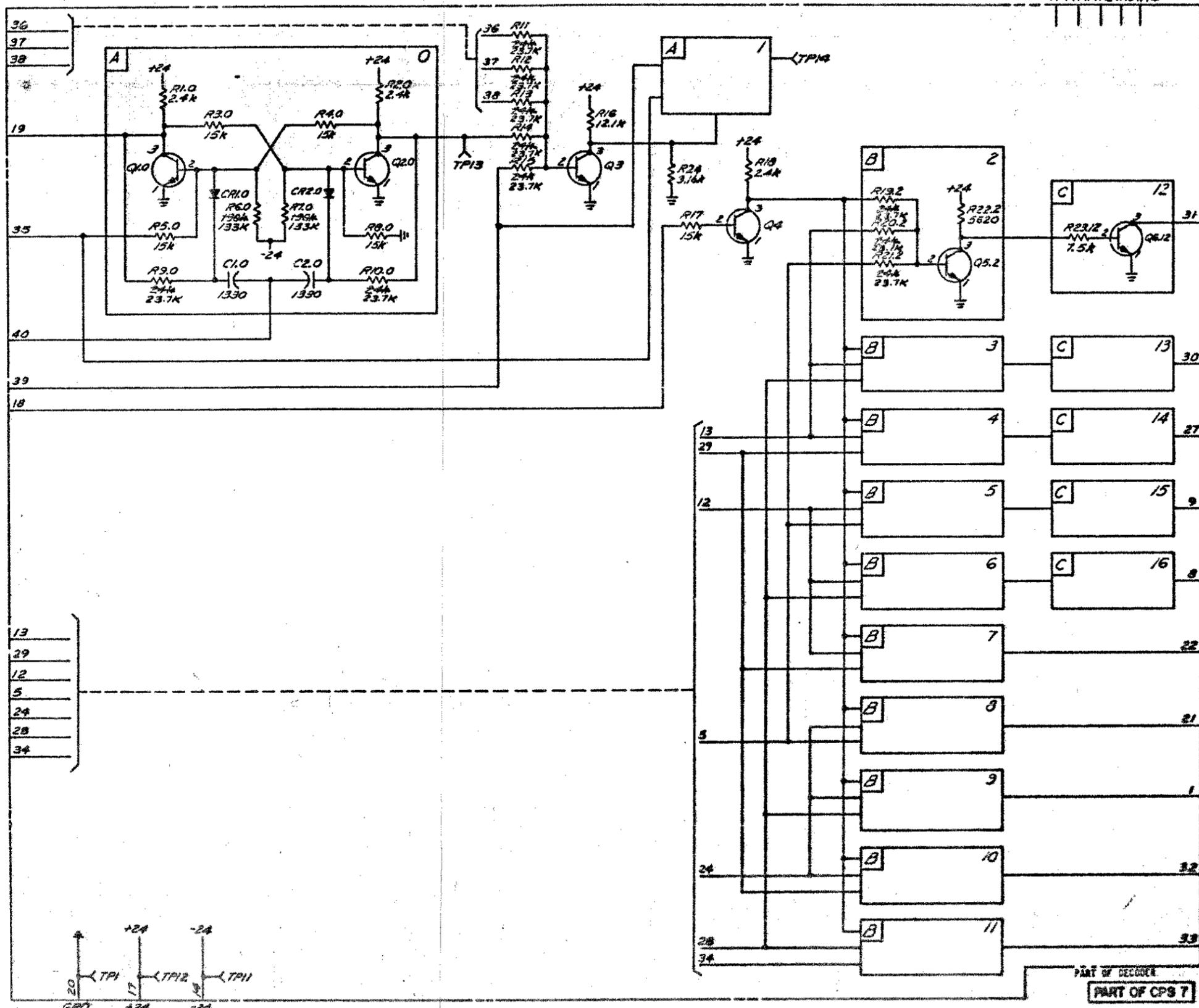
DESIG	CODE
[2] CR1.0, CR1.1	449A
[2] CR2.0, CR2.1	449A

RESISTOR

DESIG	CODE
[2] R1.0, R1.1	KS-13490, L1, 2.4k
[2] R2.0, R2.1	KS-13490, L1, 2.4k
[2] R3.0, R3.1	KS-16645, L1, KS-20616, L1A, 15k
[2] R4.0, R4.1	KS-16645, L1, KS-20616, L1A, 15k
[2] R5.0, R5.1	KS-16645, L1, KS-20616, L1A, 15k
[2] R6.0, R6.1	KS-16645, L1, 150k, KS-20616, L1A, 133k
[2] R7.0, R7.1	KS-16645, L1, 150k, KS-20616, L1A, 133k
[2] R8.0, R8.1	KS-16645, L1, KS-20616, L1A, 15k
[2] R9.0, R9.1	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
[2] R10.0, R10.1	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
R11-R15	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
R16	297A, KS-20616, L1A, 12.1k
R17	KS-16645, L1, KS-20616, L1A, 15k
R18	KS-13490, L1, 2.4k
[10] R19.2-R19.11	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
[10] R20.2-R20.11	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
[10] R21.2-R21.11	KS-16645, L1, 24k, KS-20616, L1A, 23.7k
[10] R22.2-R22.11	259A, KS-20616, L1A, 5620
[5] R23.12-R23.16	KS-16645, L1, KS-20616, L1A, 7.5k
R24	297A, KS-20616, L1A, 3.16k

TRANSISTOR

DESIG	CODE
[2] Q1.0, Q1.1	16L 66J
[2] Q2.0, Q2.1	16L 66J
Q3, Q4	16L 66J
[10] Q5.2-Q5.11	16L 66J
[5] Q6.12-Q6.16	16L 66L



TPI TPI TPI TPI TPI TPI

PART OF DECODER

PART OF CPS 7

DRAWING	ISSUE
1	1A
2A	1B
	1C
	1D
	1E
	1F
	1G
	1H

ISSUE 10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

BELL TELEPHONE LABORATORIES INCORPORATED

SD-IC245-01-J7A

8S

SD-IC245-01-J7A

PART OF CPS7
PART OF DECODER

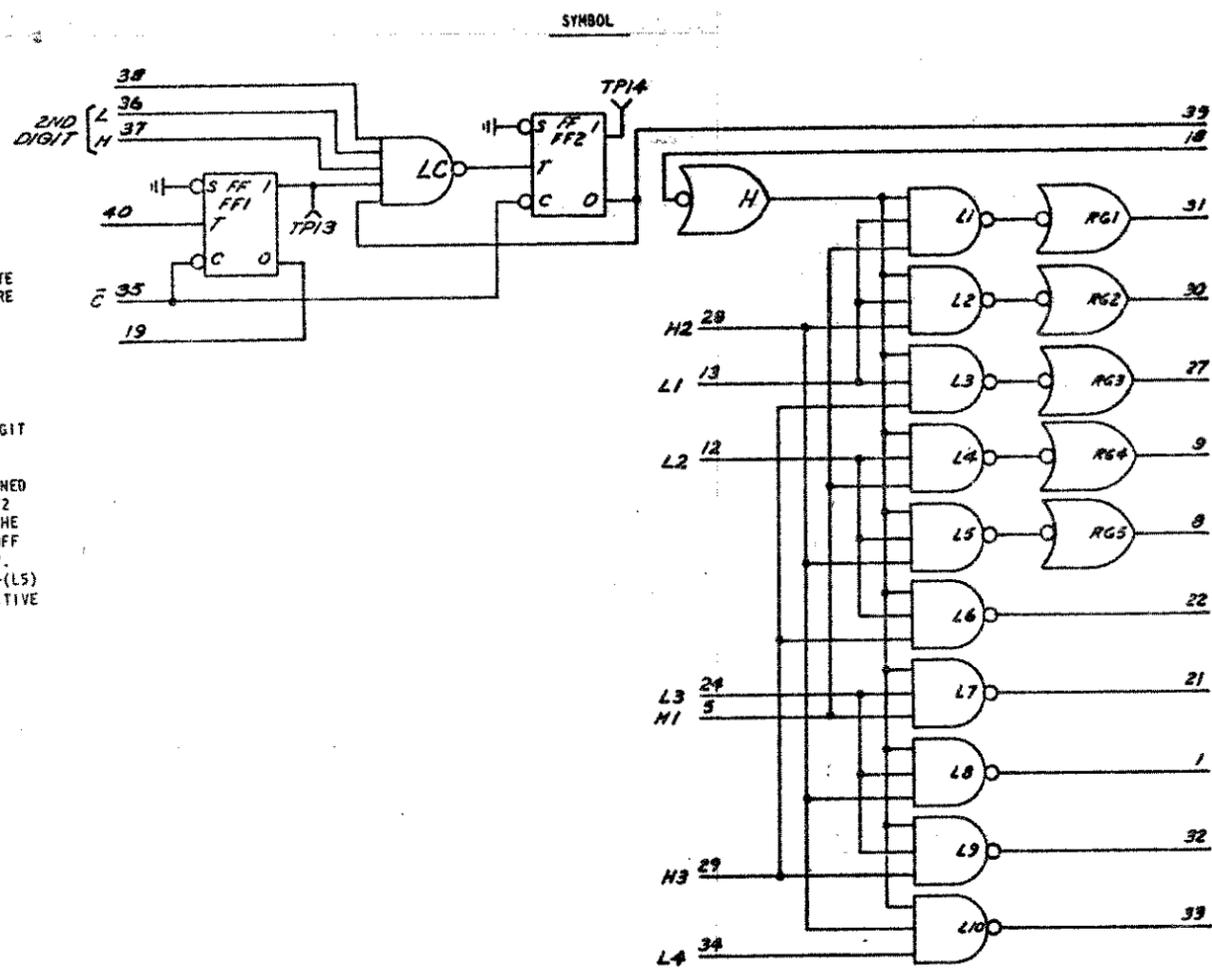
INPUT/OUTPUT INFORMATION

FUNCTION: WHEN USED AS PART OF THE DECODER IT PROVIDES THE TRANSLATION REQUIRED FOR THE MIDDLE AND LAST DIGITS OF A STATION CODE.

LEVELS: LOGIC LEVELS REQUIRED FOR OPERATION OF THE VARIOUS TRL CIRCUITS ARE GIVEN IN NOTE 302.

CIRCUIT DESCRIPTION

THE FIRST DIGIT OF AN OFFICE CODE CAUSES THE INPUT CONNECTING CIRCUIT TO FURNISH A POSITIVE PULSE ON PIN 40 WHICH TOGGLES FLIP-FLOP (FF1). THE "1" OUTPUT OF (FF1) ASSUMES THE "1" STATE AND IS FED TO THE LC GATE. TWO OTHER INPUTS TO THE LC GATE ARE THE MIDDLE DIGIT OF A THREE DIGIT OFFICE CODE WHICH APPEARS ON PINS 36 AND 37 AND ARE IN THE "1" STATE DURING THE DIGIT PERIOD. THE FOURTH INPUT TO THE LC GATE IS THE "0" OUTPUT OF FLIP-FLOP (FF2) WHICH IS CLEARED AND IN THE "1" STATE. UNDER THESE CONDITIONS THE LC GATE PRODUCES A POSITIVE PULSE DURING THE DIGIT PERIOD AND TOGGLES (FF2). THE "0" OUTPUT OF FF2 ASSUMES THE "0" STATE AND, WITH PINS 18 AND 39 STRAPPED (3 DIGIT OPERATION), TURNS ON THE H GATE. THE OUTPUT OF THE H GATE IS ONE OF THREE INPUTS TO EACH L GATE (L1)-(L10). FOR 2 DIGIT OPERATION, PIN 18 AND 19 ARE STRAPPED, AND THE H GATE IS TURNED ON BY THE "0" OUTPUT OF FF1. THUS, FOR 2 DIGIT OPERATION, FF2 IS IGNORED AND THE L GATES ARE ENABLED BY THE FIRST DIGIT. THE OTHER TWO INPUTS TO GATES (L1)-(L10) ARE WIRED TO BE TURNED OFF BY THE LAST DIGIT OF AN OFFICE CODE, 1 THROUGH 0 RESPECTIVELY. GATES (RG1)-(RG5) ARE TURNED ON BY THE OUTPUTS OF GATES (L1)-(L5) RESPECTIVELY. FLIP-FLOP (FF1) AND (FF2) ARE CLEARED BY A POSITIVE PULSE APPEARING ON PIN 35. OPERATION OF THE INDIVIDUAL TRL CIRCUITS IS DESCRIBED IN NOTE 302.



MANUFACTURING REFERENCES	
CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C310
CONNECTOR ON FRAME	908C

DRAWING ISSUE	1
	2A
	4B

- NOTES:**
- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; CAPACITANCE VALUES ARE IN PICOFARADS; VALUES PRECEDED BY THE SYMBOL "P" (PLUS) OR "-" (MINUS) ARE IN VOLTS.
 - GROUND RETURN

SD-1C245-01-J7B

PART OF DECODER
PART OF CPS7

4

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-1C245-01-J7B
BELL TELEPHONE LABORATORIES INCORPORATED	65

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION				DIRECT CURRENT FLOW REQ					REMARKS
DESIG	CODE	OPT	FIG	BSP FIG	CONT PRES	ARM TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP	SEE TEST NOTE	TEST WDG	TEST FOR	AFTER TEST	TEST READ	
								CONN BAT.	CONN GRD							
K1	BF6	R		3				L(K1)	U(K1)	B/G	1	0	25.5	24.0		
K2	BF6	R		3				L(K2)	U(K2)	B/G	1	0	25.5	24.0		
K3	BJ3	R		401				L(K3)	U(K3)	B/G	1	0	32.5	30.5		
K1-K3	MB1	S		200				L REL TST	U REL TST	B/G	1	0	25.5	24.0		

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST

COMPONENT LIST

RELAY											
DESIG	K1		K2		K3		K4		K5		
CODE	BF6		BF6		BJ3		MB1		MB1		
OPTION	R		R		R		S		S		
	CONT ARR	LOC									
12					BM						
11					BM						
10					BM						
9					BM	C6					
8					BM	F6					
7					BM	F6					
6	BM	H6	BM	H6	BM	C6	EBM	H6	EBM	H6	
5	BM	C3	BM	D4	BM	E6	EBM	C3	EBM	D4	
4	BM		BM		BM	E6	EBM		EBM		
3	BM	F5	BM	H6	BM	E6	EBM	F5	EBM	H6	
2	BM	F5	BM	H6	BM	E6	EBM	F5	EBM	H6	
1	BM		BM		BM	G6	EBM		EBM		
COIL	F3		G3		H3		F3		G3		

RELAY		
DESIG	CODE	OPTION
K4	MB1	S
K5	MB1	S
C5	5420	
F3		
G3		

CAPACITOR	
DESIG	CODE
C1-C5	5420

DIODE	
DESIG	CODE
CR1-CR3	400A, SEE NOTE 2

RESISTOR	
DESIG	CODE
R1	259A, KS-20616, L1A, 11
R2	259A, KS-20616, L1A, 3.01
R3, R4	259A, KS-20616, L1A, 600
R5, R6	KS-13492, L1, 82; KS-20289, L6C, 82.5

TRANSFORMER	
DESIG	CODE
T1	2532AC
T2	2586C
T3	2532AC
T4, T5	2578U

VARISTOR	
DESIG	CODE
RV1	104A, SEE NOTE 2

CPS 8, CPS 10 (MFR DISC.)
CPS 9, CPS 11
TALK BATTERY FEED CKT

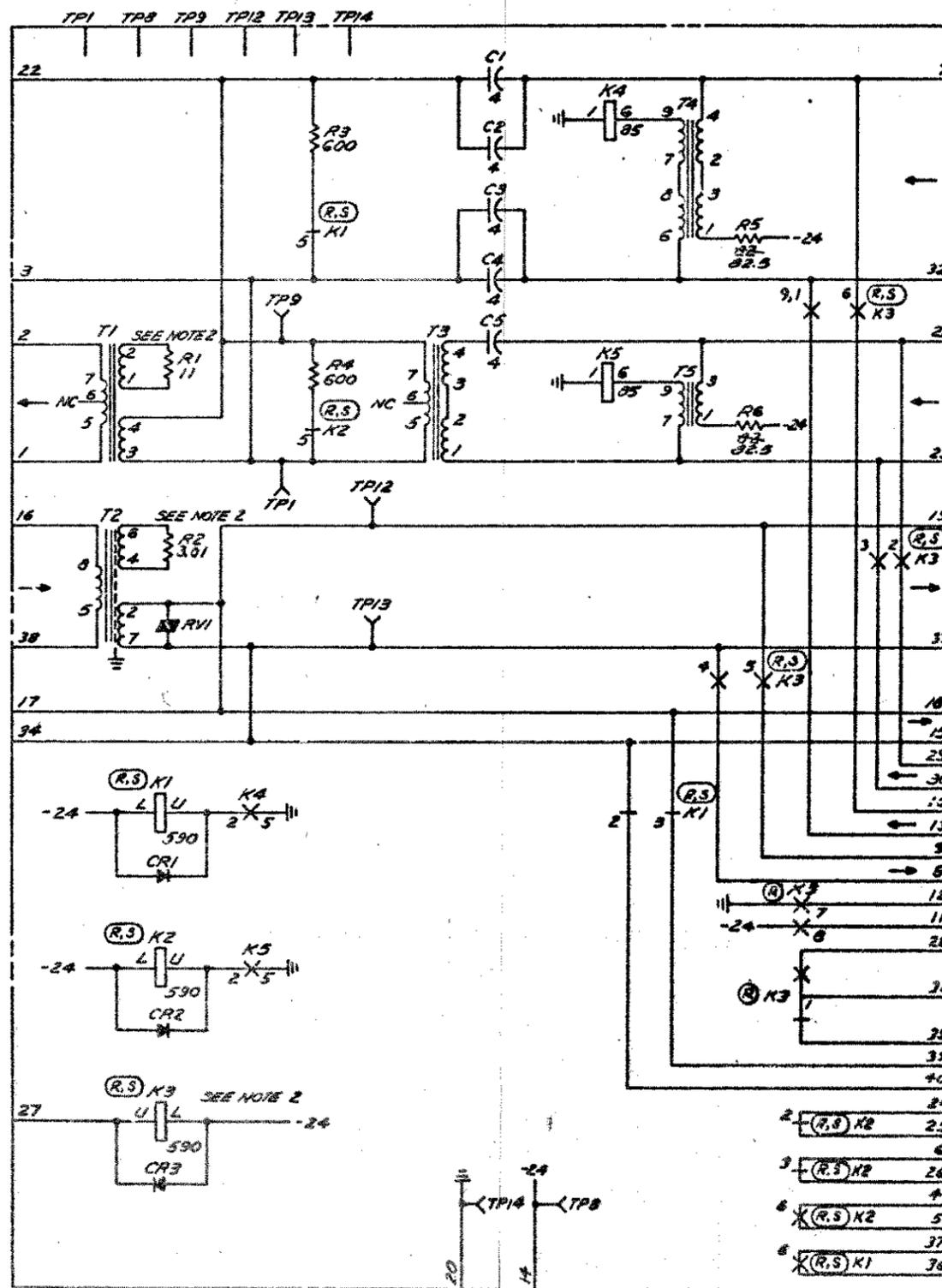
MANUFACTURING REFERENCES	
CATEGORY	NO
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	
CP 8	ED-1C311()G1
CP 9	ED-1C311()G2
CP 9	ED-1C311()G3
CP 10	ED-1C311()G3
CP 11	ED-1C311()G4
CP 11	ED-1C311()G6
CONNECTOR ON FRAME	909C

INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES CURRENT FEED FOR TELEPHONE TRANSMITTER. CP8 AND CP9 PROVIDE IMPEDANCE TRANSFORMATION FOR MATCHING MULTIPLE TALK BATTERY FEEDS (CP10 OR CP11) TO A 600 OHM AMPLIFIER CIRCUIT. CP9 AND CP11 PROVIDE PICK-UP CAPABILITY.

CIRCUIT DESCRIPTION:

CURRENT IS SUPPLIED THROUGH RELAY (K4) AND TRANSFORMER (T4) FOR A TOUCH-TONE TELEPHONE SET. CAPACITORS (C1), (C2), (C3), AND (C4) BLOCK THE DC WHILE PROVIDING AC COUPLING OF THE INPUT SIGNAL. CURRENT IS SUPPLIED THROUGH RELAY (K5) AND TRANSFORMER (T5) FOR A 50 OHM TELEPHONE TRANSMITTER. CAPACITOR (C5) BLOCKS THE DC WHILE TRANSFORMER (T3) STEPS UP THE AC IMPEDANCE FROM 50 TO 600 OHMS. IDLE CIRCUIT TERMINATING RESISTORS (R3) AND (R4) ARE REMOVED FROM THE CIRCUIT BY THE OPERATION OF RELAYS (K4) OR (K5) WHICH OPERATE RELAYS (K1) AND (K2) RESPECTIVELY. CP8 AND CP9 ARE EQUIPPED WITH TRANSFORMERS (T1) AND (T2). (T1) STEPS UP THE IMPEDANCE TO 600 OHMS AT TERMINALS 1 AND 2. (T2) STEPS UP THE IMPEDANCE TO 600 OHMS AT TERMINALS 16 AND 38. THIS ALLOWS UP TO FOUR CP10 OR 11 TO BE MULTIPLIED WITH A CP8 OR 9 WITH NO SIGNIFICANT CHANGE IN TRANSMIT OR RECEIVE LEVEL. CP9 AND 11 ARE EQUIPPED WITH A PICK UP RELAY (K3) WHICH OPERATES UNDER CONTROL OF AN ASSOCIATED STATION SET KEY. THE OPERATION OF (K3) CONNECTS THE TRANSMISSION LEADS OF THE ASSOCIATED STATION SET TO THE 4-WIRE LINE. CONTACTS ON RELAYS (K1) AND (K2) PROVIDE LOUSPEAKER CUT-OFF AS REQUIRED.



NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS. CAPACITANCE VALUES ARE IN MICROFARADS. VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.

OPTIONAL CPS	ONLY COMPONENT
(K)	8, 43, CR3
(R, S)	9 NONE
(G)	10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37

- GROUND RETURN.

RECORD OF CHANGES				
DATE	PREP	STD	BY	SEE NOTE
78	R	S	R	

TALK BATTERY FEED CKT

CPS 8, 9, 10, 11

ISSUE 10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-J8

BELL TELEPHONE LABORATORIES INCORPORATED

65

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION				TEST SET	SEE TEST	DIRECT CURRENT FLOW REQT				REMARKS
DESIG	CODE	OPT	FIG.	BSP FIG.	CONT PRES	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA		PREP	NOTE	TEST WDG	TEST FOR	TEST FOR	TEST FOR	TEST FOR	
RELAY								CONN DAT.	CONN GRD.				MA	MA	MA	MA	
K1	BF6	R		3				L(K1)	U(K1)	B/G	1,2	0	23.2	24.0			
K1	MB1	S		200				L(K1)	U(K1)	B/G	1,2	0	23.2	24.0			

CPS 12
PRE-EMPT CIRCUIT

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C312()G1 ED-1C312()G2
CONNECTOR ON FRAME	908C

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST.
2. USE + BATTERY.

COMPONENT LIST

RELAY	DESIG	CODE	DESIG	CODE
	K1	BF6	K1	MB1
		R		S
		LOC		LOC
	6	BM	F7	EBM
	5	BM	E7	EBM
	4	BM	E7	EBM
	3	BM	D7	EBM
	2	BM	C7	EBM
	1	BM	C7	EBM
COIL		X	C5	C5

CAPACITOR

DESIG	CODE
C1, C2	602C

DIODE

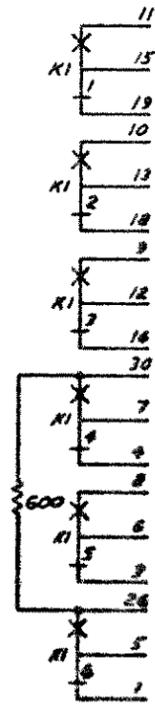
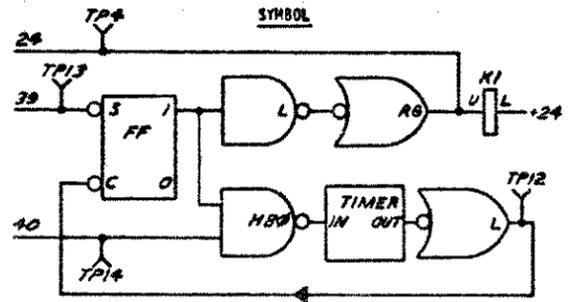
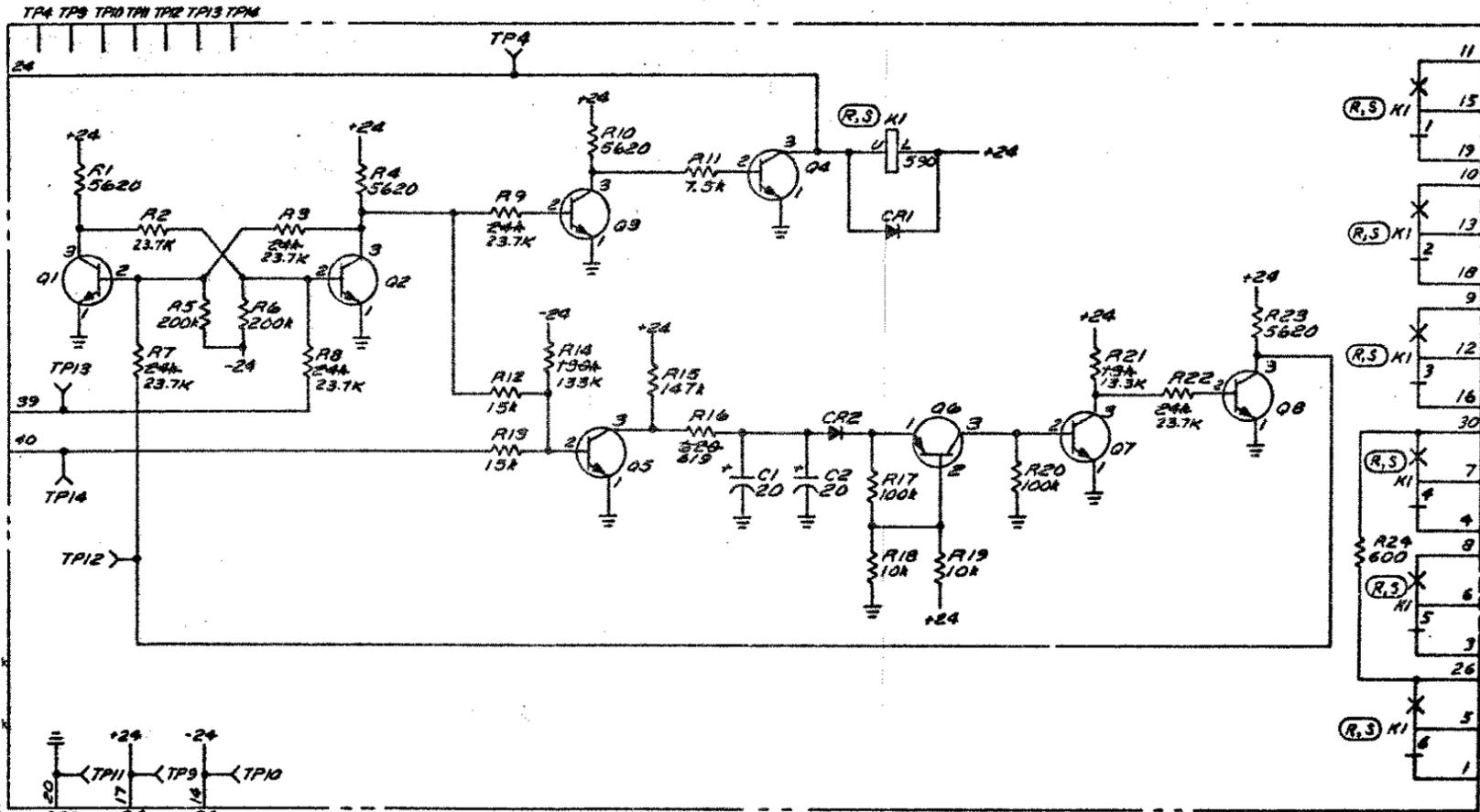
DESIG	CODE
CR1	400J
CR2	458A

RESISTOR

DESIG	CODE
R1	237A, KS-20616, L1A, 5620
R2, R3	KS-16645, L1, 24K KS-20616, L1A, 23.7k
R4	237A, KS-20616, L1A, 5620
R5, R6	KS-16645, L1, KS-20616, L1A, 200k
R7-R9	KS-16645, L1, 24K KS-20616, L1A, 23.7k
R10	237A, KS-20616, L1A, 5620
R11	KS-16645, L1, KS-20616, L1A, 7.5k
R12, R13	KS-16645, L1, KS-20616, L1A, 15k
R14	KS-16645, L1, 130K KS-20616, L1A, 133k
R15	237A, KS-20616, L1A, 147k
R16	KS-16645, L1, 620K KS-20616, L1A, 619
R17	KS-16645, L1, KS-20616, L1A, 100k
R18, R19	237A, KS-20616, L1A, 10k
R20	KS-16645, L1, KS-20616, L1A, 100k
R21	KS-16645, L1, 13K KS-20616, L1A, 13.3k
R22	KS-16645, L1, KS-20616, L1A, 24k
R23	237A, KS-20616, L1A, 5620
R24	237A, KS-20616, L1A, 600

TRANSISTOR

DESIG	CODE
Q1-Q3	166 66J
Q4	166 66L
Q5	166 66J
Q6	2N3254 51A
Q7, Q8	166 66J



INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES RELAY TRANSFER CONTACTS WHICH OPERATE WHEN THE INPUT IS PULSED BY A CONNECTING CIRCUIT. A TIMER CIRCUIT RELEASES THE RELAY AFTER A 5 SECOND INTERVAL.

INPUT: RELAY (K1) OPERATES WITH POSITIVE PULSE GREATER THAN +6 VOLTS APPLIED TO PIN 39. A PULSE GREATER THAN THE +6 VOLTS APPLIED TO PIN 40 DURING TIME-OUT PERIOD RESTARTS TIMER. GROUND APPLIED TO PIN 24 OPERATES RELAY (K1).

CIRCUIT DESCRIPTION

TRANSISTORS (Q1) AND (Q2) AND ASSOCIATED COMPONENTS FORM A FLIP-FLOP WHICH SETS WHEN A POSITIVE PULSE IS APPLIED TO TERMINAL 39. THIS, IN SEQUENCE, TURNS OFF L GATE (Q3); TURNS ON RG GATE (Q4) AND OPERATES RELAY (K1). THE 1 OUTPUT OF THE FLIP-FLOP ALSO TURNS OFF THE HB0 GATE (Q5). THIS STARTS A TIMER COMPOSED OF (Q6), (Q7), AND ASSOCIATED COMPONENTS WHICH, AFTER APPROXIMATELY 5 SECONDS, TURNS OFF L GATE (Q8) AND RESETS THE FLIP-FLOP. A POSITIVE PULSE APPEARING ON PIN 40 DURING THE 5 SECOND TIME-OUT PERIOD WILL RESTART THE TIMER. RELAY (K1) CAN BE OPERATED EXTERNALLY BY APPLYING GROUND TO PIN 24. OPERATION OF THE INDIVIDUAL TR1 CIRCUITS IS DESCRIBED IN NOTE 302.

- NOTES:
- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; K FOR KILOHMS; CAPACITANCE VALUES ARE IN MICROFARADS; VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
 - GROUND RETURN.
 - RECORD OF CHANGES

DWG	PREV	STD	A & H	APR	SEE
118	R		S	R	
119	S		S		4
 - RATING CHANGED TO STANDARD FOR USE IN FS 32.

PRE-EMPT LET
CPS 12

ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-J9

BELL TELEPHONE LABORATORIES
INCORPORATED

65

SD-1C245-01-J9

CIRCUIT REQUIREMENTS

CPS 13 (MFR DISC.)
CALL-RECALL ALERT CKT

DRAWING
ISSUE
1
2A
4B

DESIG	CODE	OPT	FIG	MECH REQ			CIRCUIT PREPARATION				TEST SET PREP	SEE TEST NOTE	DIRECT CURRENT FLOW REQ			REMARKS
				BSP FIG.	CONT RES	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST WDG			TEST FOR	AFTER TEST MA	TEST READJ MA	
K1	3F24			211				L(K1)	U(K1)	GRD	1	0	34.6	33.0		
K2	3F24			211				L(K2)	U(K2)	GRD	1	0	34.6	33.0		
K3	3F2			1				L(K3)	U(K3)	GRD	1	0	14.0	13.3		
K4	3F6			3				L(K4)	U(K4)	GRD	1	0	25.5	24.0		
K5	3F6			3				L(K5)	U(K5)	GRD	1	0	25.5	24.0		
K6	3F6			3				L(K6)	U(K6)	GRD	1	0	25.5	24.0		
K7	3F2			1				L(K7)	U(K7)	GRD	1	0	14.0	13.3		
K8	3F6			3				L(K8)	U(K8)	GRD	1	0	25.5	24.0		

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C313
CONNECTOR ON FRAME	908C

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST

DESIG	K1	K2	K3	K4	K5	K6	K7	K8
CODE	3F24	3F24	3F2	3F6	3F6	3F6	3F2	3F6
OPTION								
CONV ARR	LOC	CONV ARR	LOC	CONV ARR	LOC	CONV ARR	LOC	CONV ARR
6	EDM H3	EDM	DM G4	DM	C5	DM	DM G5	DM H5
5	EDM	EDM G5	DM D5	DM D5	DM D5	DM D5	DM F5	DM E4
4	EDM	EDM	F3	DM E4	DM E4	DM F5	DM	DM F4
3	EDM G3	EDM G5	DM C5	DM H4	DM G4	DM	DM	DM G4
2	EDM E3	EDM G5	DM F4	DM F4	DM	DM	DM H3	DM H3
1	EDM F3	EDM G5	DM G4	DM H4	DM	DM	DM H3	DM H3
COIL	C4	D4	G4	E4	F4	G4	H4	H4

DIODE
DESIG CODE
CR1-CR3 400J

INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES RELAY LOGIC AND MEMORY FUNCTIONS WHEN CONNECTED TO AN EXTERNAL SIGNALLING RECEIVER OR DECODER AND STATION ALARM CIRCUITS.

CIRCUIT DESCRIPTION

THIS CIRCUIT PROVIDES THREE MODES OF OPERATION AS FOLLOWS:

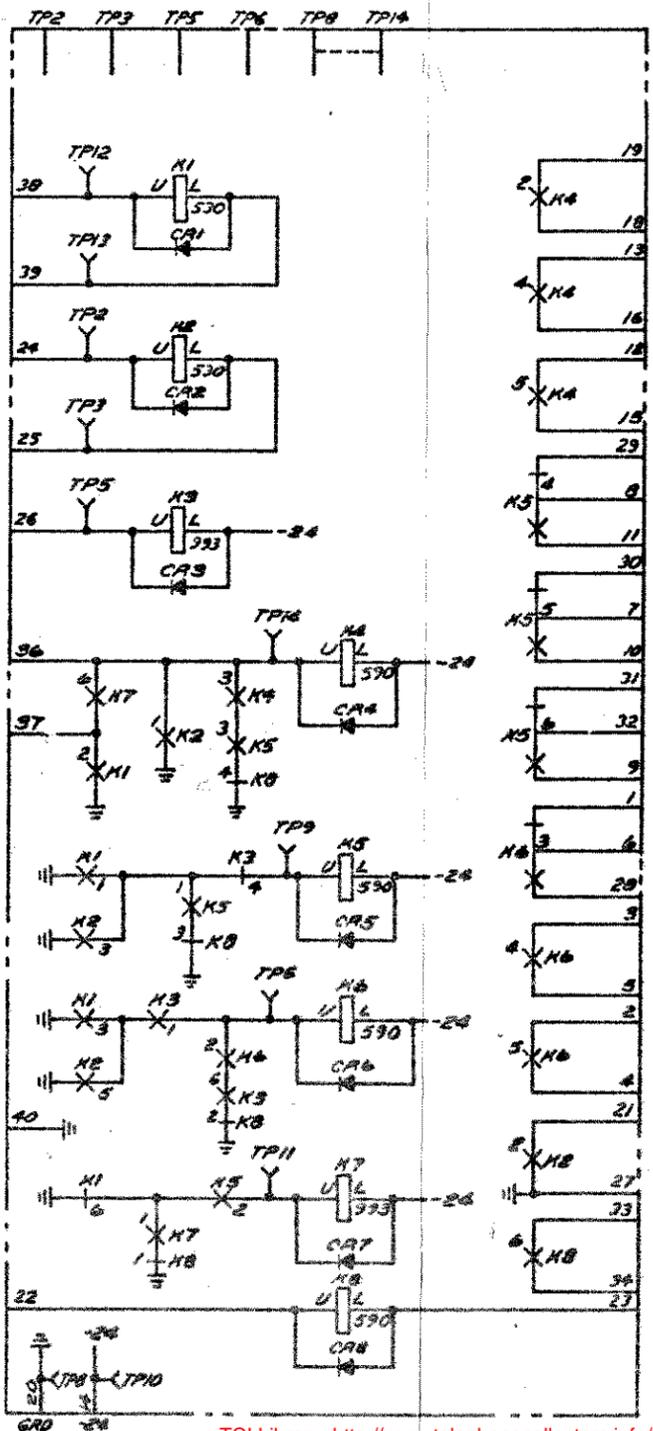
CALL - EXTERNAL OPERATION OF RELAY (K1), WHILE RELAY (K3) IS RELEASED, OPERATES RELAY (K5). THE SUBSEQUENT RELEASE OF (K1) OPERATES RELAY (K7) WHICH SERVES AS A REGISTER TO STORE THE INFORMATION THAT AN UNANSWERED CALL IS PRESENT. EXTERNAL OPERATION OF RELAY (K3) RELEASES (K5) WHICH RELEASES (K7) RESTORING THE CIRCUIT TO NORMAL.

RECALL - EXTERNAL OPERATION OF RELAY (K1), WHILE RELAY (K3) IS OPERATED, OPERATES RELAY (K6). EXTERNAL RELEASE THEN RE-OPERATION OF (K3) RESTORES THE CIRCUIT TO THE INITIAL STATE.

ALERT - THE ALERT OUTPUT, OPERATION OF RELAY (K4) IS A MOMENTARY ADDITION TO THE RECALL STATE OR A LOCKED UP ADDITION TO THE CALL STATE. THE ALERT OUTPUT, PLUS THE CALL OR RECALL, MAY BE ACHIEVED IN ONE OF THREE WAYS:

- (1) EXTERNAL OPERATION OF RELAY (K2).
- (2) TWO SEQUENTIAL OPERATIONS OF RELAY (K1) WITH NO INTERVENING OPERATION OF RELAY (K3).
- (3) A SINGLE OPERATION OF RELAY (K1) DURING THE PERIOD IN WHICH THERE IS AN EXTERNAL CONTACT CLOSURE BETWEEN TERMINALS 36 AND 37. THE ALERT MODE IS RELEASED IN THE SAME MANNER AS THE CALL OR RECALL.

ALL THREE MODES OF OPERATION ARE RENDERED NON-LOCKING BY EXTERNAL OPERATION OF RELAY (K8).



- NOTES:
1. UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS. VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
 2. $\frac{1}{2}$ GROUND RETURN

CALL-RECALL-ALERT CKT

CPS 13

ISSUE
7B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT SD-1C245-01-J10

BELL TELEPHONE LABORATORIES INCORPORATED 65

SD-1C245-01-J10

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION		TEST SET	SEE	DIRECT CURRENT FLOW REQ			REMARKS	
DESIG	CODE	OPT	FIG.	BSP FIG.	CONT PRES	ARM TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP	TEST NOTE	TEST WDG	TEST FOR		TEST READJ
								CONN BAT.	CONN GRO				HA		HA
RELAY															
K1	BF2	R		1				L(K1)	U(K1)	B/G	1	0	18.0	13.3	
K2	BJ2	R		600				L(K2)	U(K2)	B/G	1	0	35.5	33.5	
K1-K3	MB1	S		200				L REL	U REL	B/G	1	0	25.5	24.0	
								TST	TST						

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST.

COMPONENT LIST

RELAY	K1		K2		K1		K2		K3	
DESIG	K1		K2		K1		K2		K3	
CODE	BF2		BJ2		MB1		MB1		MB1	
OPTION	R		R		S		S		S	
	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC				
12			EMB	DS						
11			B							
10			B	GS						
9			B	ES						
8			B	F5						
7			EBM	C5						
6	BM		EBM	C3	EBM		EBM	DS	EBM	C3
5			M		EBM		EBM		EBM	F5
4	B	G4	M		EBM	G4	EBM	GS	EBM	GS
3			M		EBM		EBM	ES	EBM	F4
2			M		EBM		EBM	F5	EBM	E5
1	BM	E5	EBM	B3	EBM	E5	EBM	C5	EBM	B3
COIL		D4		E4		D4		E4		F4

DIODE

DESIG	CODE
CR1, CR2	400J

RESISTOR

DESIG	CODE
R1-R4	257A, KS-20616, L1A, 600

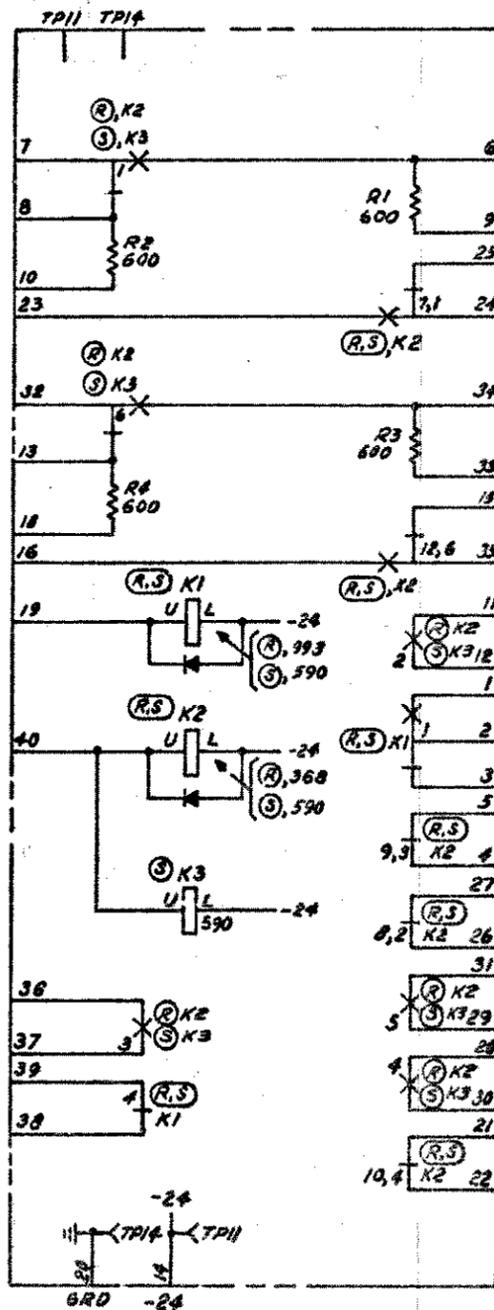
CPS 14

LINE CUT CKT

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C314(101) ED-1C314(102)
CONNECTOR ON FRAME	908C

SYMBOL
SHOWN IN DETAIL IN FS 12



NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS. VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.
- GROUND RETURN.

RECORD OF CHANGES

DWG	PREP	STD	APP	SEE
ISS	FURN	S	DISC	NOTE
78	R	S	N	

LINE CUT CKT

CPS 14

ISSUE
10B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

2

SD-1C245-01-J11

BELL TELEPHONE LABORATORIES

63

SD-1C245-01-J11

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION				TEST SET	SEE TEST	DIRECT CURRENT FLOW REQ			REMARKS
DESIG	CODE	OPT	FIG	BSP FIG.	CONT RES	ARM TRVL	BLOCK OR INSULATE	TEST CLIP DATA		PREP	NOTE	TEST WDG	TEST FOR	TEST MA	TEST READ	
RELAY								CONN PAT	CONN BRD							
K1-K9	M8T			200				L REL TST	U REL TST	B/S	1		0	25.5	24.0	

TEST NOTES:
1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST

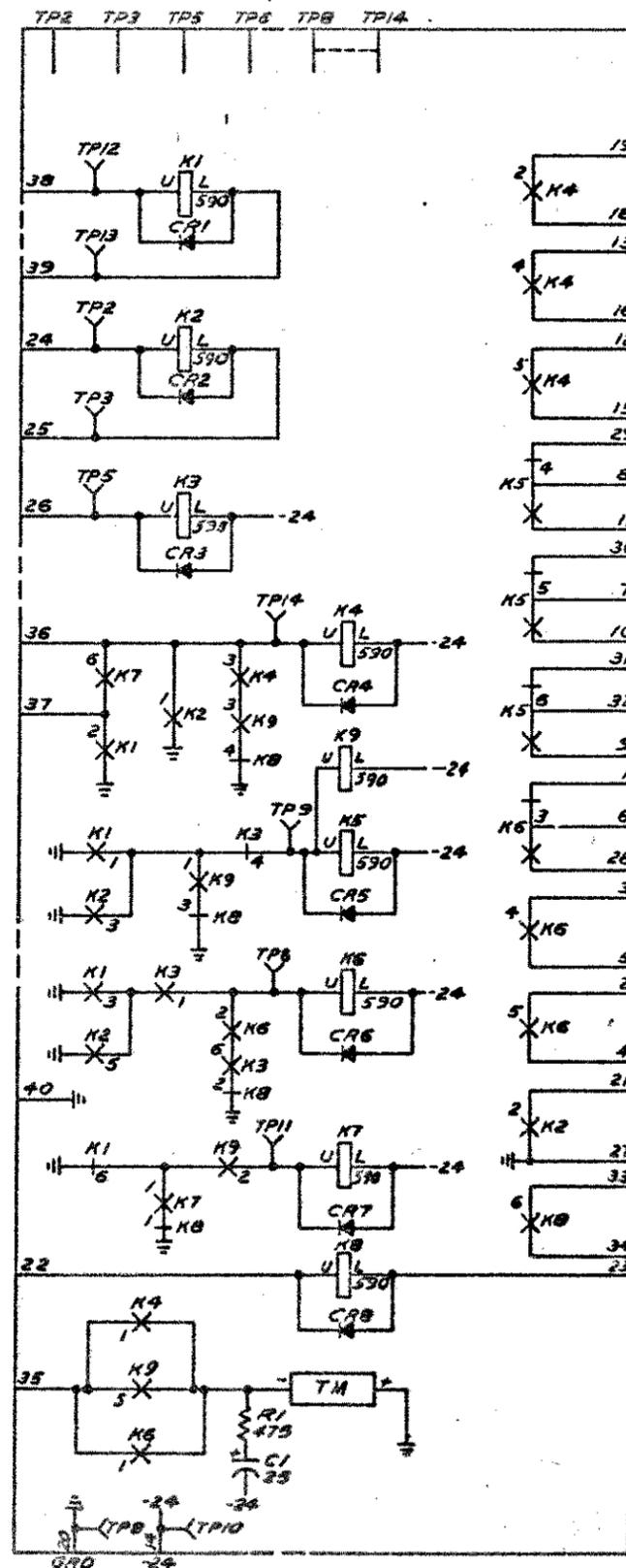
DESIG	K1	K2	K3	K4	K5	K6	K7	K8	K9		
CODE	M8T	M8T	M8T	M8T	M8T	M8T	M8T	M8T	M8T		
OPTION	CONT ARR	LOC	CONT ARR	LOC							
6	EBM F5		EBM F6		EBM 07		EBM 05		EBM F7		EBM G5
5	EBM	EBM F5	EBM	EBM C7	EBM 07	EBM F7	EBM	EBM	EBM	EBM	EBM
4	EBM	EBM	EBM E6	EBM 07	EBM C7	EBM E7	EBM	EBM	EBM D6	EBM	EBM
3	EBM E5	EBM E5	EBM	EBM 06	EBM E7	EBM	EBM E6	EBM	EBM D6	EBM	EBM
2	EBM 05	EBM F7	EBM	EBM 07	EBM	EBM F6	EBM	EBM F6	EBM F6	EBM	EBM
1	EBM E5	EBM 05	EBM E5	EBM 05	EBM	EBM G5	EBM F5	EBM F5	EBM F6	EBM	EBM
COIL	B5	B5	C5	D6	E6	E6	F6	G6	F6	D6	D6

CAPACITOR	
DESIG	CODE
CT	KS16390, L1 25uF, 60V
DIODE	
DESIG	CODE
CR1-CR8	400J
RESISTOR	
DESIG	CODE
R1	KS-20810, L1A, 475
TIMER	
DESIG	CODE
TM	KS-20919

INPUT/OUTPUT INFORMATION
FUNCTION: PROVIDES RELAY LOGIC AND MEMORY FUNCTIONS WHEN CONNECTED TO AN EXTERNAL SIGNALLING RECEIVER OR DECODER AND STATION ALARM CIRCUITS.

CIRCUIT DESCRIPTION
THIS CIRCUIT PROVIDES THREE MODES OF OPERATION AS FOLLOWS:
CALL - EXTERNAL OPERATION OF RELAY (K1), WHILE RELAY (K3) IS RELEASED, OPERATES RELAYS (K8) AND (K9). THE SUBSEQUENT RELEASE OF (K1) OPERATES RELAY (K7) WHICH SERVES AS A REGISTER TO STORE THE INFORMATION THAT AN UNANSWERED CALL IS PRESENT. EXTERNAL OPERATION OF RELAY (K3) RELEASES (K9) WHICH RELEASES (K7) RESTORING THE CIRCUIT TO NORMAL.
RECALL - EXTERNAL OPERATION OF RELAY (K1), WHILE RELAY (K3) IS OPERATED, OPERATES RELAY (K6). EXTERNAL RELEASE THEN RE-OPERATION OF (K3) RESTORES THE CIRCUIT TO THE INITIAL STATE.
ALERT - THE ALERT OUTPUT, OPERATION OF RELAY (K4), IS A MOMENTARY ADDITION TO THE RECALL STATE OR A LOCKED UP ADDITION TO THE CALL STATE. THE ALERT OUTPUT, PLUG THE CALL OR RECALL, MAY BE ACHIEVED IN ONE OF THREE WAYS:
(1) EXTERNAL OPERATION OF RELAY (K2).
(2) TWO SEQUENTIAL OPERATIONS OF RELAY (K1) WITH NO INTERVENING OPERATION OF RELAY (K3).
(3) A SINGLE OPERATION OF RELAY (K1) DURING THE PERIOD IN WHICH THERE IS AN EXTERNAL CONTACT CLOSURE BETWEEN TERMINALS 36 AND 37. THE ALERT MODE IS RELEASED IN THE SAME MANNER AS THE CALL OR RECALL.
THE THREE MODES REMAIN LOCKED-UP UNTIL OPERATION OF A RELEASE RELAY. AN EXTERNAL STRAP PROVIDES A 3 MINUTE LOCK-UP LIMIT USING TIMER (TM) AND RELAY (K8). ALL THREE MODES OF OPERATION ARE RENDERED NON-LOCKING BY EXTERNAL OPERATION OF RELAY (K8).

CPS 15
CALL-RECALL ALERT CRT WITH TIMED LOCK-UP



MANUFACTURING REFERENCES	
CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-1C767()
CONNECTOR ON FRAME	908C

SYMBOL SHOWN IN DETAIL

- NOTES:
- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS, CAPACITANCE VALUES ARE IN MICROFARADS; VALUES PRECEDED BY THE SYMBOL + (PLUS OR -MINUS) ARE IN VOLTS.
 - ⊥ GROUND RETURN.

CALL-RECALL-ALERT CRT WITH TIMED LOCK-UP

CPS 15

ISSUE 9A

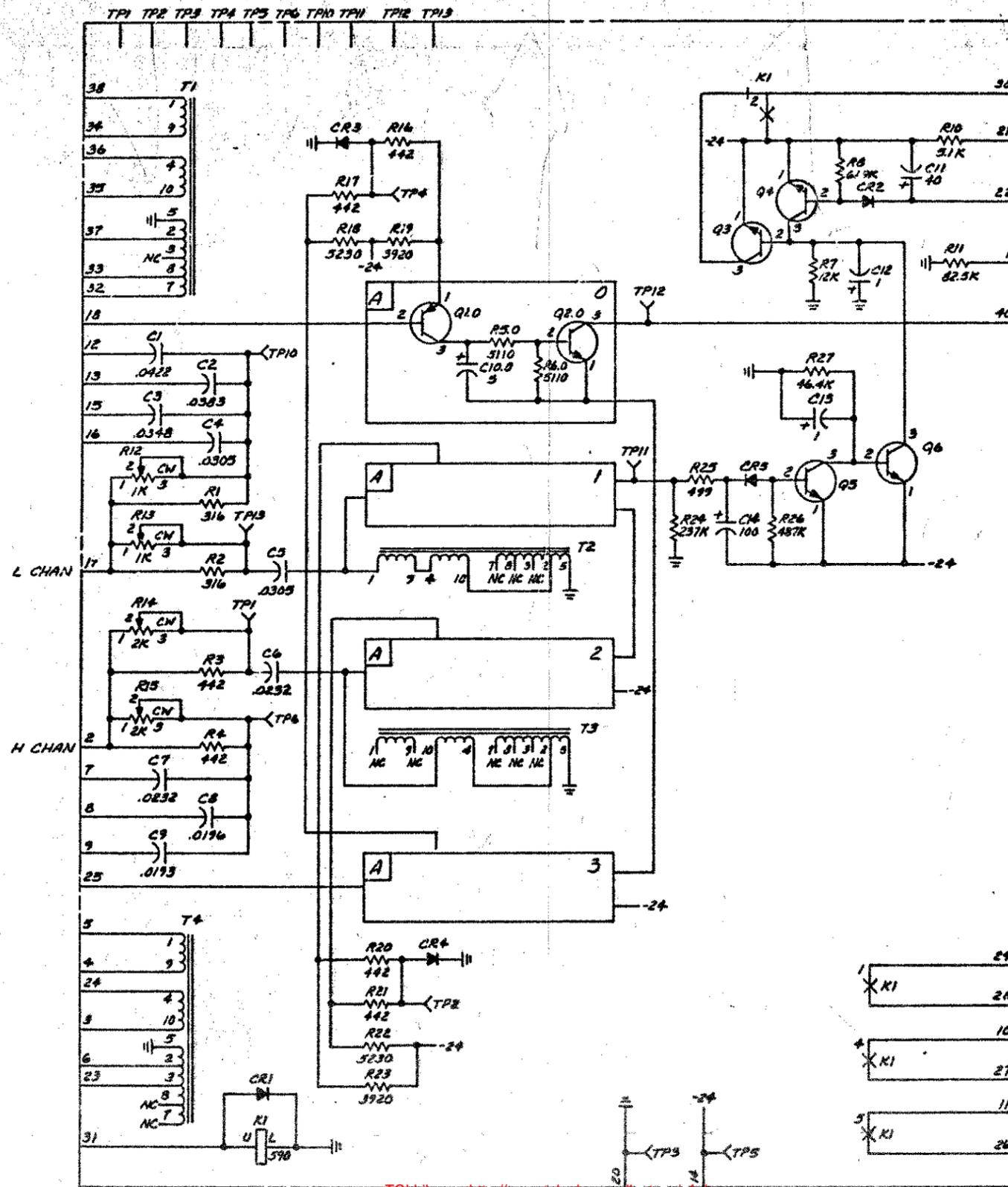
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-J12

BELL TELEPHONE LABORATORIES INCORPORATED

6S

PART OF CPS 16
 SUPPLEMENTARY SIGNAL DETECTOR AND
 INHIBIT CIRCUIT



SUPPLEMENTARY SIGNAL DETECTOR AND INHIBIT

PART OF CPS 16

11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-J13A

SD-1C245-01-J13A

PART OF CPS 16
SUPPLEMENTARY SIGNAL DETECTOR AND
INHIBIT CIRCUIT

CIRCUIT REQUIREMENTS

APPARATUS				MECH REQ			CIRCUIT PREPARATION				DIRECT CURRENT FLOW REQ			REMARKS		
DESIG	CODE	OPT	FIG	BSP FIG.	CONT PRES	ARM TRVL	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP	SEE TEST NOTE	TEST WDG	TEST FOR		AFTER SOAK	TEST READJ
								CONN RAT	CONN GRD							
RELAY																
K1	MB1			200				U(K1)	L(K1)	8/6	1				25.5	24.00

TEST NOTES:

1. REMOVE CIRCUIT PACK FROM EQUIPMENT UNIT TO TEST.

COMPONENT LIST

RELAY		OPTION			
DESIG	KT	CONT ARR	LOC	CONT ARR	LOC
CODE	MB1A				
5	EBM		A/H6		
4	EBM		A/H6		
3	EBM				
2	EBM		A/B6		
1	EBM		A/G6		
COIL			A/H3		

CAPACITOR

DESIG	CODE
C1	577D, .0422
C2	577D, .0383
C3	577D, .0348
C4	577D, .0309
C5	577D, .0305
C6	577D, .0232
C7	577D, .0232
C8	577D, .0196
C9	577D, .0193
[4] C10.0-C10.3	601A
C11	602A
C12	600A
C13	600A
C14	602A

DIODE

DESIG	CODE
CR1	400J
CR2	446D
CR3	446B
CR4	446B
CR5	446B

POTENTIOMETER

DESIG	CODE
R12	KS-20231, L2A, 1K
R13	
R14	KS-20231, L2A, 2K
R15	

COMPONENT LIST (CONT)

RESISTOR

DESIG	CODE
R1	KS-20616, L1A, 316
R2	KS-20616, L1A, 316
R3	KS-20616, L1A, 442
R4	KS-20616, L1A, 442
[4] R5.0-R5.3	KS-20616, L1A, 316
[4] R6.0-R6.3	KS-20616, L1A, 3110
R7	KS-13490, L1, 12K
R8	KS-20616, L1A, 61.9K
R10	KS-20616, L1A, 9.1K
R11	KS-20616, L1A, 82.5K
R16	KS-20616, L1A, 442
R17	KS-20616, L1A, 442
R18	KS-20810, L1A, 3230
R19	KS-20810, L1A, 3920
R20	KS-20616, L1A, 442
R21	KS-20616, L1A, 442
R22	KS-20810, L1A, 3230
R23	KS-20810, L1A, 3920
R24	KS-20616, L1A, 237K
R25	KS-20616, L1A, 499
R26	KS-20616, L1A, 487K
R27	KS-20616, L1A, 46.4K

TRANSFORMER

DESIG	CODE
T1-T4	2597A

TRANSISTOR

DESIG	CODE
[4] Q1.0-Q1.3	51C
[4] Q2.0-Q2.3	66F
Q3	66L
Q4	66J
Q5	66J
Q6	66J

INPUT/OUTPUT INFORMATION

FUNCTION: PROVIDES TWO DIGIT DETECTORS WHICH RESPOND TO VALID TOUCH-TONE DIGITS WHEN USED IN CONJUNCTION WITH CP3. A TIMER CIRCUIT PROVIDES AN INTERDIGITAL TIME-OUT PERIOD OF APPROXIMATELY 2.5 SECONDS.

A TIMER INHIBIT CIRCUIT BLOCKS THE TIMER CIRCUIT FOR APPROXIMATELY FIVE SECONDS AFTER THE RECEIPT OF THE TOUCH-TONE DIGIT (X).

INPUT: SQUARE WAVE SIGNAL OF APPROXIMATELY 1.25 VOLTS PEAK-TO-PEAK.

CIRCUIT DESCRIPTION

TWO HIGH CHANNEL AND TWO LOW CHANNEL TUNED CIRCUITS ARE PROVIDED WHICH ARE DRIVEN FROM THE HIGH AND LOW LIMITERS, RESPECTIVELY, OF CP3. ONE HIGH CHANNEL (T3) AND (C6) AND ONE LOW CHANNEL (T2) AND (C5) ARE CONNECTED TO RESPOND TO TONES L4 (941 HZ) AND H1 (1209 HZ). THE OTHER TWO TUNED CIRCUITS MUST BE STRAPPED IN ACCORDANCE WITH NOTE 218. OPERATION OF THE DETECTORS IS IDENTICAL WITH THAT OF CP3. THE TIMER CIRCUIT CONSISTS OF TRANSISTORS (Q3) AND (Q4), REFERENCE DIODE (CR2), AND ASSOCIATED RESISTORS AND CAPACITORS. IT PROVIDES AN INTERDIGITAL TIME-OUT PERIOD OF APPROXIMATELY 2.5 SECONDS WHEN USED WITH CP3. WITH RELAY (K1) RELEASED AND PINS 21 AND 22 CONNECTED EXTERNALLY, CAPACITOR (C11) IS DISCHARGED, TRANSISTOR (Q4) IS OFF AND (Q3) IS ON. THIS APPLIES -24 VOLTS TO PIN 30. WITH THE CONNECTION BETWEEN PINS 21 AND 22 REMOVED AND PINS 22 AND 1 CONNECTED EXTERNALLY, CAPACITOR (C11) CHARGES THROUGH RESISTOR (R11). IN APPROXIMATELY 2.5 SECONDS THE REFERENCE VOLTAGE OF DIODE (CR2) IS OVERCOME. THIS FORWARD BIASES (Q4) WHICH TURNS OFF (Q3) REMOVING VOLTAGE FROM PIN 30, RECONNECTING PINS 21 AND 22 DISCHARGES (C11) THROUGH (R10) PROVIDING A TIMER RECOVERY TIME OF APPROXIMATELY 0.2 SECONDS. WITH RELAY (K1) OPERATED CONTACT 2 CONNECTS -24 VOLTS TO PIN 30 DIRECTLY.

THE TIMER INHIBIT CIRCUIT CONSISTS OF TRANSISTORS (Q5) AND (Q6), REFERENCE DIODE (CR5), AND ASSOCIATED RESISTORS AND CAPACITORS. NORMALLY, TRANSISTOR (Q5) IS ON AND TRANSISTOR (Q6) IS OFF. WHEN THE L4-H1 TONE COMBINATION IS DETECTED, TRANSISTOR (Q2.1) APPLIES -24 VOLTS TO THE INHIBIT CIRCUIT, CAPACITOR (C13) DISCHARGES THROUGH RESISTOR (R24) AND TRANSISTOR (Q2.1). WHEN THE VOLTAGE ACROSS CAPACITOR (C13) GOES BELOW THE REFERENCE LEVEL OF DIODE (CR5), TRANSISTOR (Q3) TURNS OFF AND TRANSISTOR (Q4) TURNS ON. TRANSISTOR (Q4) APPLIES -24 VOLTS TO THE BASE OF TRANSISTOR (Q3) HOLDING TRANSISTOR (Q3) OFF. WHEN THE L4-H1 TONE IS GONE, TRANSISTOR (Q2.1) TURNS OFF ALLOWING CAPACITOR (C13) TO RECHARGE THROUGH RESISTORS (R24) AND (R25). WHEN THE VOLTAGE ACROSS CAPACITOR (C13) REACHES THE REFERENCE LEVEL OF DIODE (CR5), TRANSISTOR (Q5) TURNS ON, TRANSISTOR (Q6) TURNS OFF, AND THE TIMER CIRCUIT IS RESTORED TO NORMAL.

MANUFACTURING REFERENCES

CATEGORY	NO.
CIRCUIT PACK CODE AND ASSEMBLY DRAWING	ED-16307-()G2
CONNECTOR ON FRAME	908C

SYMBOL

SHOWN IN FS31

NOTES:

- UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS; VALUES PRECEDED BY THE SYMBOL + (PLUS) ARE - (MINUS) ARE IN VOLTS.
- $\frac{1}{2}$ GROUND RETURN.

SUPPLEMENTARY SIGNAL DETECTOR AND INHIBIT

PART OF CPS 16

1118

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-01-J138

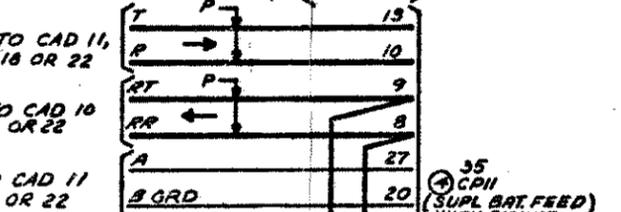
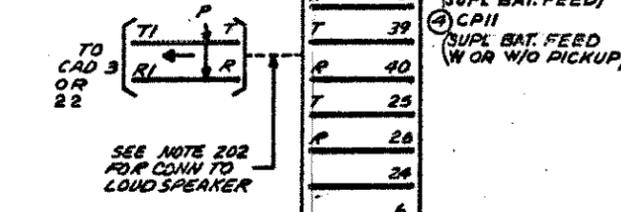
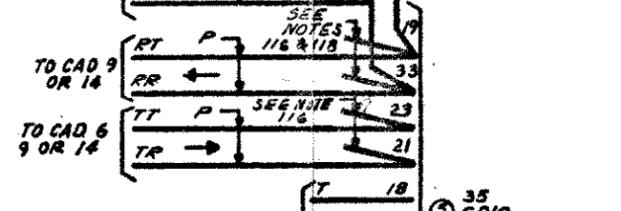
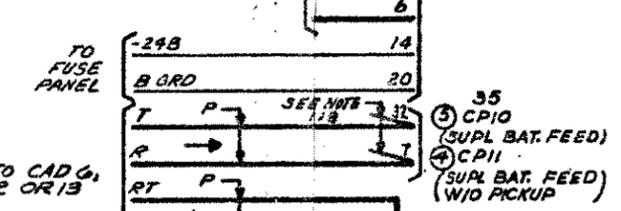
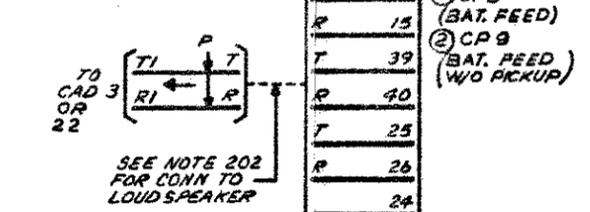
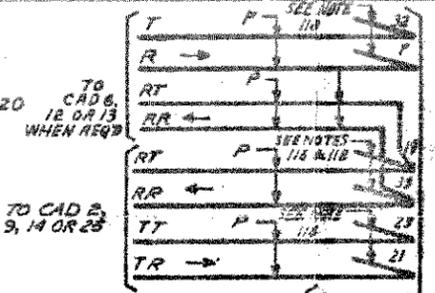
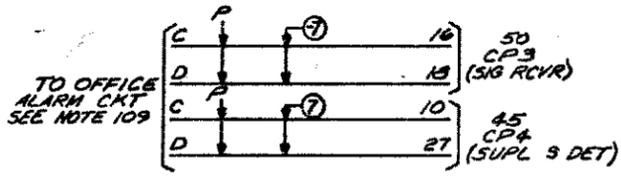
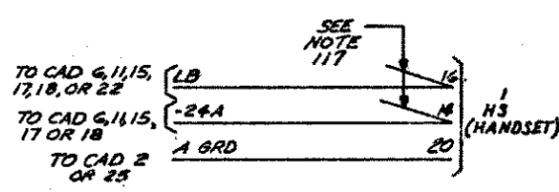
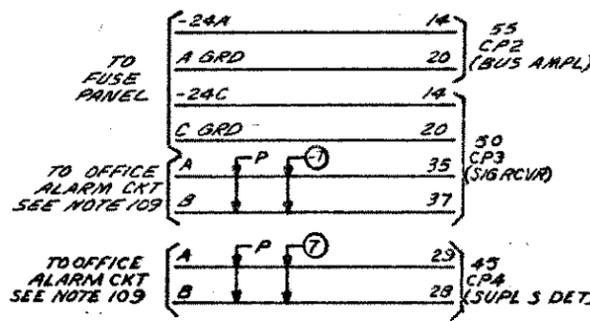
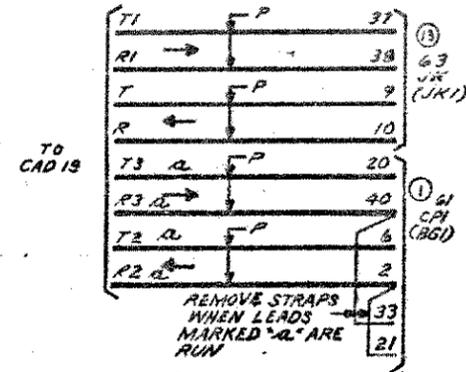
SELL TELEPHONE LABORATORIES
INCORPORATED

65

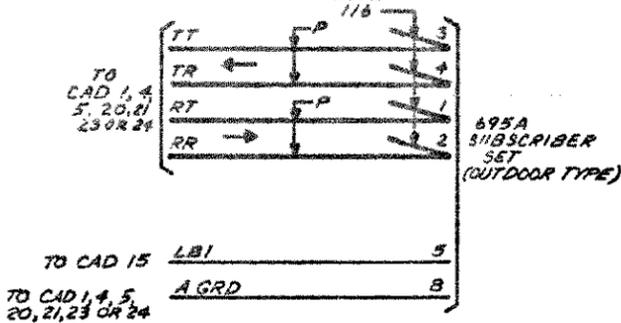
PRINTED IN U.S.A.

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3A	
4B	
5B	

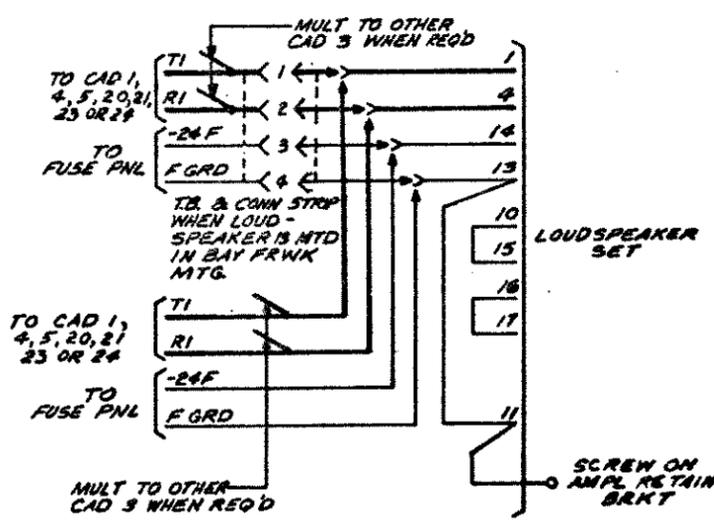
CAD 1
FOR J39340A, L1-L10
FOR APP FIG. 1, 3-7, 13, 19, & 20



CAD 2 (MFR DISC.)
FOR APP FIG. 27



CAD 3
FOR APP FIG. 25



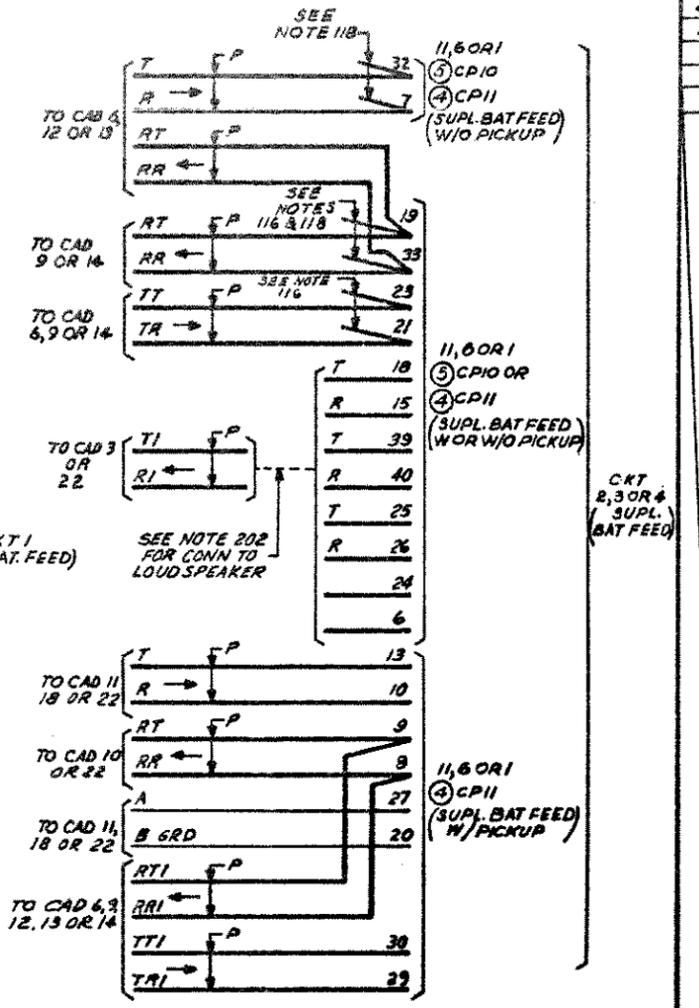
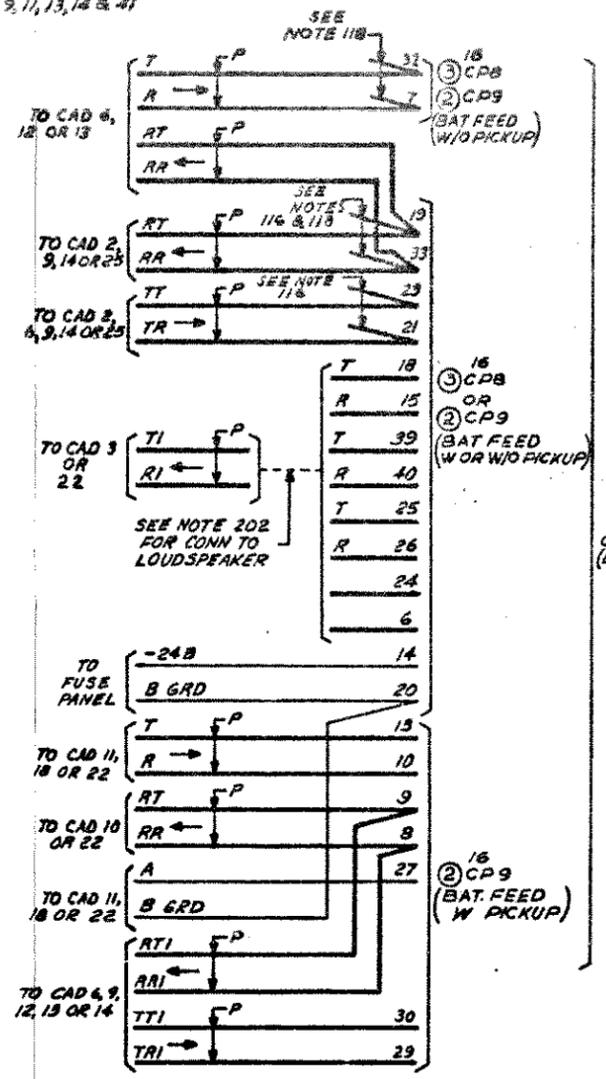
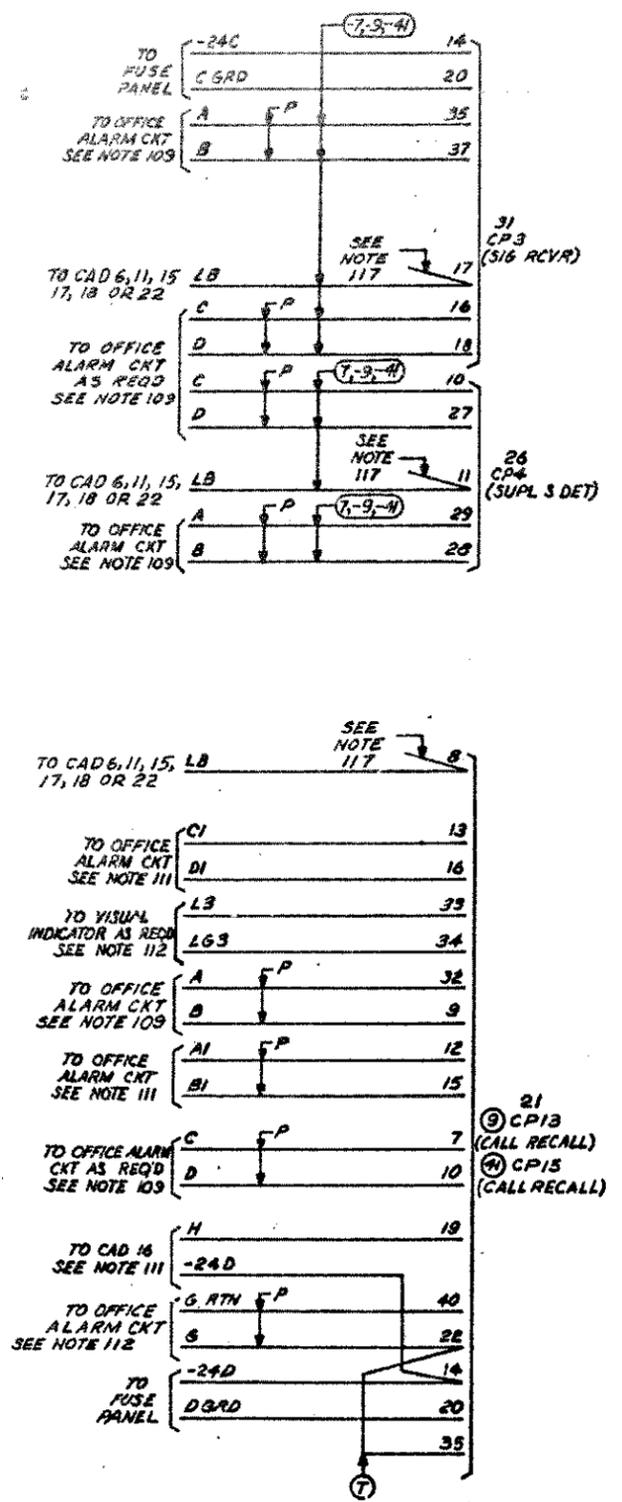
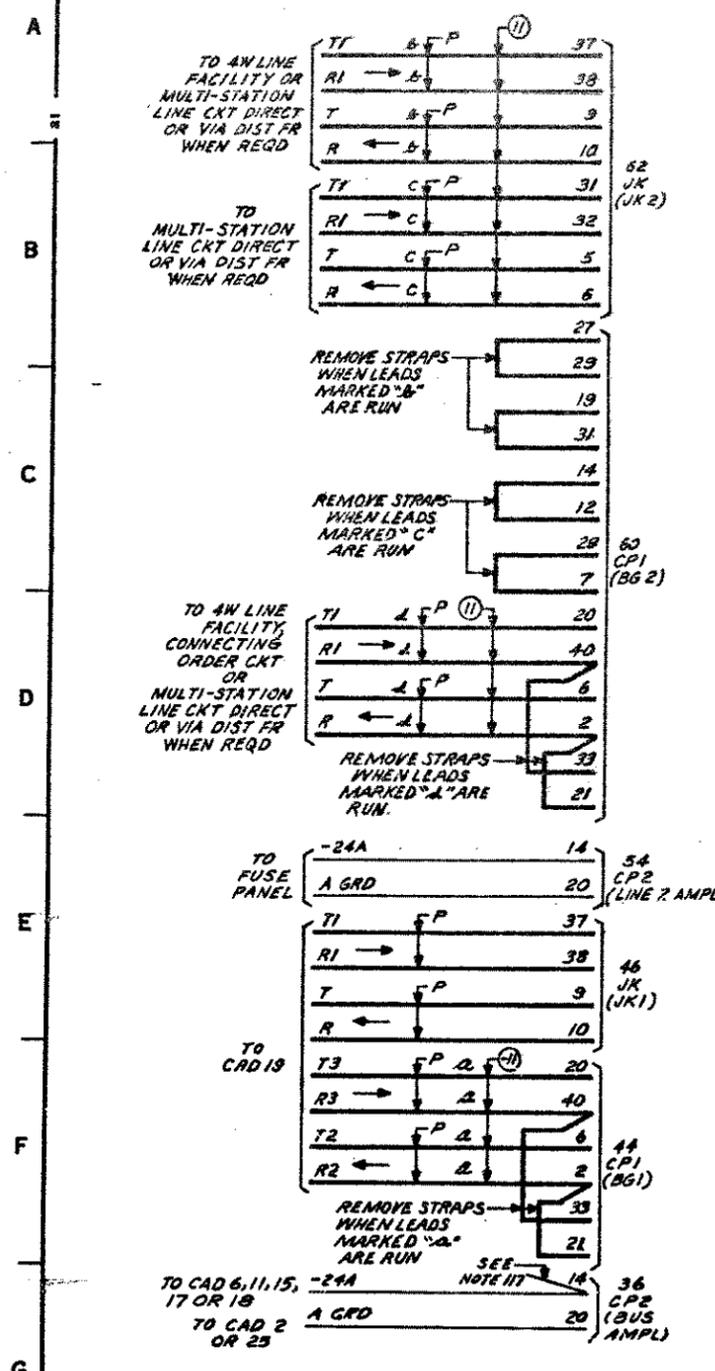
SD-1C245-01-01

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-1C245-01-01
BELL TELEPHONE LABORATORIES INCORPORATED	6S

ISSUE 11B

CAD 4
 FOR J99390B, LI-112
 FOR APP FIG. 1-7, 9, 11, 13, 14 & 41

DRAWING	1
ISSUE	118
2A	
3A	
4B	
5B	



CKT 2, 3 OR 4 (SUPL. BAT FEED)

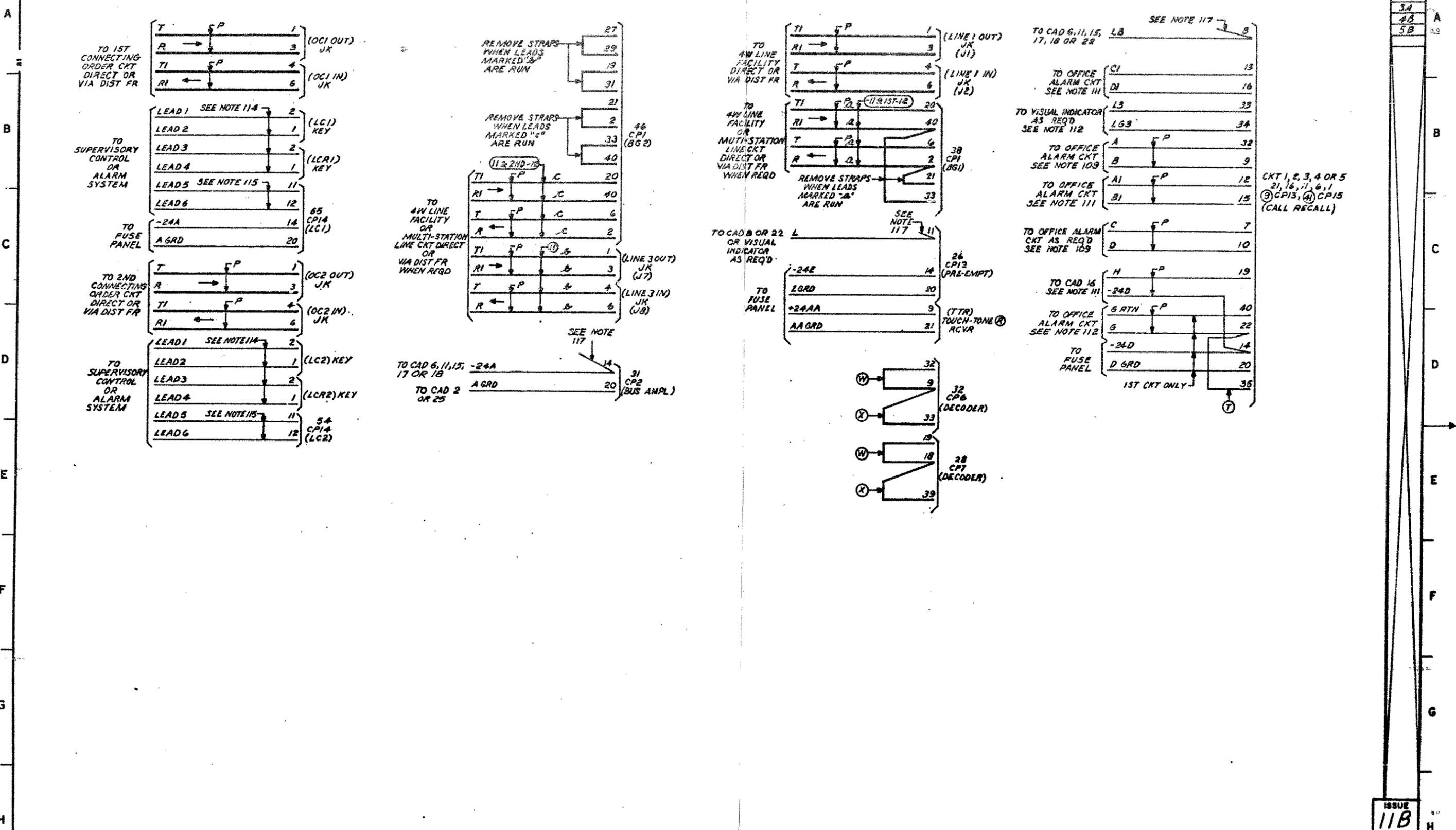
SD-1C245-01-62

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-1C245-01-62
BELL TELEPHONE LABORATORIES INCORPORATED	6S

PART OF CAD 5

FOR J99340C
FOR APP FIG. 1-3, 8-12, 15-18 & 41

DRAWING ISSUE	
1	
2A	
3A	
4B	
5B	



ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT

SD-1C245-CI-03

BELL TELEPHONE LABORATORIES
INCORPORATED

65

SD-1C245-01-03

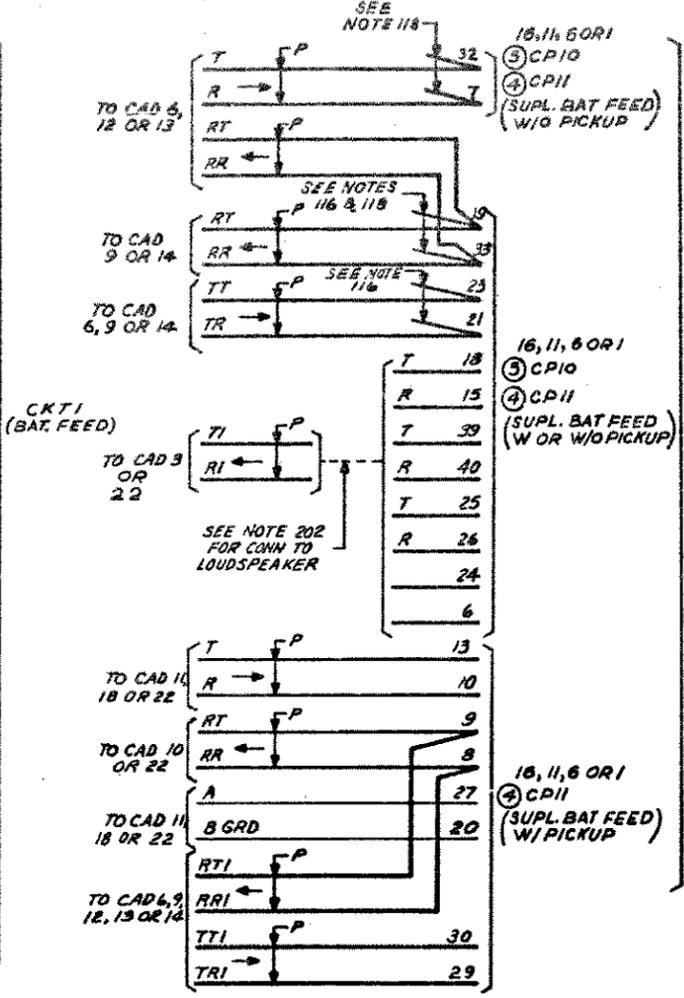
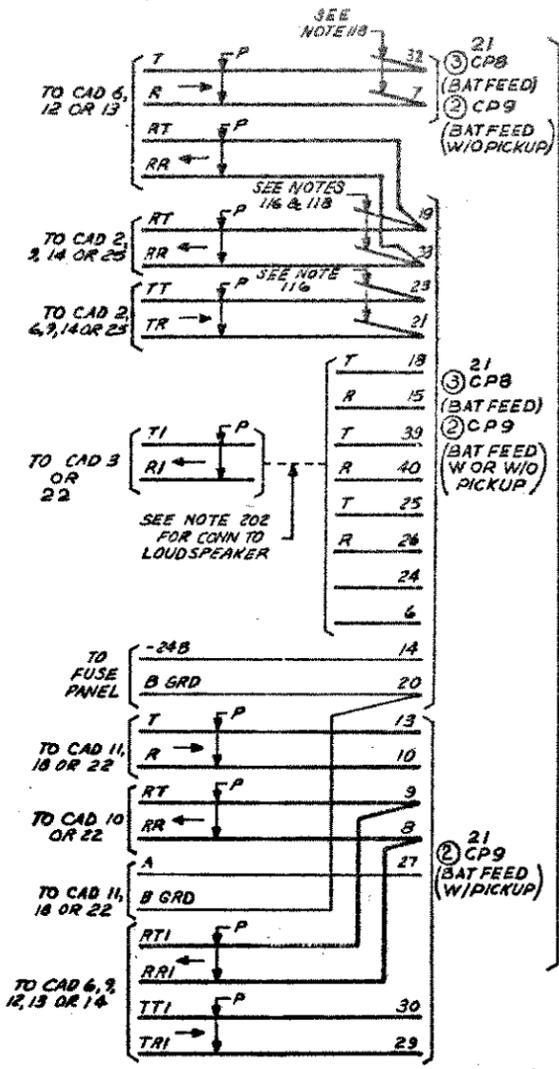
PART OF CAD 5

FOR J39340C
FOR APP FIG. 1-5, 8-12, 15-18 & 41

DRAWING	ISSUE
1	22
	2A
	3A
	4B
	5B

A
B
C
D
E
F
G
H

A
B
C
D
E
F
G
H

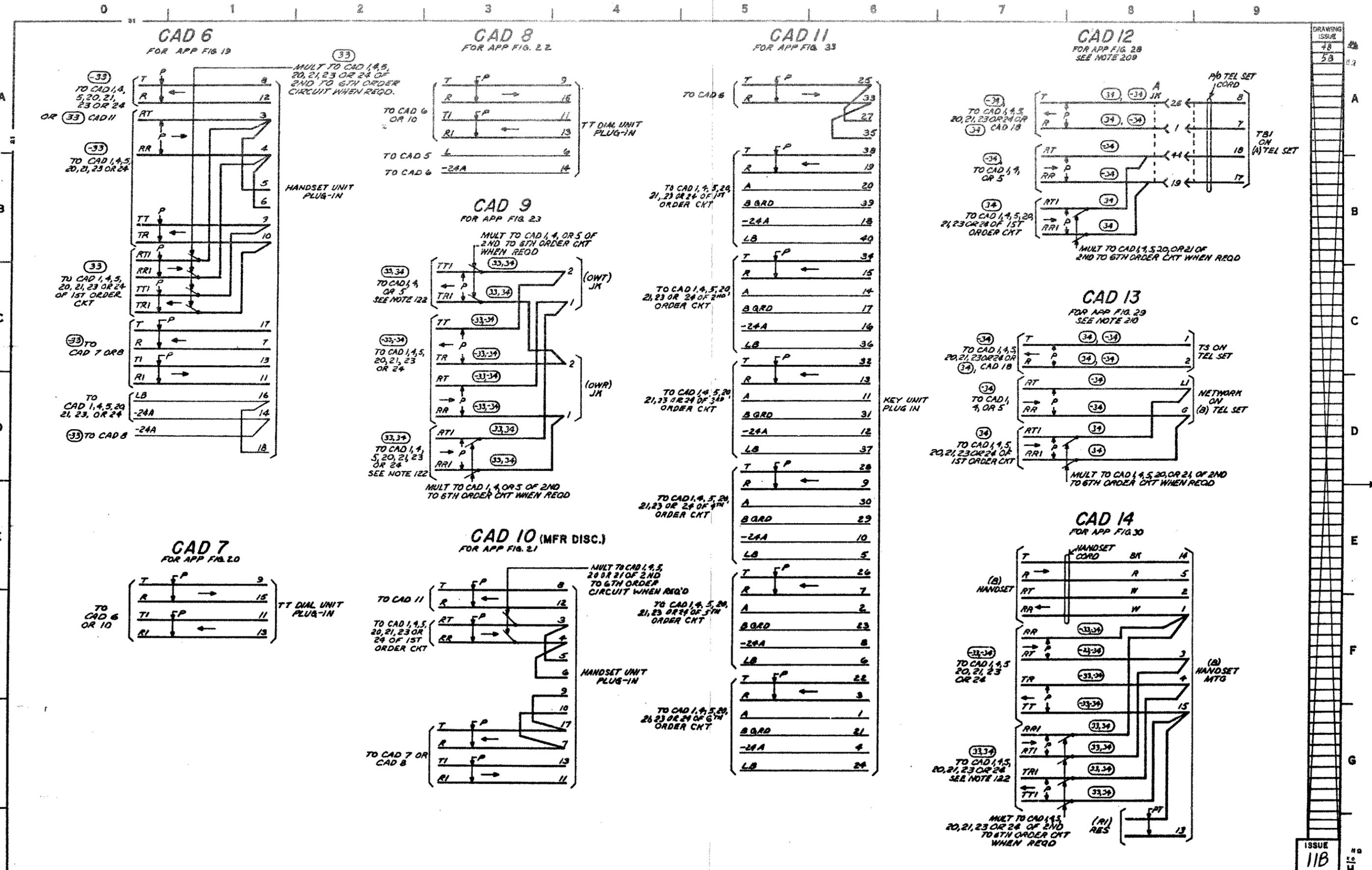


CKT 2, 3, 4 OR 5 (SUPL. BAT FEED)

ISSUE 11B

SD-1C245-01-G4

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	SD-1C245-01-G4
BELL TELEPHONE LABORATORIES INCORPORATED	65



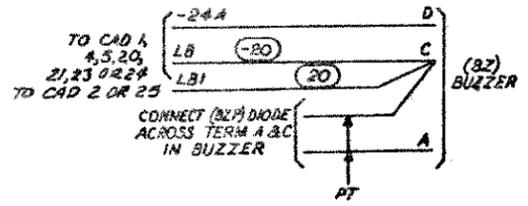
DRAWING
ISSUE
48
58

ISSUE
11B

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT		SD-IC245-01-G5
BELL TELEPHONE LABORATORIES INCORPORATED		
65		

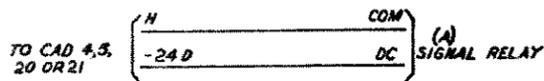
CAD 15

FOR APP FIG. 31



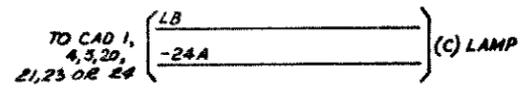
CAD 16

FOR APP FIG. 26



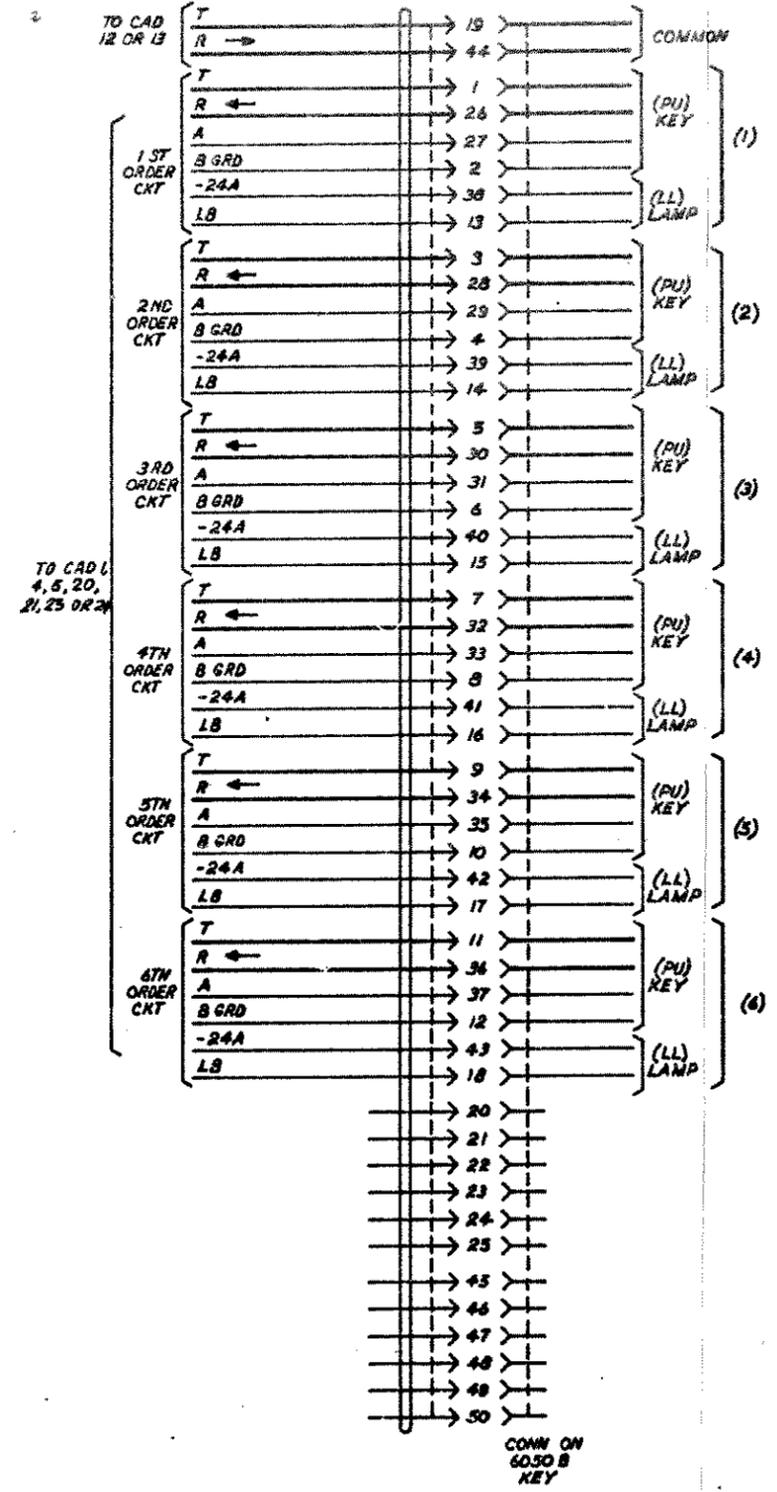
CAD 17

FOR APP FIG. 32



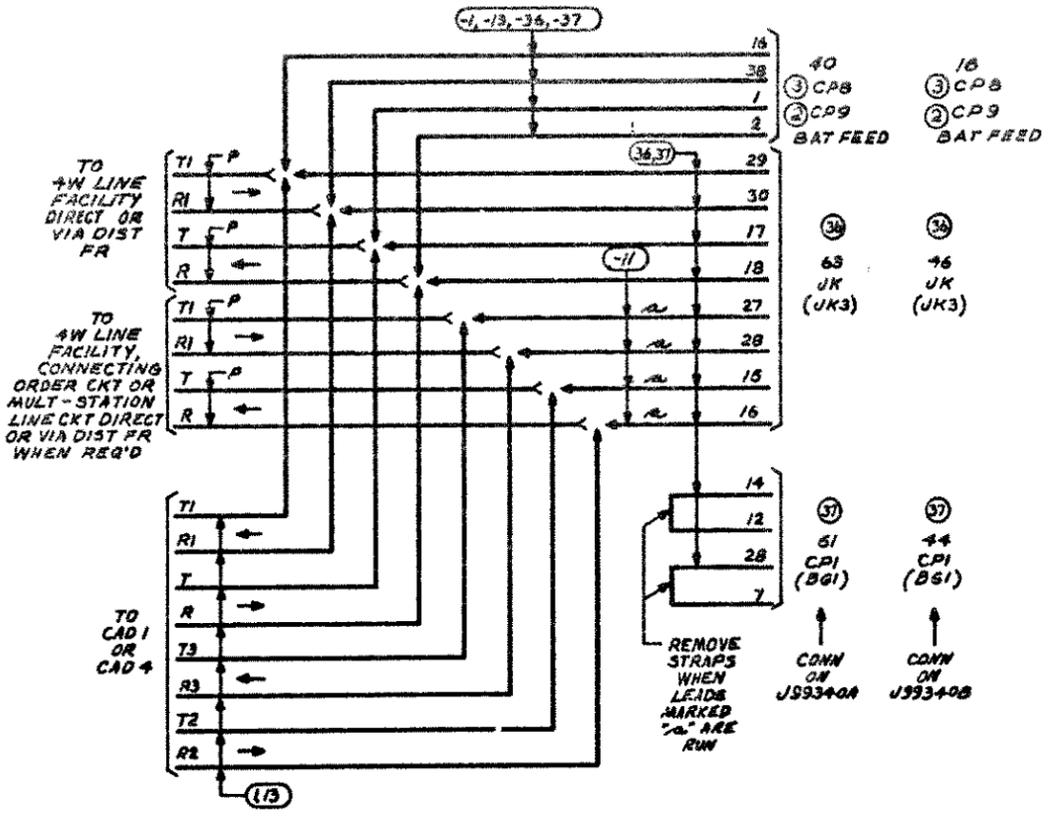
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FOR APP FIG. 34



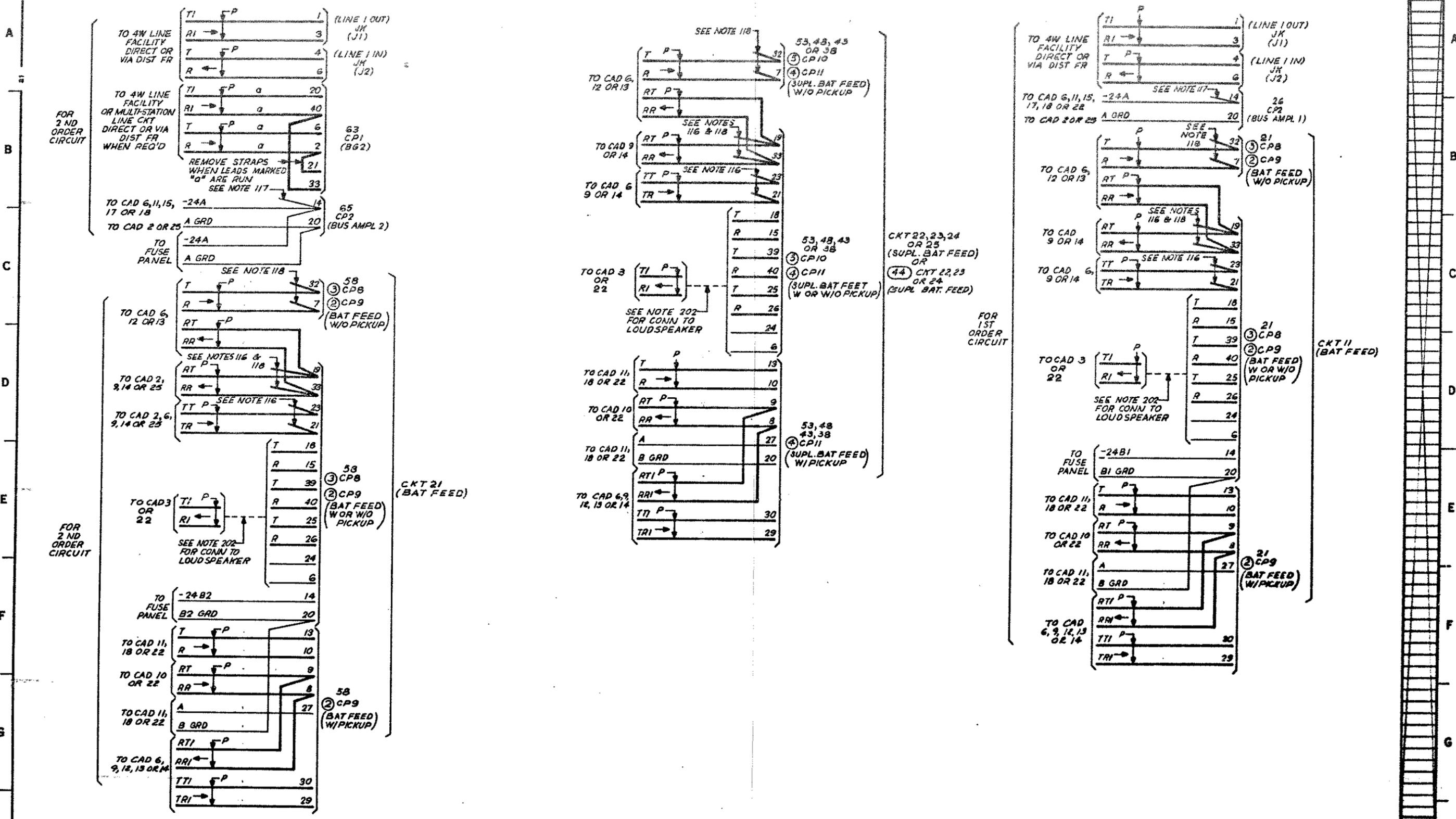
CAD 19

FOR J99340 A, L11-L15 OR FOR J99340 B, L13-L17 FOR APP FIG. 36, 37



PART OF CAD 20
 FOR 199540D
 FOR APP FIG. 1-5, 8, 9, 15, 39, 40 & 41

DRAWING ISSUE 58



SD-1C245-01-67

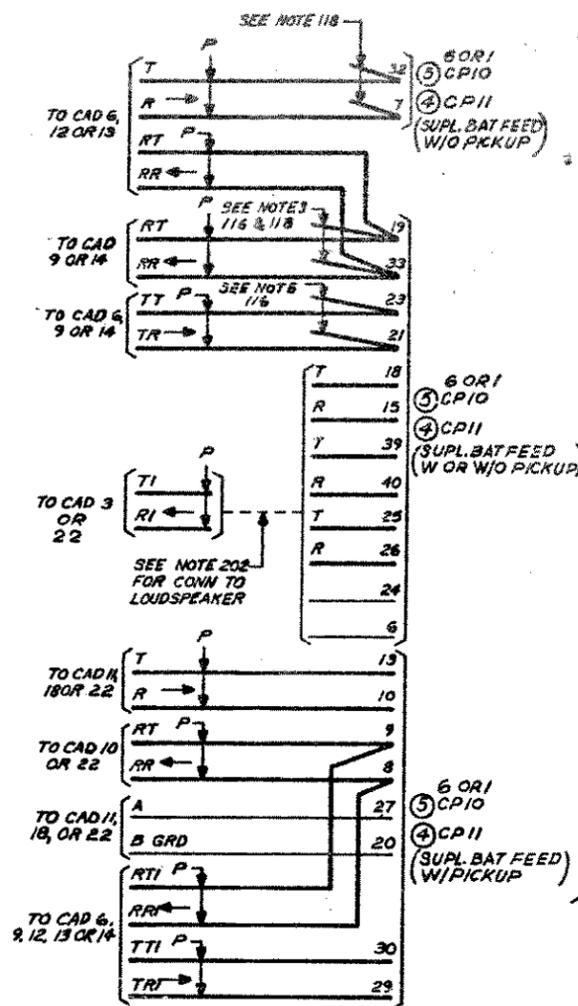
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT		SD-1C245-01-67
BELL TELEPHONE LABORATORIES		65

PART OF CAD 21

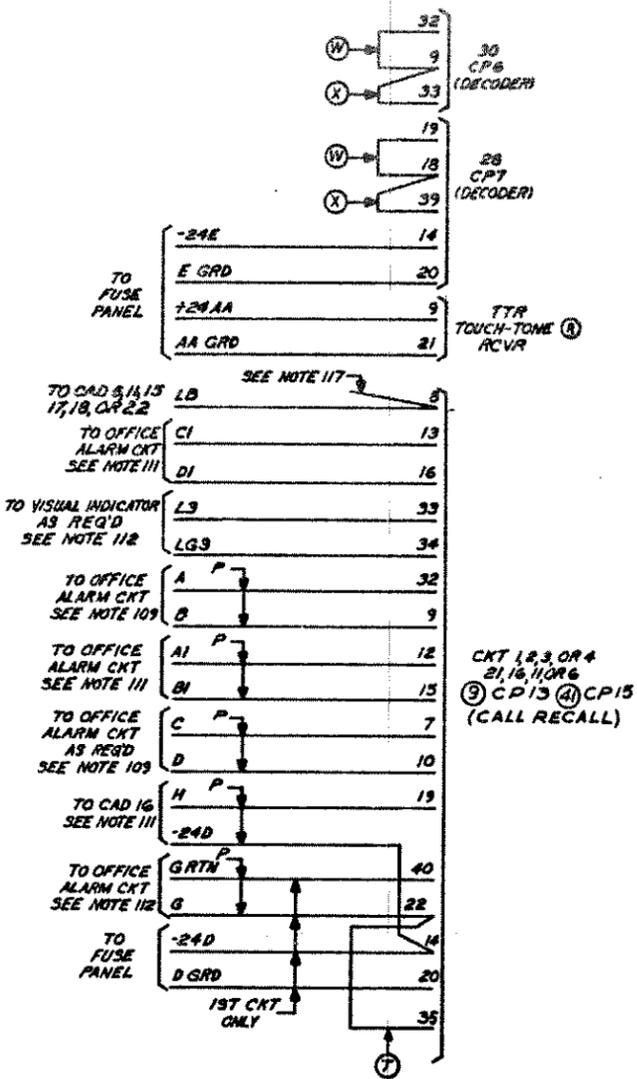
FOR J93540E
FOR APP FIG. 1-3, 5, 9, 13, 18, 20 & 41

CAD 22

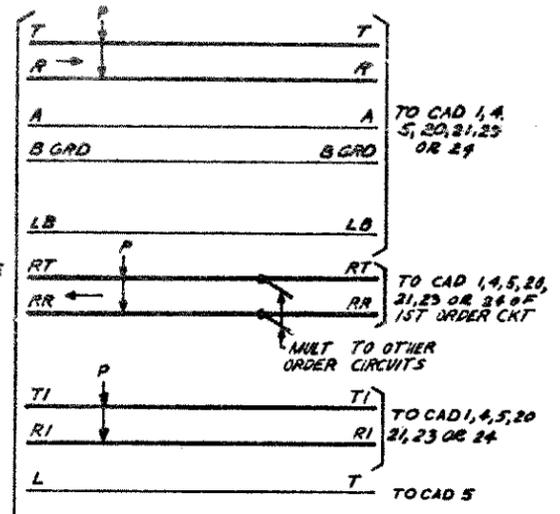
DRAWING
ISSUE
5 B



CKT 12, OR 13 (SUPL. BAT FEED)



TO MAINTENANCE CONTROL CONSOLE CKT



ISSUE
11B

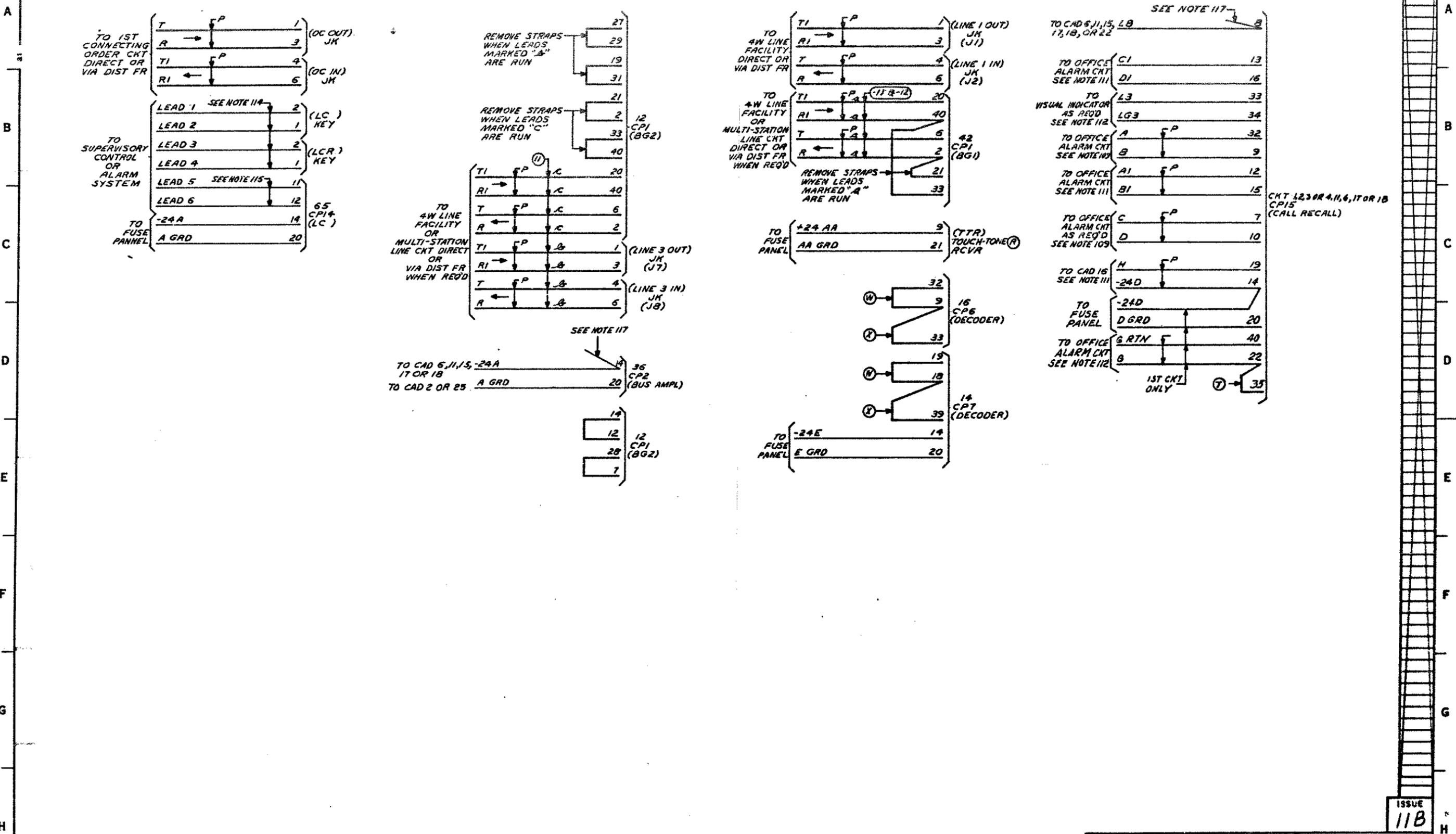
GENERAL PURPOSE 4-WIRE ORDER CIRCUIT ② SD-1C245-01-G10

BELL TELEPHONE LABORATORIES INCORPORATED 65

SD-1C245-01-G10

PART OF CAD 24
 FOR J99340G
 FOR APP FIG. 1, 2, 4, 8, 11, 12, 15-18, 41

DRAWING
ISSUE
11B



SD-IC245-01-G12

GENERAL PURPOSE 4-WIRE ORDER CIRCUIT	②	SD-IC245-01-G12
BELL TELEPHONE LABORATORIES		

PART OF CAD 24

FOR J993406
FOR APP FIG. 1, 2, 4, 8, 11, 12, 13-18, 41

CAD 25

FOR APP FIG 48

A
B
C
D
E
F
G
H

DRAWING ISSUE
A
B
C
D
E
F
G
H

