

DATE	ISSUED	DATE	ISSUED	DESIGN	APPROV
11-10-71	1	11-18-72	1	APPLIA	JCD FAC VAM
	2A				JSD VAM JES VAM

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	J98718AE-( )

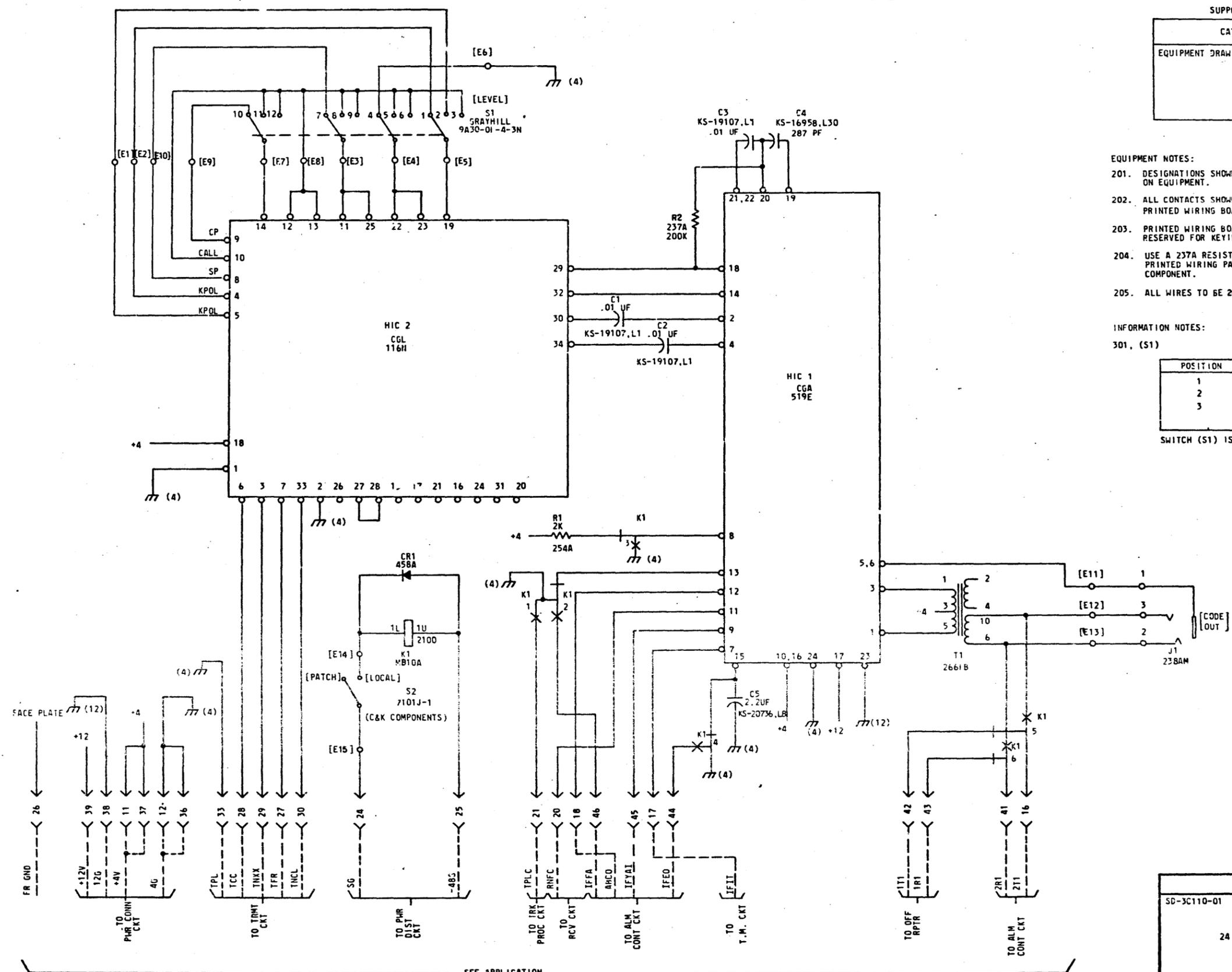
- EQUIPMENT NOTES:
- DESIGNATIONS SHOWN IN BRACKETS [ ] SHALL APPEAR ON EQUIPMENT.
  - ALL CONTACTS SHOWN WITH ARROW  $\rightarrow$  ARE PART OF PRINTED WIRING BOARD FINGER CONTACTS.
  - PRINTED WIRING BOARD FINGER CONTACTS 9 & 34 ARE RESERVED FOR KEYING.
  - USE A 237A RESISTOR IN PLACE OF A 254A WHENEVER PRINTED WIRING PATHS MUST BE RUN BENEATH THE COMPONENT.
  - ALL WIRES TO BE 26 GA STRANDED.

INFORMATION NOTES:

301. (S1)

POSITION	DESIGNATION
1	[0 dBmD]
2	[RCVR]
3	[IDLE]

SWITCH (S1) IS SHOWN IN POSITION 1.



SEE APPLICATION SCHEMATIC

SD-3C110-01	IN22	AT&T CO STANDARD
COMMON SYSTEMS 24 CHANNEL PCM BANK TYPE D3 1KHZ CODE GENERATOR CIRCUIT		SD-3C110-01
BELL TELEPHONE LABORATORIES INCORPORATED		65

SD-3C110-01