

SHEET INDEX	
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CIRCUIT NOTES(CONT):

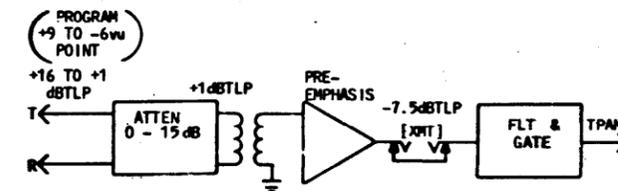
- TO PREVENT ELECTRICAL INTERFERENCE, THE FOLLOWING INSTALLATION RULES MUST BE ADHERED TO:
 - PGCU-ST MAY BE LOCATED IN CHANNEL POSITIONS 2 THROUGH 11 ONLY.
 - PGCU-ST MAY NOT BE LOCATED EXACTLY FOUR CHANNEL POSITIONS ON EITHER SIDE OF A PGCU-ST.
 - FOR EACH LOCATION SELECTED FOR INSTALLATION OF PGCU-ST, THE CHANNEL UNIT LOCATED TWELVE POSITIONS GREATER MUST BE REMOVED AND A BLANK UNIT INSTALLED IN THE VACATED CHANNEL POSITION.
- THE ATTENUATOR SETTING IS INDICATED BY THE SUM OF THE DIGITS EXPOSED UPON OPERATION OF THE SLIDERS. TO DETERMINE THE REQUIRED SETTING, ADD 6 TO THE VOLUME UNIT(VU) LEVEL EXPECTED AT THE T AND R TERMINALS. FOR EXAMPLE, IF THE EXPECTED LEVEL IS +8vu, THE ATTENUATOR SETTING SHOULD BE 14 dB. (+8vu +6=14). THE INPUT RANGE IS +9vu TO -6vu. UNITS ARE TO BE SHIPPED WITH MAXIMUM ATTENUATOR SETTING.

EQUIPMENT NOTES:

- DESIGNATIONS SHOWN IN BRACKETS [] SHALL BE SHOWN ON EQUIPMENT.
- ALL CONTACTS SHOWN WITH 50 ARE PART OF PRINTED WIRING BOARD FINGER CONTACTS (DESIGNATED AS PART OF P1 FOR IDENTIFICATION ONLY).
- WIRING FROM PINS 4 AND 5 OF IC1 TO CONNECTING RESISTORS SHALL BE LESS THAN 1 INCH IN LENGTH AND PAIRED. A GROUND PLANE CONNECTED TO 12 GROUND SHALL BE APPLIED TO BOTH SIDES OF PWB IN AREA OF IC1 AND ALL DIRECTLY CONNECTED COMPONENTS.
- UNLESS OTHERWISE SPECIFIED:
ALL RESISTORS ARE KS-20810,L1A.

INFORMATION NOTES:

- UNLESS OTHERWISE SPECIFIED:
ALL RESISTANCE VALUES ARE IN OHMS.
ALL CAPACITANCE VALUES ARE IN MICROFARADS.
ALL VALUES PRECEDED BY THE SYMBOL +(PLUS) OR -(MINUS) ARE IN VOLTS.



CIRCUIT LEVELS SHOWN IN dBTP ARE OBTAINED WITH STANDARD D3 CHANNEL ACCESS UNIT (CAU) TESTS. IF STANDARD PROGRAM TESTS ARE USED (APPLICATION OF 0dBm 1KHZ TEST TONE AT A PROGRAM +8vu POINT) IN A PROPERLY ADJUSTED PROGRAM CIRCUIT, THE LEVEL MEASURED AT THE XMT JACK, USING THE CAU WITH EXTERNAL DETECTOR, WILL BE -15dBm. ALSO, FOR STANDARD PROGRAM TEST TONE INJECTION AT THE XMT JACK THE CAU WITH EXTERNAL OSCILLATOR SET AT -15dBm SHOULD BE USED.

CD	CD	DATE
1	1	10-6920E
2A	2A	3-7-72

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER

102.

FEATURE OR OPTION	PROVIDE		
	FIG	APP OR WRG	QUANTITY

103.

RECORD OF FIGURES, WIRING & APPARATUS CHANGES						
CHANGED ON ISSUE	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NONE	USE IN CIRCUIT		
				STD	ASM	MD

104. FOR CONNECTING INFORMATION SEE APPLICATION SCHEMATIC FOR THE D3 BANK SD-3C104-().

HIGHEST COMPONENT USED ON THIS DRAWING			
C10	Q6	R23	Rv2
NOT USED			

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	J98718CA
X-SPEC	X-78908

NOTICE- NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

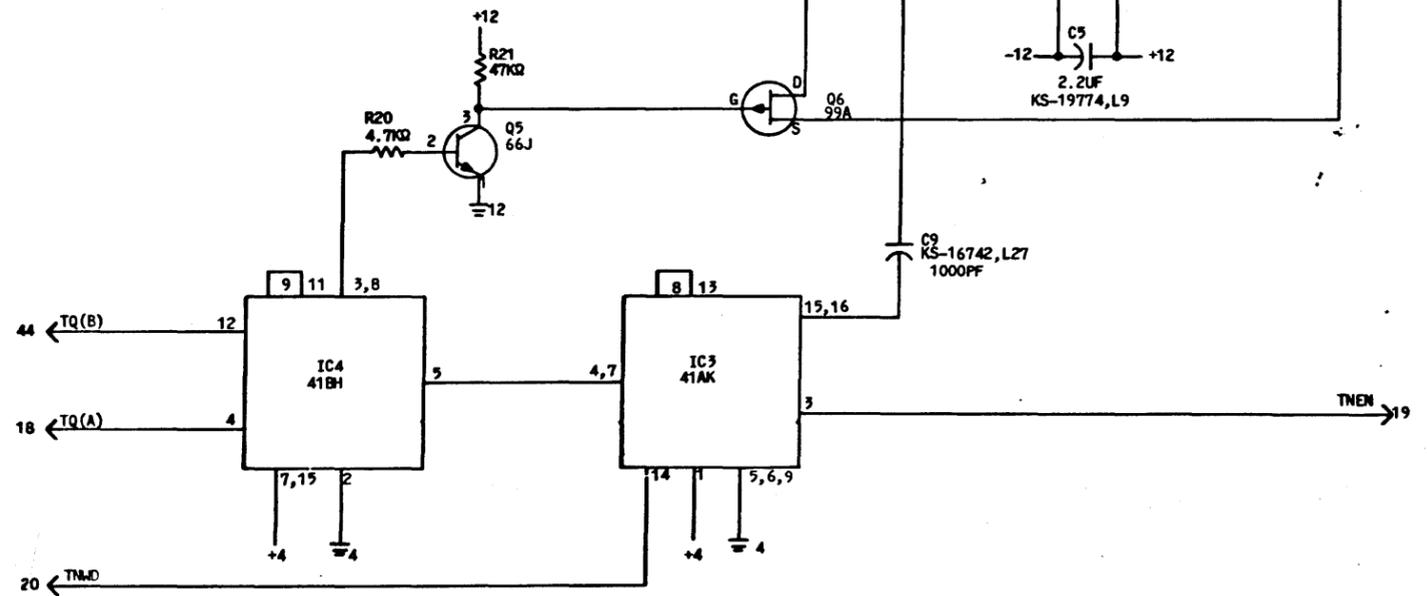
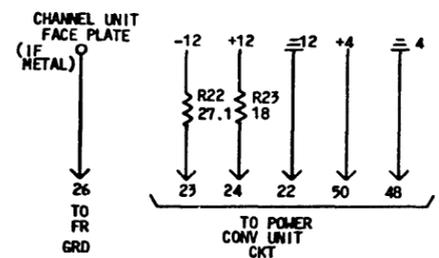
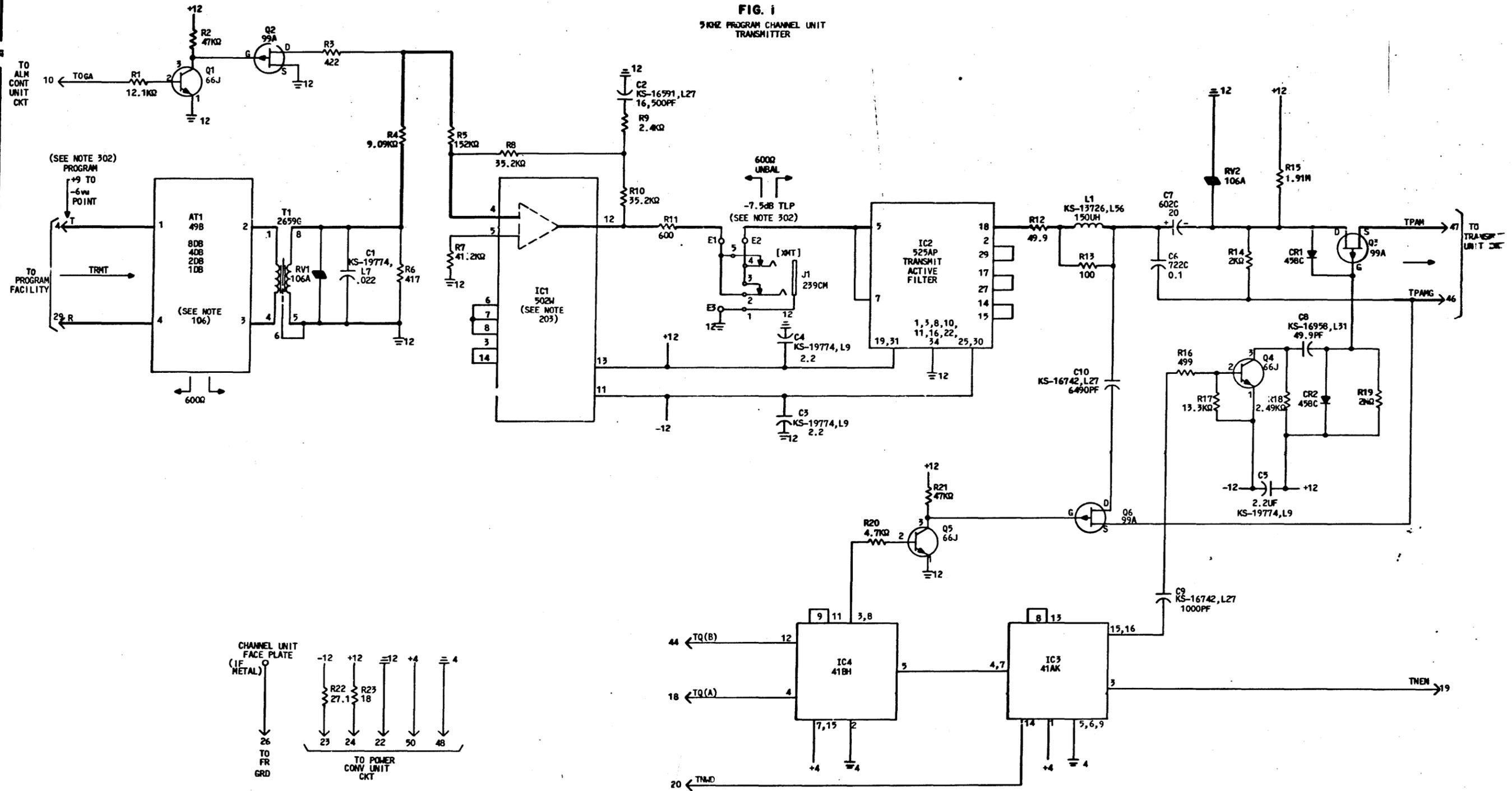
7M22
COMMON SYSTEMS
24 CHANNEL PCM BANK TYPE D3
5 KHZ PROGRAM
CHANNEL UNIT TRANSMITTER CIRCUIT

(D3CP) (PGCU-ST)
BELL TELEPHONE LABORATORIES INCORPORATED
6S

AT&TCO PROVISIONAL
SD-3C269-01-1
2 SHEETS

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FIG. 1
5 KHZ PROGRAM CHANNEL UNIT
TRANSMITTER



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SD-3C269-01-2